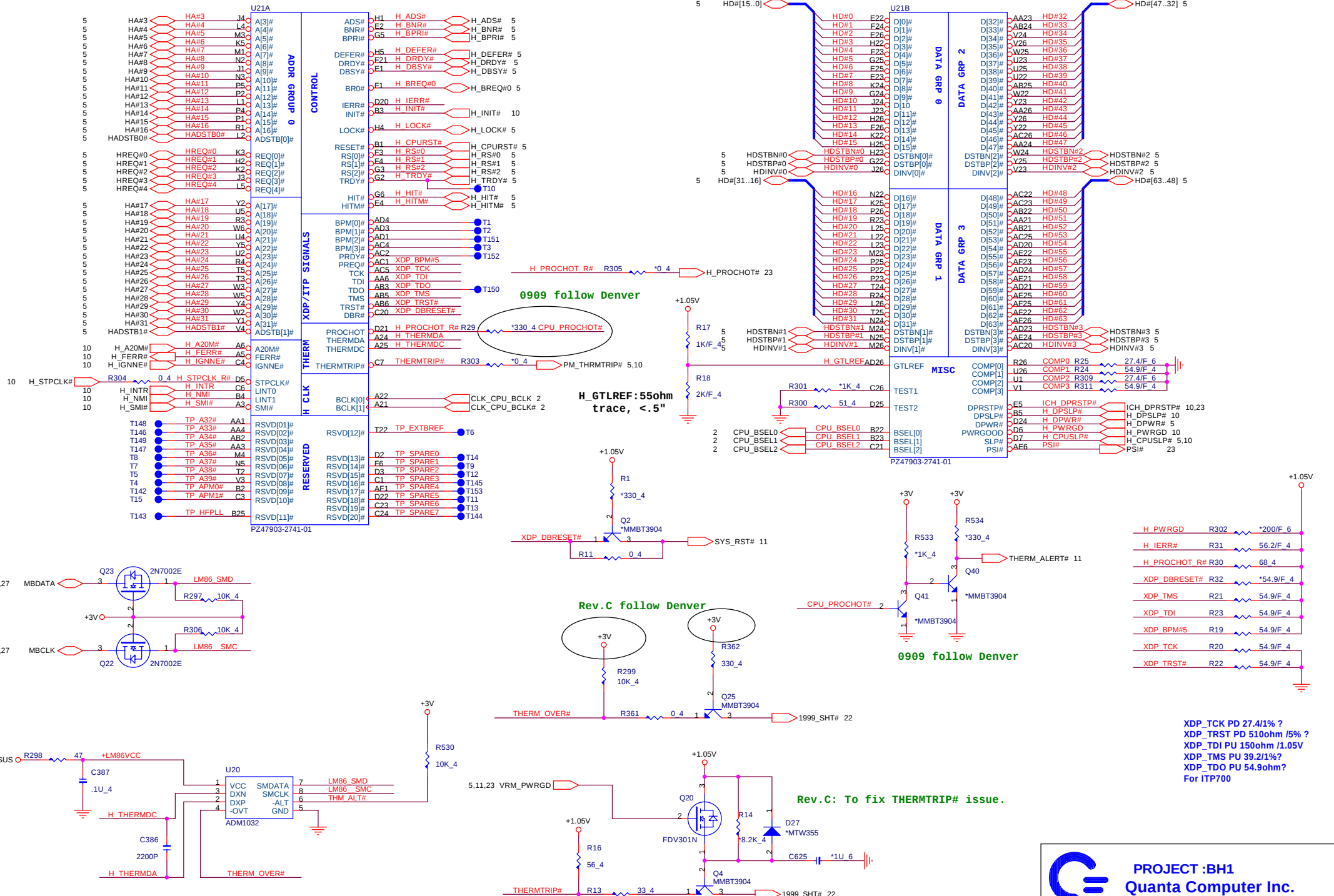
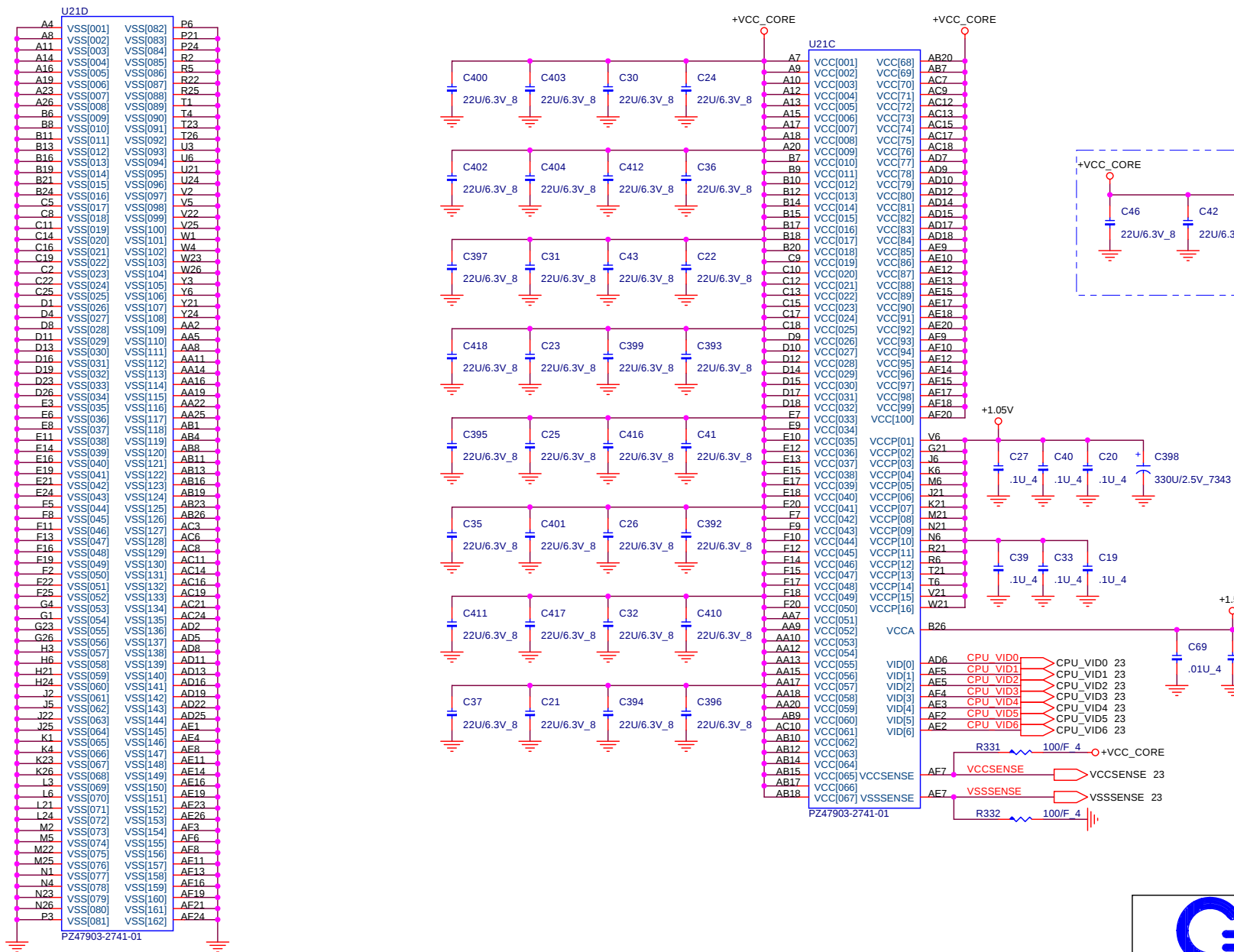
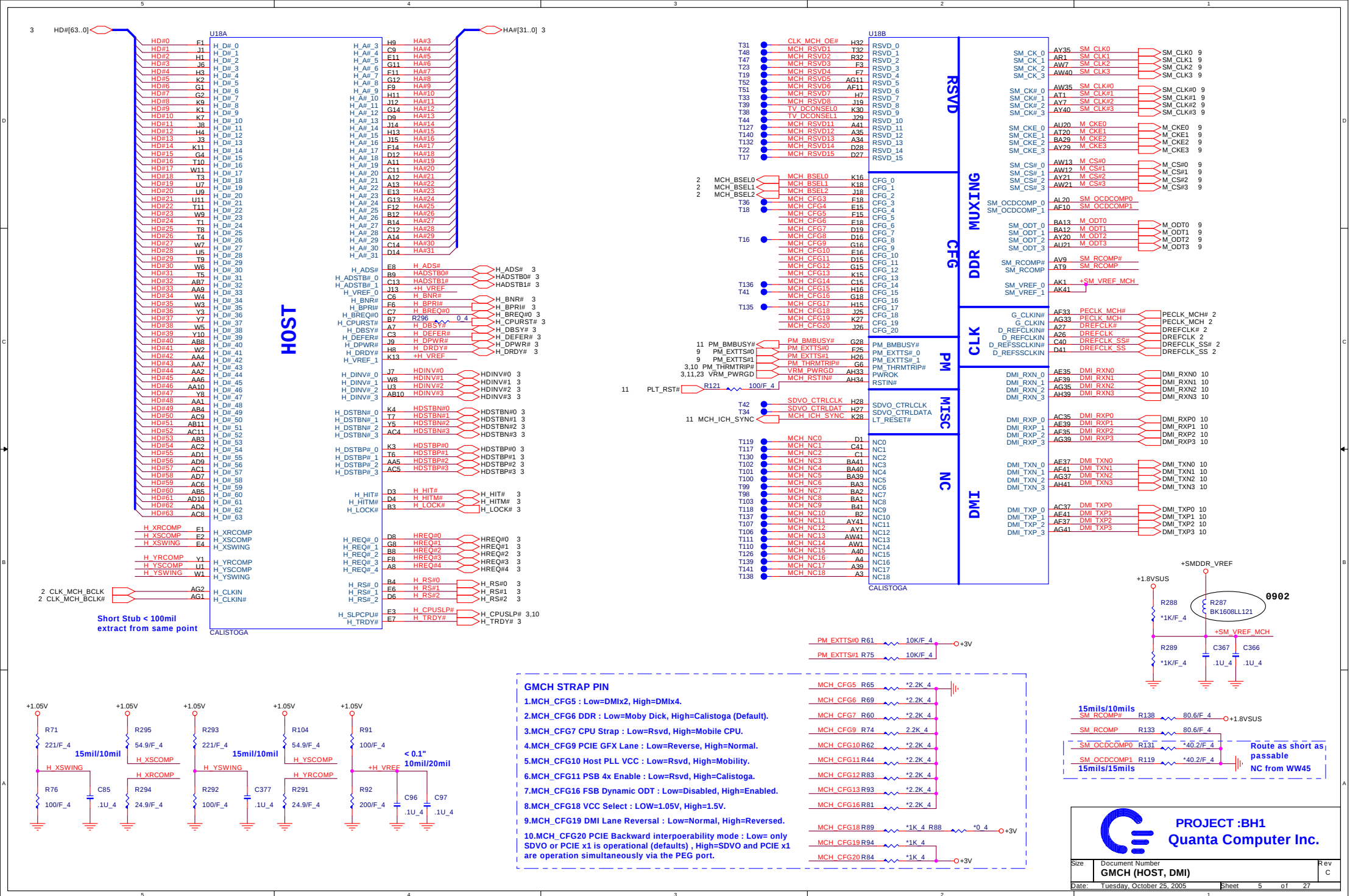
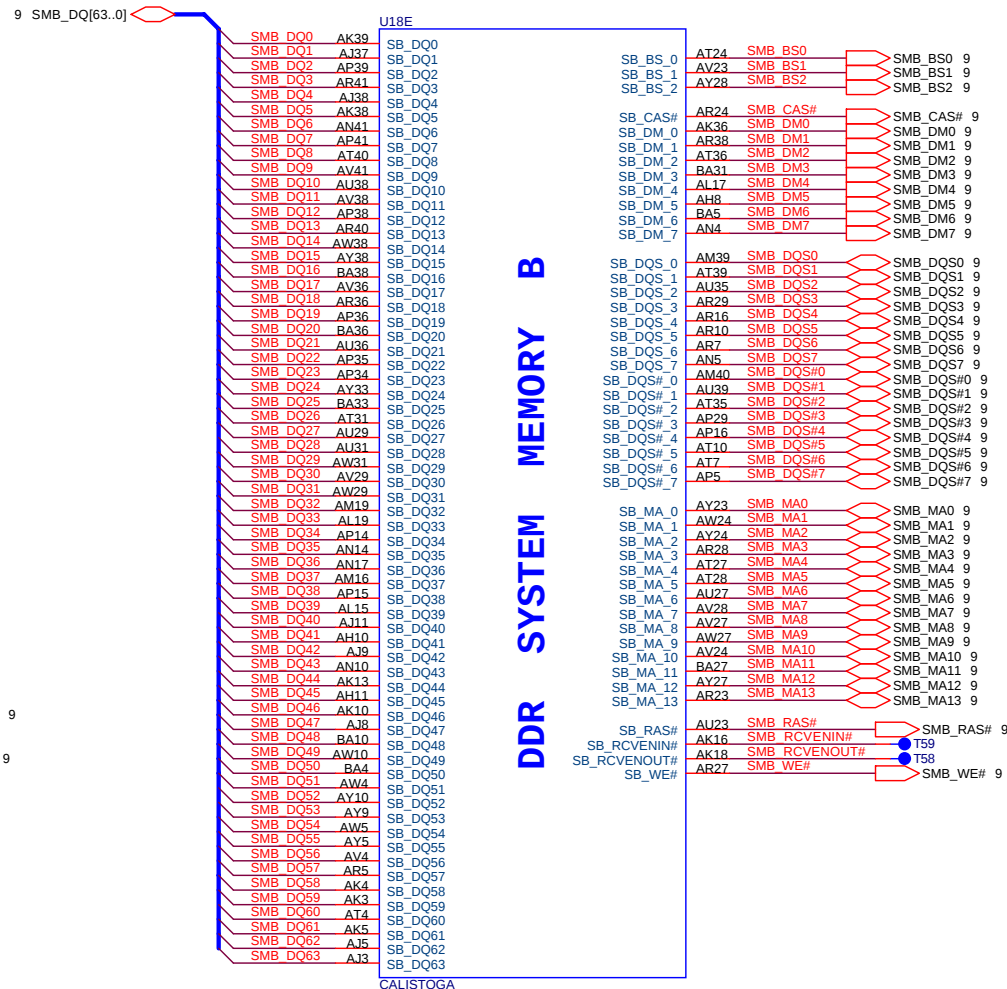
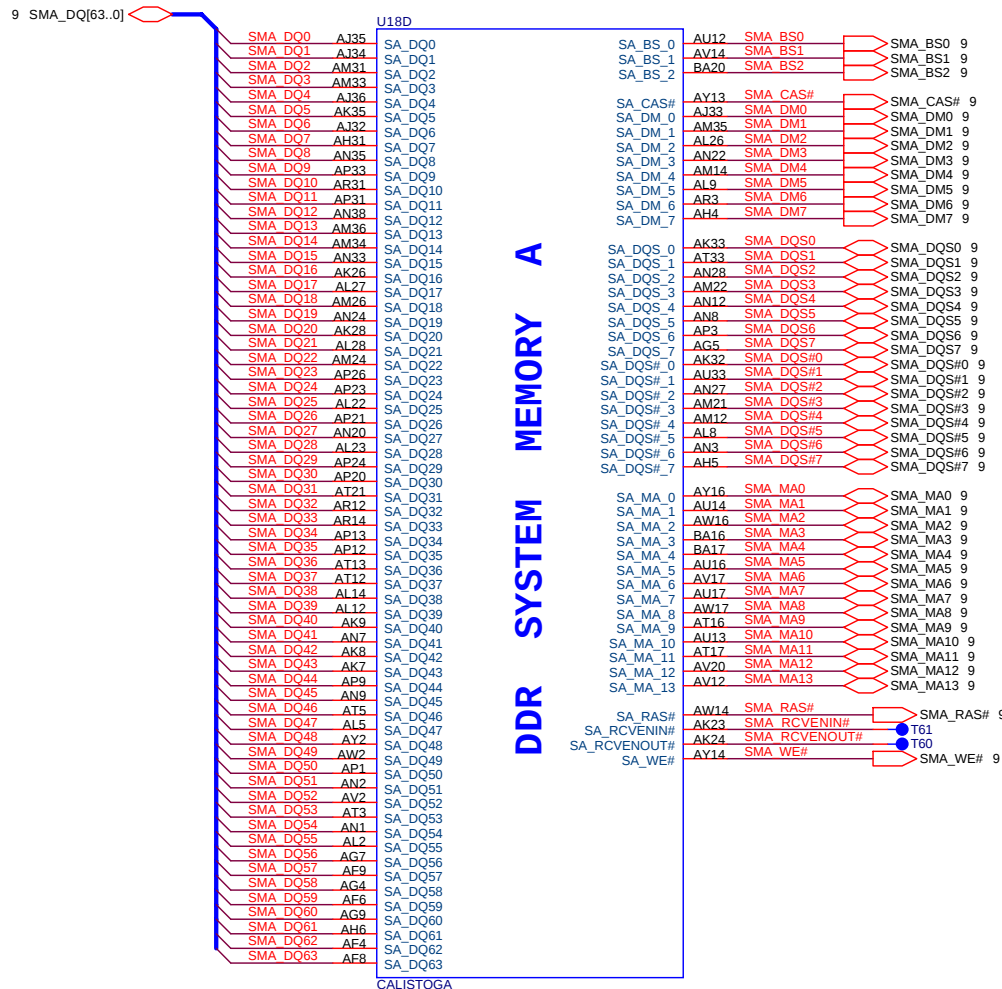




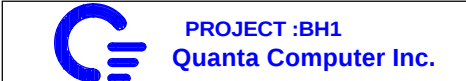
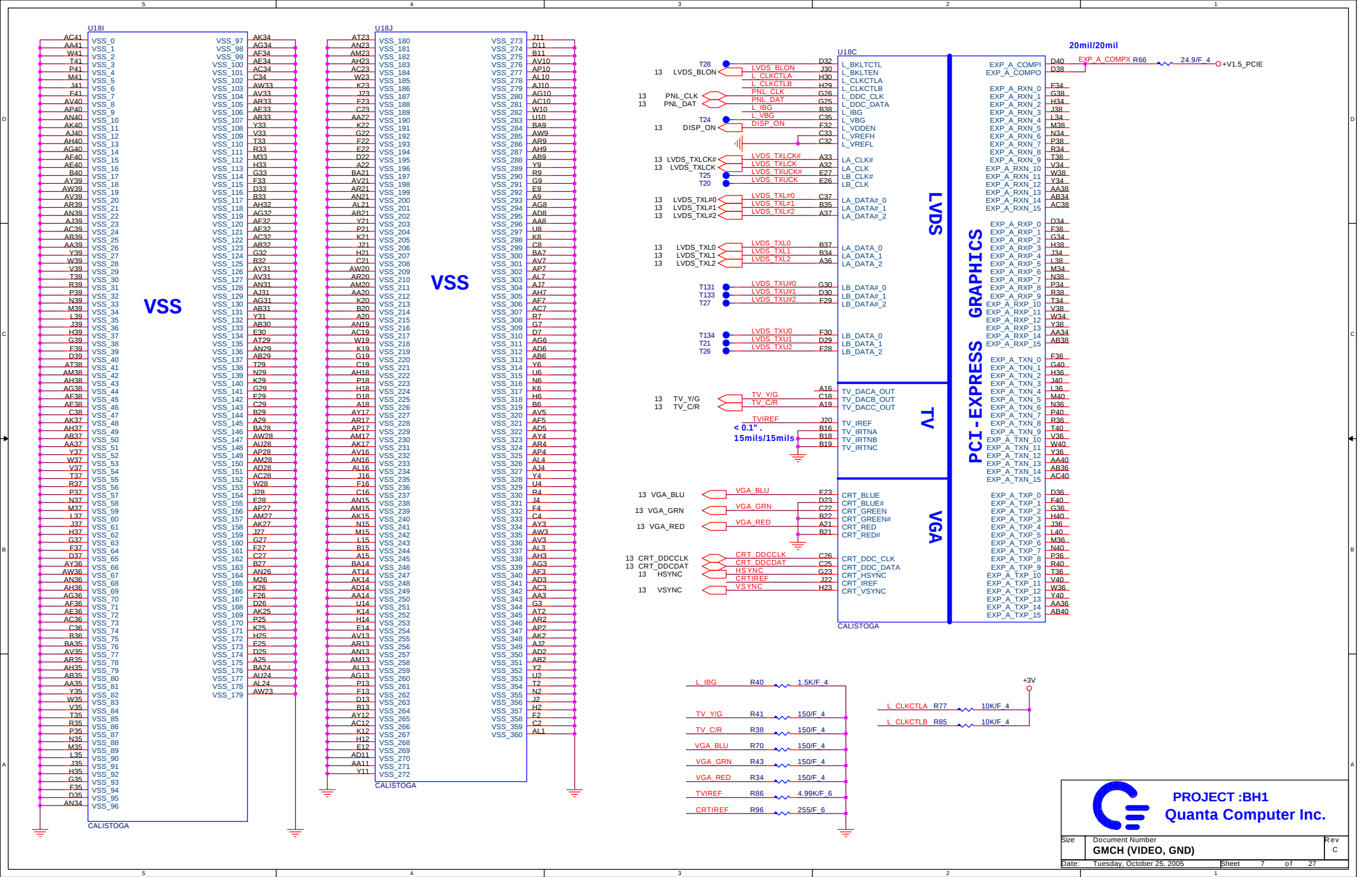




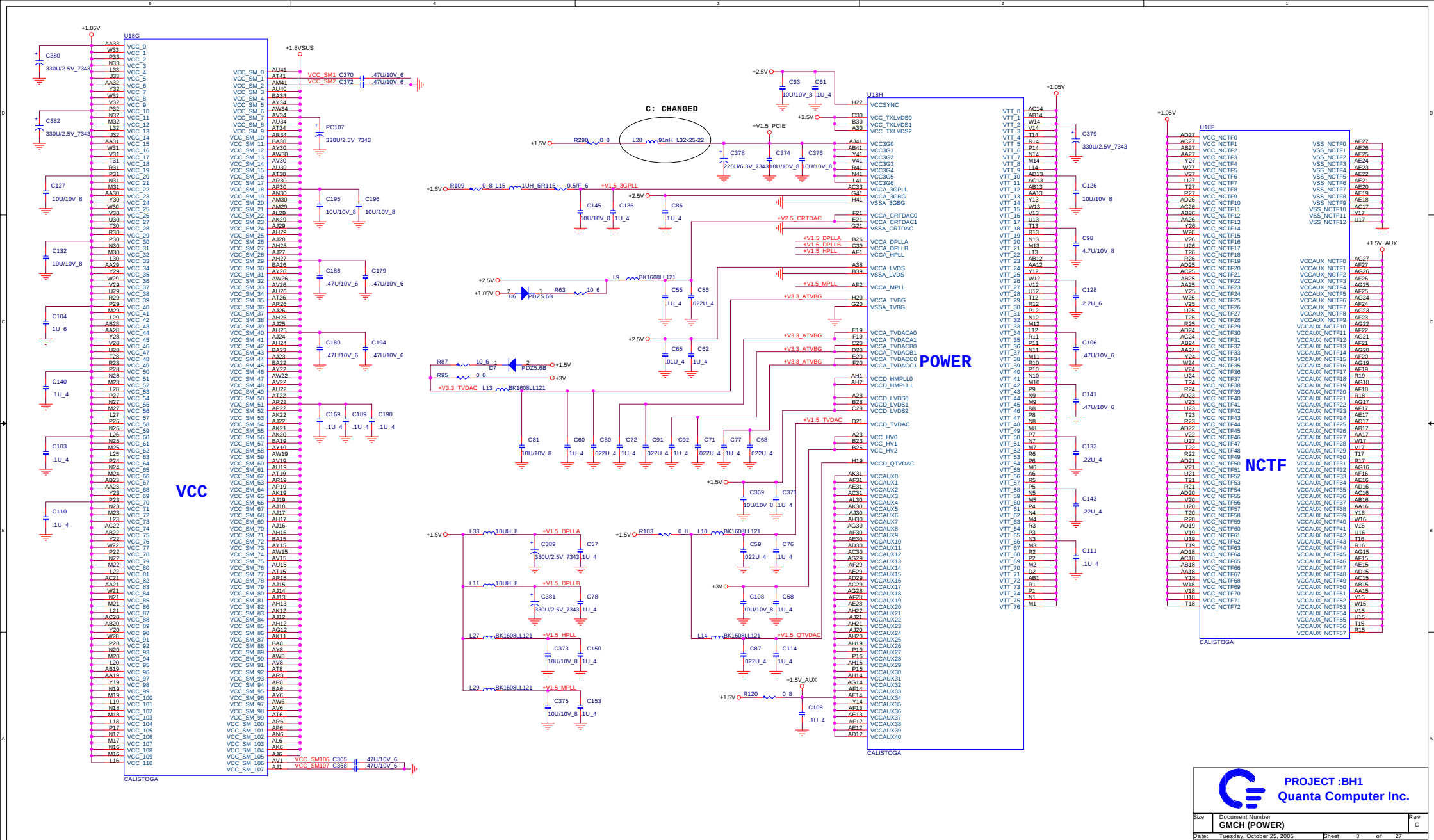


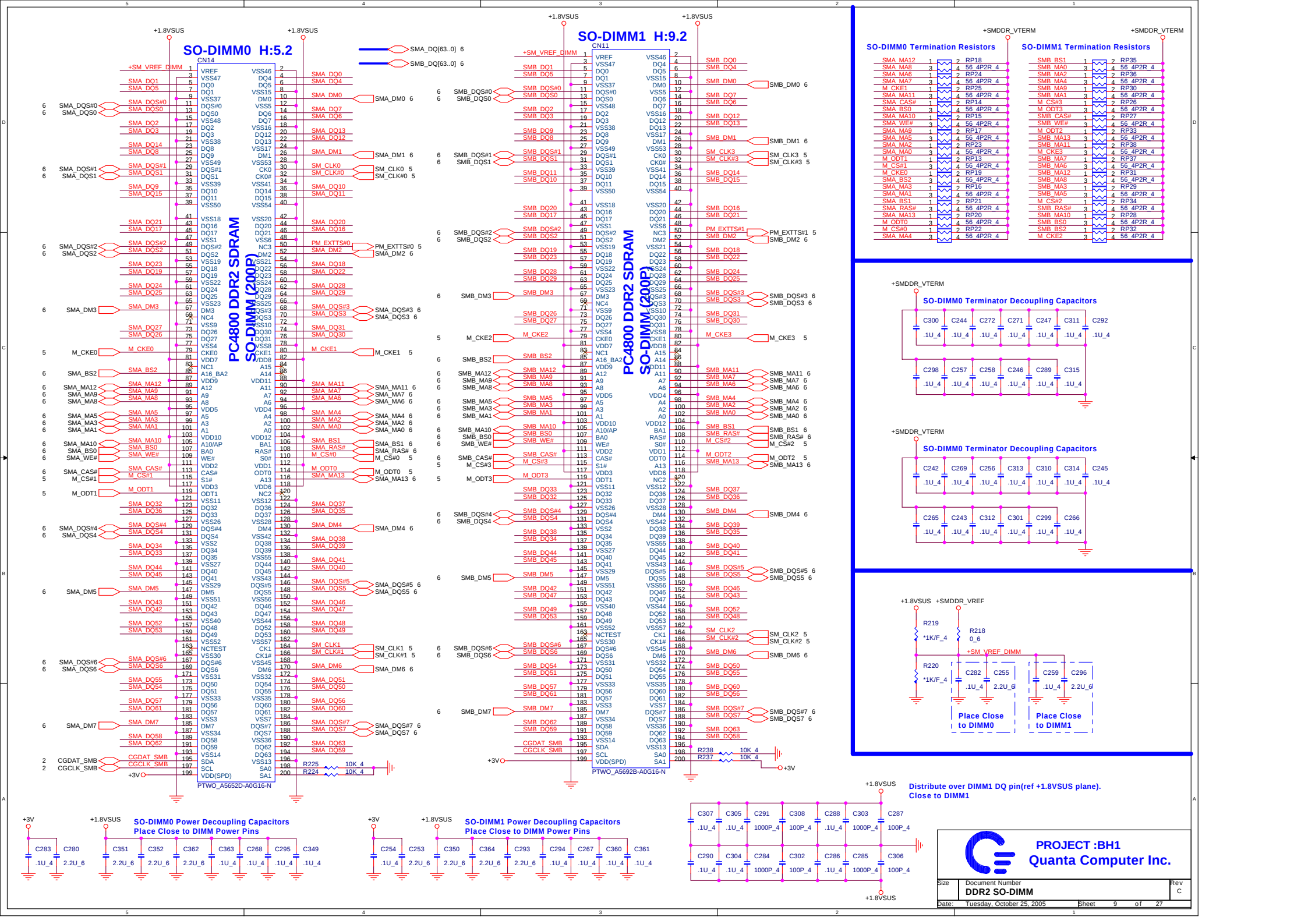
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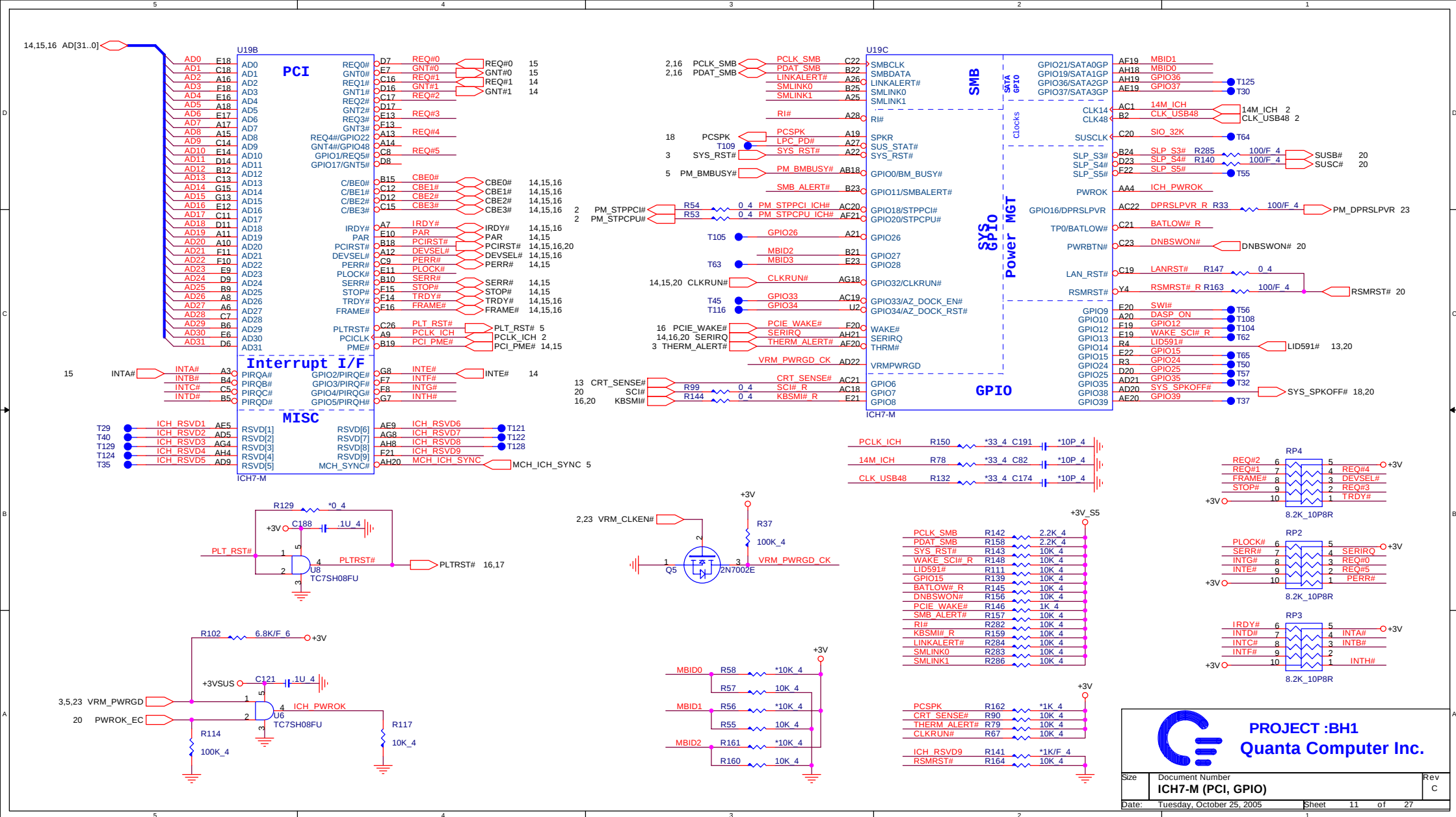
Size	Document Number	Rev
	GMCH (MEMORY)	C
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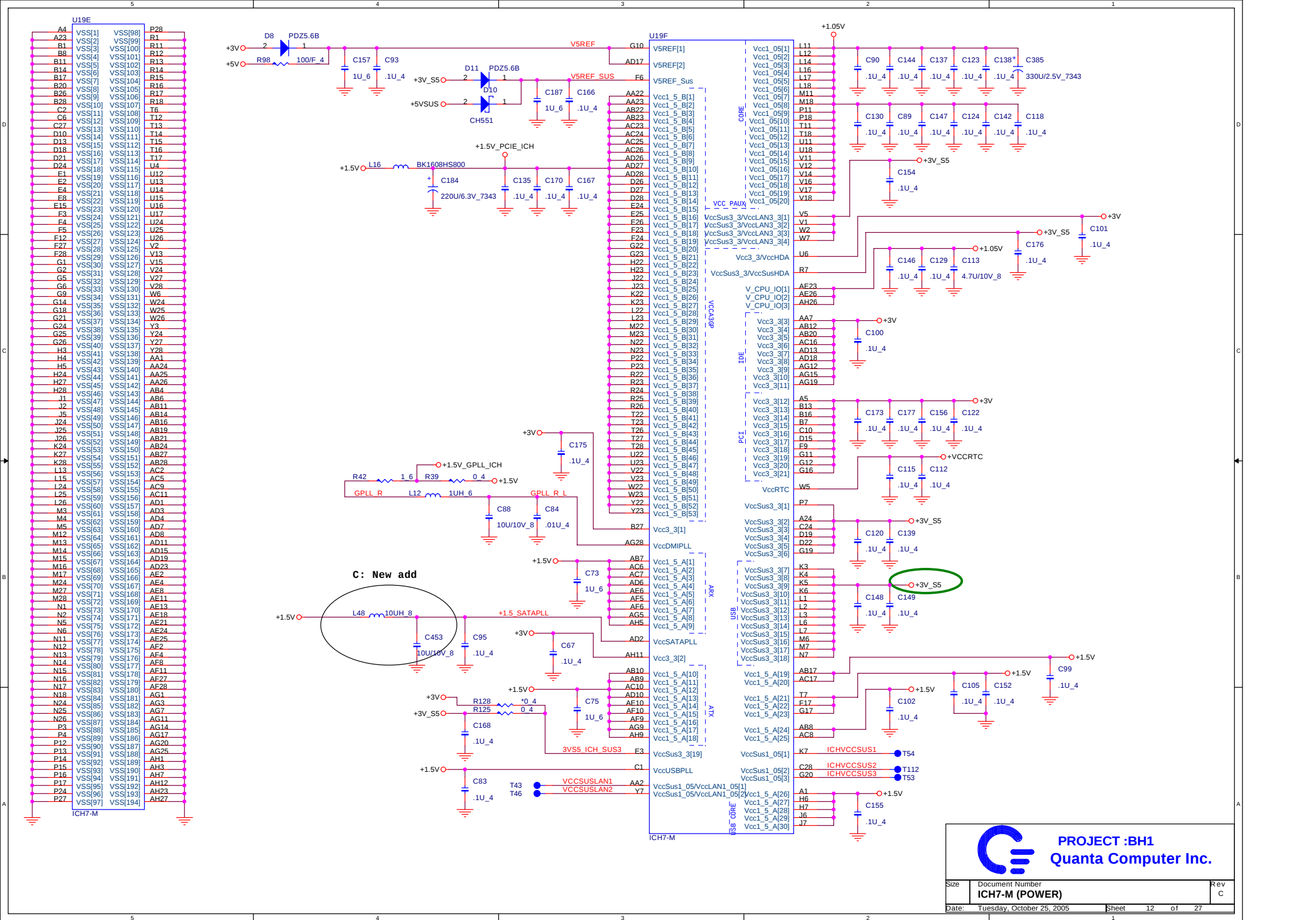


Size	Document Number	Rev
	GMCH (VIDEO, GND)	C
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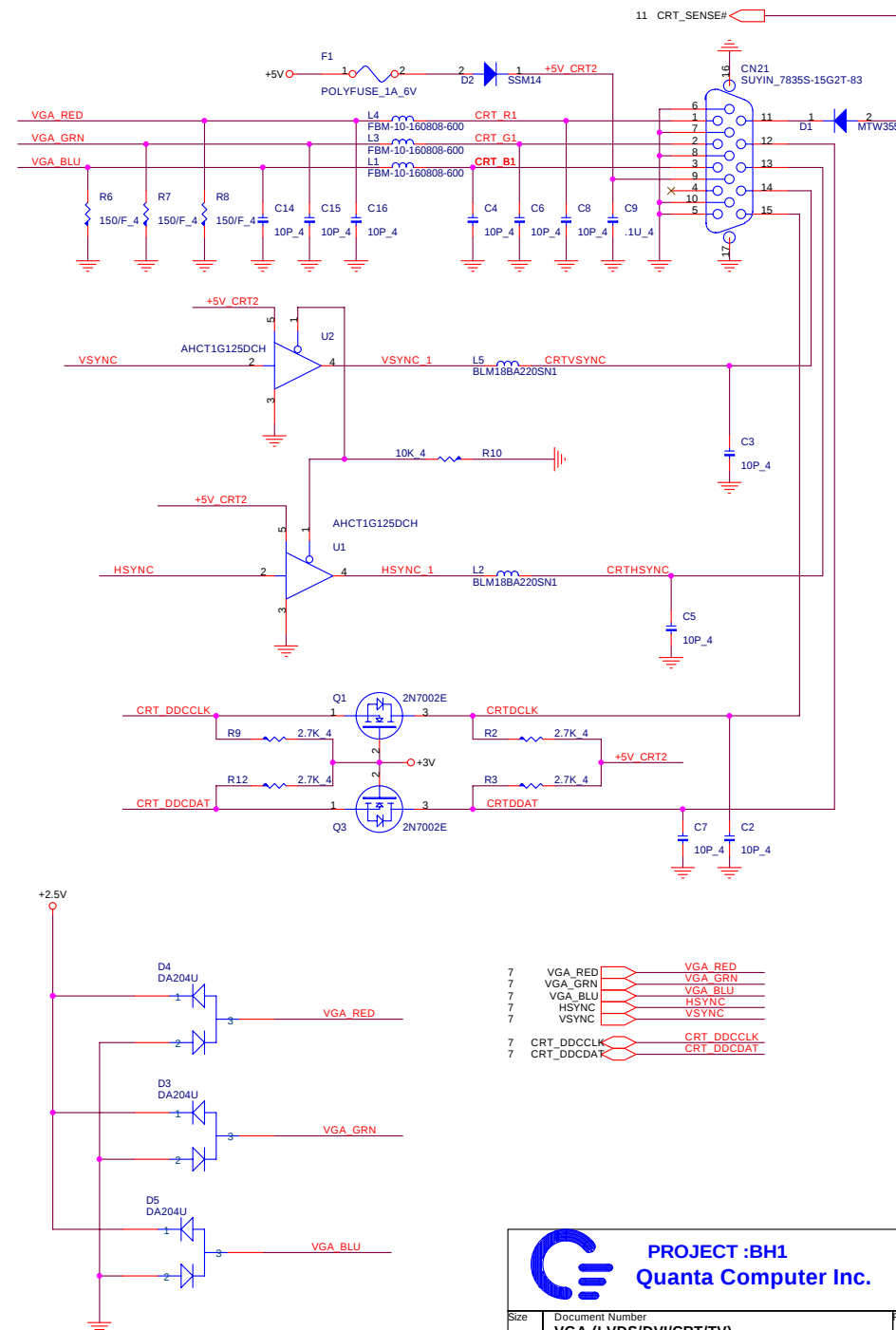
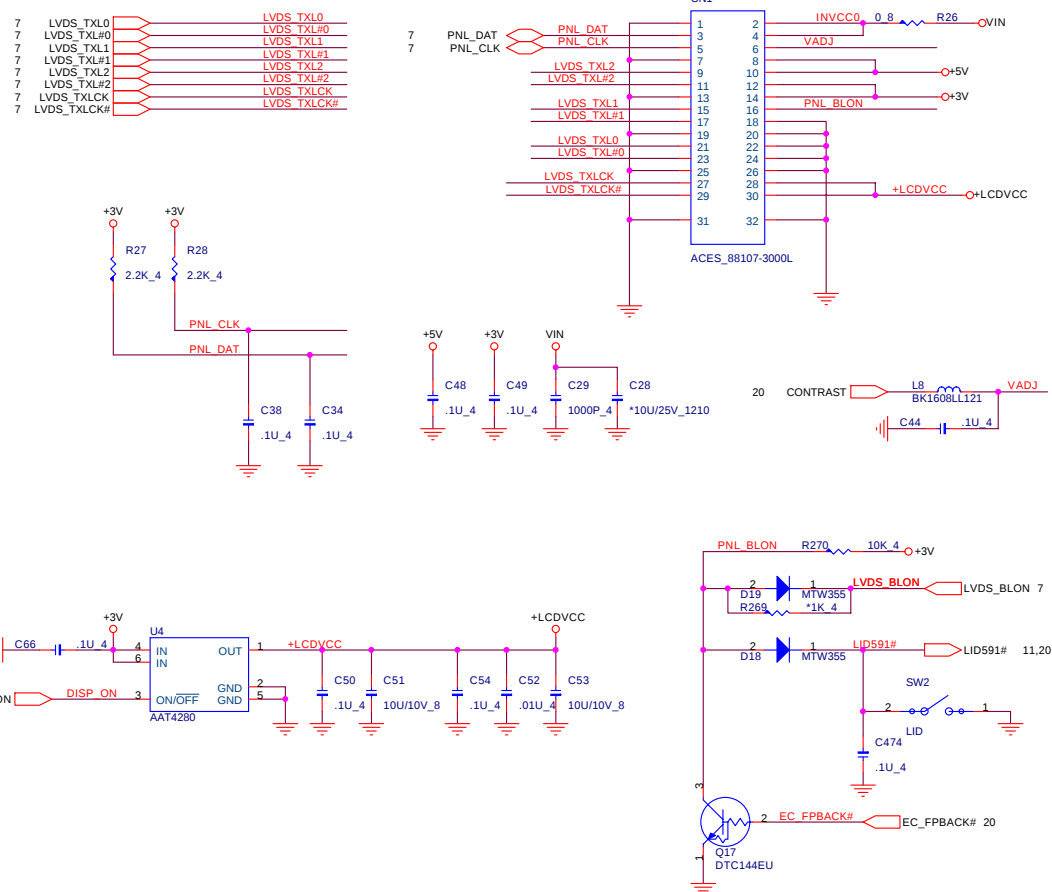
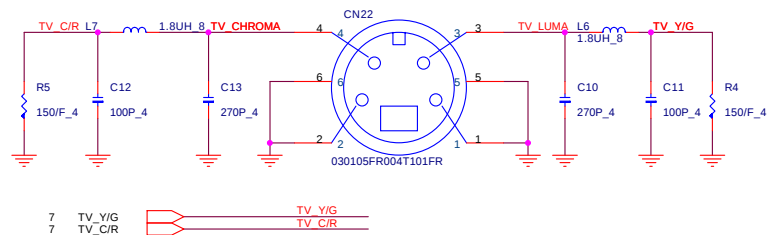




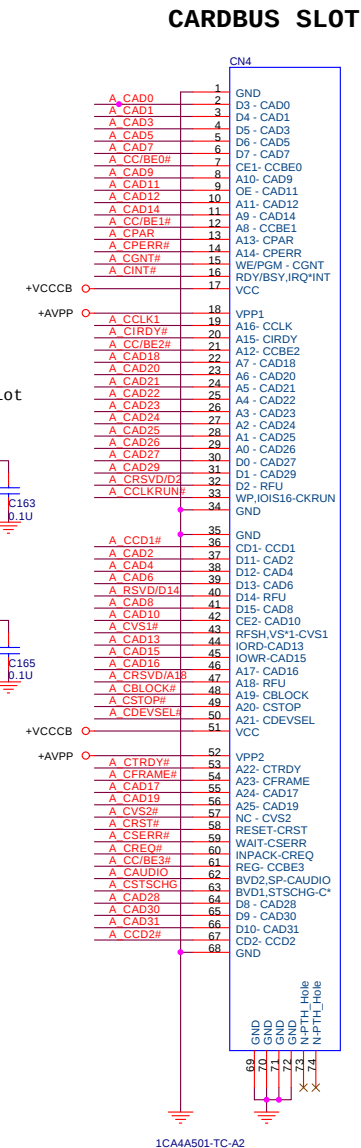
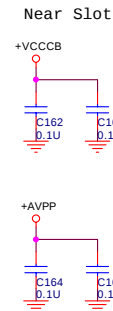
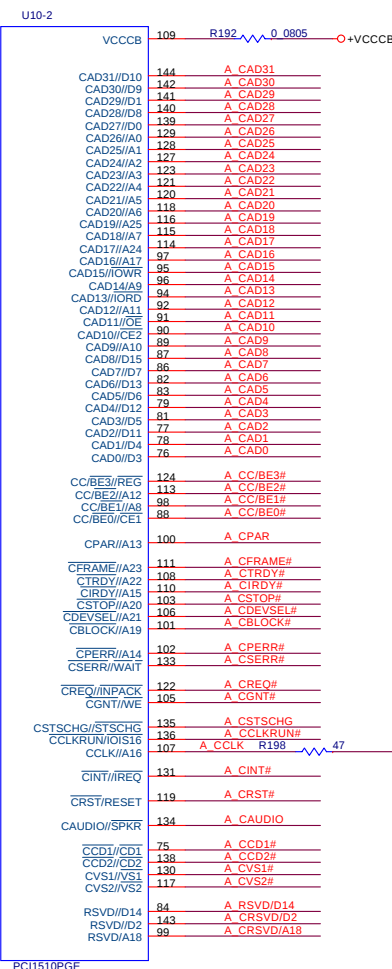
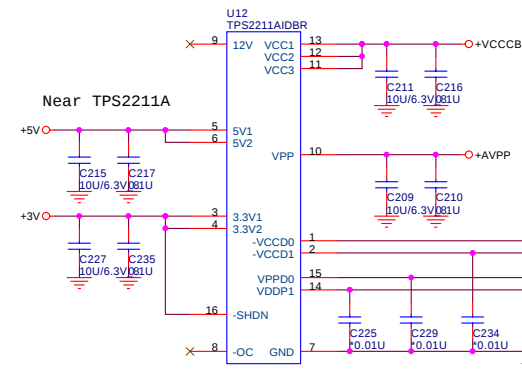
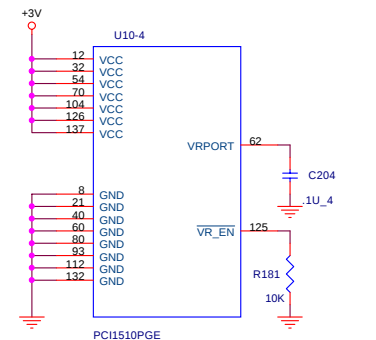
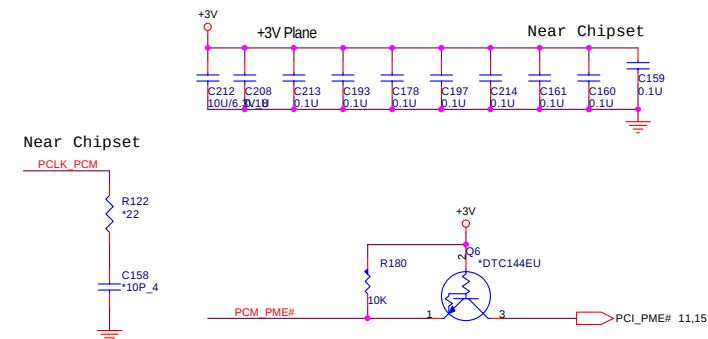
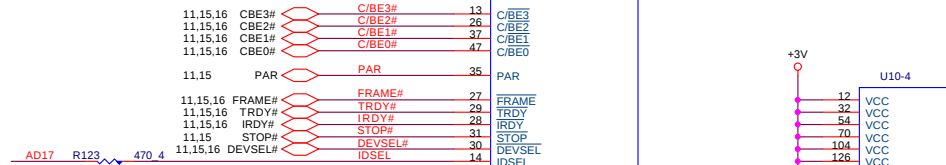
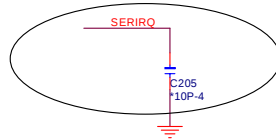




CRT

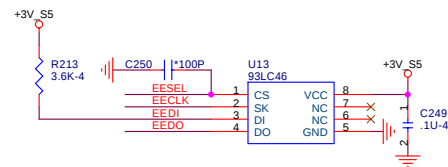
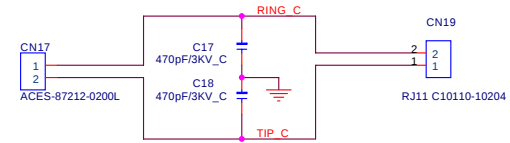
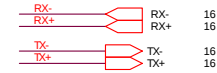
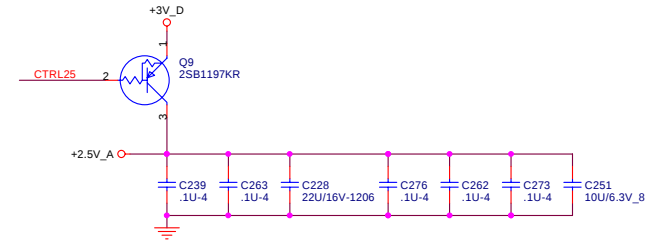
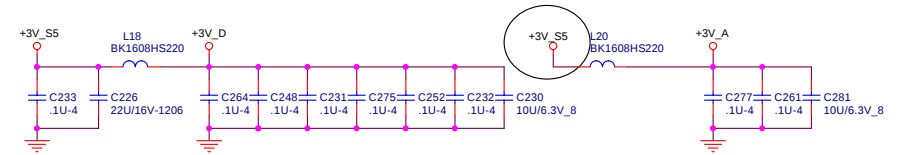


CBS



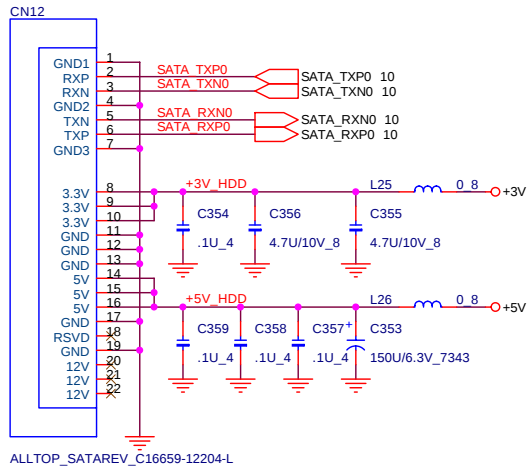
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LAN

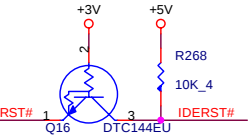
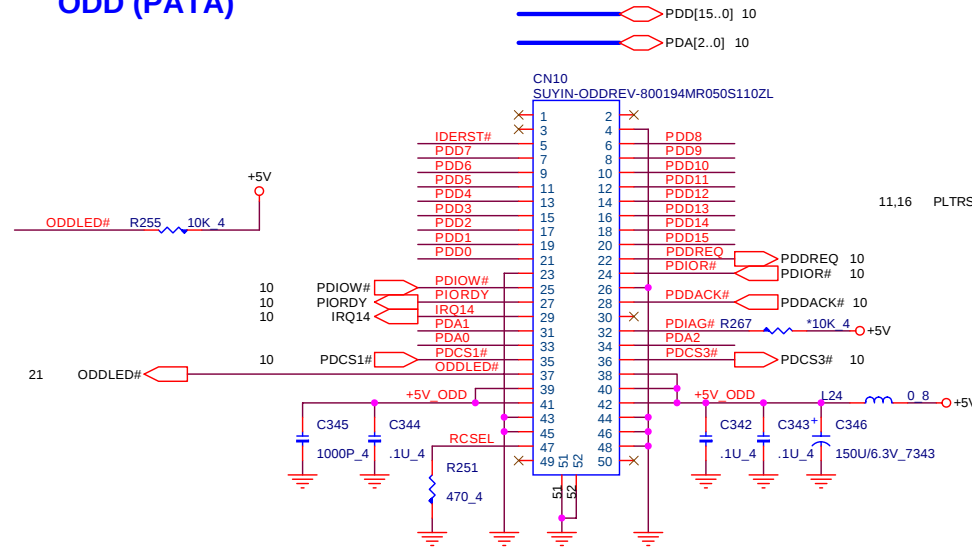


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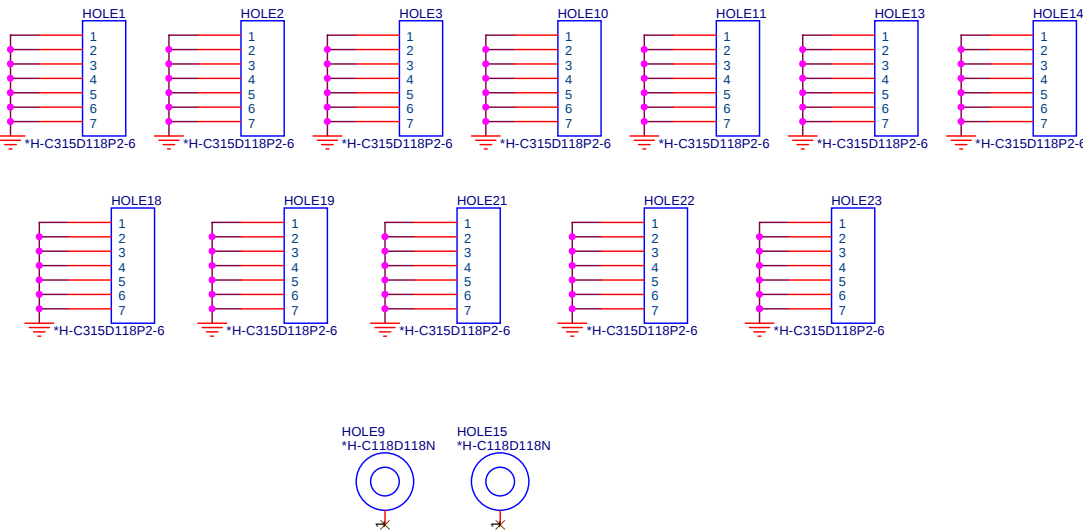
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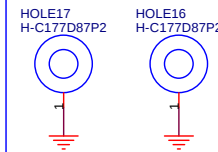
ODD (PATA)



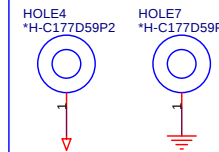
SCREW HOLE



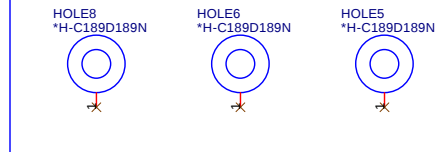
MINI CARD



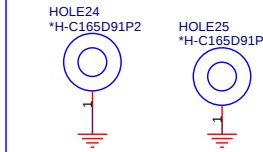
MODEM



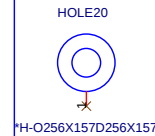
CPU



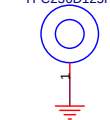
BATT



Wireless Antenna



NB

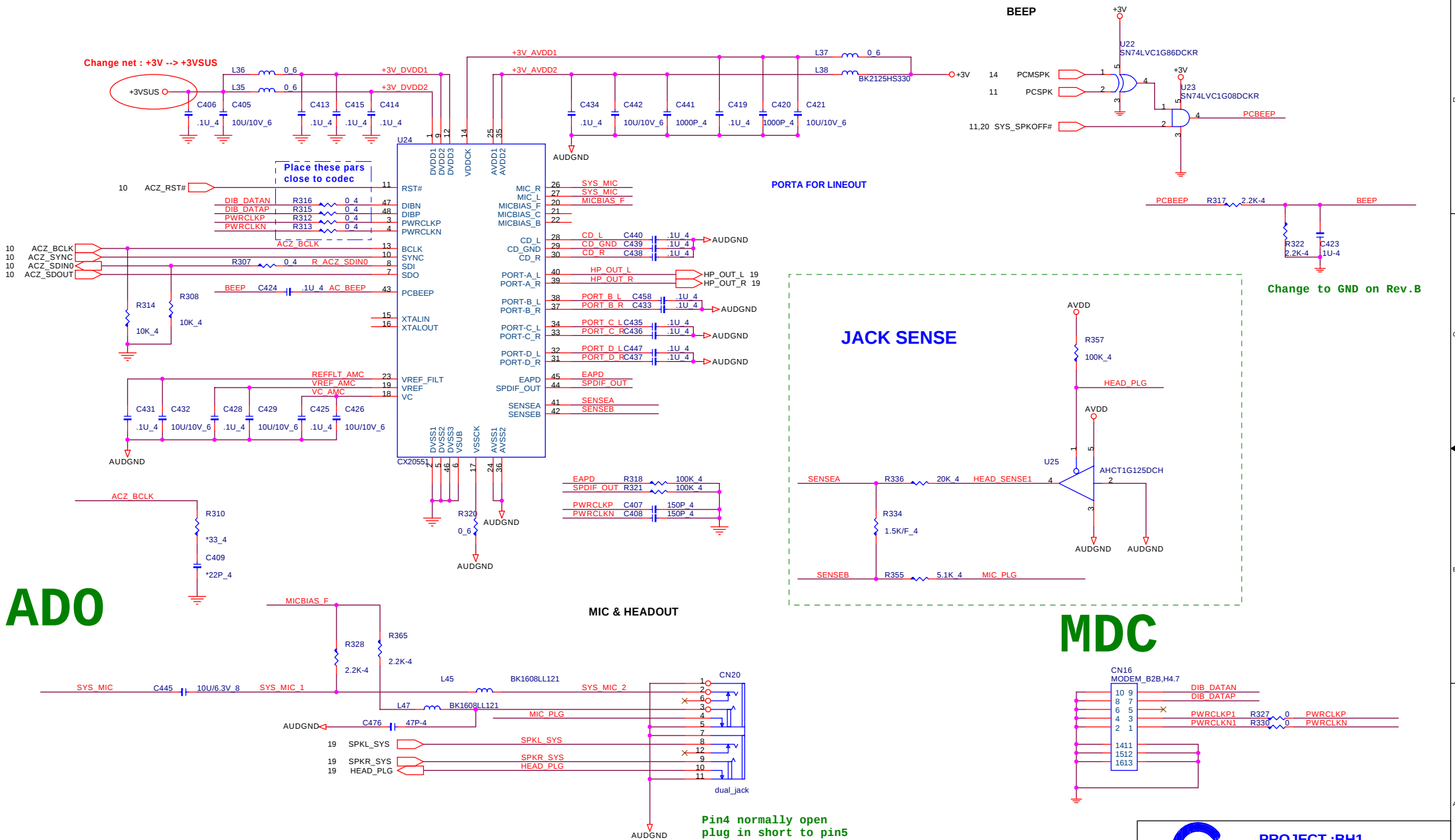


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	HDD/ODD/SCREW	C

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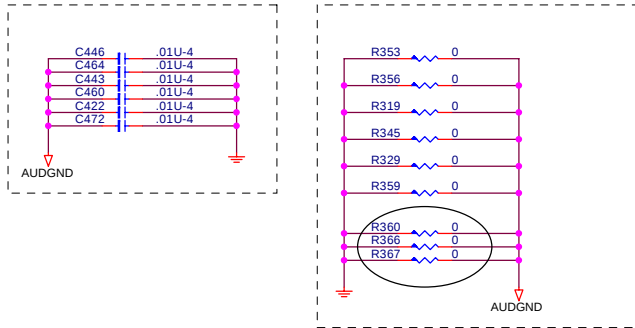
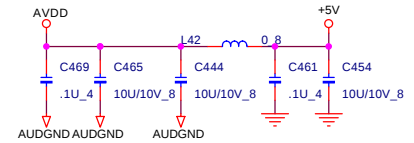
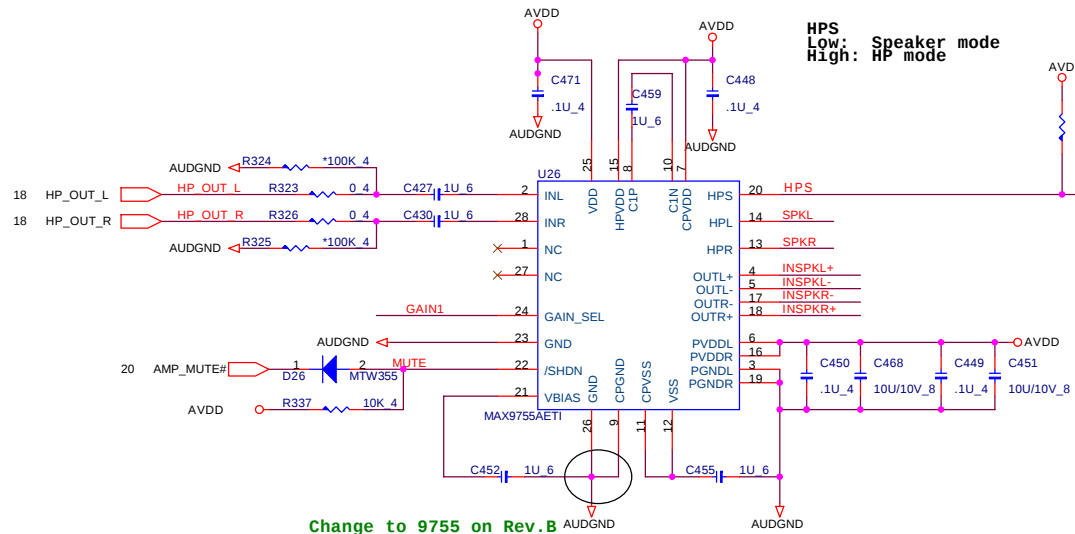
ADO



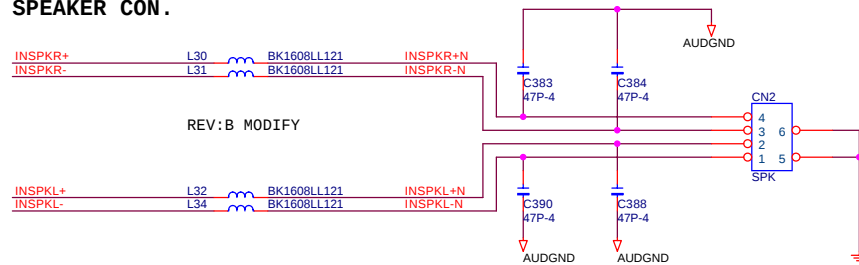
PROJECT :BH1
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	AUDIO (CODEC/AMP)	C
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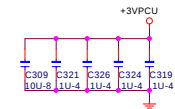
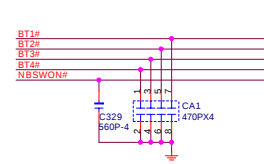
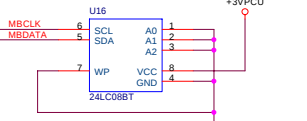
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



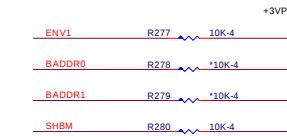
SPEAKER CON.



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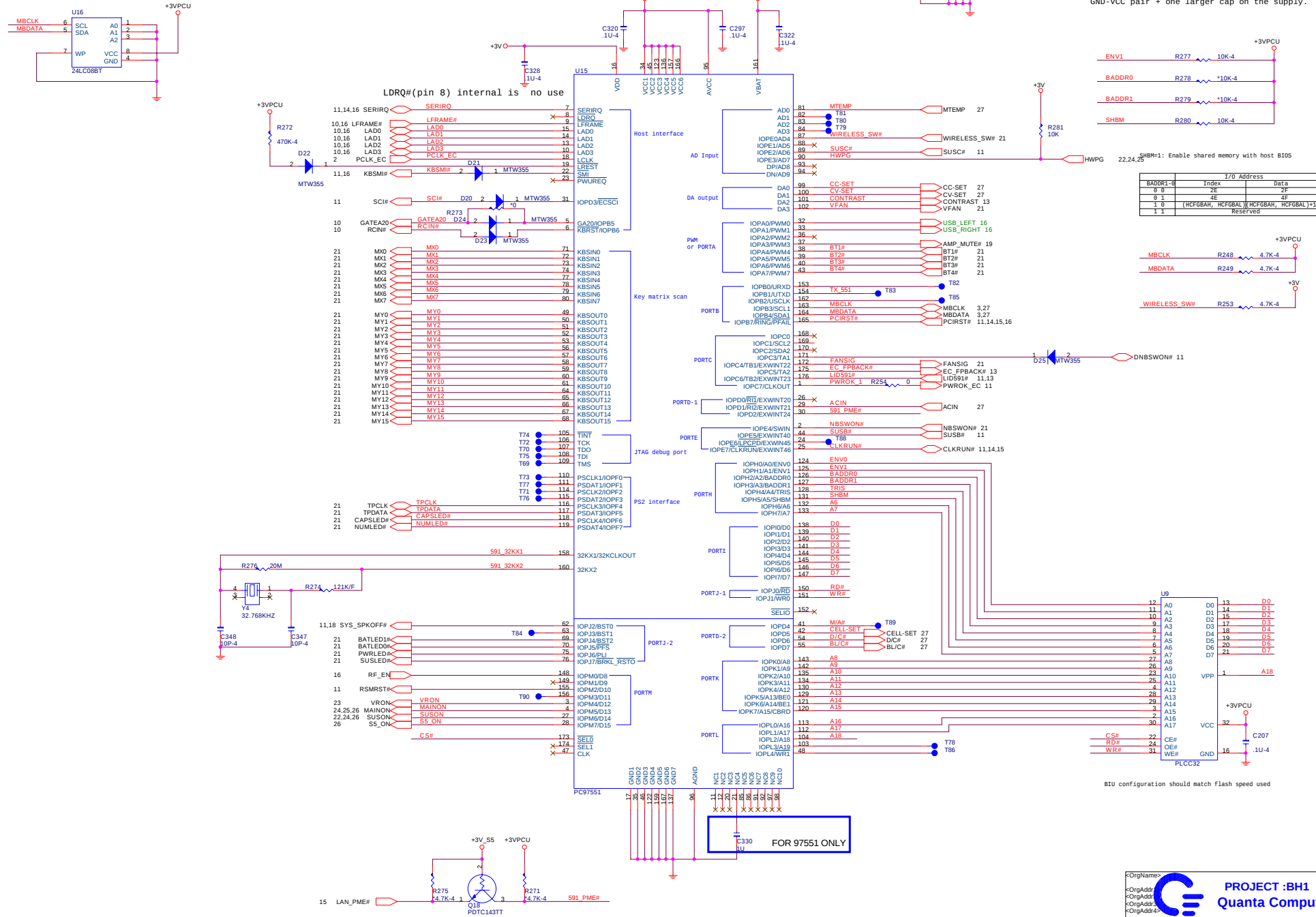
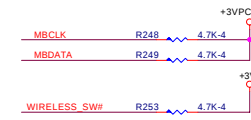


Should have a 0.1uF capacitor close to every GND-VCC pair + one larger cap on the supply.



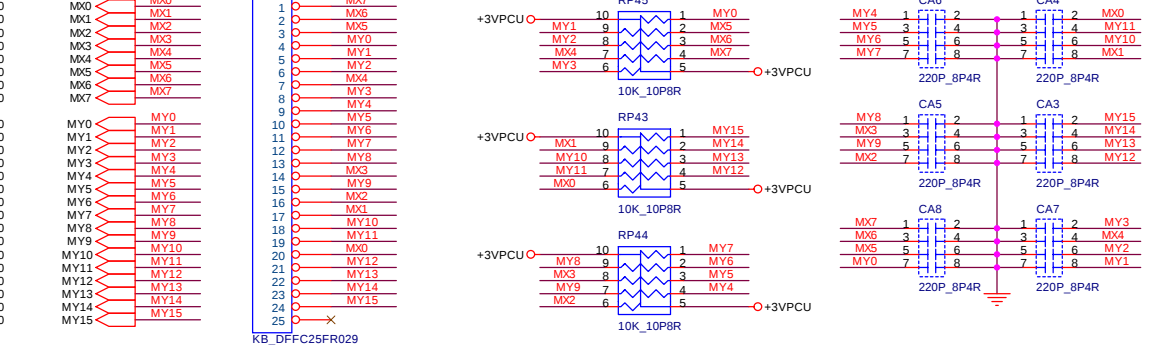
I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL) / (HCFGBAH, HCFGBAL) * 2	
1 1	Reserved	

```
SHBM=1: Enable shared memory with host BIOS
```

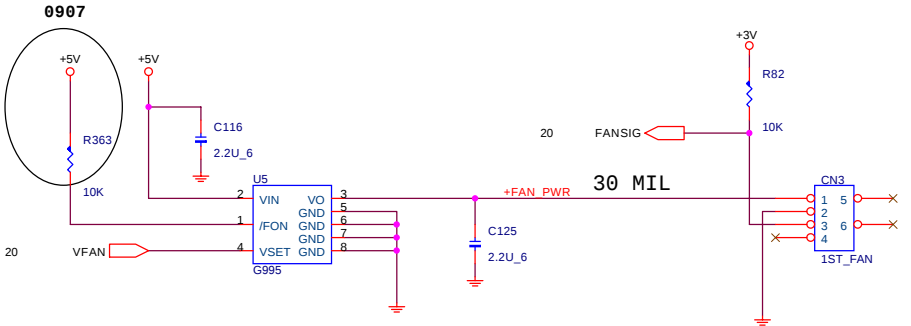


BIU configuration should match flash speed used

INT K/B

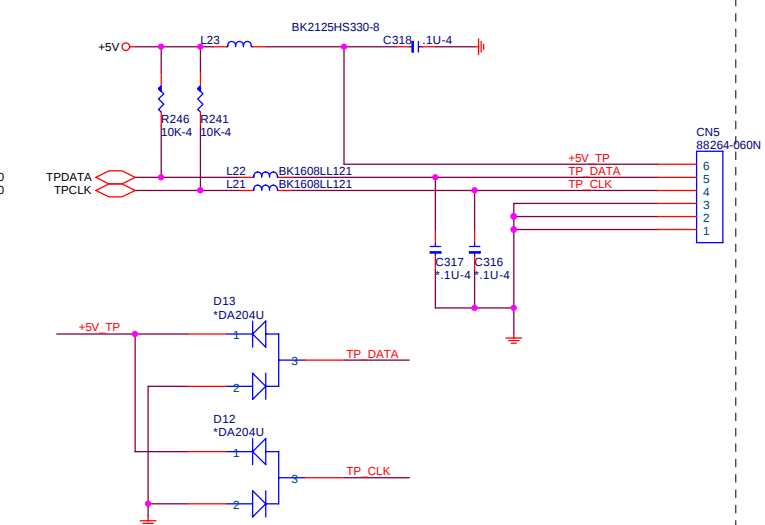


FAN CONTROL

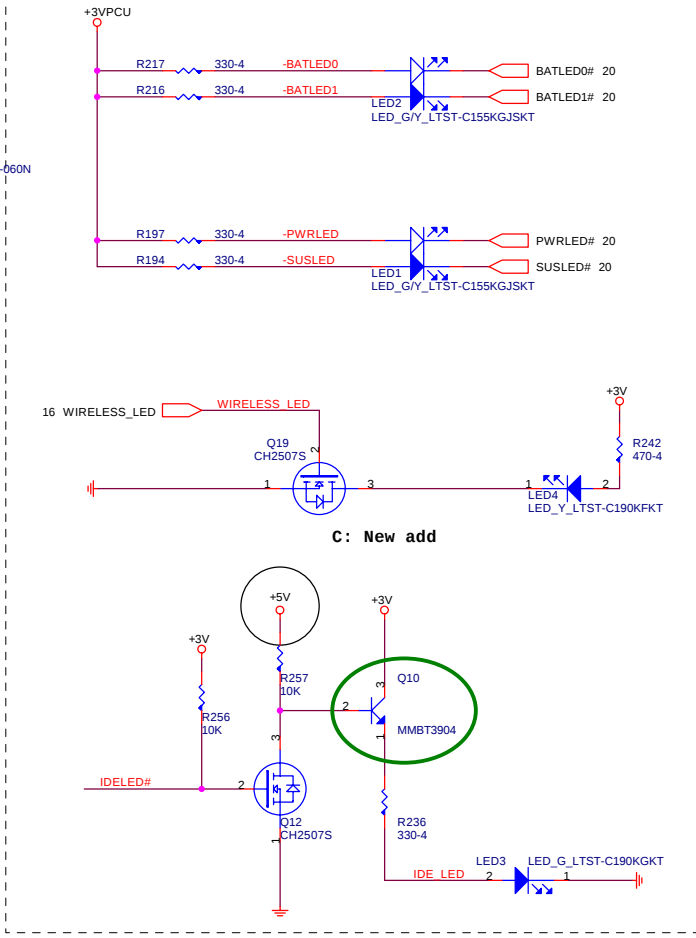


TP

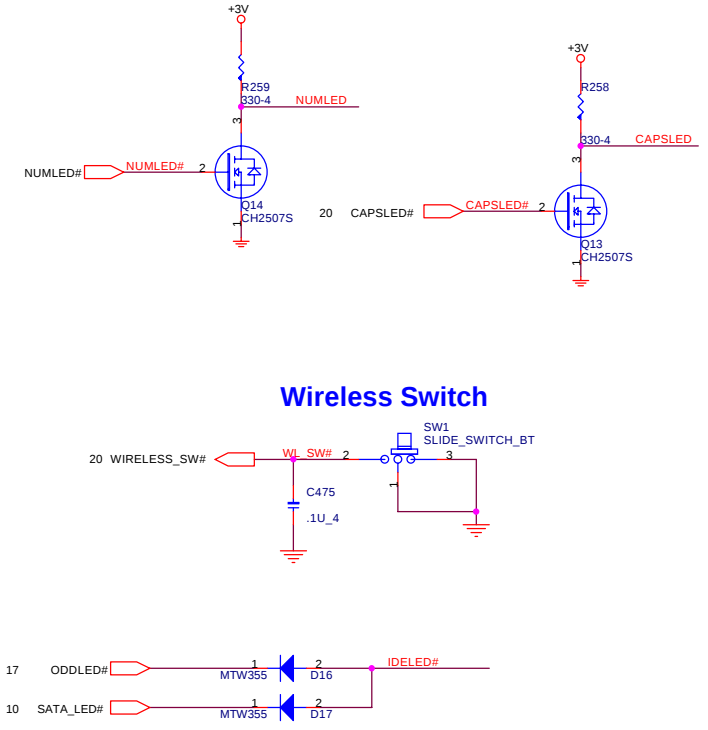
20 MIL



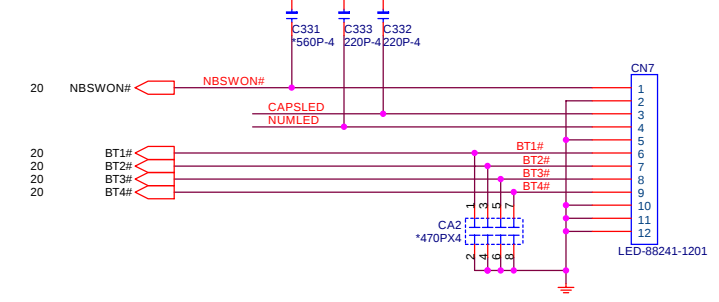
LED

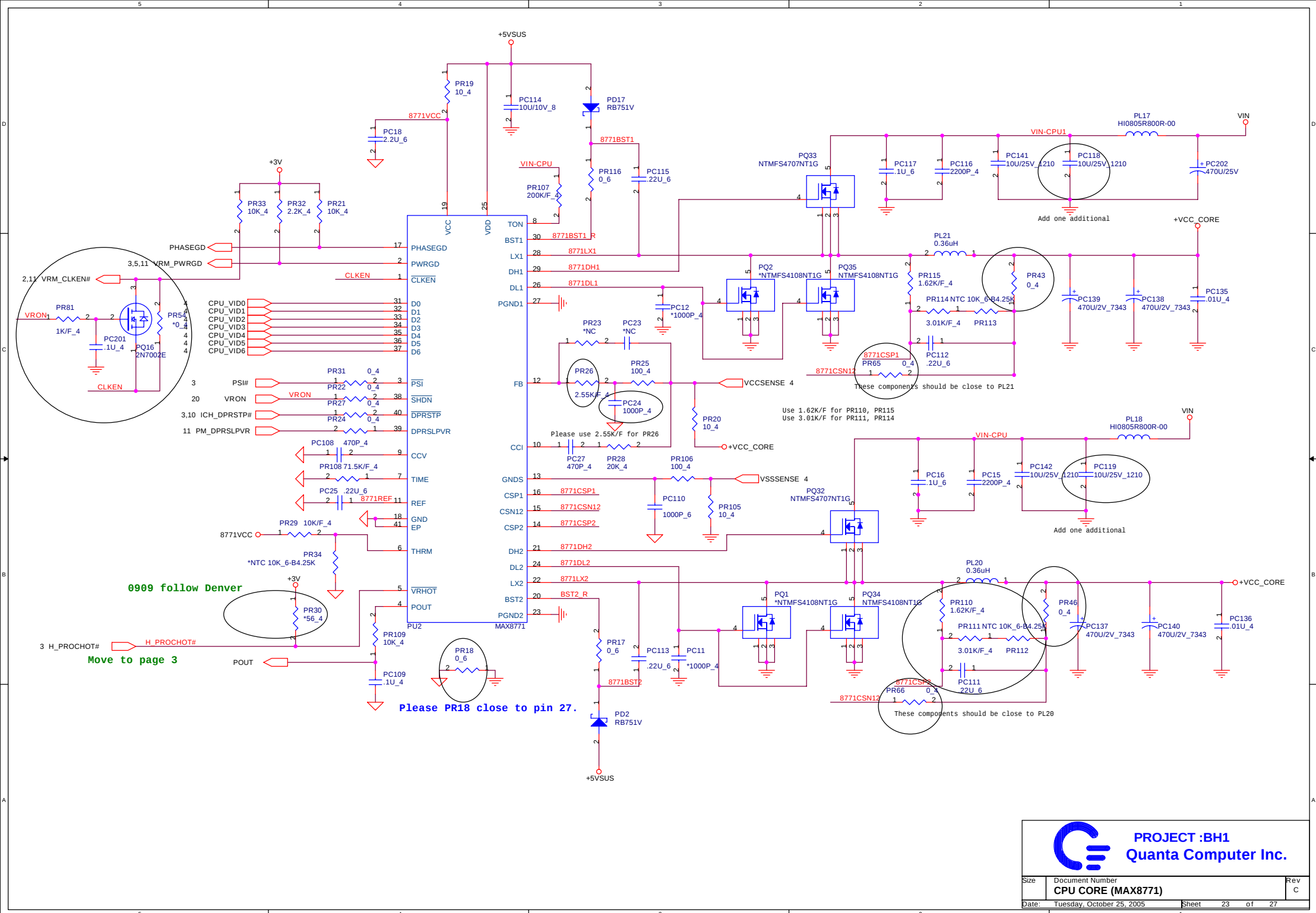


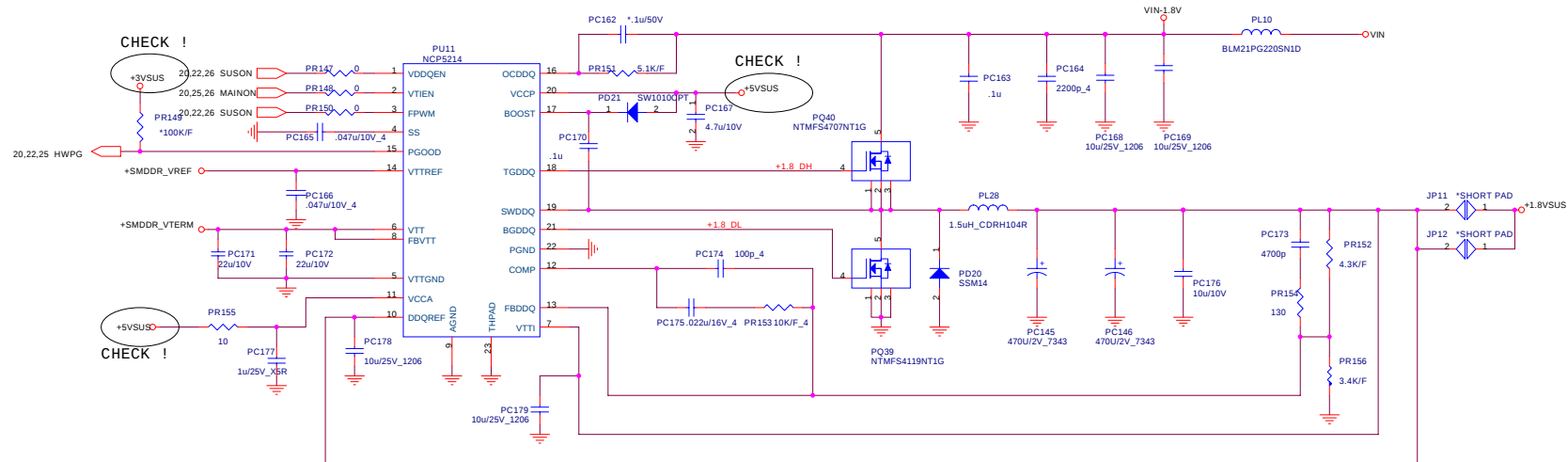
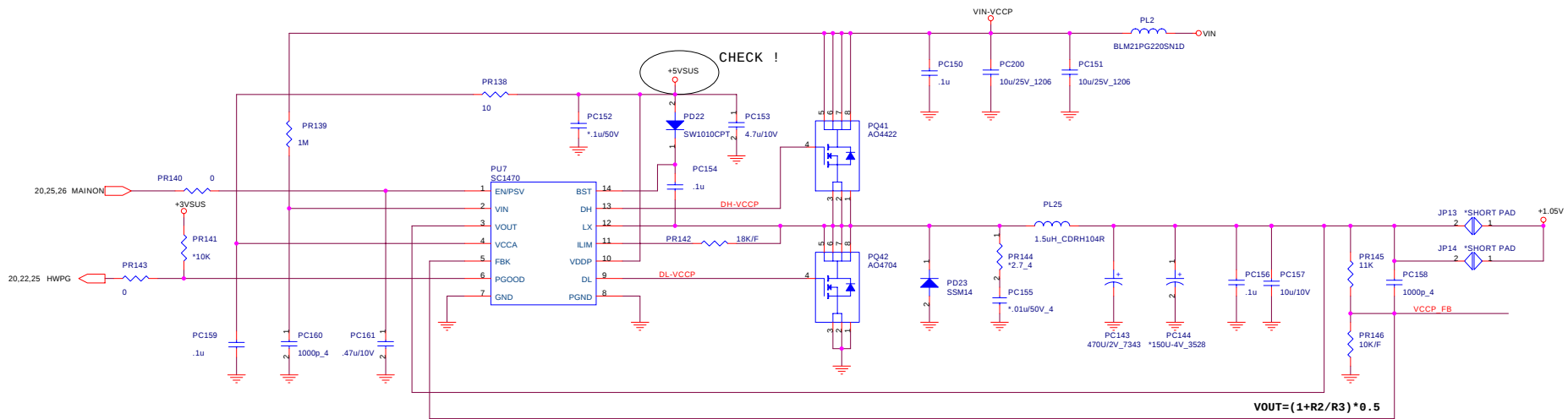
Wireless Switch



LED BD







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