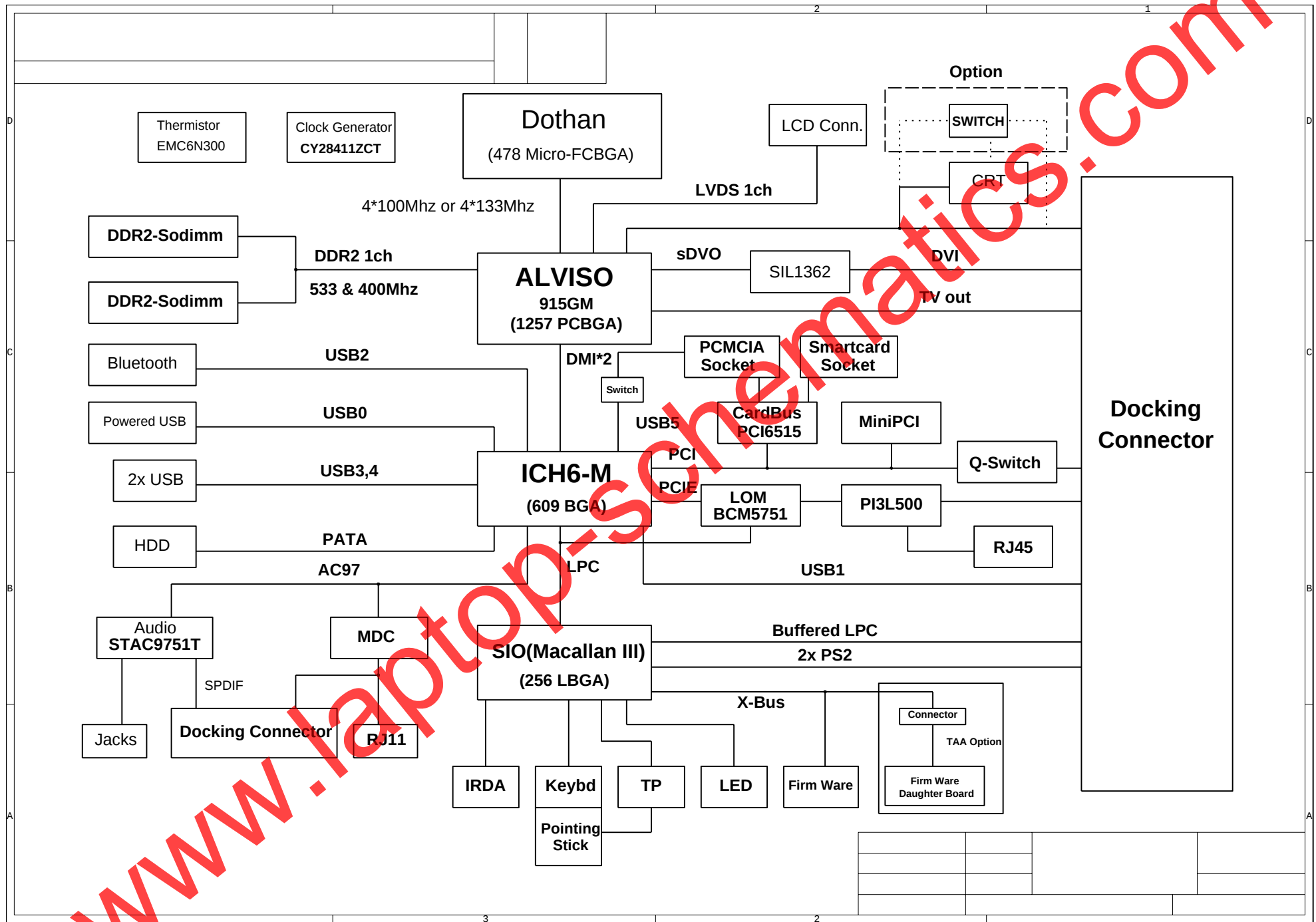

SCORPION

CPU : DOTHAN
Chip Set : ALVISO
Remark : OEM

Model Name : Scorpion Main
PCB Code : BA41-00192B
Dev. Step : PV
Revision : A00
Update : 2004.10.01

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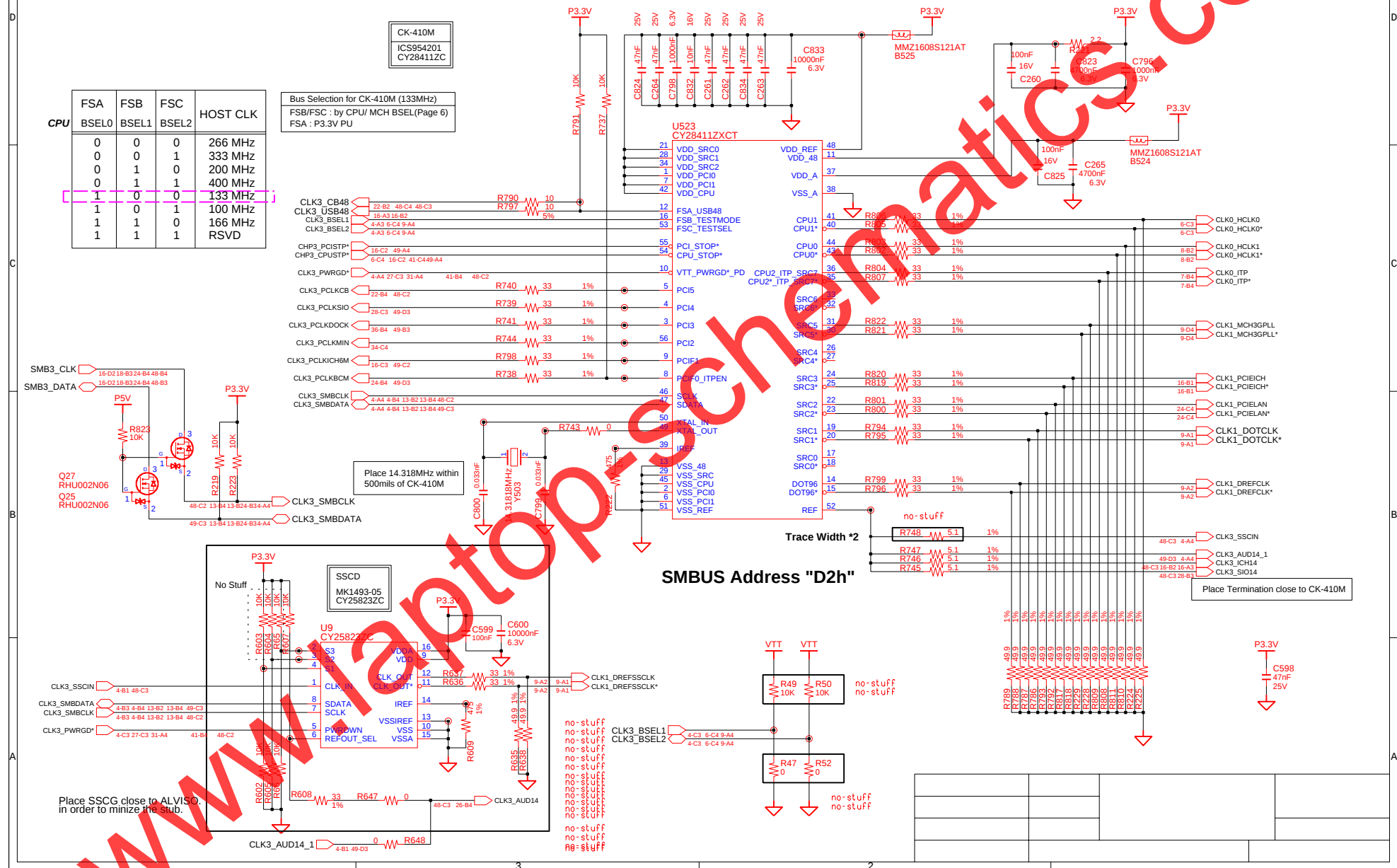
NO.	Description
1.	COVER --(SHEET1)
2.	BLOCK DIAGRAM --(SHEET2)
3.	BOARD INFORMATION --(SHEET3)
4.	CLOCK GENERATOR --(SHEET4)
5.	DOTHAN CPU --(SHEET5/6/7)
6.	GMCH-M <ALVISO-GM> --(SHEET8/9/10/11/12)
8.	DDR2 SODIMM --(SHEET13)
9.	DDR2 TERMINATION --(SHEET14)
10.	ICH6-M --(SHEET15/16/17/18)
11.	LCD INTERFACE --(SHEET19)
12.	CRT INTERFACE --(SHEET20)
13.	DVI INTERFACE --(SHEET21)
13.	TCARD INTERFACE --(SHEET22)
14.	LOM (BCM5751M) --(SHEET24/25)
15.	AUDIO --(SHEET26)
16.	MACALLEN III --(SHEET27/28)
17.	FIRM WARE --(SHEET29)
18.	SEQUENTIAL POWER --(SHEET30)
19.	THERMAL MONITOR & FAN --(SHEET31)
20.	USB & IDE --(SHEET32)
21.	KEYB'D, TOUCHPAD, MDC, BLUETOOTH, FIR --(SHEET33)
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23.	PCI BUFFER --(SHEET35)
24.	DOCKING CONNECTOR --(SHEET36)
25.	CHARGER --(SHEET37)
26.	P3.3V_AUX, P5V_AUX, P15V_AUX, P3.3V_LAN --(SHEET38)
27.	P1.8V_AUX, MEM_0.9V, MEM1_VREF --(SHEET39)
28.	VTT, P1.5V_AUX --(SHEET40)
29.	VCC_CORE --(SHEET41)
30.	P5V, P3.3V, P1.8V, P1.5V --(SHEET42)
31.	SEQUENTIAL DISCHARGE --(SHEET43)
27.	LED CONNECTOR --(SHEET44)
28.	MOUNT HOLE --(SHEET45)
29.	TOUCHPAD BOARD --(SHEET46)
30.	FIRMWARE DAUGHTER BOARD --(SHEET47)

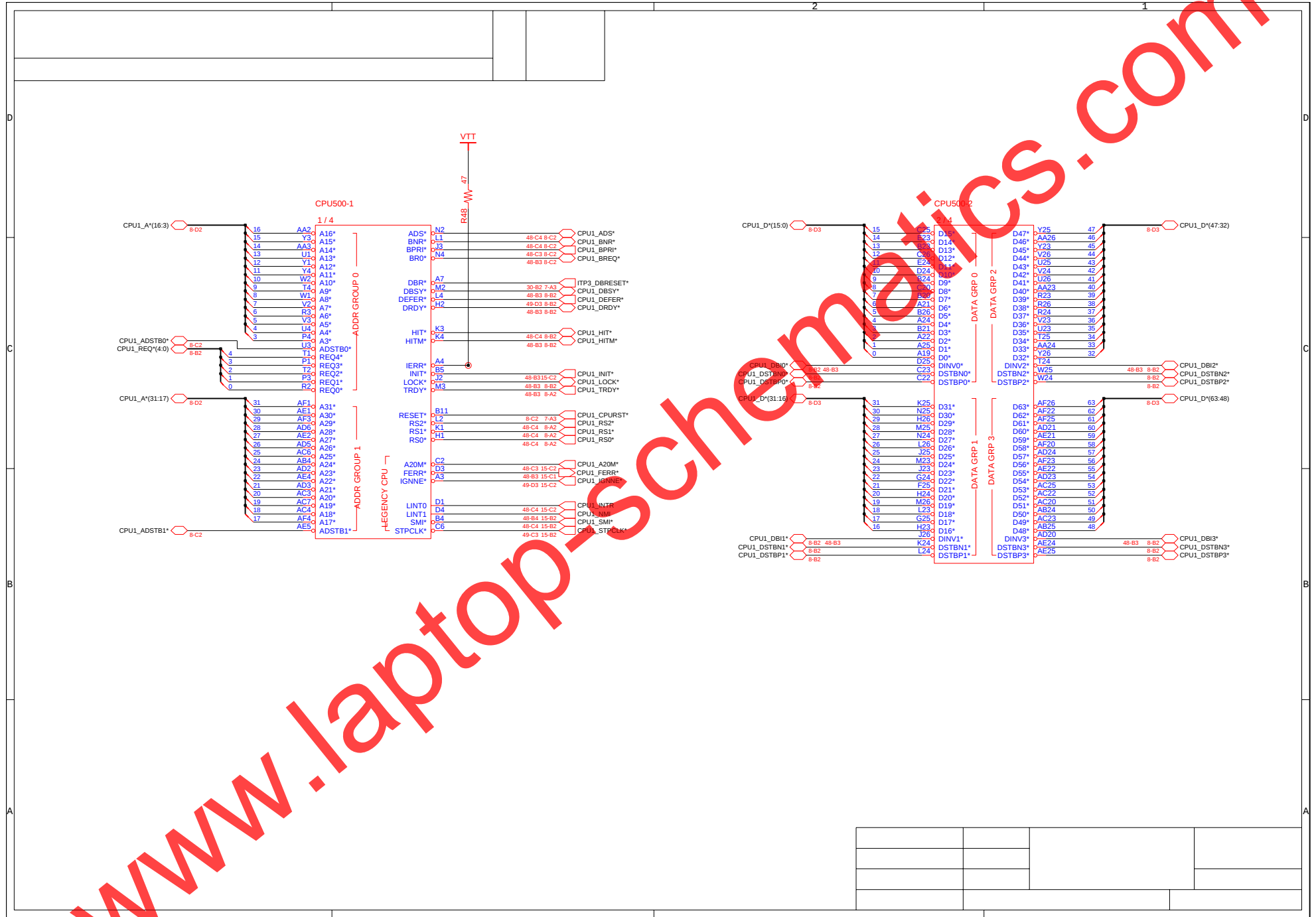


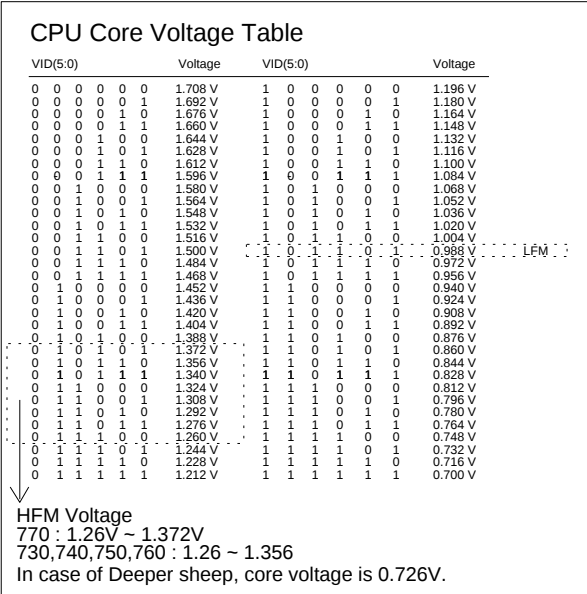
CY28411ZXCT : Lead free version

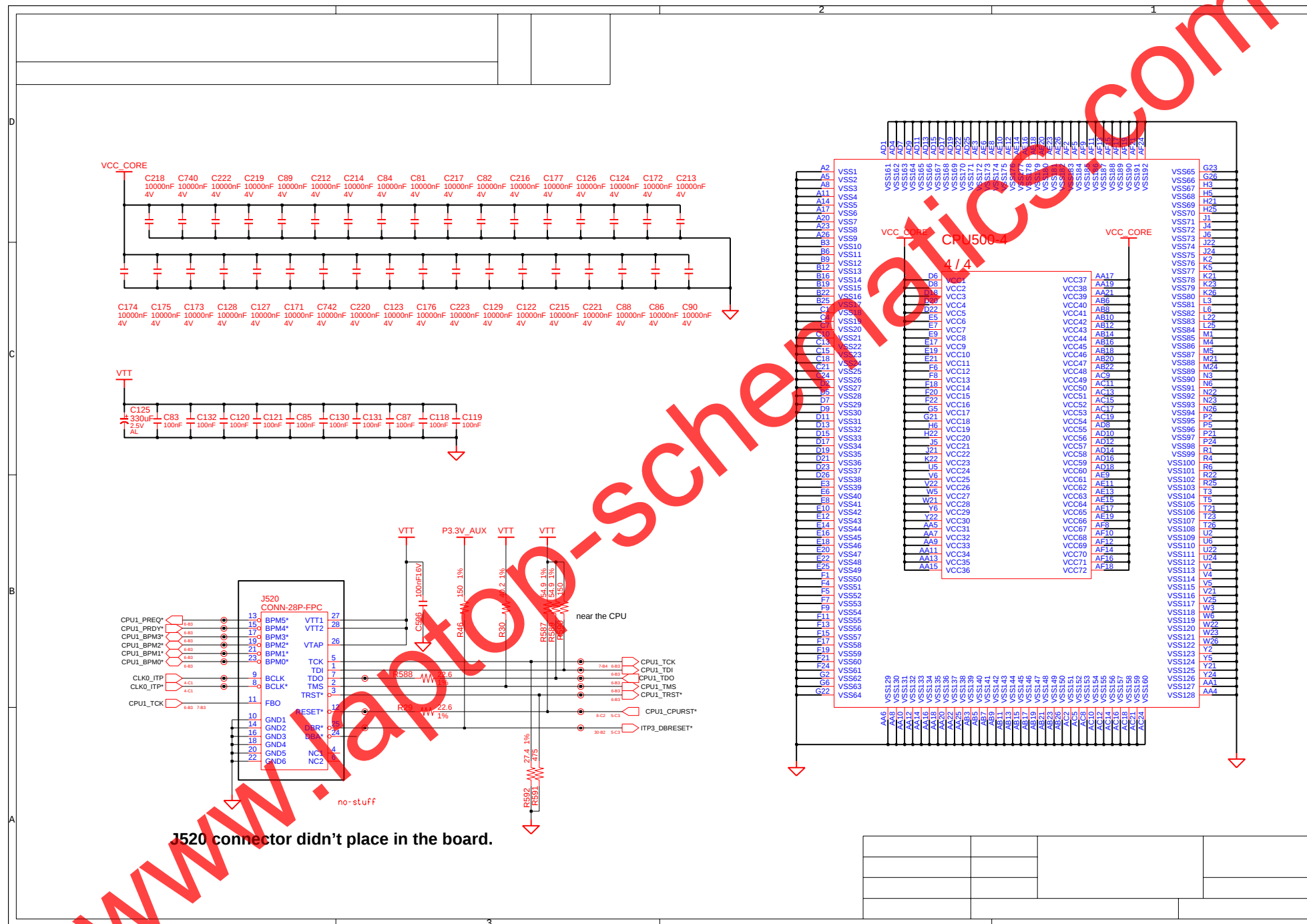
CPU	FSA	FSB	FSC	HOST CLK
	BSEL0	BSEL1	BSEL2	
	0	0	0	266 MHz
	0	0	1	333 MHz
	0	1	0	200 MHz
	0	1	1	400 MHz
	1	0	0	133 MHz
	1	0	1	100 MHz
	1	1	0	166 MHz
	1	1	1	RSVD

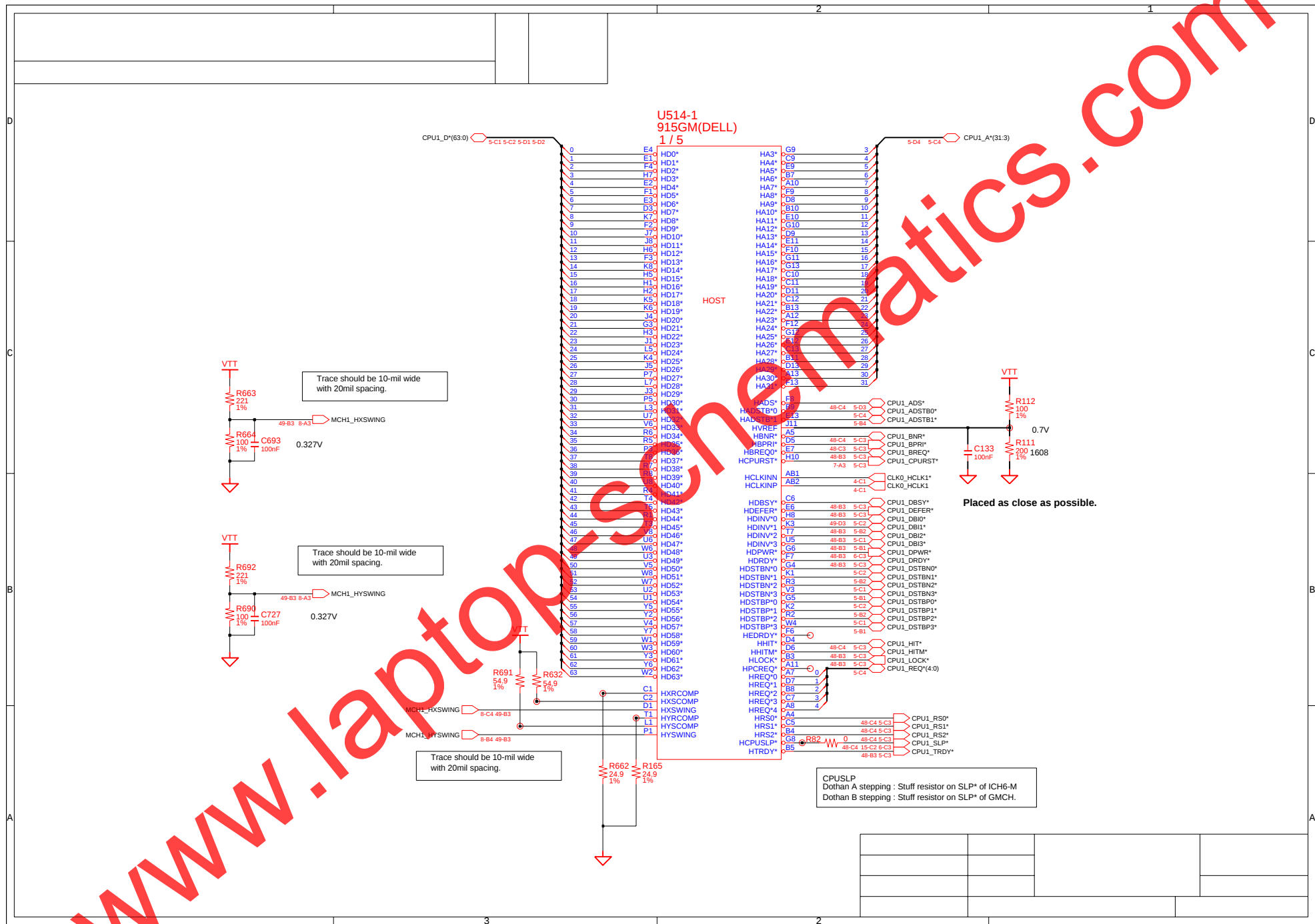
Bus Selection for CK-410M (133MHz)
FSB/FSC : by CPU/ MCH BSEL(Page 6)
FSA : P3.3V PU

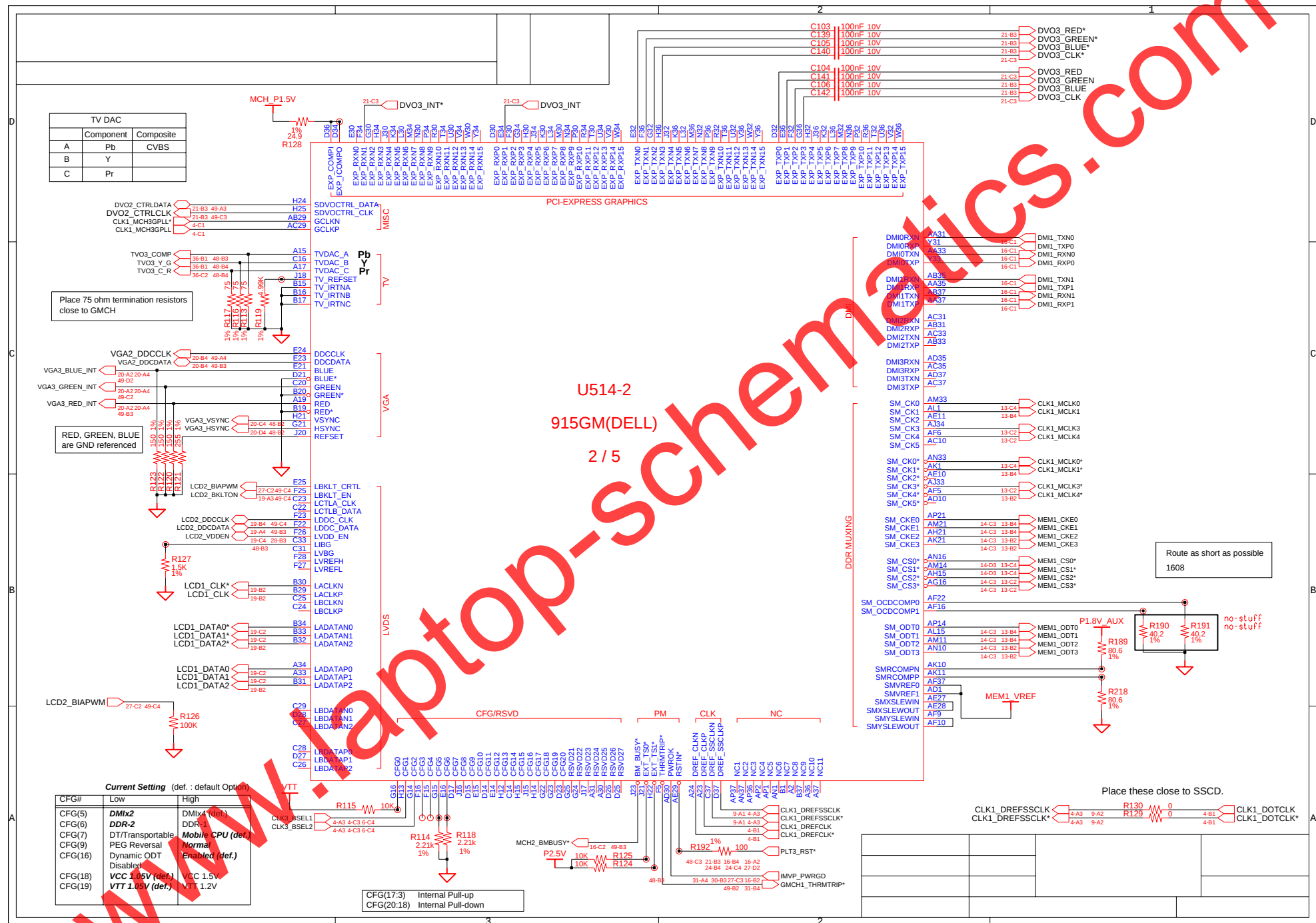


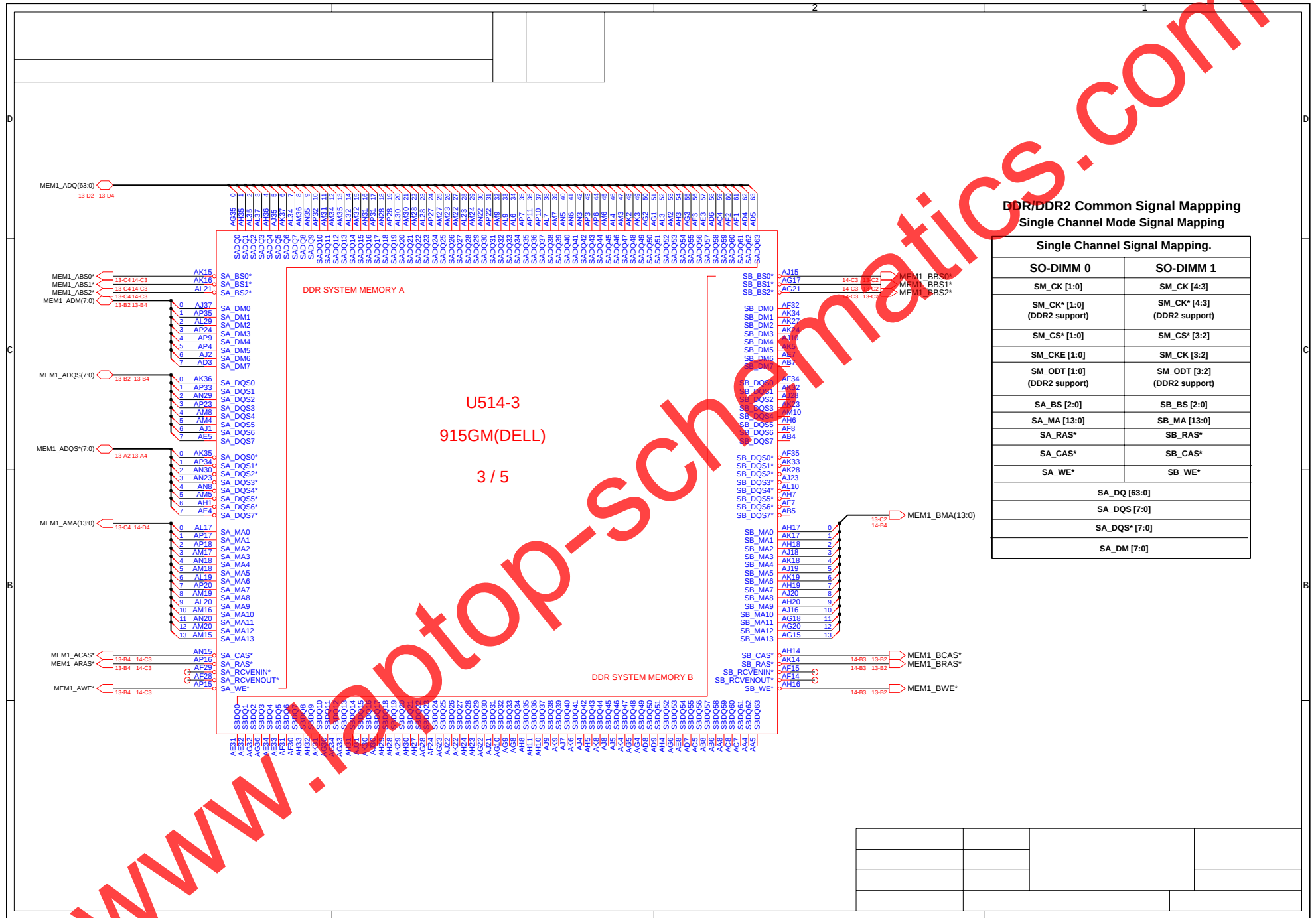


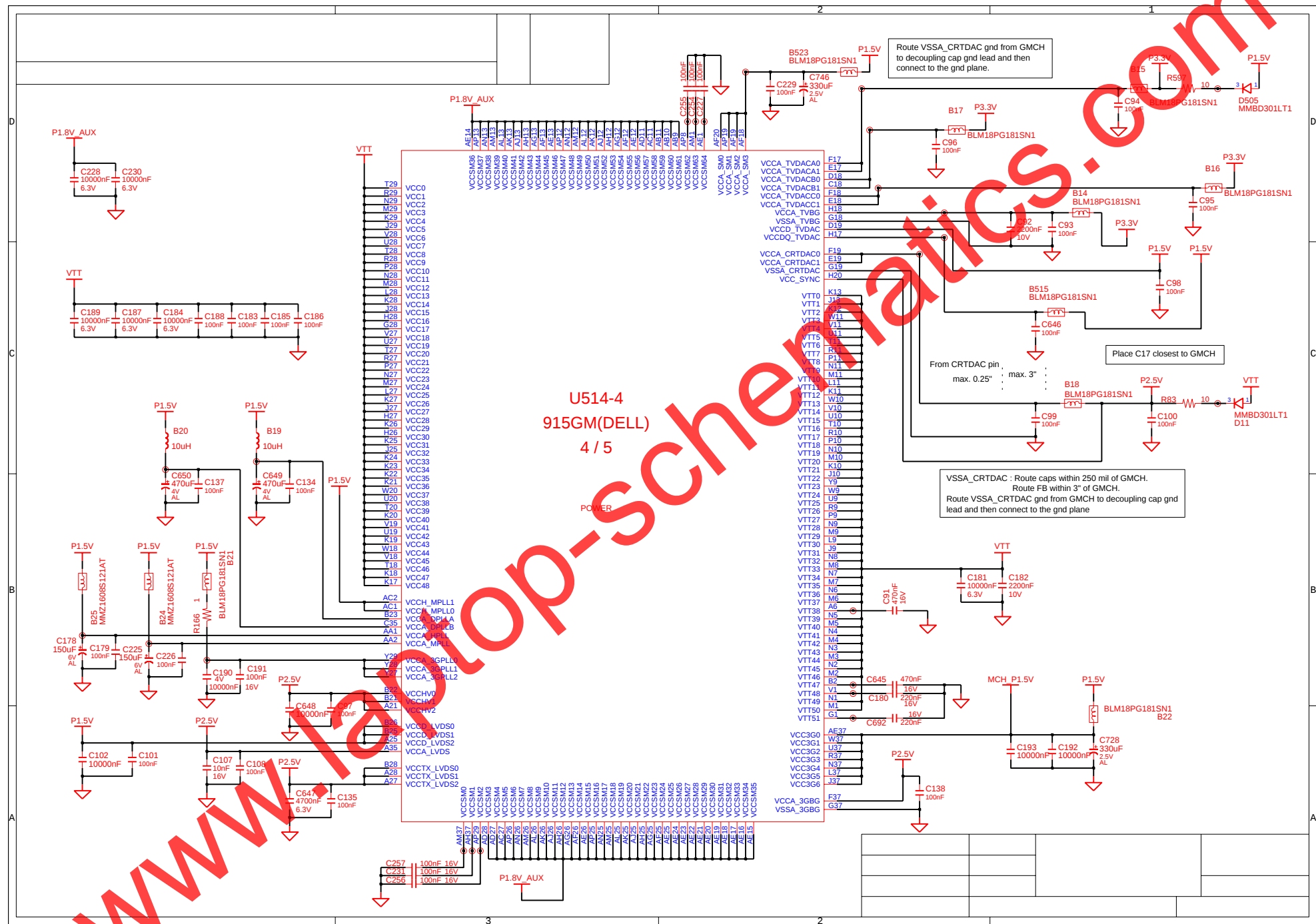


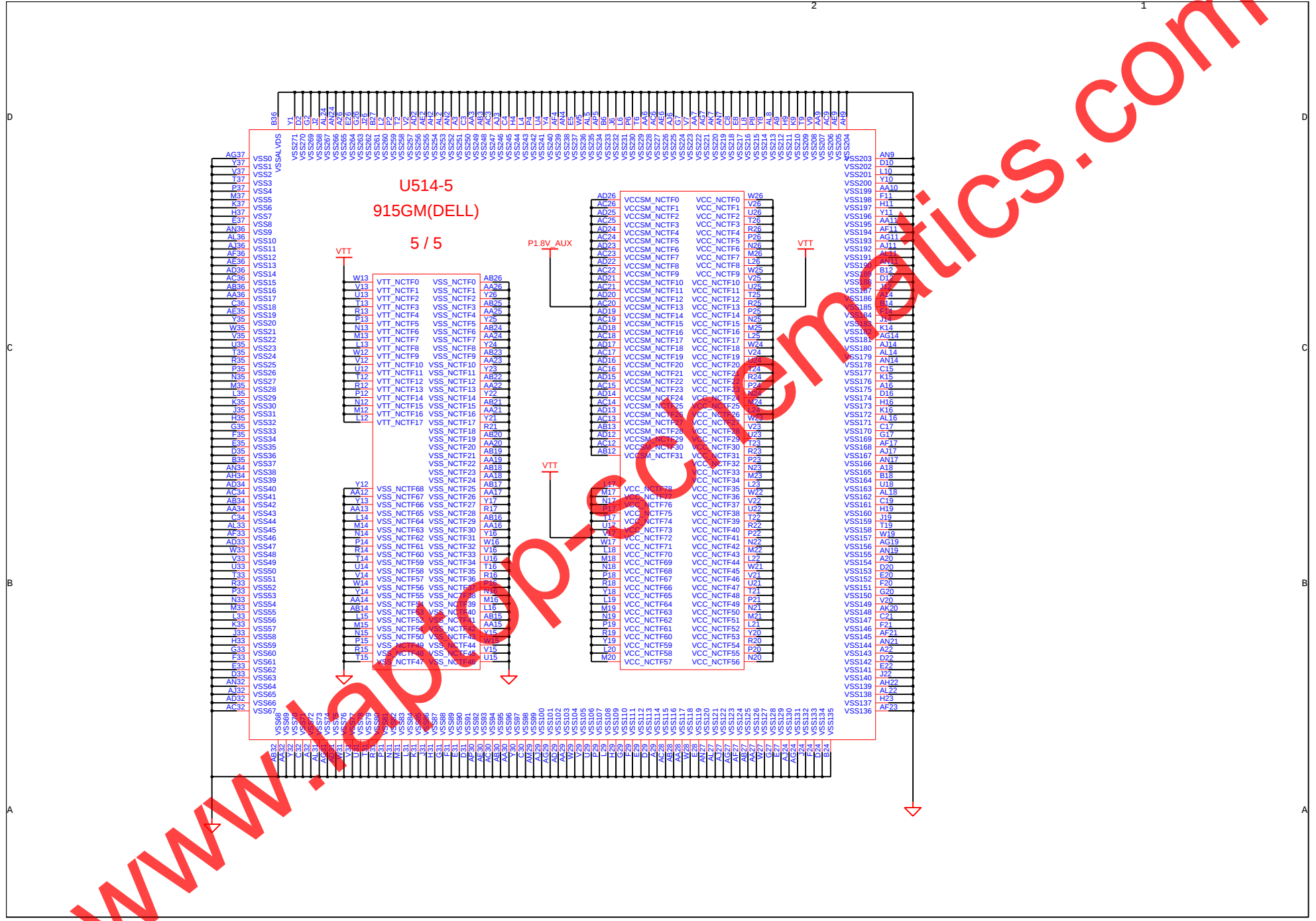




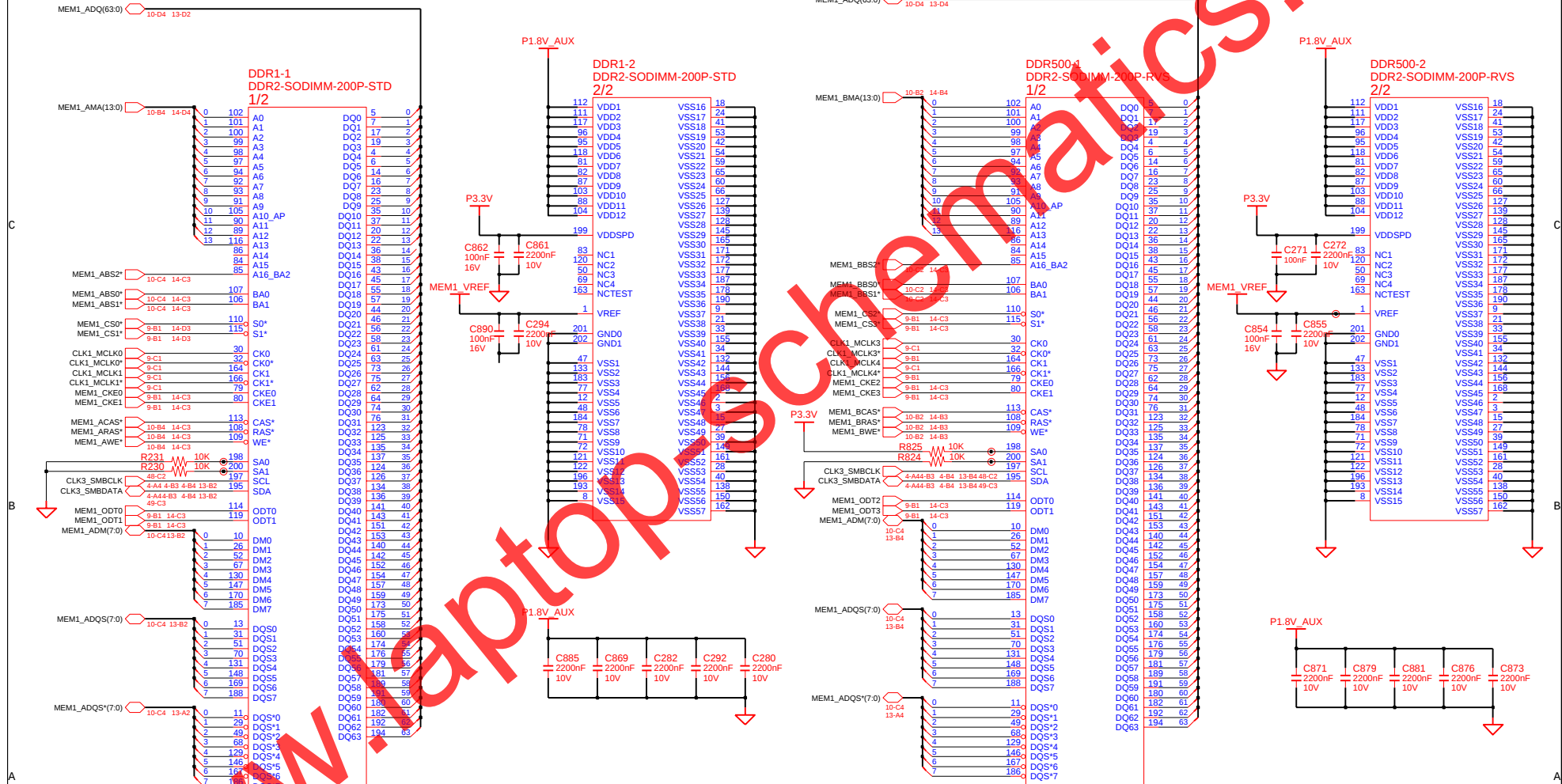


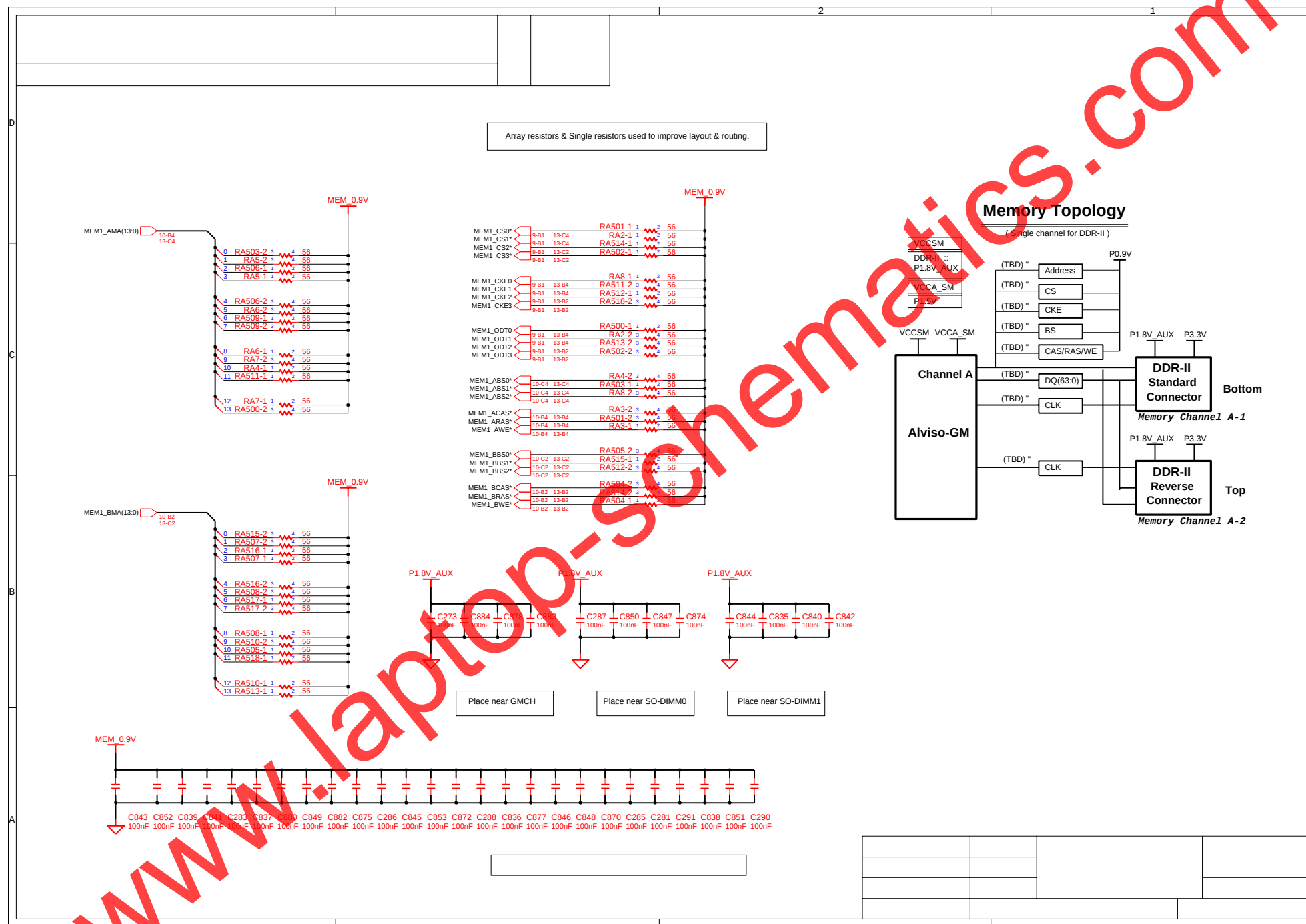


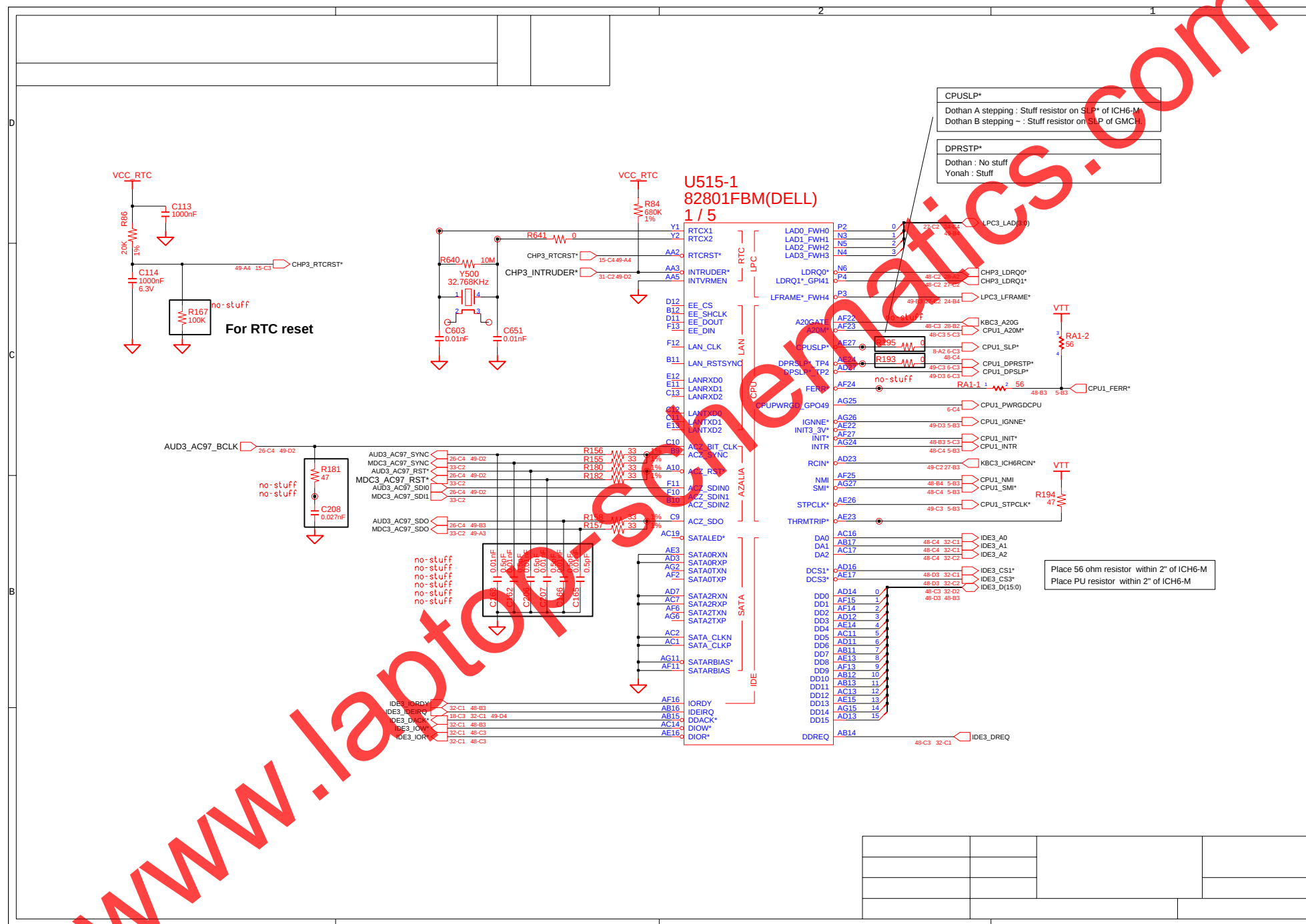




DDR2 SO-DIMM #1
Top

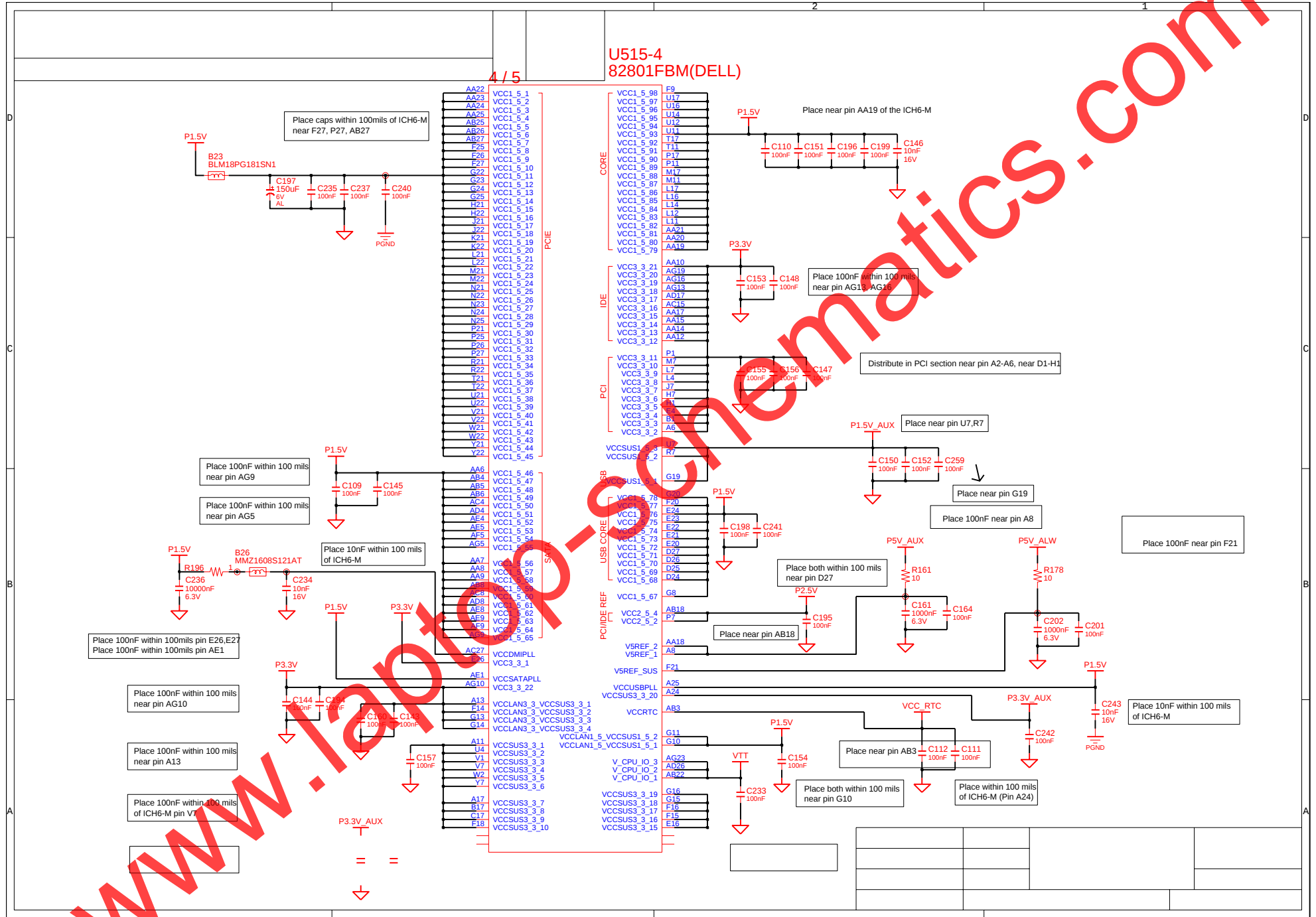




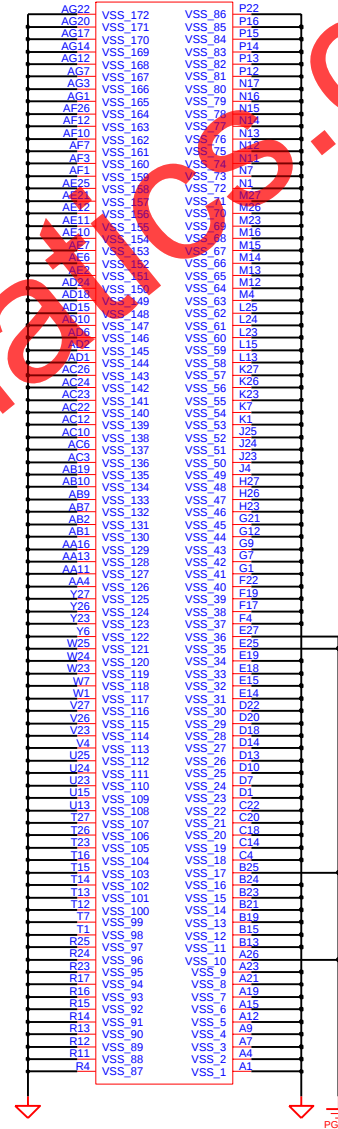
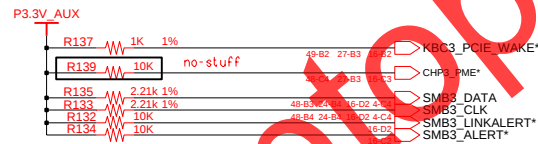
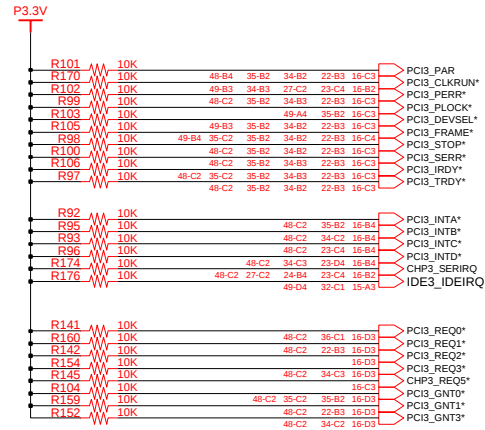


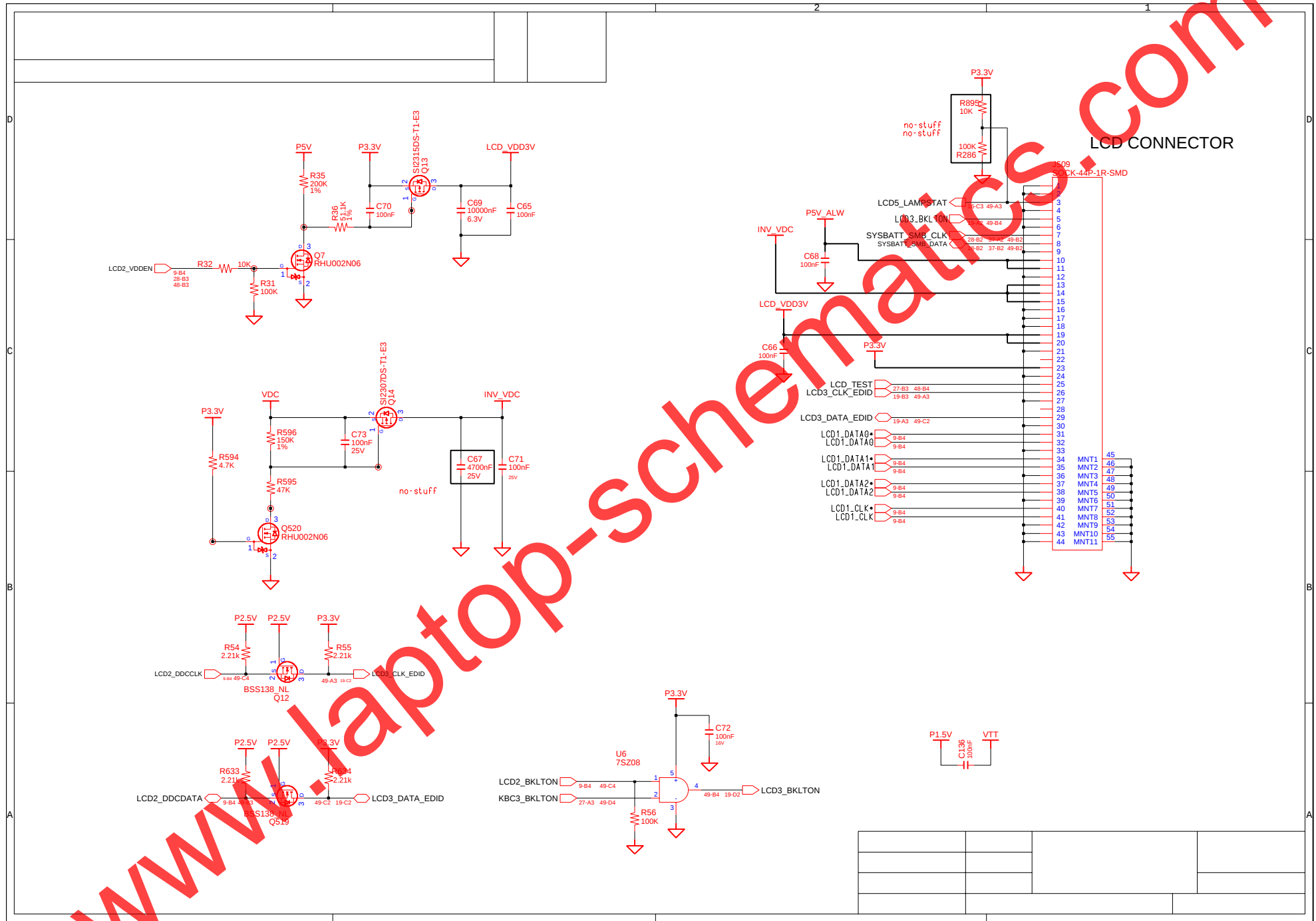
U515-4
82801FBM(DELL)

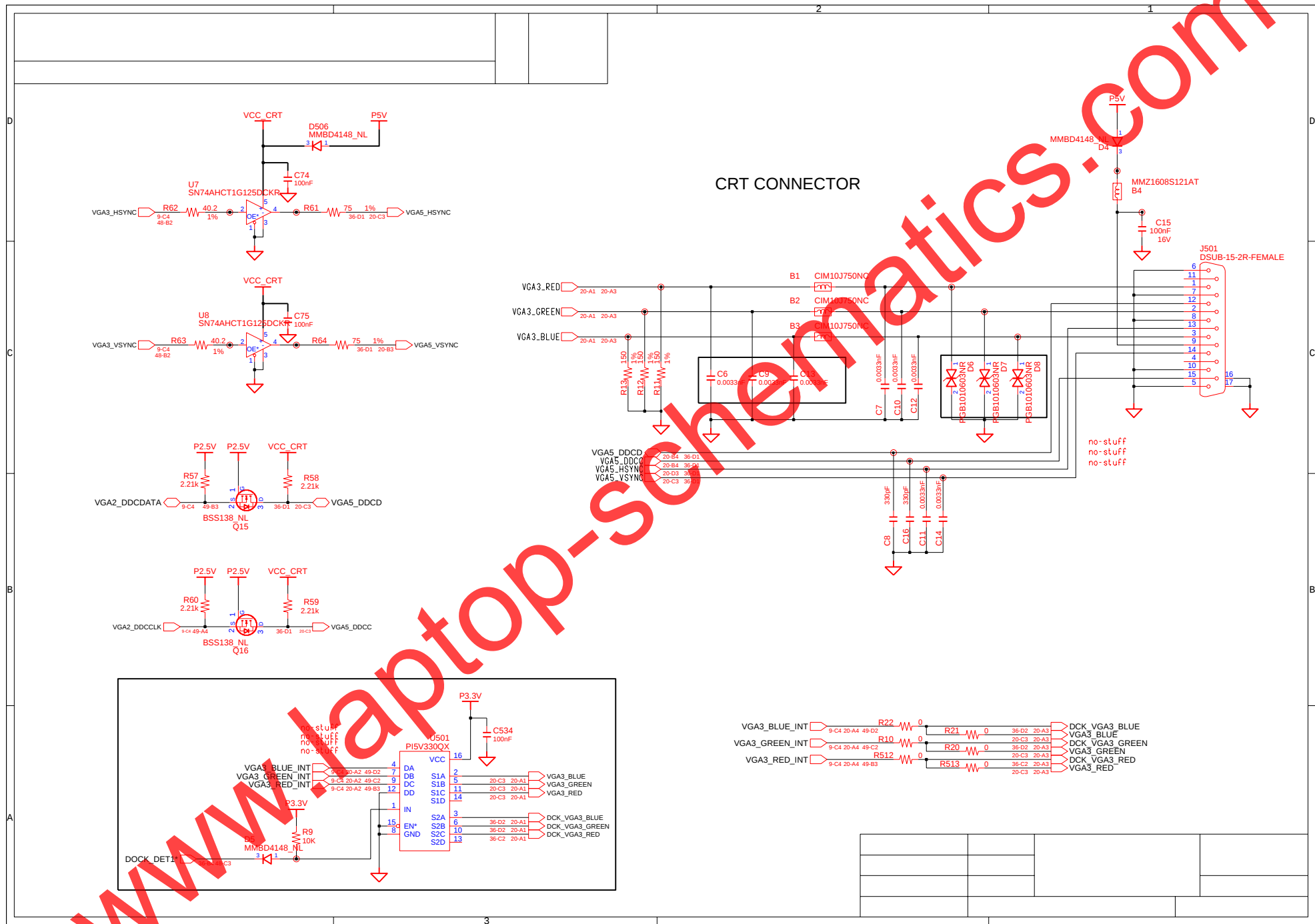
4 / 5

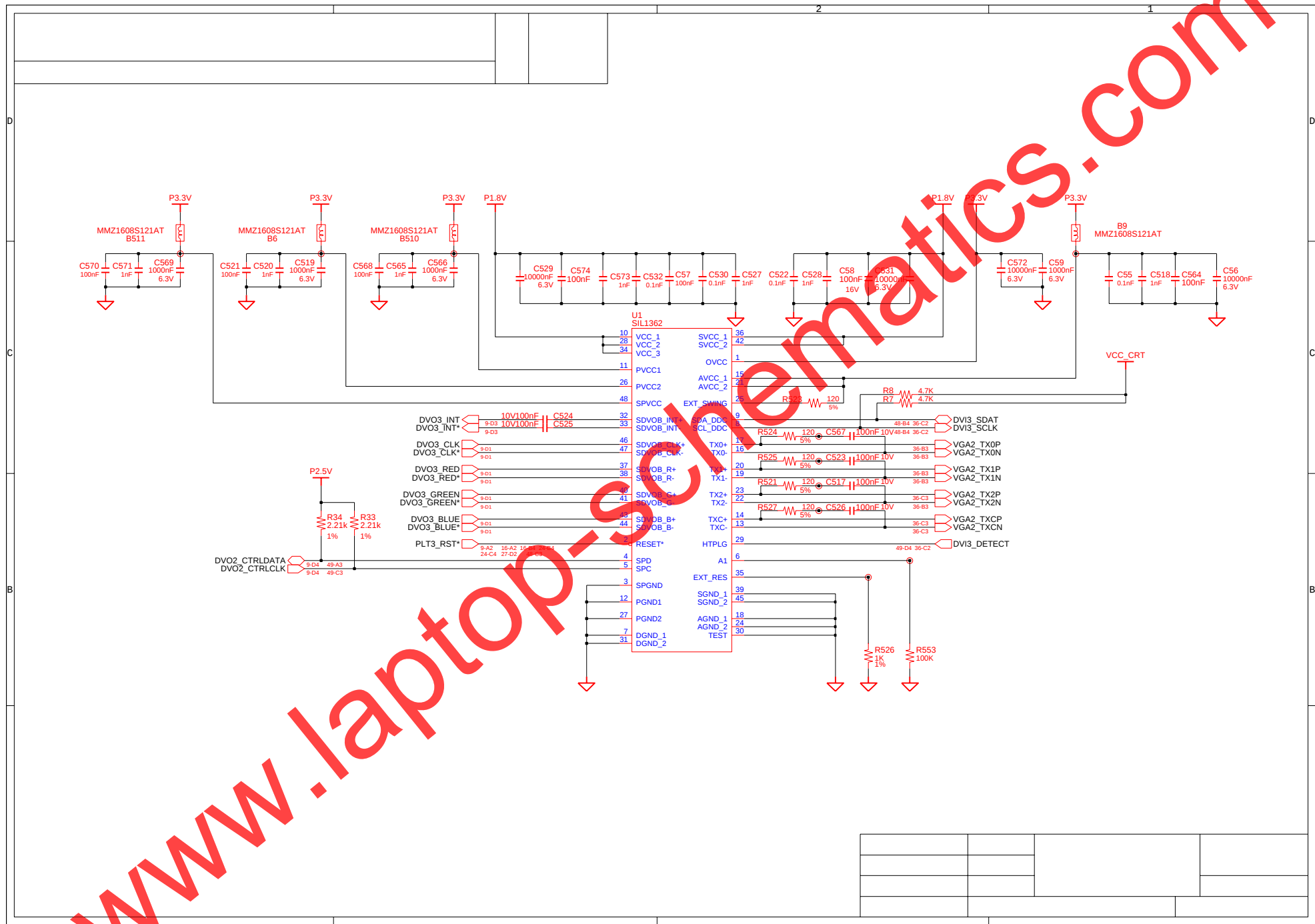


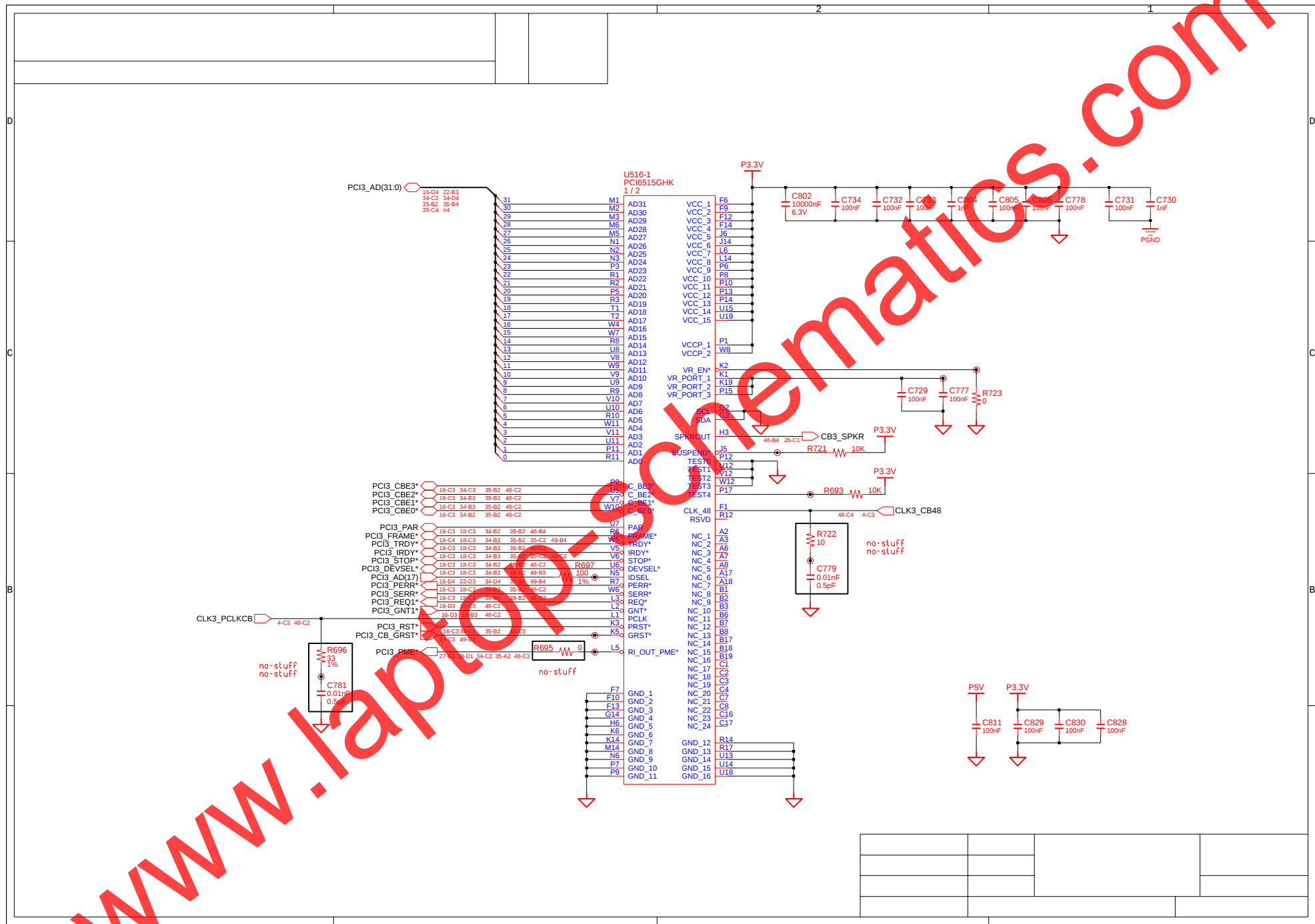
U515-5
82801FBM(DELL)
5 / 5







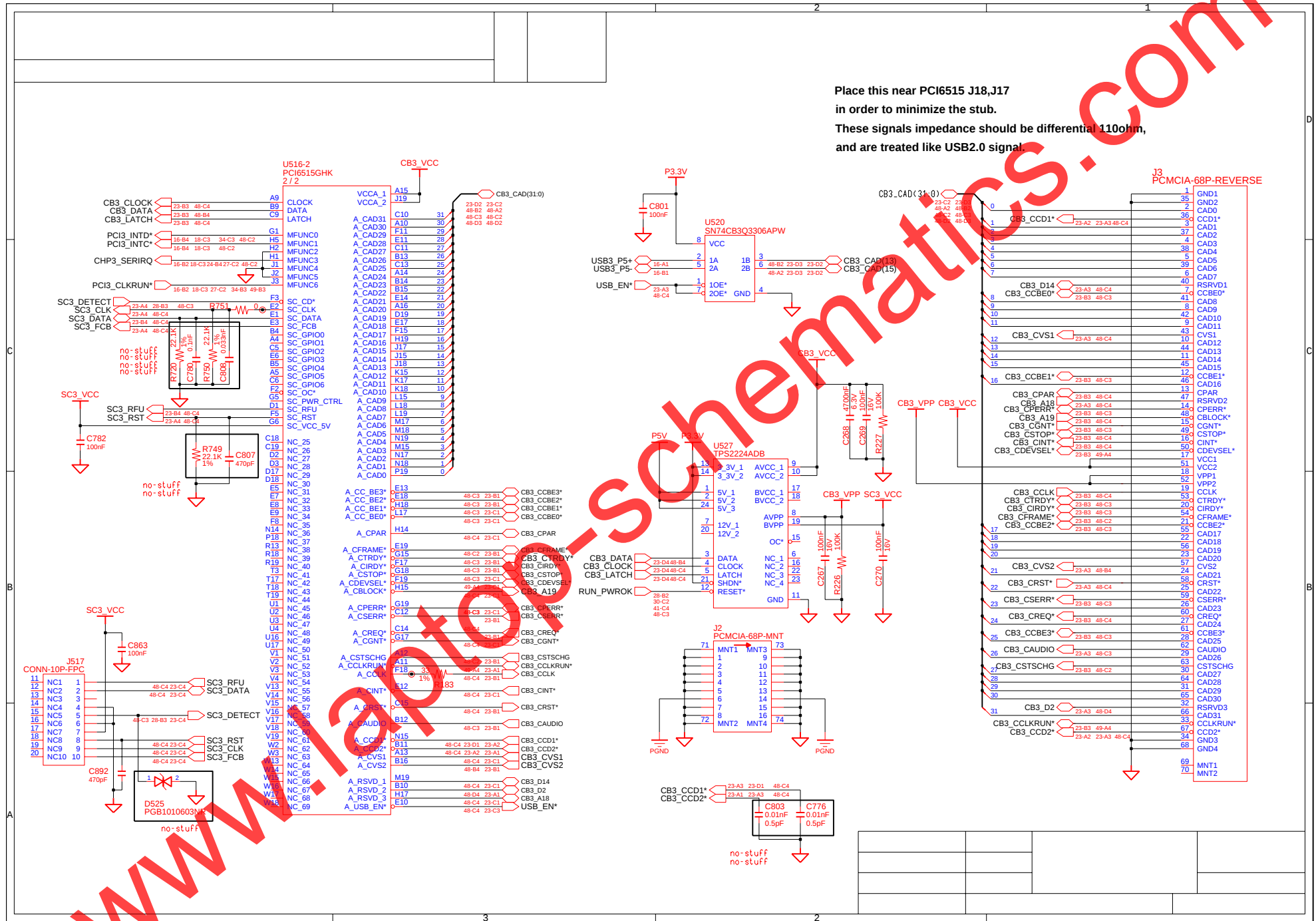


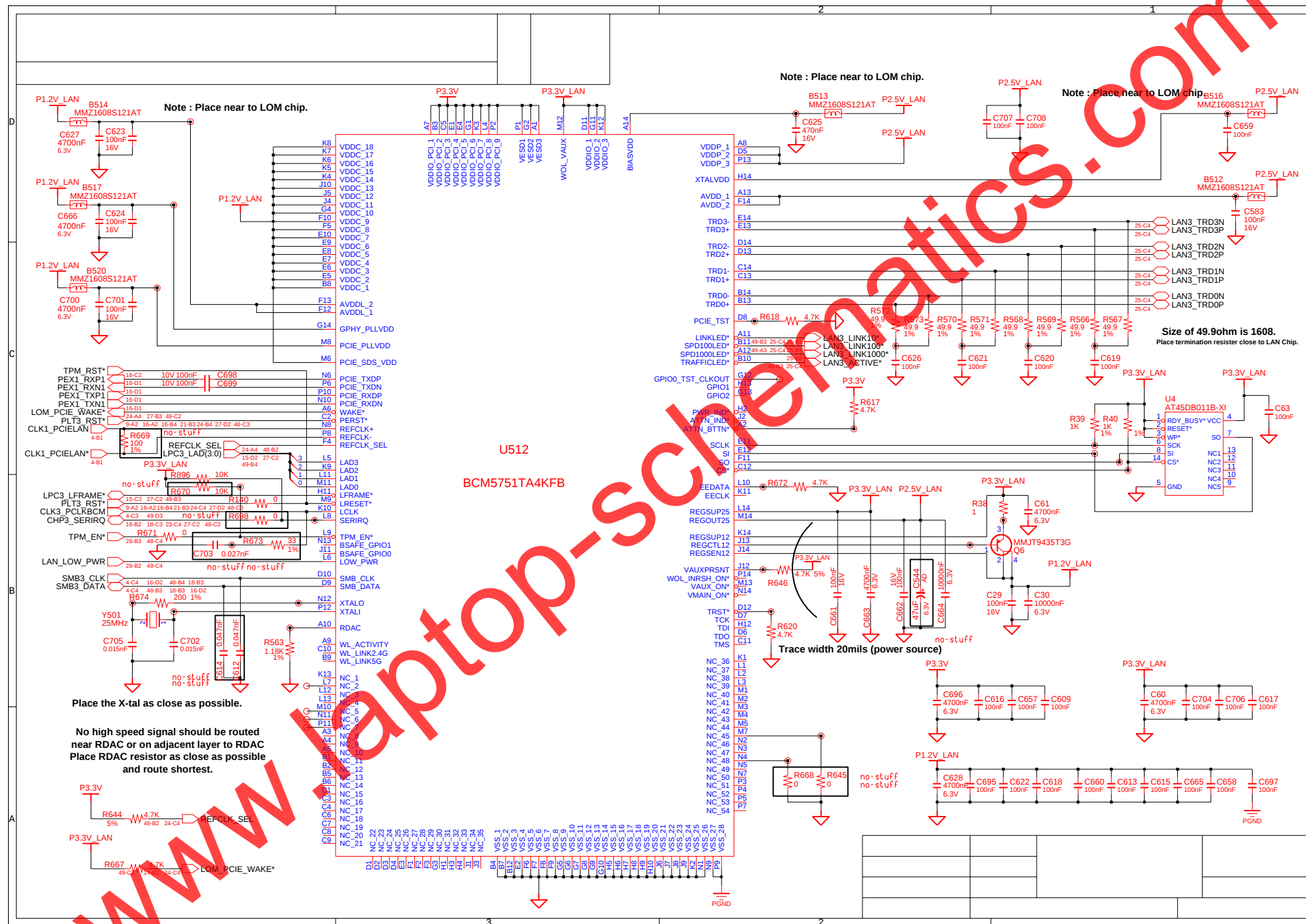


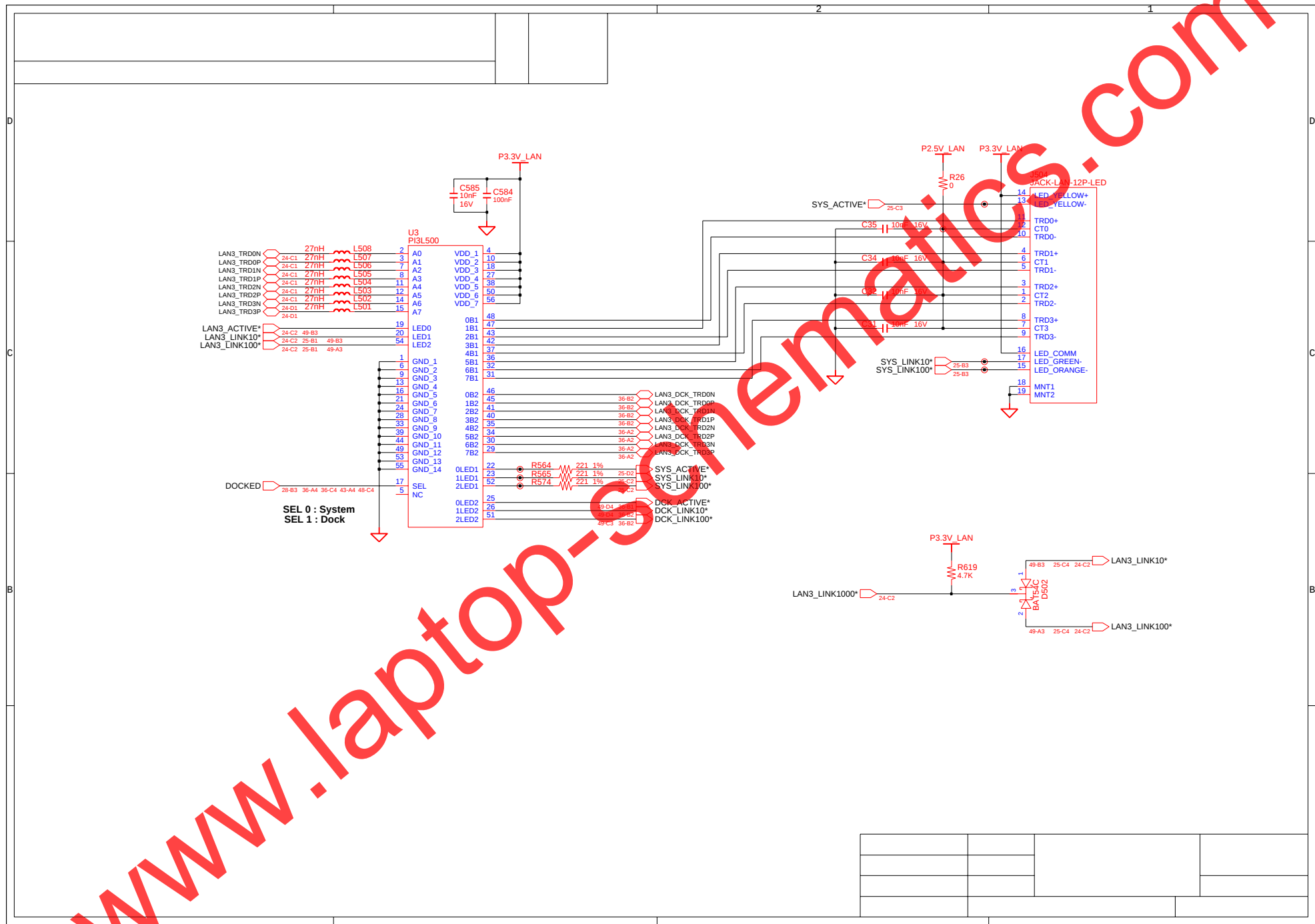
Place this near PCI6515 J18,J17

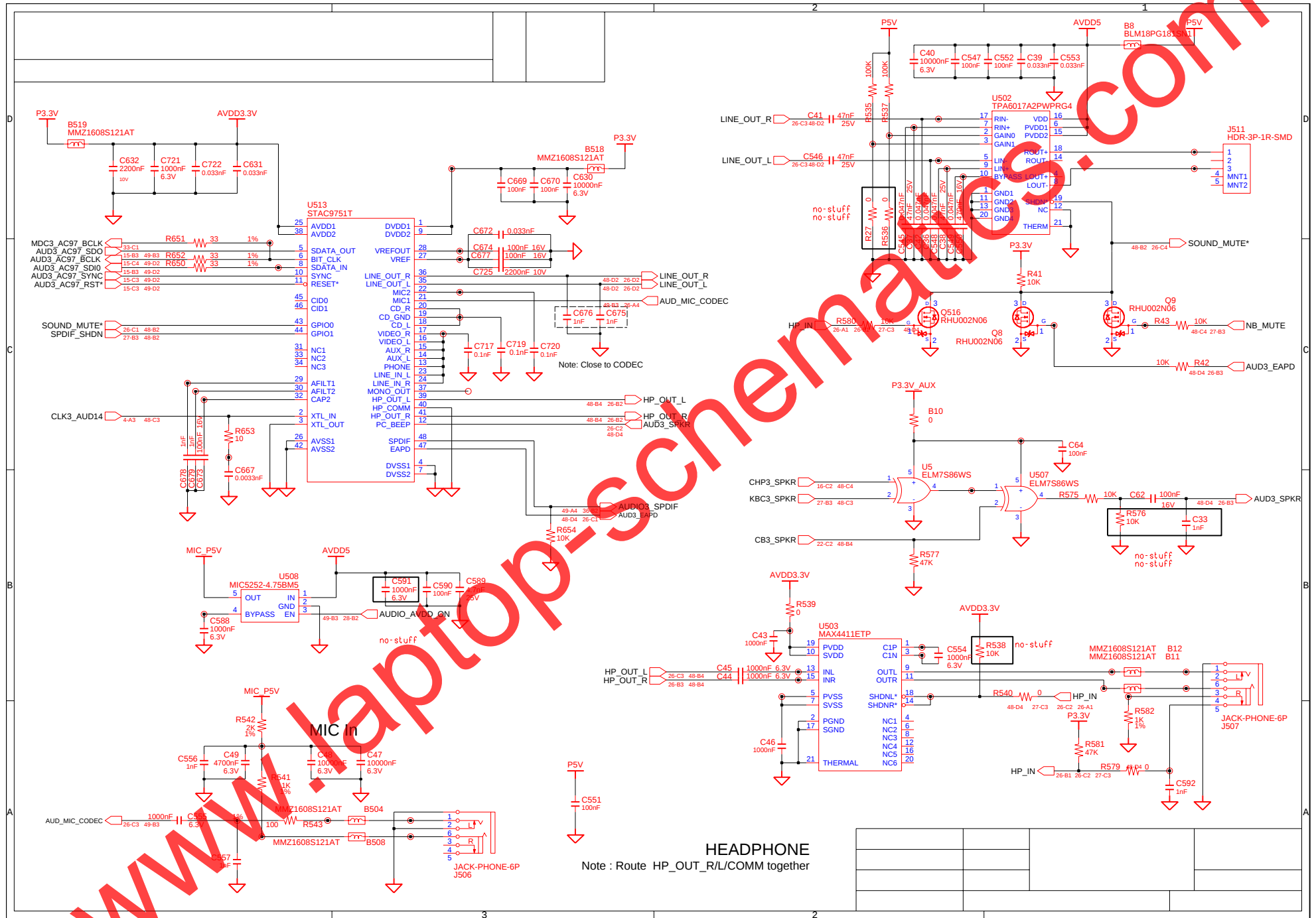
in order to minimize the stub.

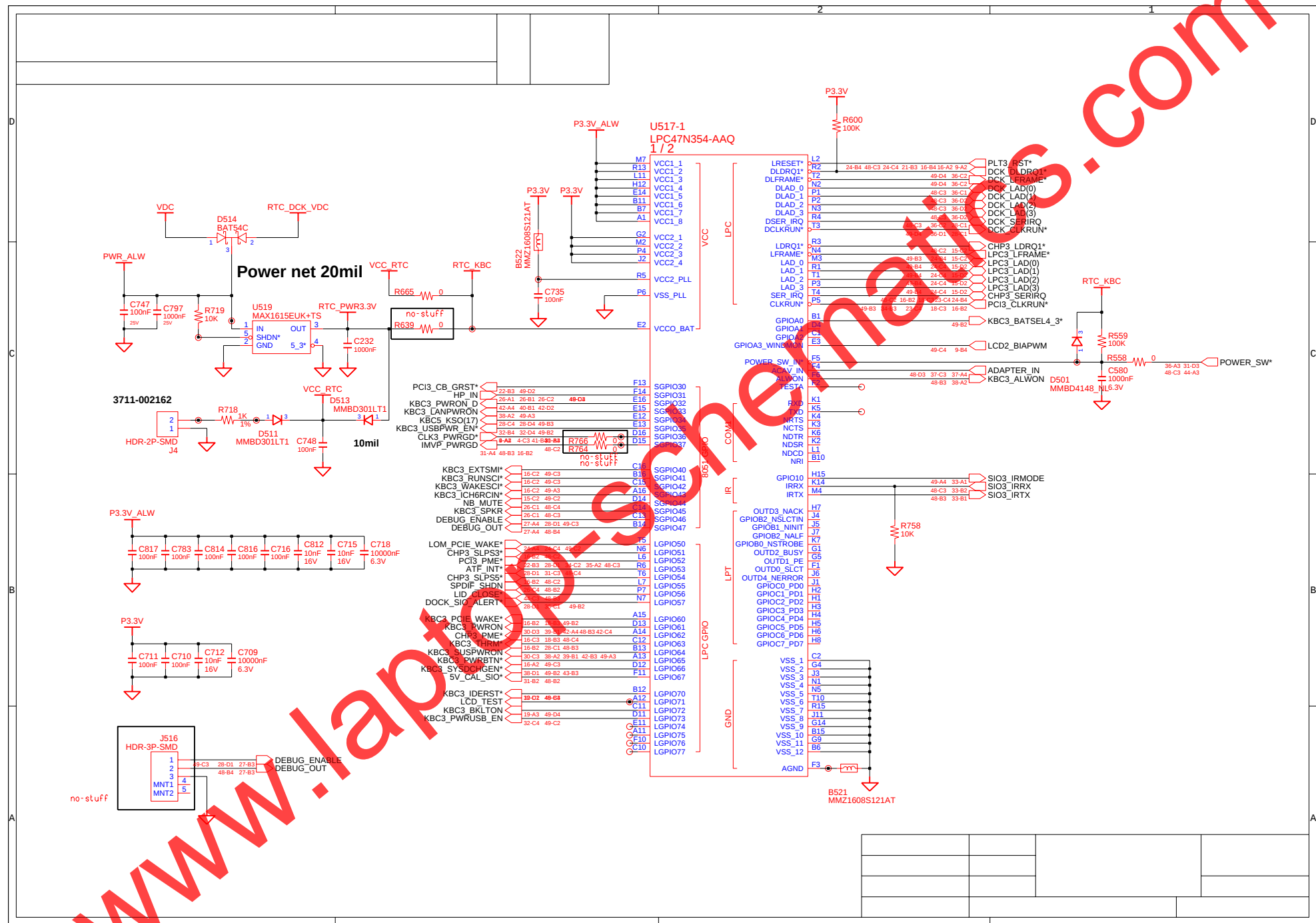
These signals impedance should be differential 110ohm,
and are treated like USB2.0 signal.

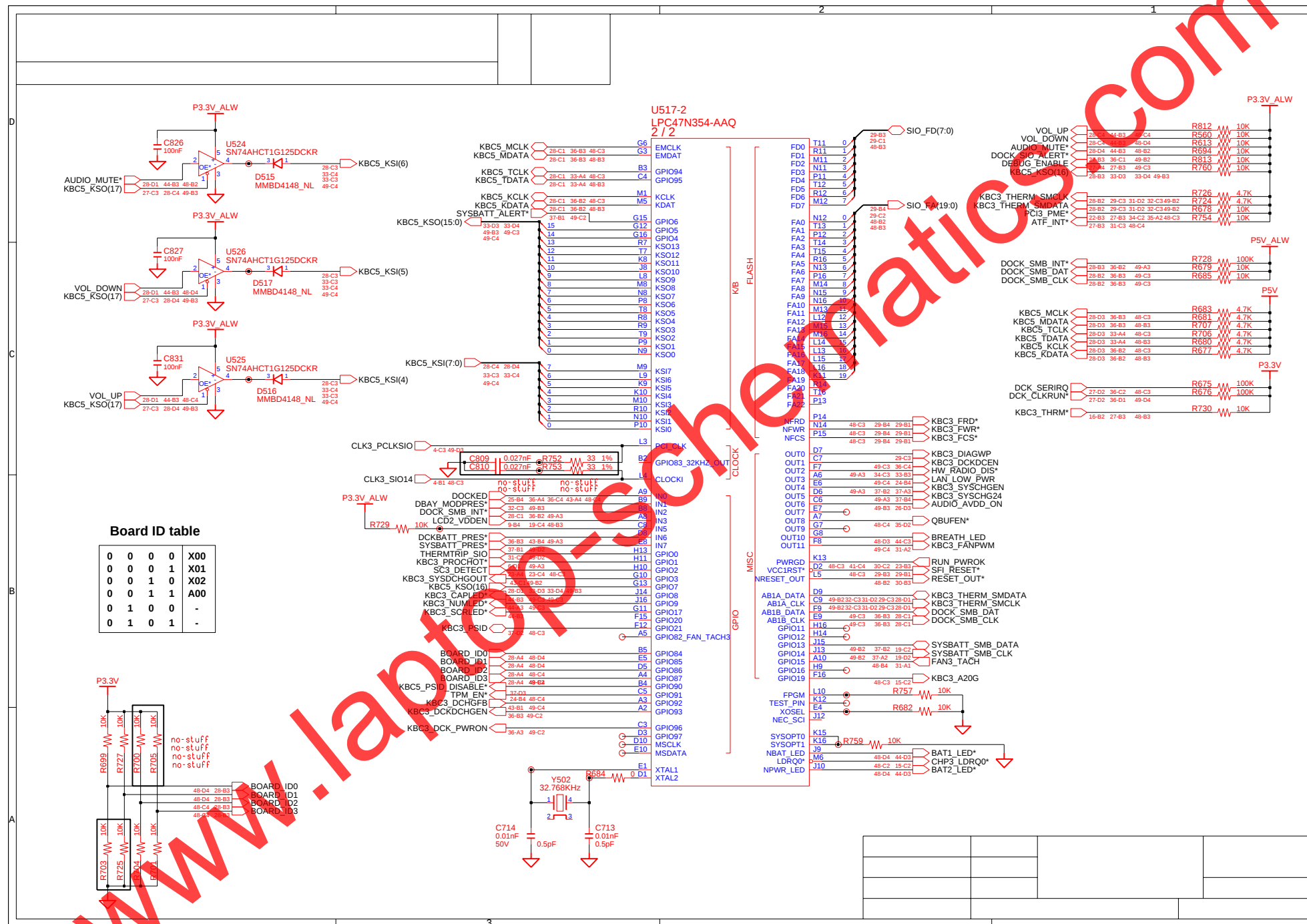


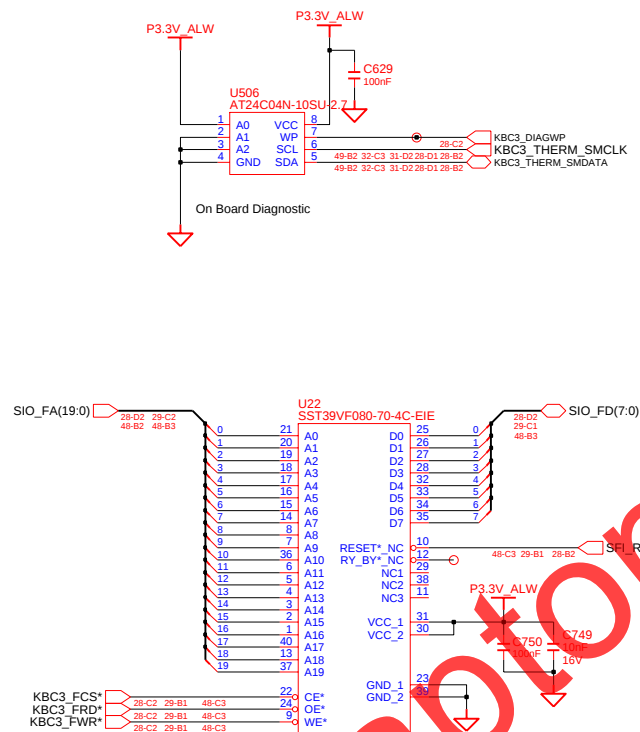






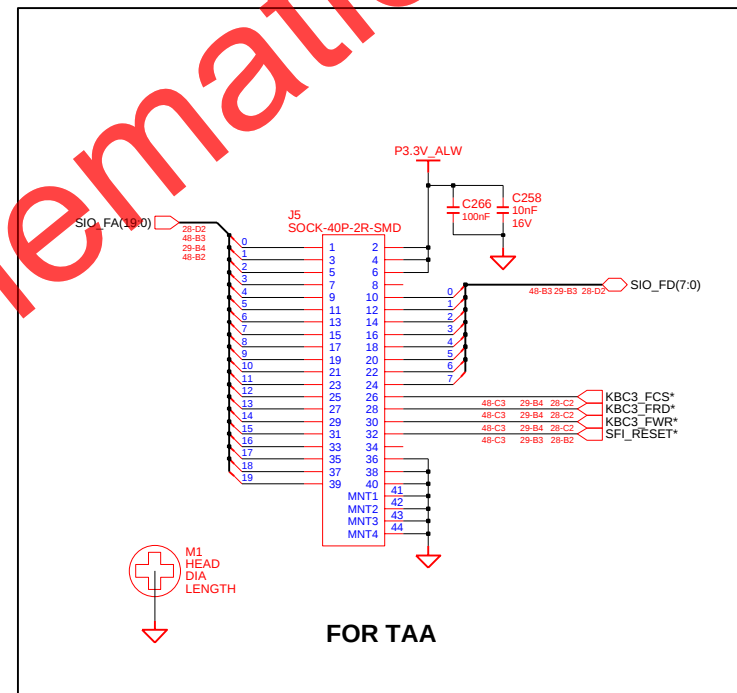


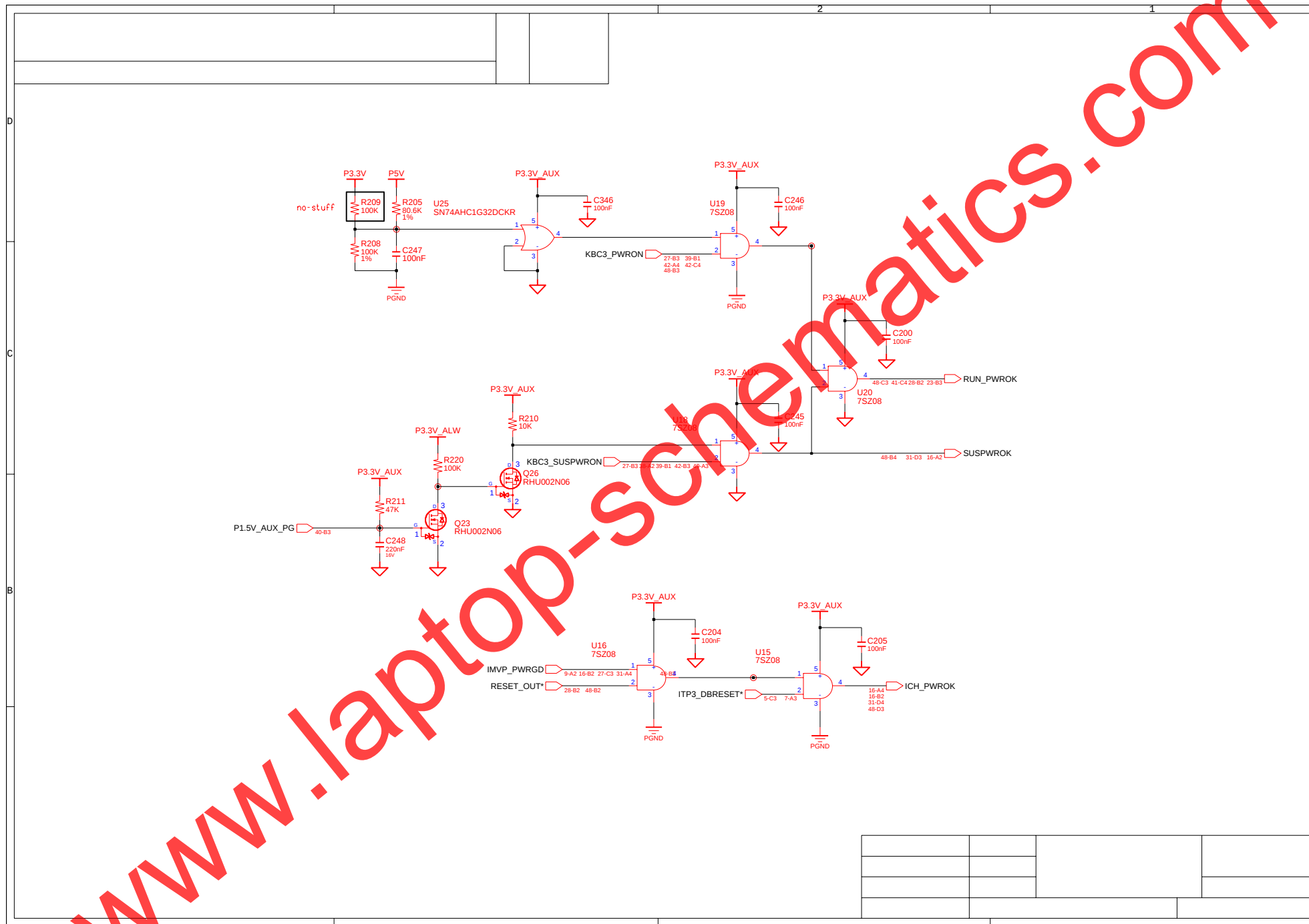


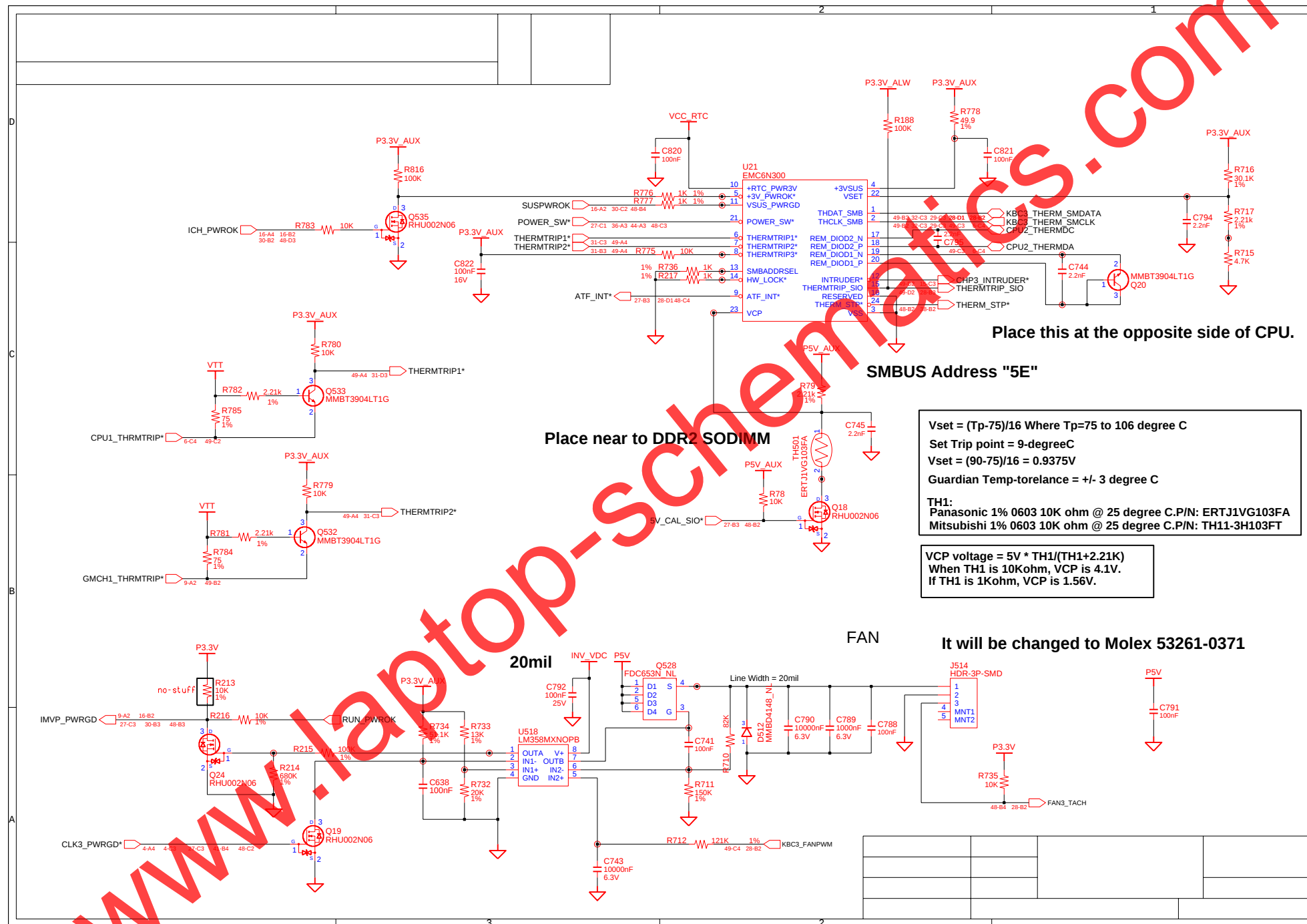


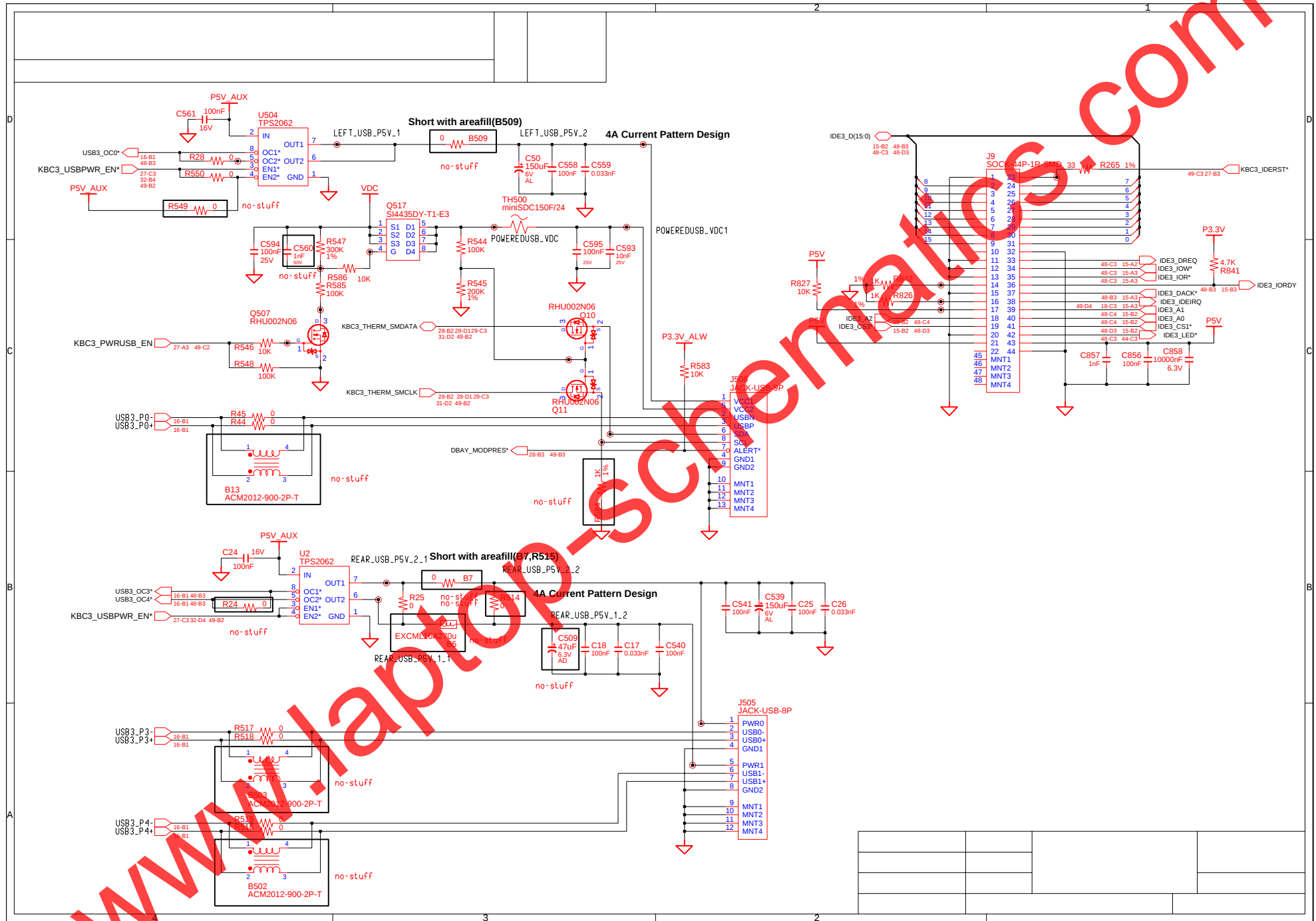
AMD : Pin 10 is RESET* ; Pin12 is RY/BY*
SST : Pin 10, 12 are NC.

1. AMD-29LV081B require MAX 500ms Tready for it's hardware reset. And MAX6326_UR29 has > 100ms reset timing. So we can tie it's reset* pin to +3VALW directly.
2. SIO has internal 20ms dela of VCC1_pIPWROK.

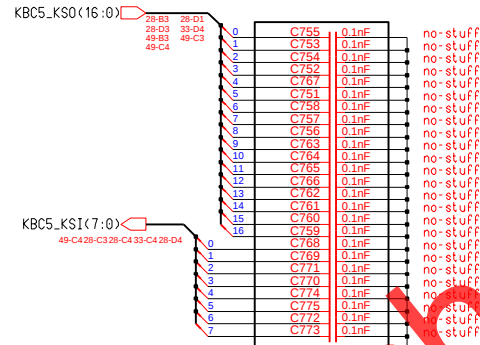
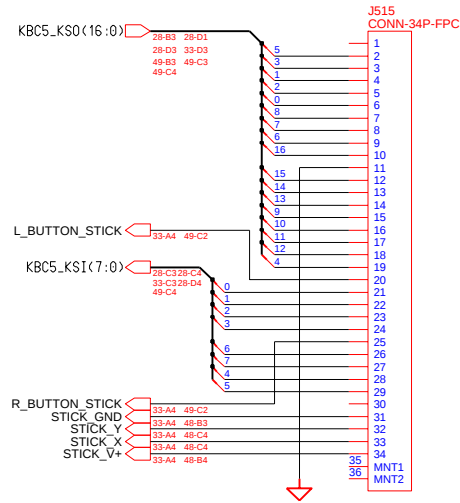




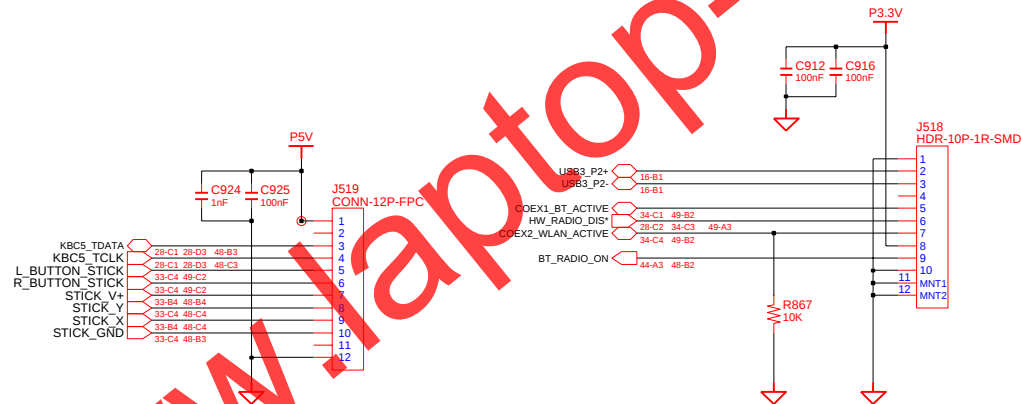




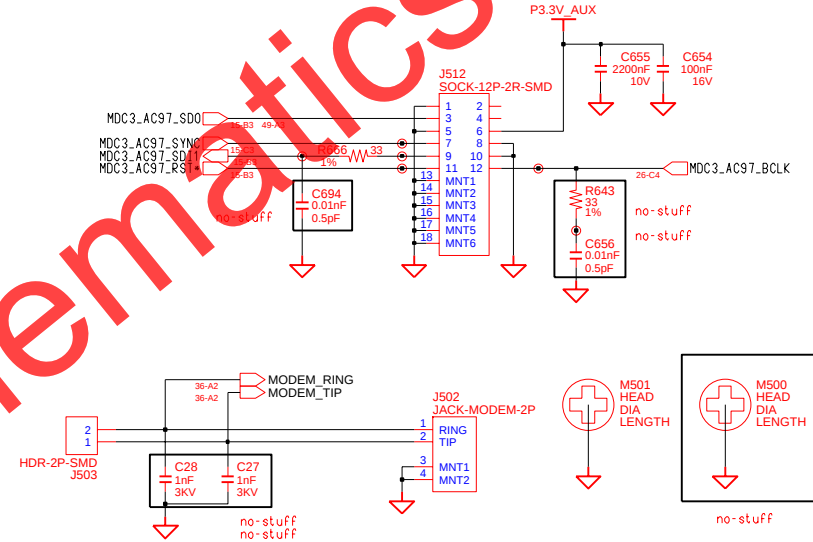
KEYBOARD



Bluetooth (Type A Module)

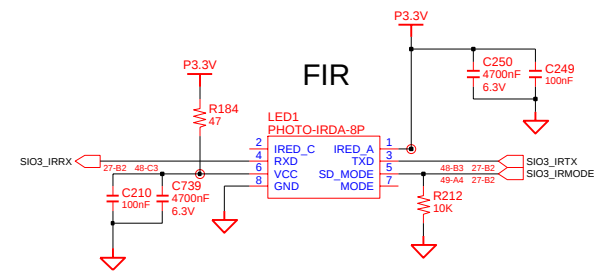


MDC connector



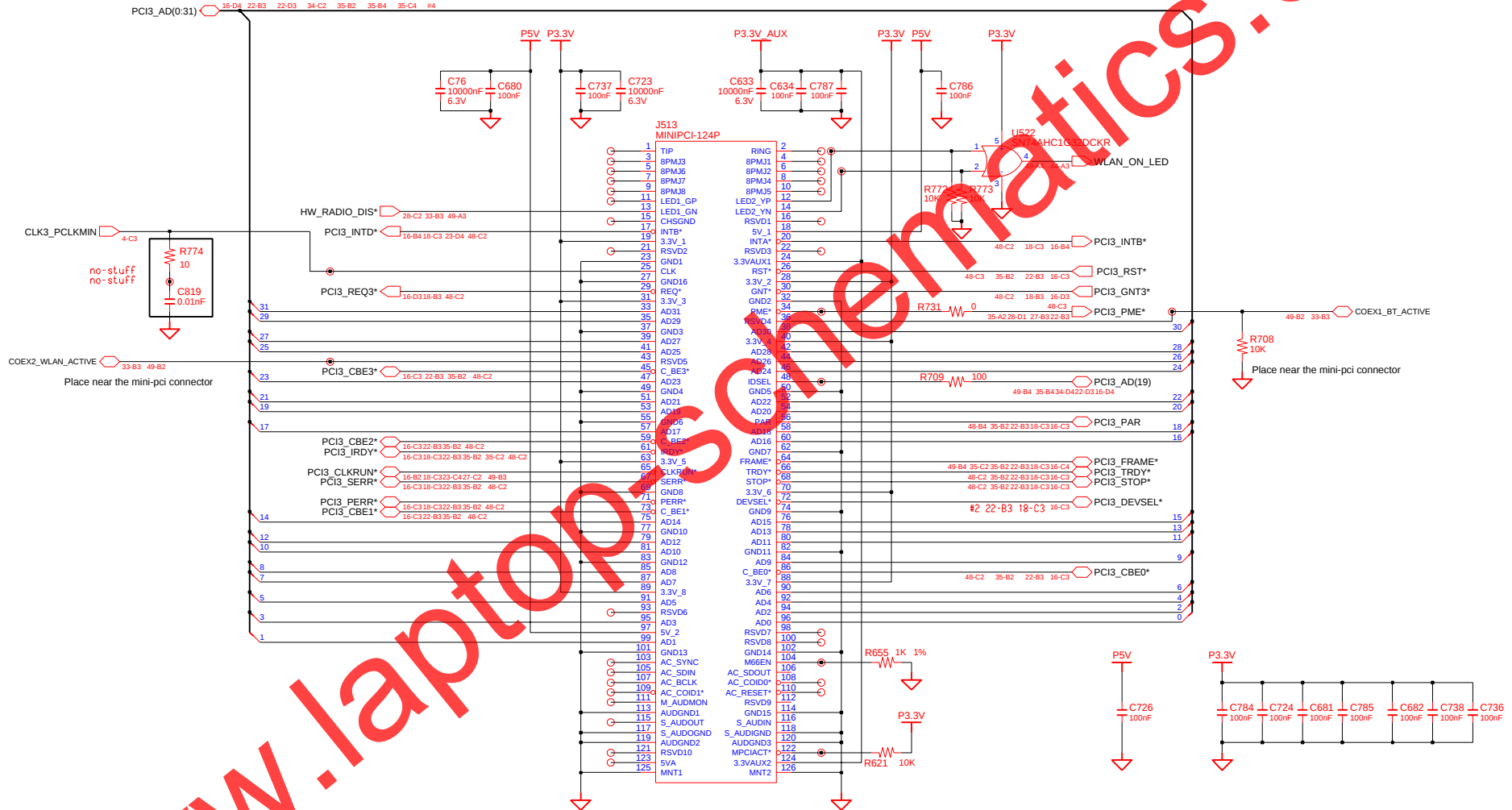
This supporter will be used for MDC cable.

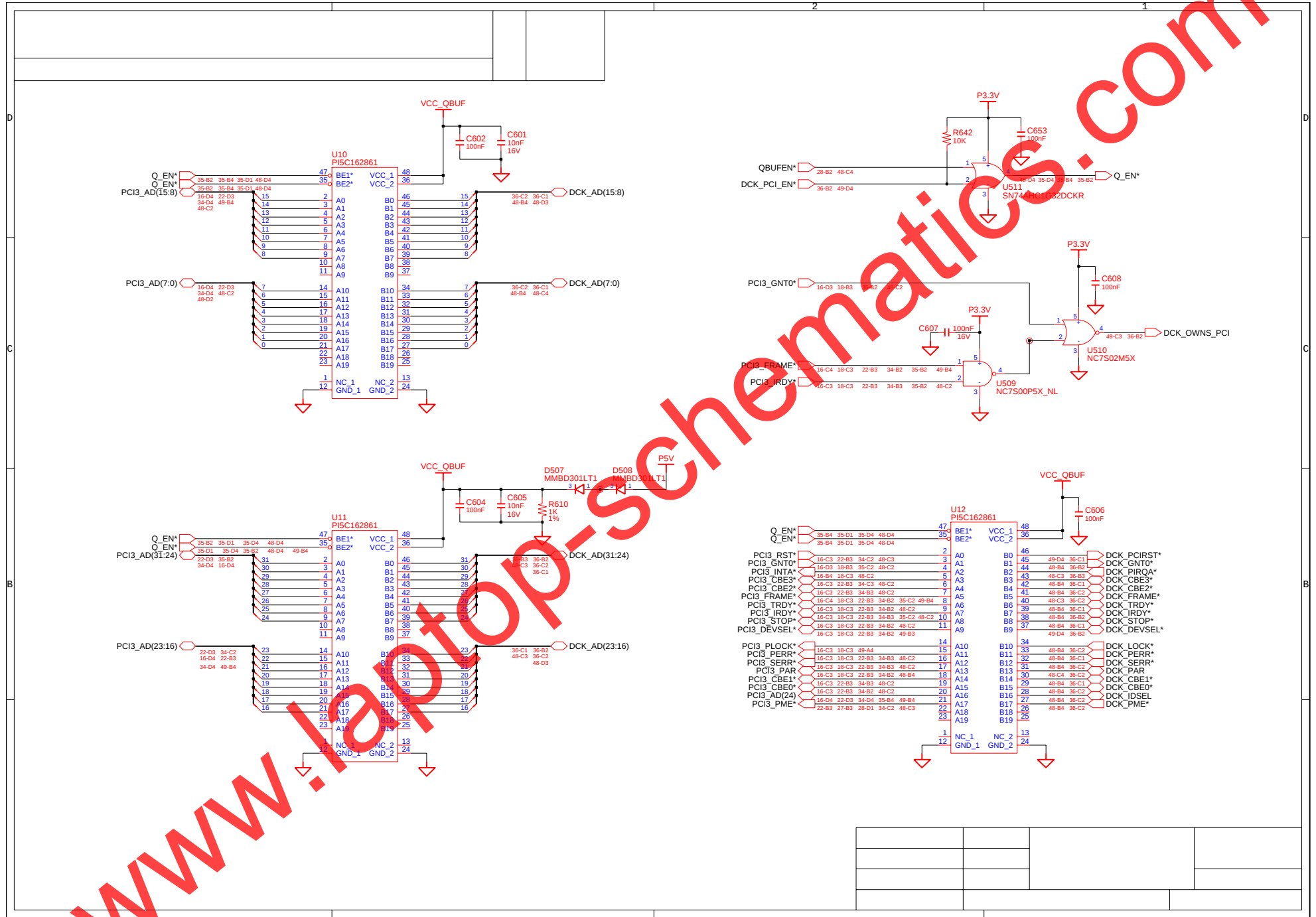
FIR

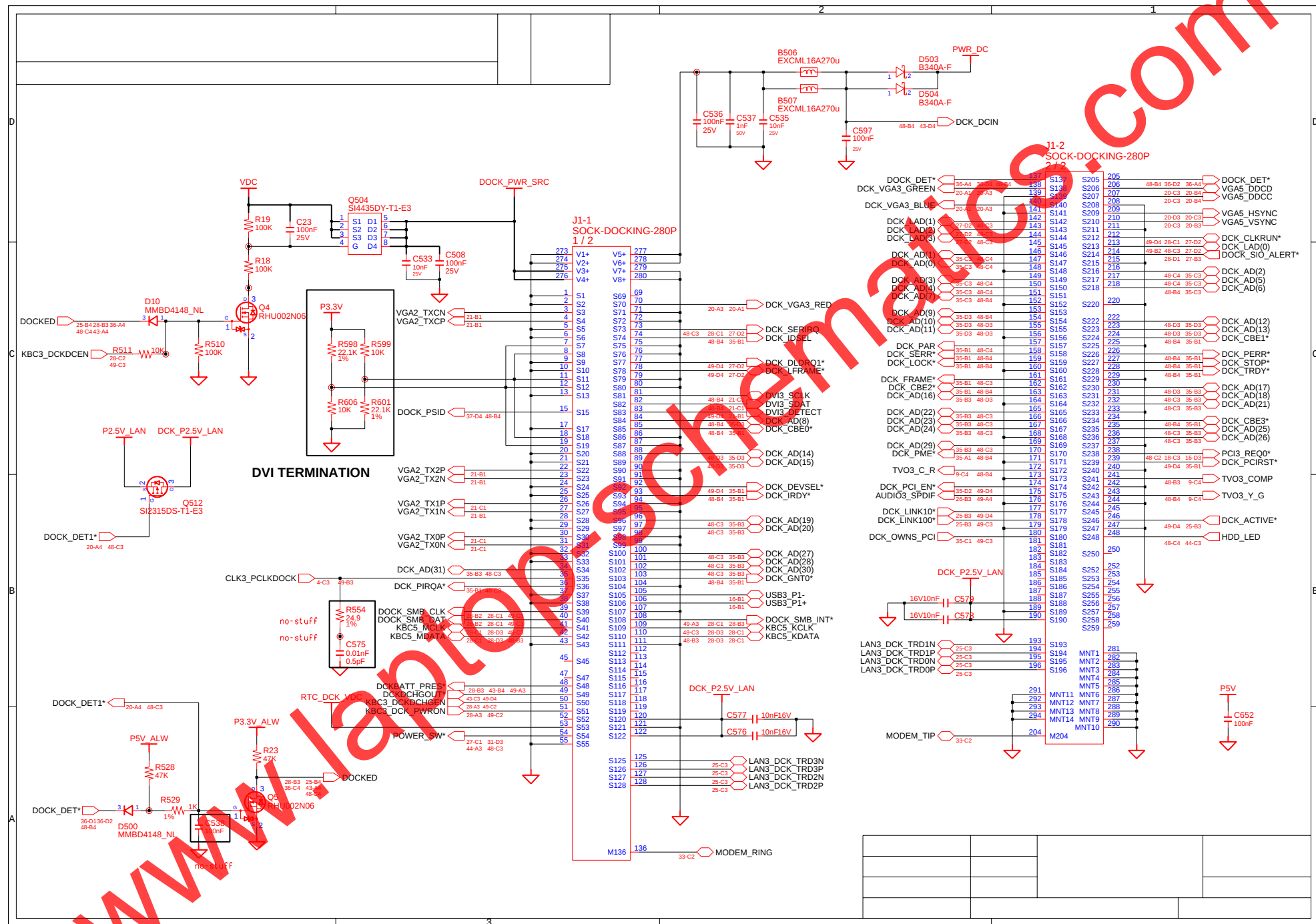


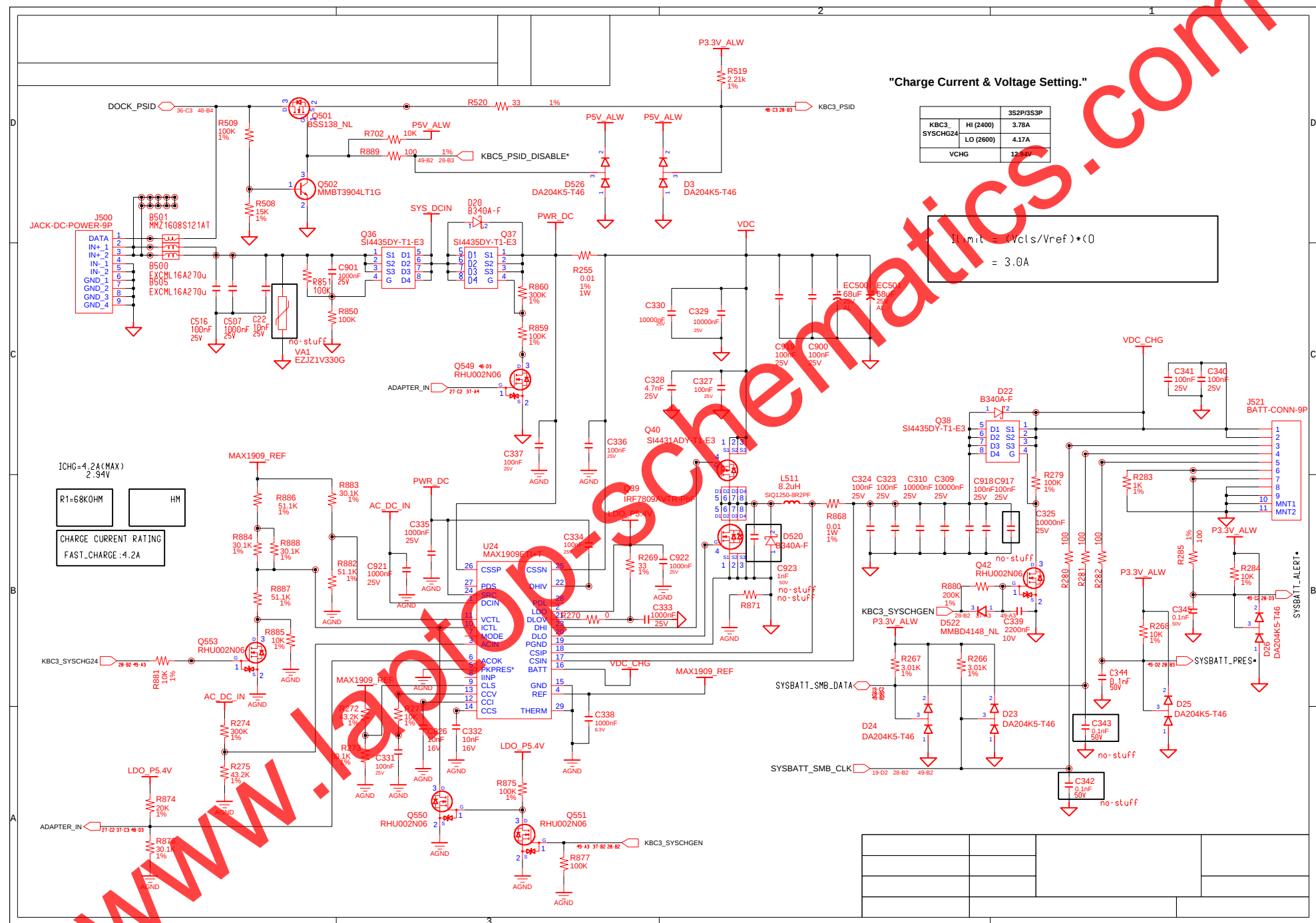
MINIPCI

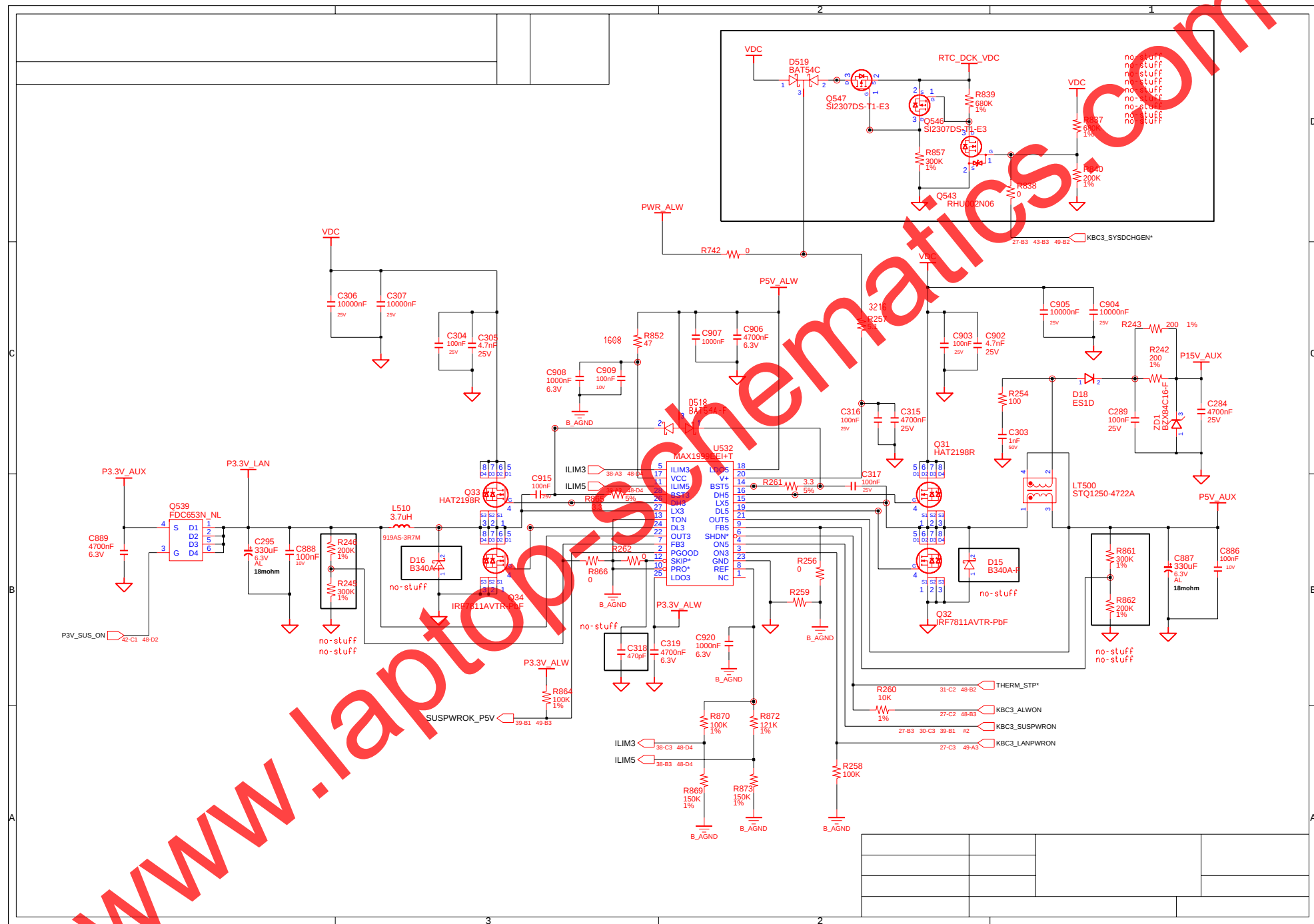
No Wake On Wireless LAN

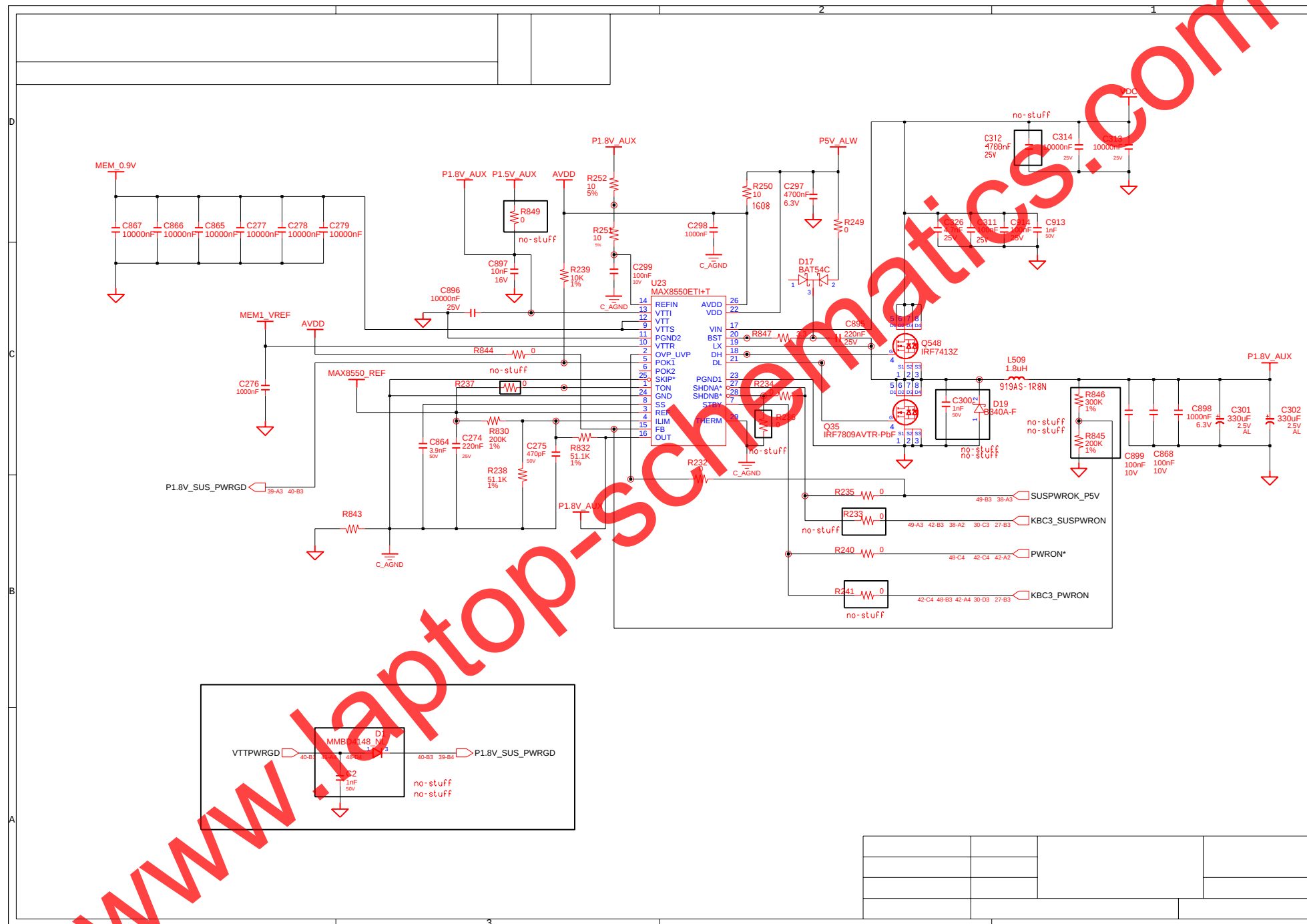


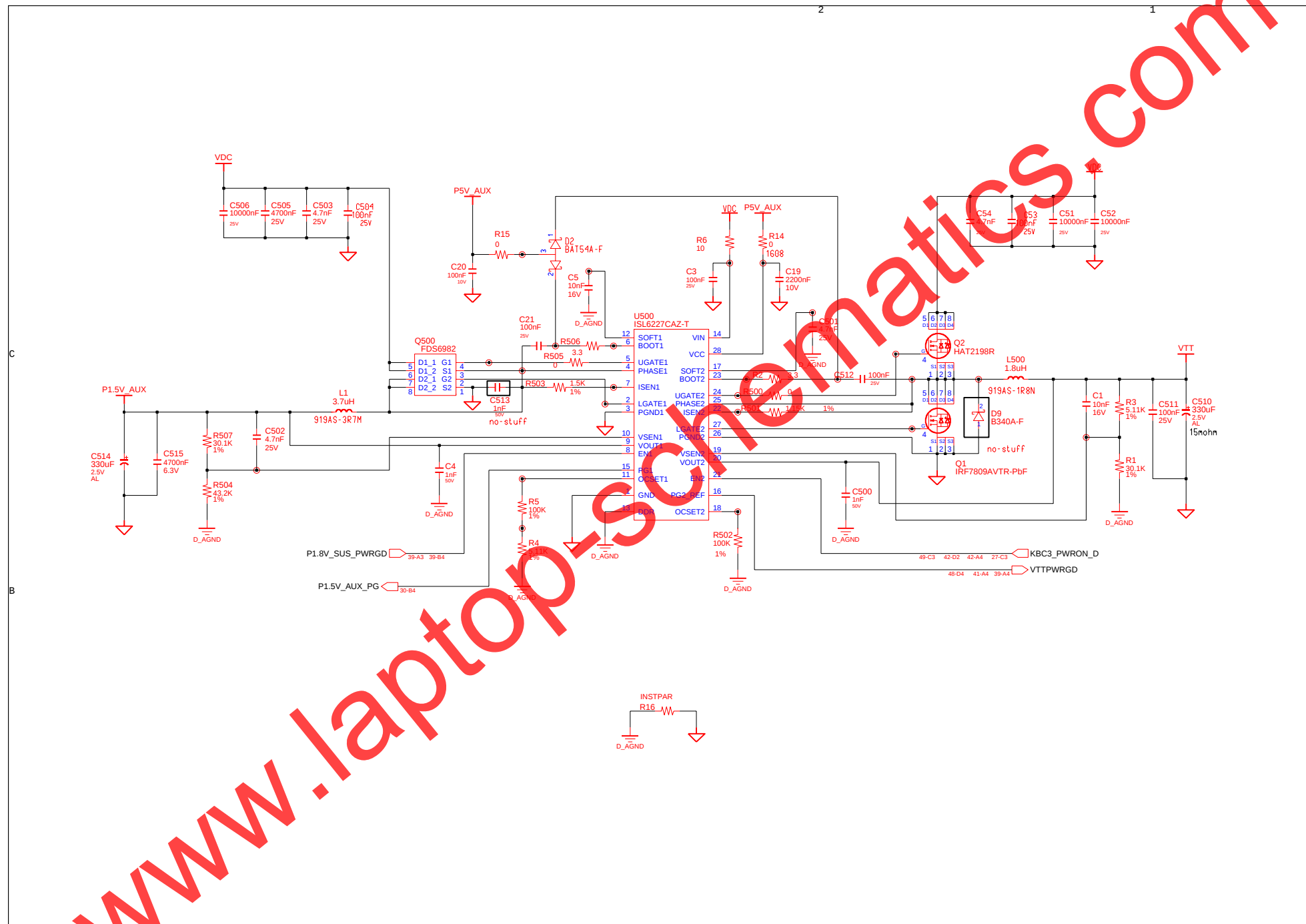


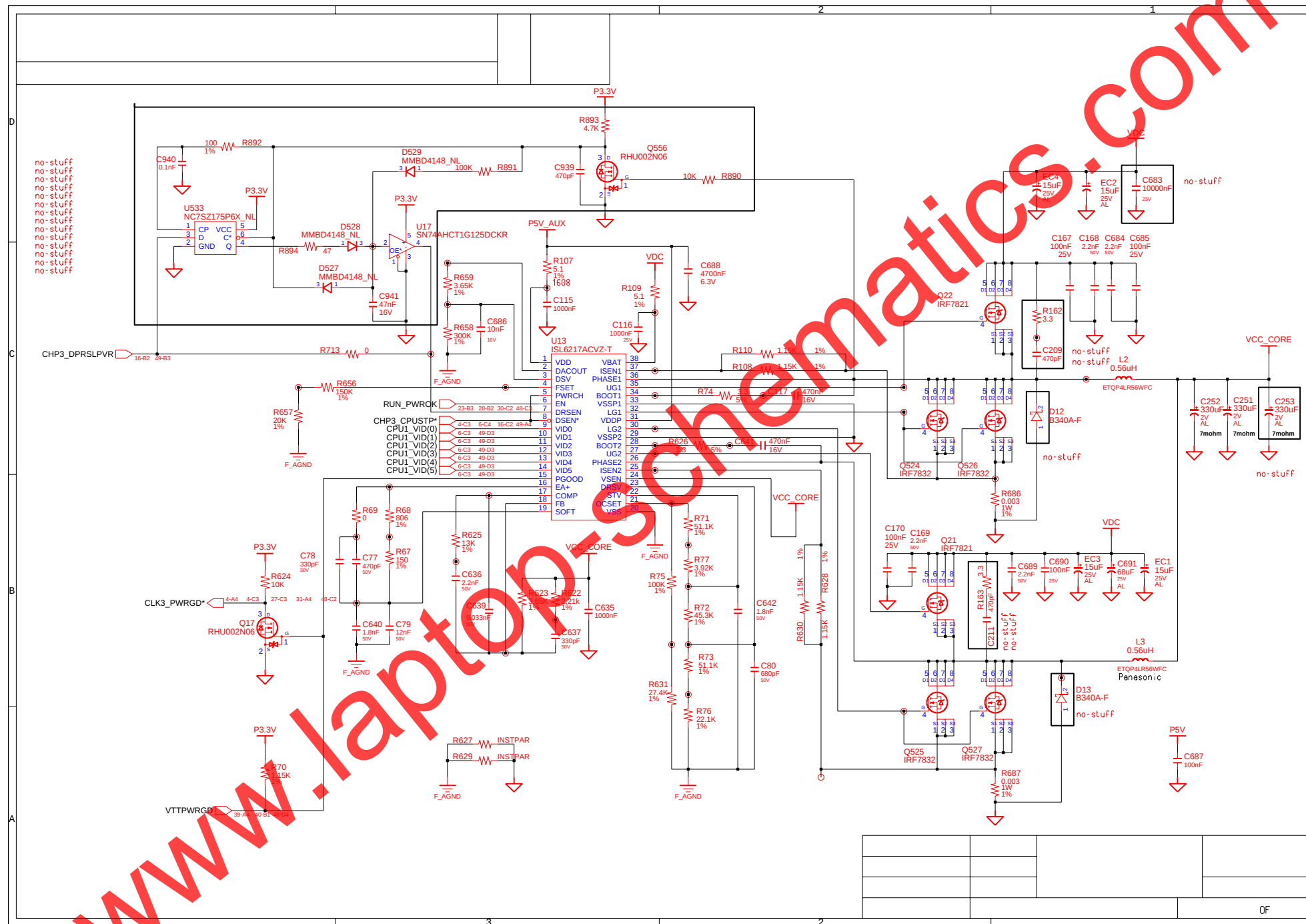


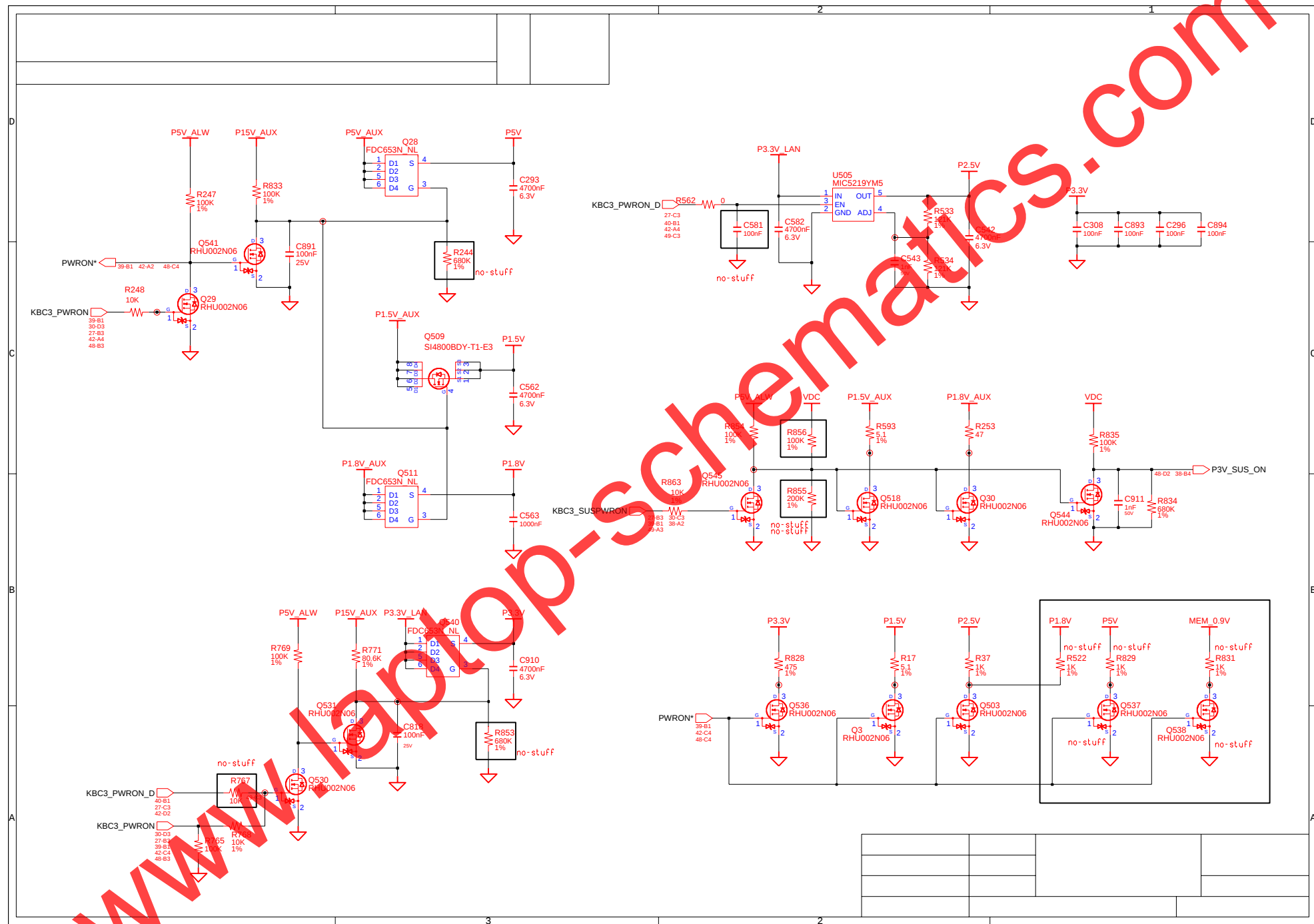


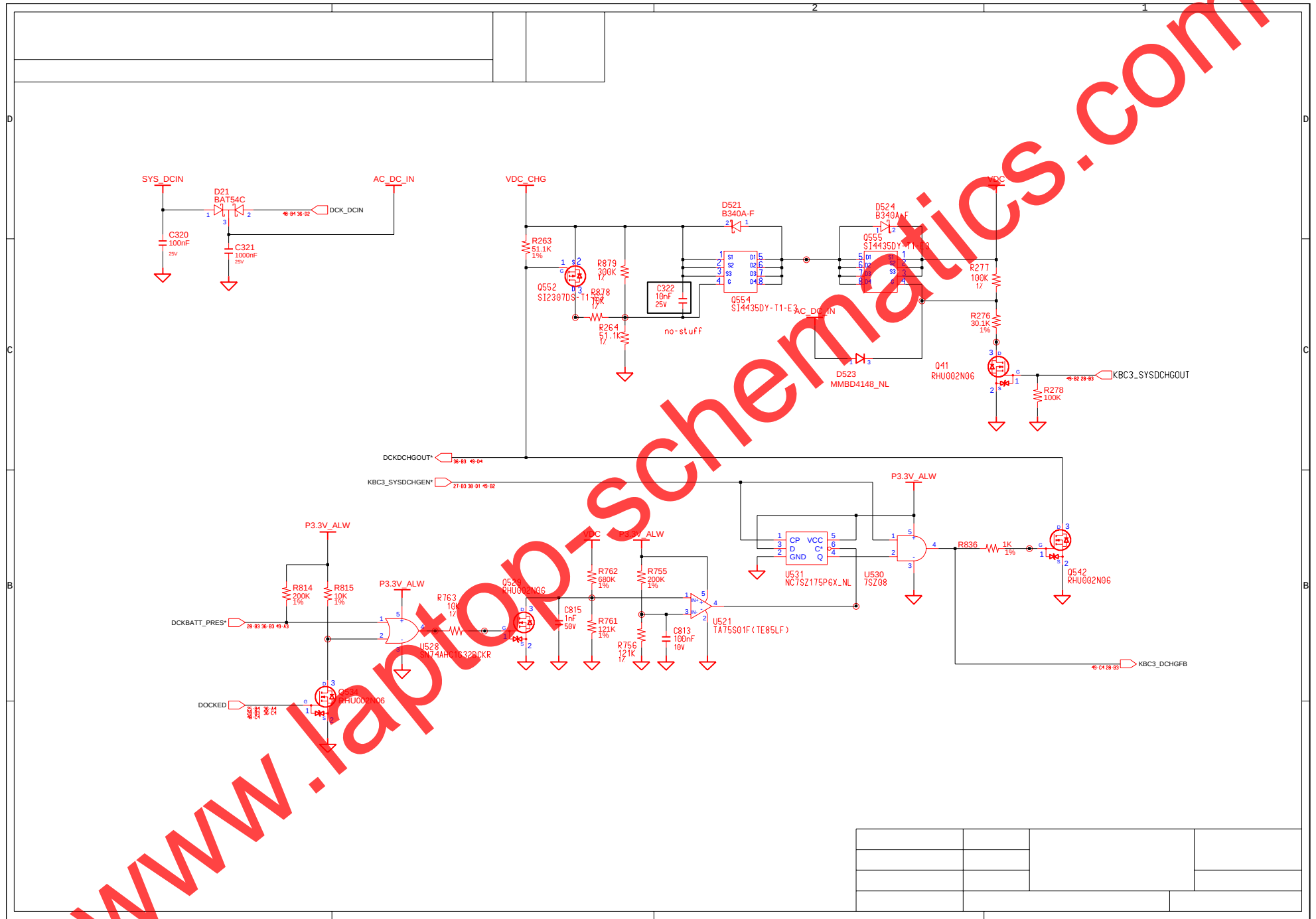




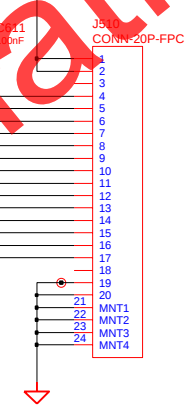
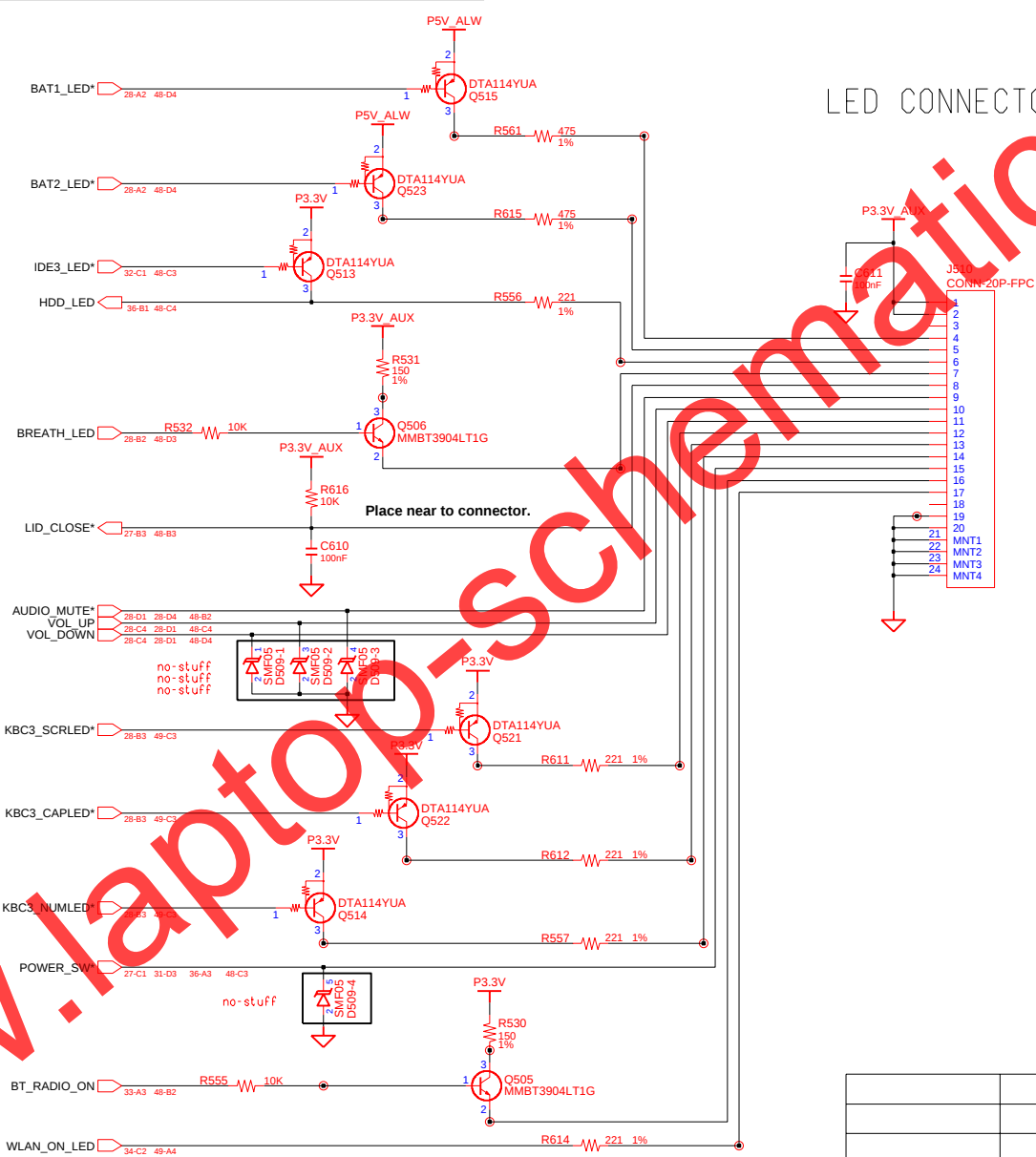


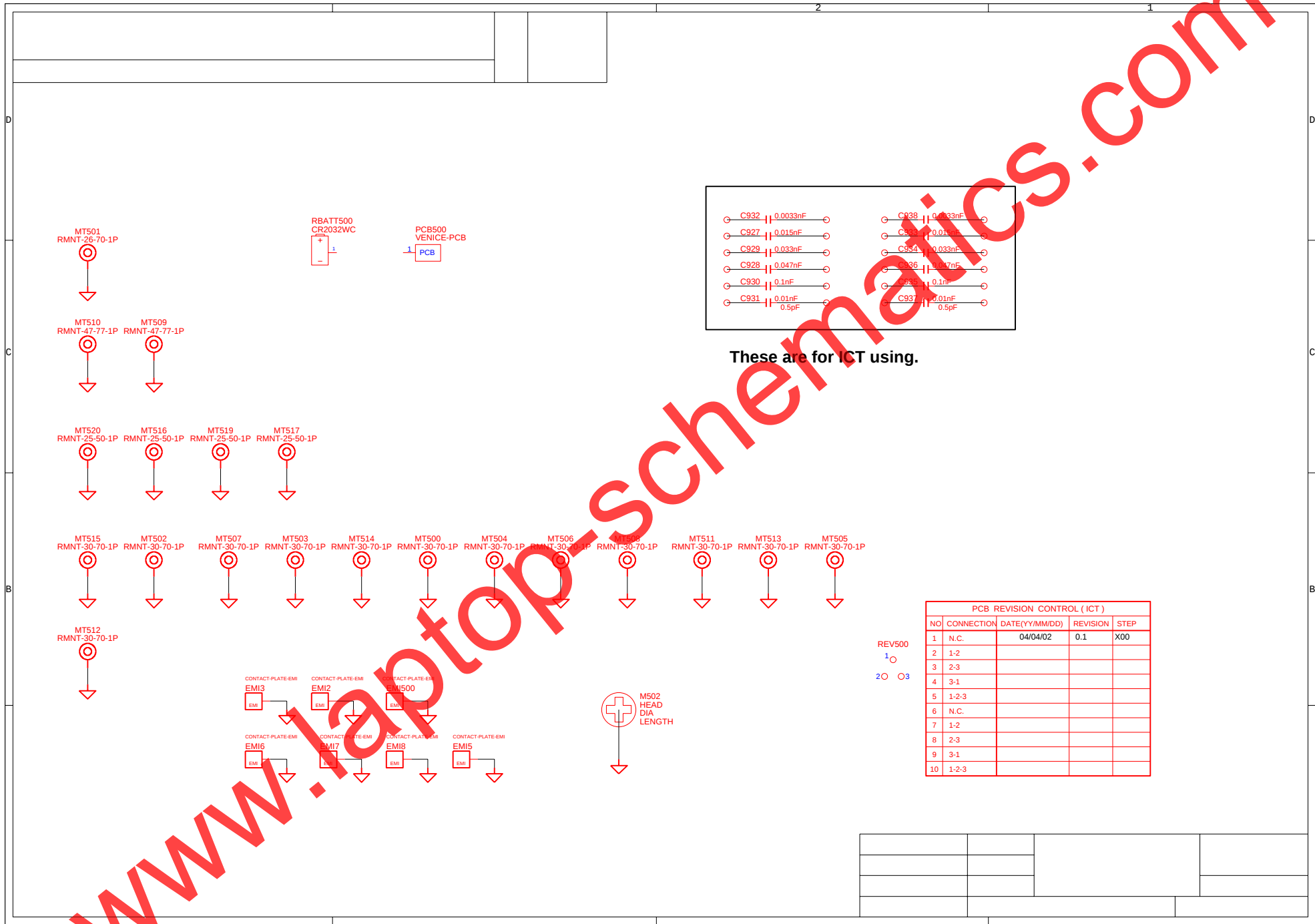






LED CONNECTOR





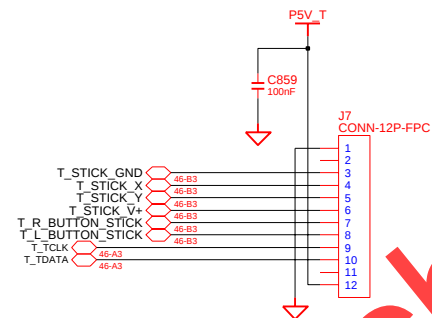
C932	0.0033nF
C927	0.015nF
C929	0.033nF
C928	0.047nF
C930	0.1nF
C931	0.01nF
C938	0.033nF
C937	0.015nF
C934	0.033nF
C936	0.047nF
C935	0.1nF
C933	0.01nF

These are for ICT using.

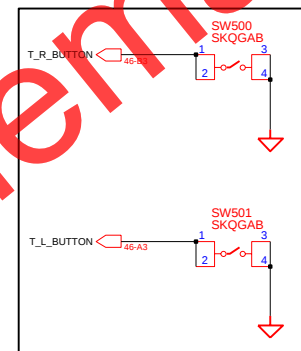
PCB REVISION CONTROL (ICT)				
NO	CONNECTION	DATE(Y/M/DD)	REVISION	STEP
1	N.C.	04/04/02	0.1	X00
2	1-2			
3	2-3			
4	3-1			
5	1-2-3			
6	N.C.			
7	1-2			
8	2-3			
9	3-1			
10	1-2-3			

TOUCHPAD IF BOARD

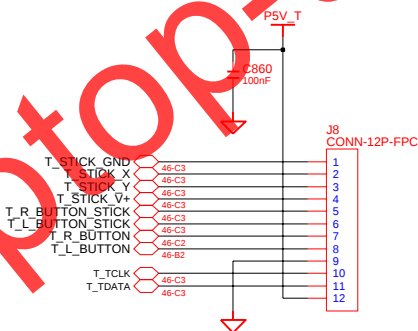
Main to Touchpad IF board



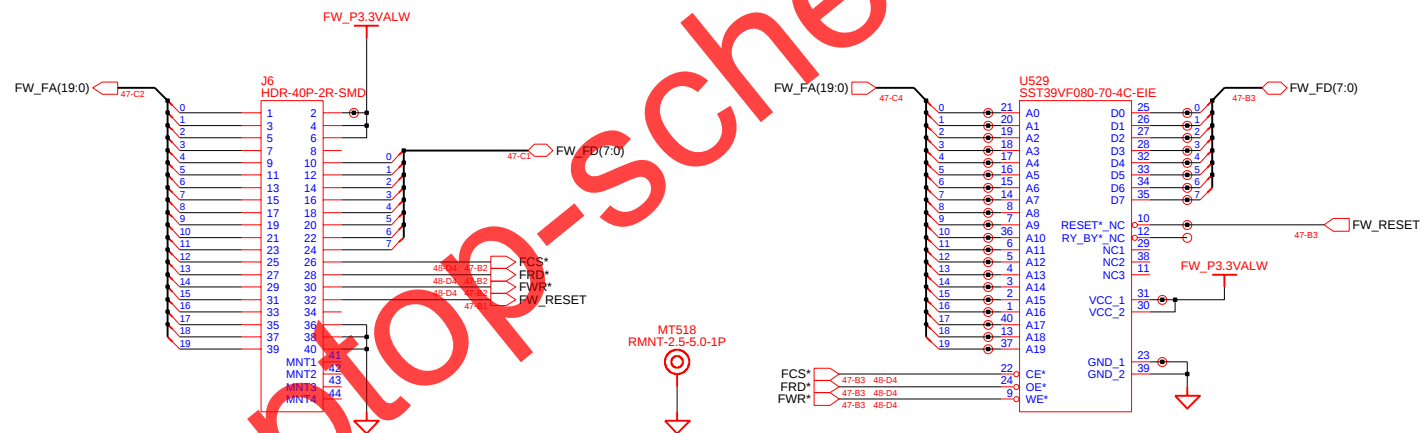
Place on Top side



Touchpad IF board to Touchpad



Firm Ware Daughter Board



AMD : Pin 10 is RESET* ; Pin12 is RY/BY*
SST : Pin 10, 12 are NC.