

ISTANBUL

CPU : Intel PENRYN
Chip Set : Intel CANTIGA PM + ICH9M
Remarks : Montevina Platform (NB9M)

Model Name : ISTANBUL
PBA Name : MAIN
PCB Code : TPT : BA41-00919A
GCE : BA41-00920A
NAN : BA41-00921A

Dev. Step : PV
Revision : 00
T.R. Date : 2008.07.18

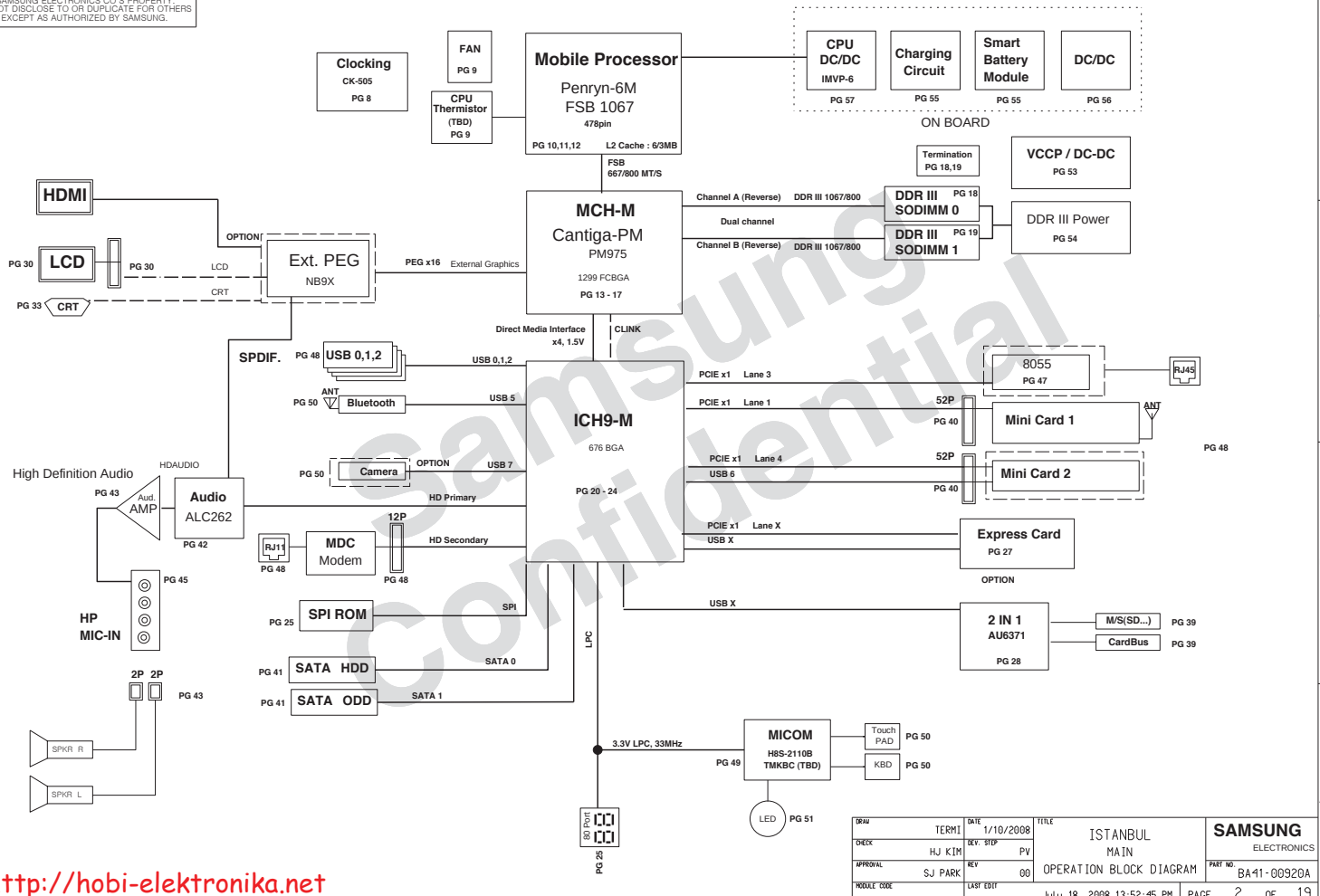
DRAW	CHECK	APPROVAL

Owner : SEC Mobile R & D

Signature : X

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BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
Cardbus	AD25	3	A,B,C
USB	AD29(internal)	-	USB2.0 #0 (USB0) : A USB2.0 #1 (USB1) : D USB2.0 #2 (USB4) : C USB2.0 #3 (USB5) : E USB2.0 #4 (EHCI) : H
Hub to PCI	AD30(internal)	-	
LPC bridge/IDE/AC97/SMBUS	AD31(internal)	-	B
Internal MAC	AD24(internal)	-	E
AC Link	-	-	B
GLAN	-	-	F

Crystal / Oscillator

TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	ICH8-M	Real Time Clock
Crystal	10MHz	MICOM	HD64F2169/2160
Crystal	14.318MHz	CLOCK-Generator	CK-505
Crystal	25MHz	LAN	Intel LAN

LCD Pannel Detect (TBD)

Devices	Resolution	PANNEL_DETECT_0

Voltage Rails

VDC	Primary DC system power supply (7 to 21V)
VCC_CORE	Core Voltage for CPU
GFX_CORE	Core Voltage for GPU
P1.05V (VCCP)	VTT for CPU, Crestline & ICH8-M
P3.3V_MCOM	3.3V always power rail (for Microm)
P1.5V	1.5V switched power rail (off in S3-S5)
P1.8V	1.8V switched power rail (off in S3-S5)
P1.8V_AUX	1.8V power rail for DDR (off in S4-S5)
P0.9V	0.9V power rail for DDR (off in S3-S5)
P3.3V	3.3V switched power rail (off in S3-S5)
P3.3V_AUX	3.3V switched on power rail (off in S4-S5)
P5.0V	5.0V switched power rail (off in S3-S5)
P5.0V_AUX	5.0V switched on power rail (off in S4-S5)
P5.0V_ALV	5.0V always power rail

I C / SMB Address

Devices	Address	Hex	Bus
ICH8-m	Master	-	SMBUS Master
CPU Thermal Sensor	0111 101x	7Ah	Thermal Sensor
SODIMM0	1010 000x	A0h	-
SODIMM1	1010 010x	A0h	-
Thermal Sensor on SODIMM0	0011 000x	30h	-
Thermal Sensor on SODIMM1	0011 010x	34h	-
CK-505M (Clock Generator)	1101 001x	D2h	Clock, Unused Clock Output Disable

USB PORT Assign

PORT #	ASSIGNED TO	PORT #	ASSIGNED TO
	SYSTEM PORT 0	0	NC
	SYSTEM PORT 1	1	Mini Card 1 (WLAN)
	SYSTEM PORT 2	2	NC
	NC	3	LOM
	NC	4	Mini Card 2 (ROBSON or DVB-T)
	Bluetooth	5	NC
	Mini PCI Express 2		
	Camera		
	NC		
	NC		

PCI Express Assign

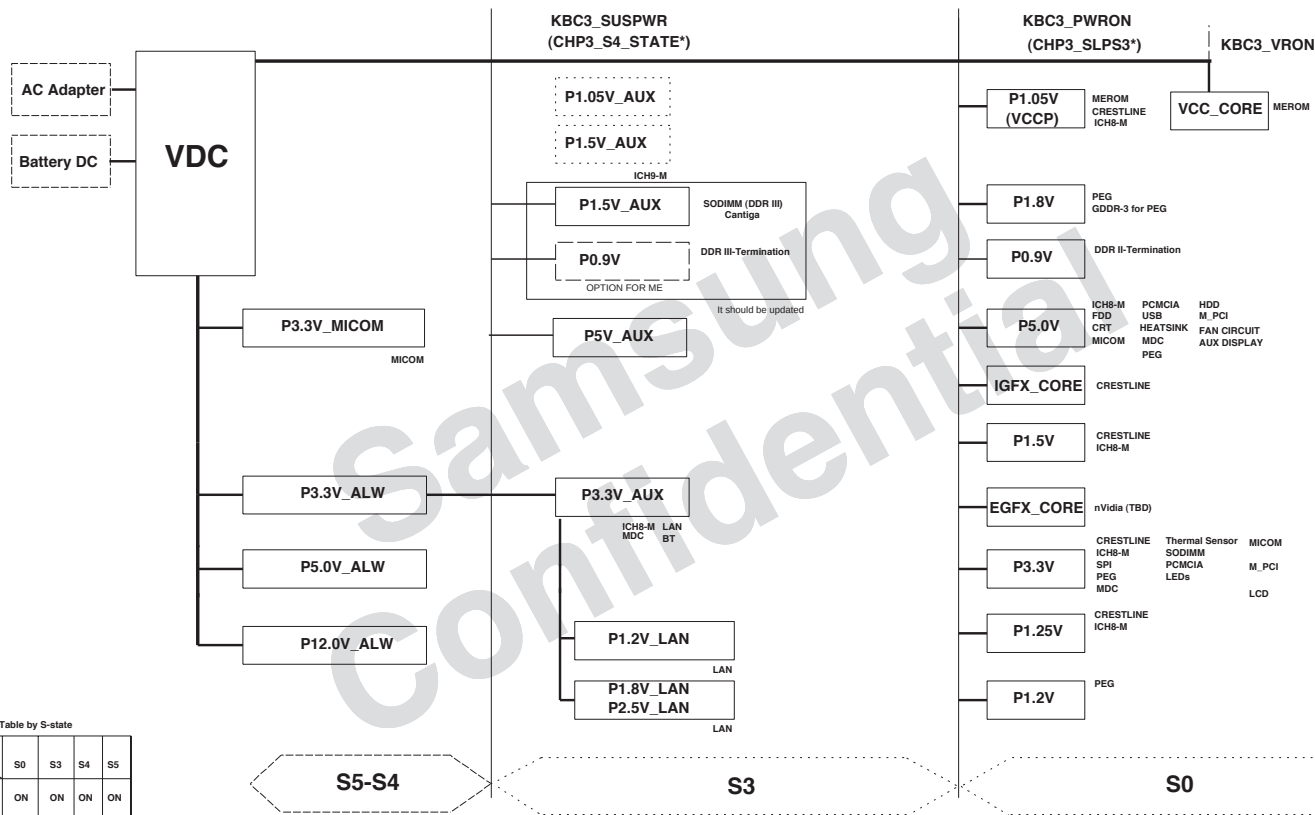
REVISION HISTORY

See rev notes for more information.

DESIGN	TERM1	DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG
CHECK	HJ KIN	REV: STEP	PV	CHIPSET POWER	UNDEFINED	ELECTRONICS
APPROVAL	SJ PARK	REV	00			PART NO.
MODEL CODE		LAST EDIT				BA41-00920A
				Jul 18, 2008 13:52:45 PM	PAGE	3 OF 19

POWER DIAGRAM

Rev 0.1



Power On/Off Table by S-state

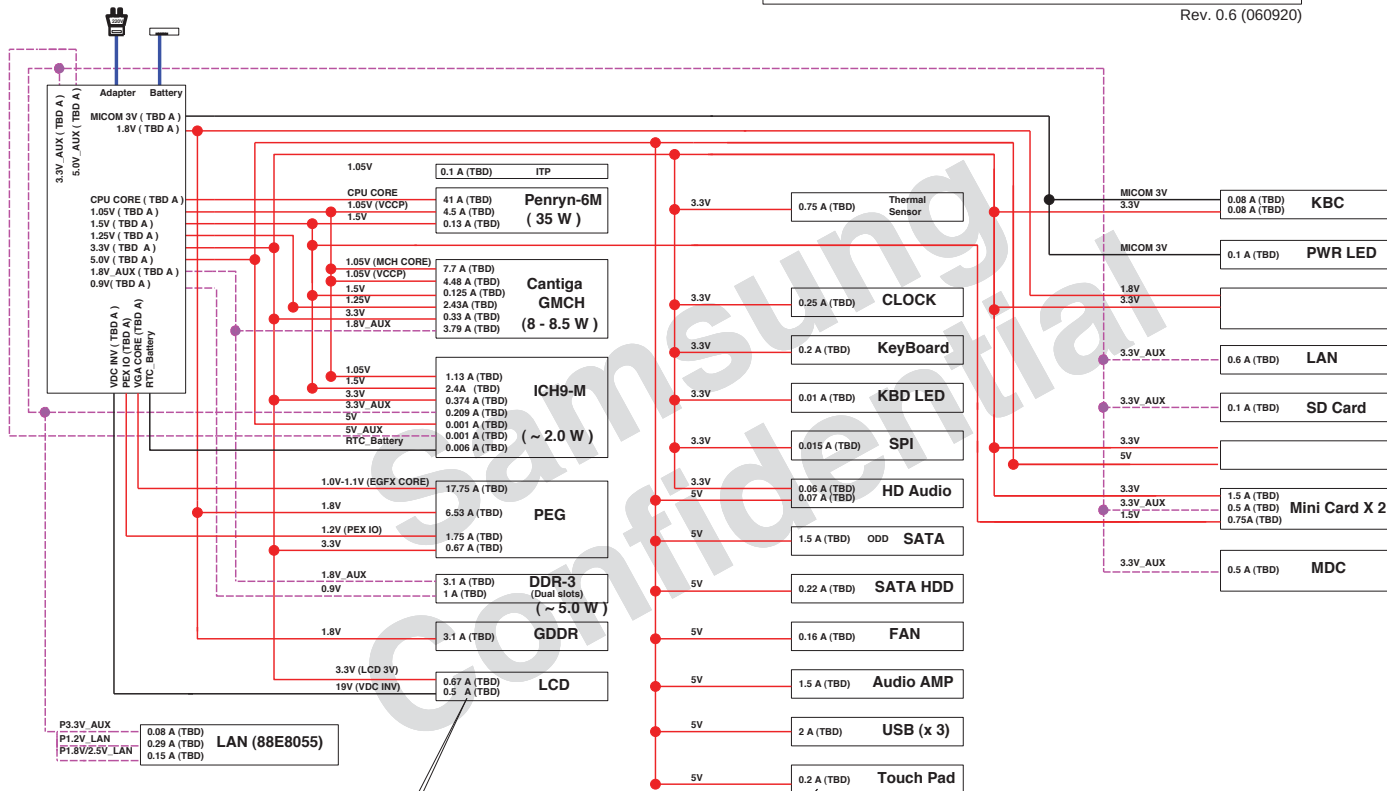
Rail	S0	S3	S4	S5
State				
+V*(LWS)	ON	ON	ON	ON
+V*LAN	ON	ON	ON	ON
+1.8V_AUX	ON	ON	ON	ON
+0.9V	ON	ON	ON	ON
+V*AUX	ON	ON	ON	ON
+V	ON	ON	ON	ON
HVL CORE	ON	ON	ON	ON

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POWER RAILS ANALYSIS

Rev. 0.6 (060920)



Value by Datasheet/Application notes (Value by measurement)

DESIGN	TERM1	DATE	1/10/2008	TITLE ISTANBUL CHIPSET POWER UNDEFINED	SAMSUNG ELECTRONICS
CHECK	HJ KIM	REV. STEP	PV		
APPROVAL	SJ PARK	REV	00		
MODULE CODE	LAST EDIT		1/10/2008 13:52:45 PM PAGE 5 OF 19		

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Host Boot / ME Off

(SLPS4* = S4_STATE*) > (SLPM* = SLPS3*)

M-1) KBC3_DDR_PWRON (TB0) = 8) KBC3_SUSPWR

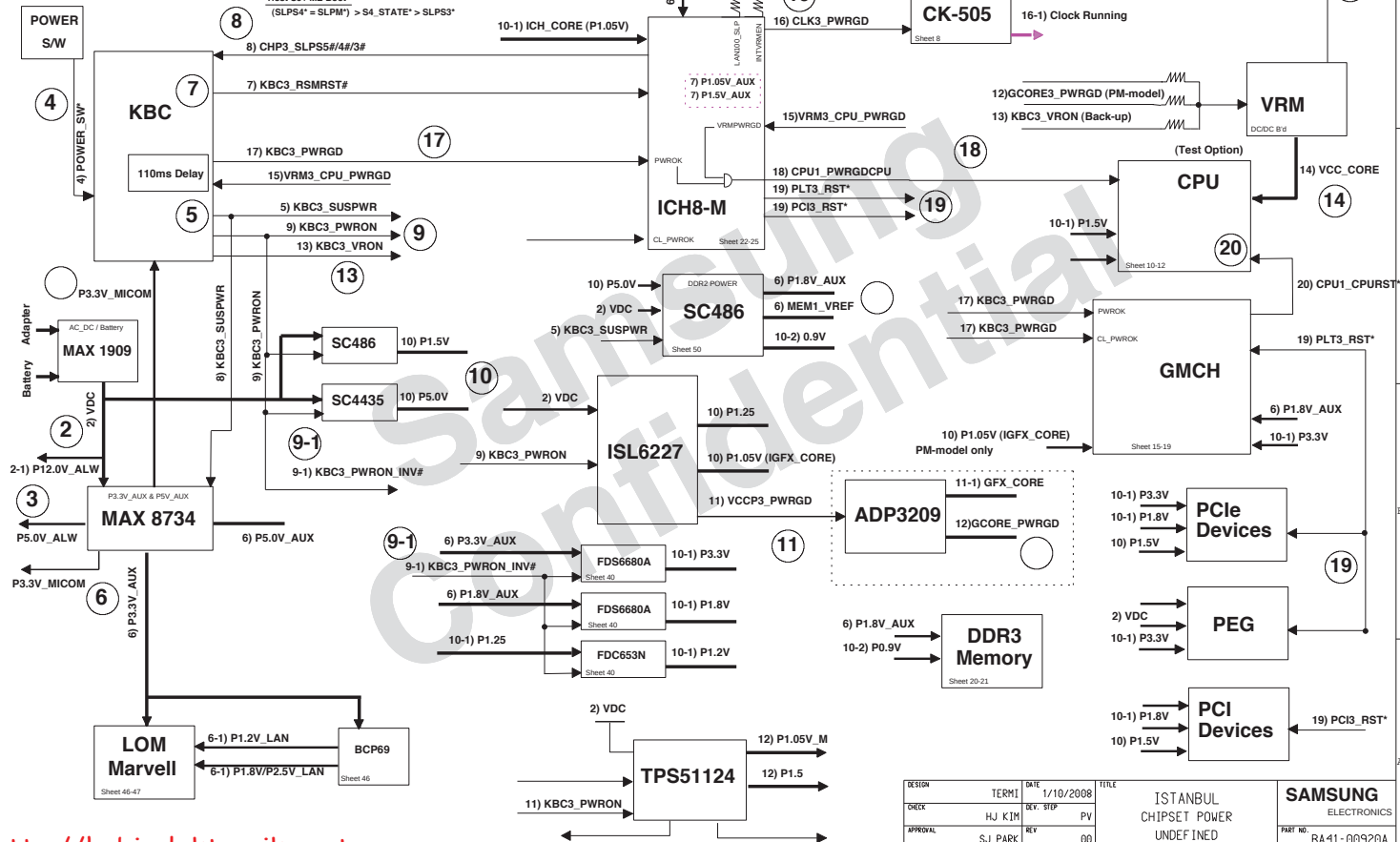
M-2) KBC3_ME_PWRON = 15) KBC3_PWRON

Host / ME Boot

(SLPS4* = S4_STATE*) > SLPM* > SLPS3*

Host S5 / ME Boot

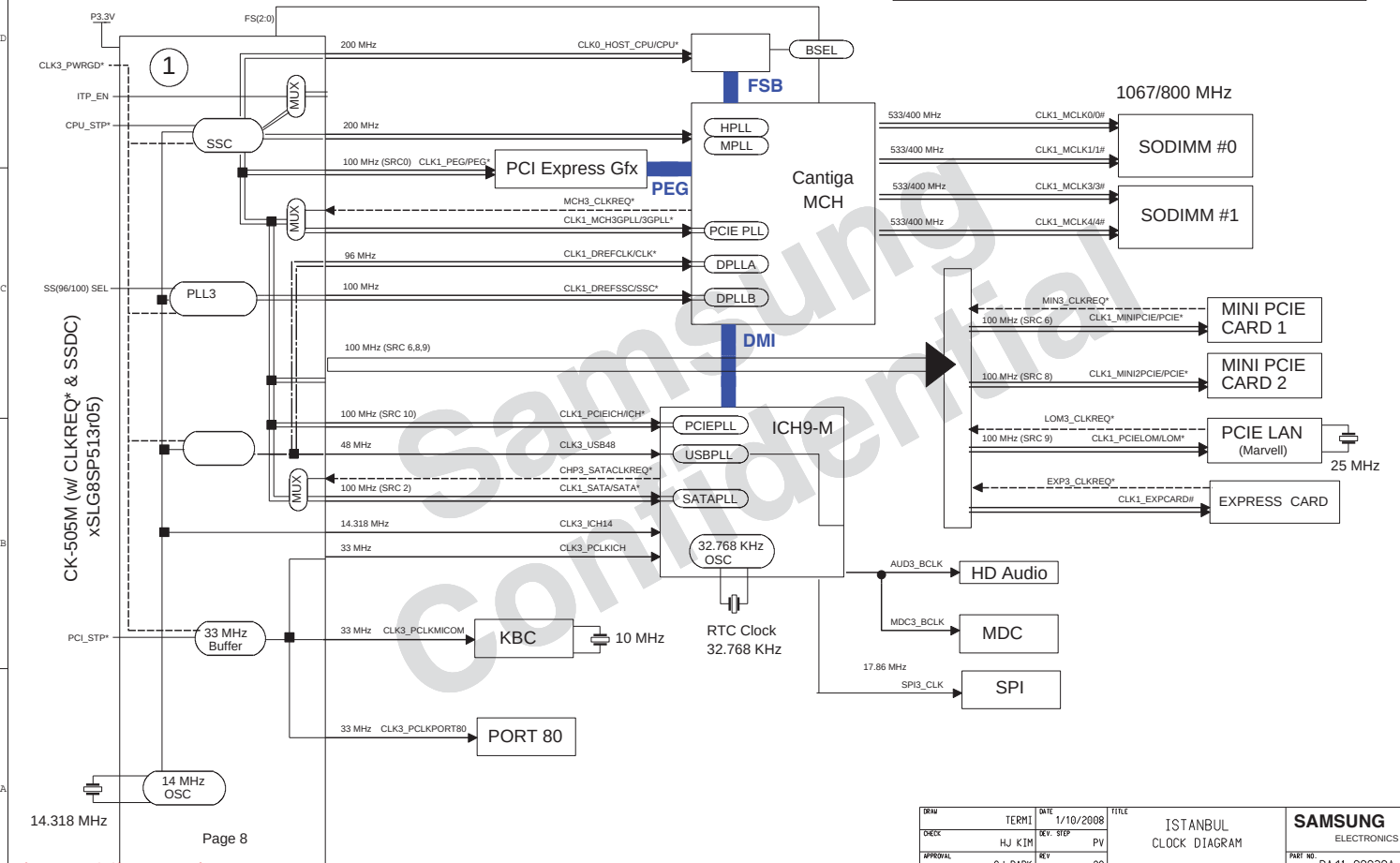
(SLPS4* = SLPM*) > S4_STATE* > SLPS3*



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CLOCK DISTRIBUTION Rev. 0.1

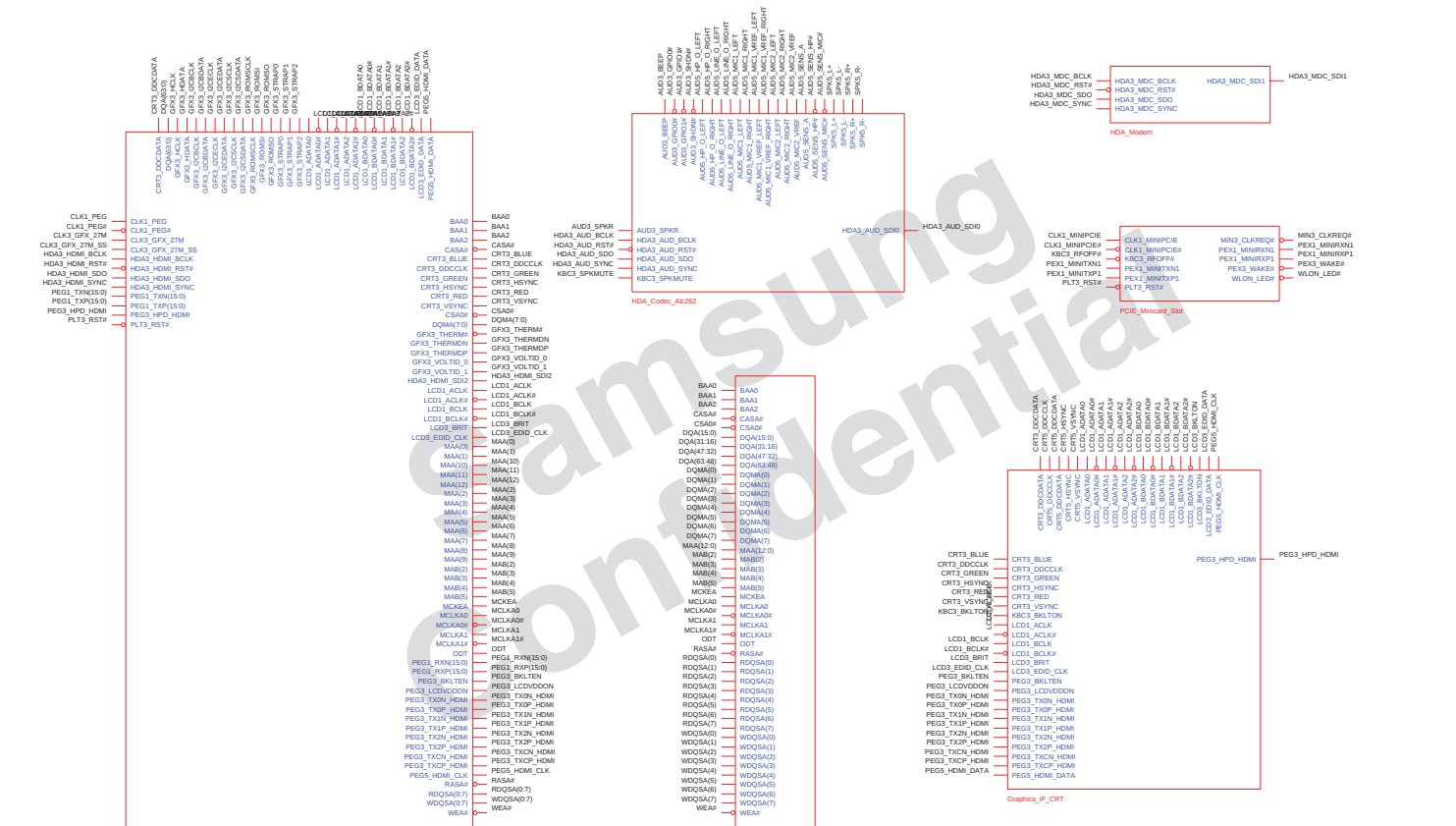


DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG
CHK	HJ KIN	REV: 0.01	CLOCK DIAGRAM	ELECTRONICS
APPROVAL	SJ PARK	REV: 00		PART NO: BA41-00920A
MODEL CODE		LAST EDIT	July 18, 2008 13:52:45 PM	PAGE 7 OF 19

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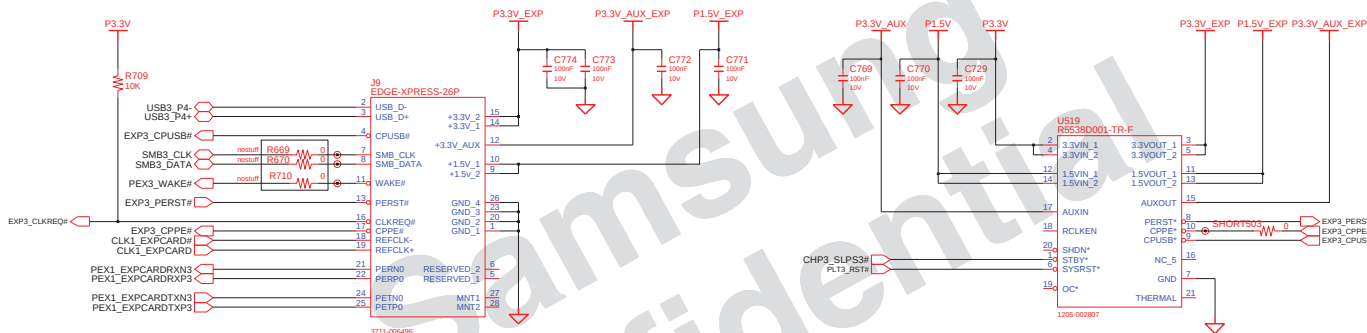


DESIGN	TERM1	DATE	1/10/2008	TITLE	ISTANBUL CHIPSET POWER UNDEFINED		SAMSUNG ELECTRONICS	
CHECK	HJ KIM	DEV. STEP	PV					PART NO.
APPROVAL	SJ PARK	REV	00					BA41-00920A
MODULE CODE	LAST EDIT		2008. 1. 10. PM 13:45			PAGE	9	OF 19



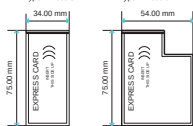
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EXPRESS CARD

Type 1 module
 Type 2 module



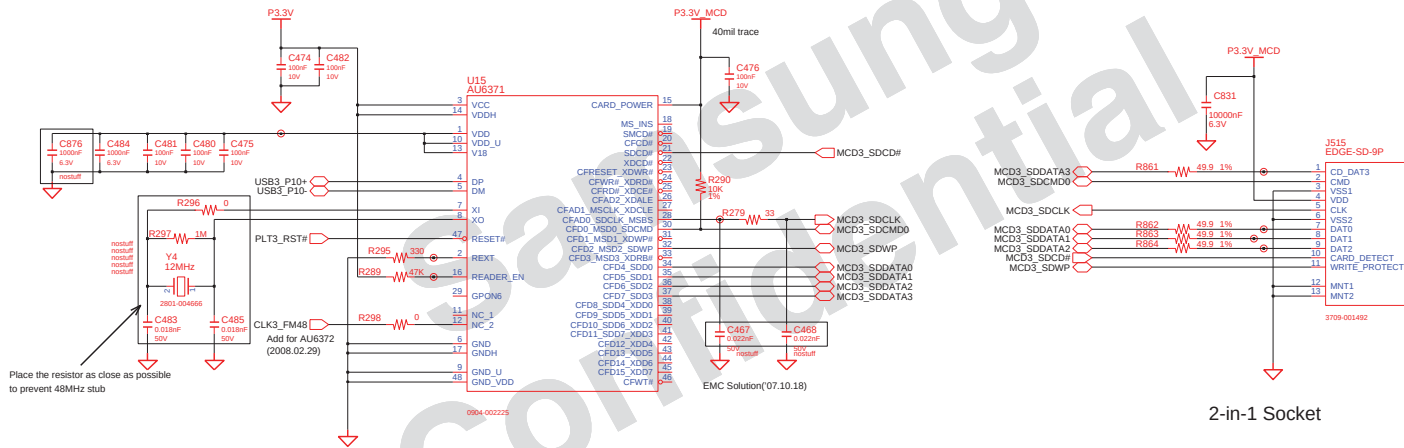
34.00 mm W
 75.00 mm L
 34.00 mm W
 75.00 mm L
 5.00 mm H

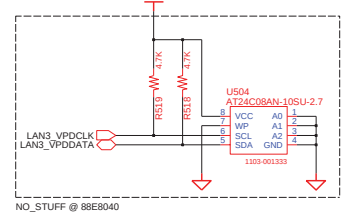
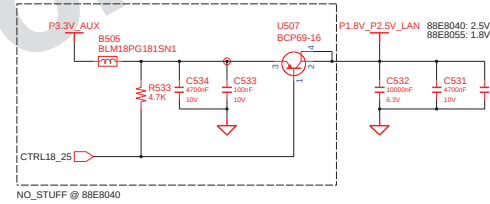
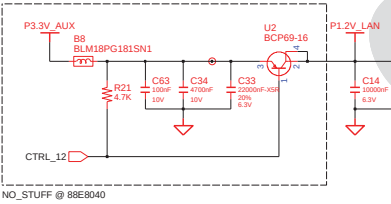
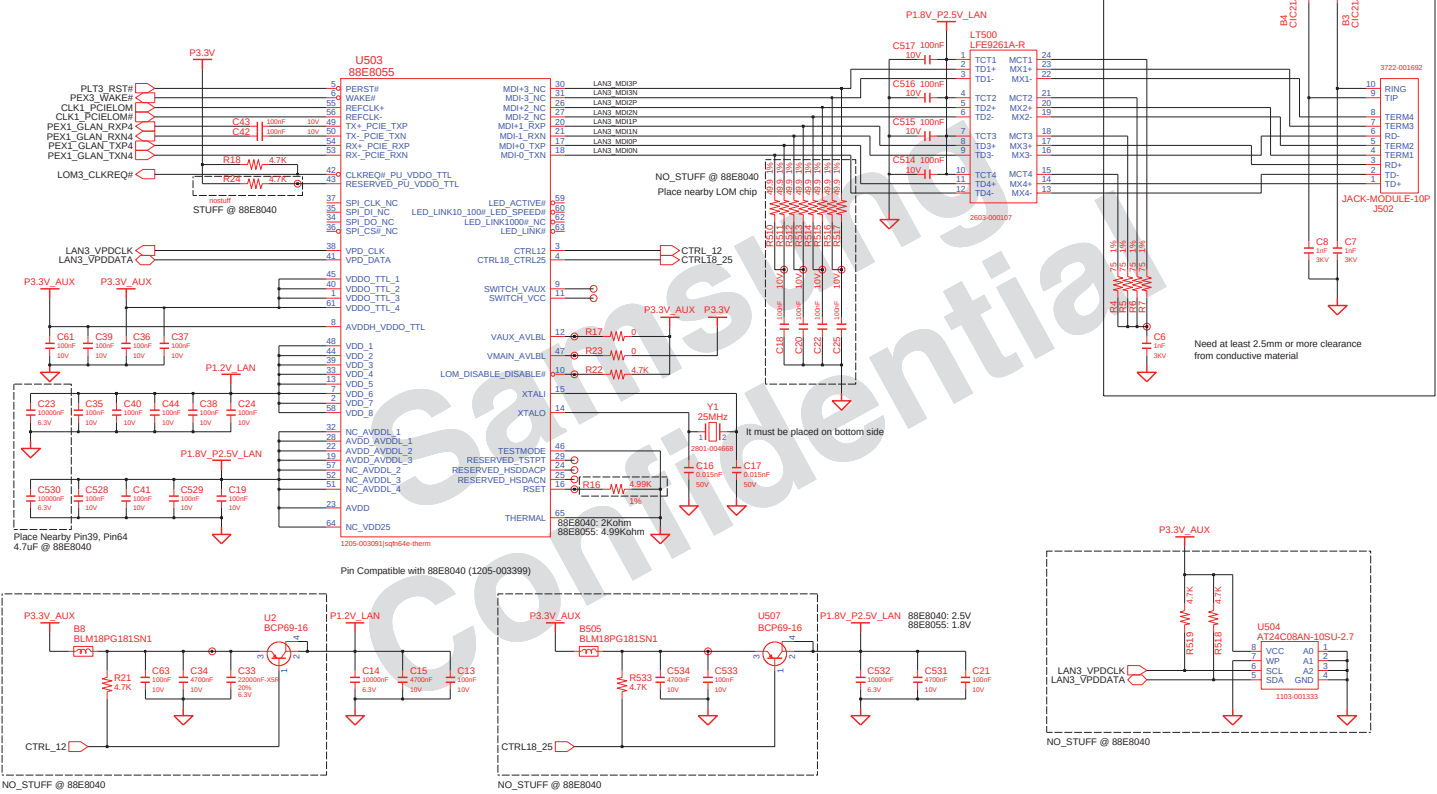


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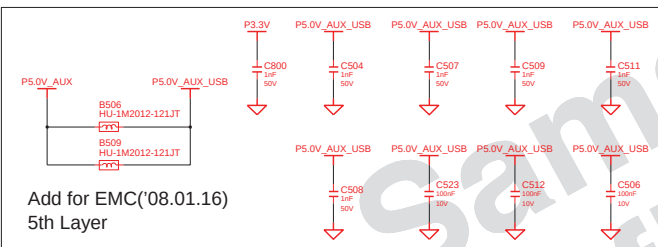
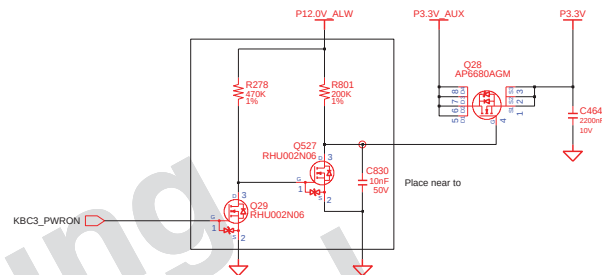
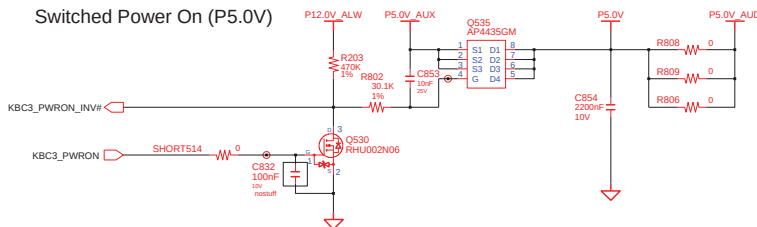
2 IN 1 CARD



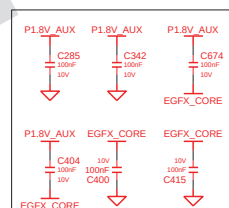
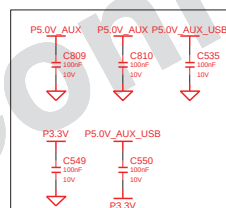
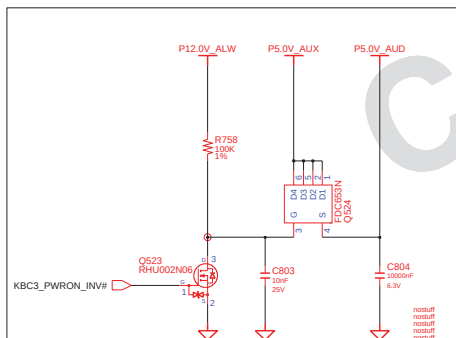


Switched Power On (P3.3V & 1.8V)

Switched Power On (P5.0V)

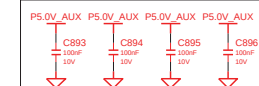


Audio Power (nostuff)

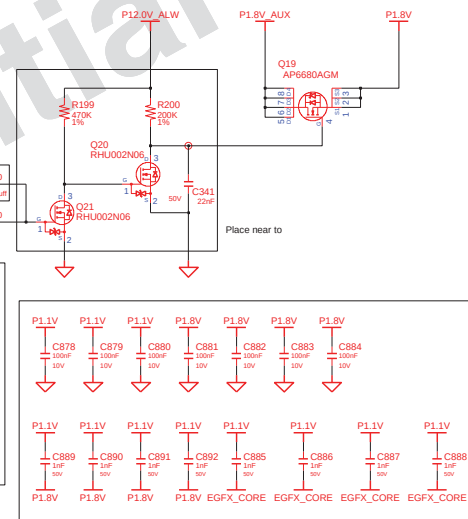


Add for SI(2007.12.6)

Add for SI(2008.03.07)



Add for EMI(2008.04.29)

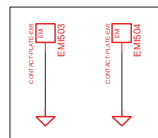
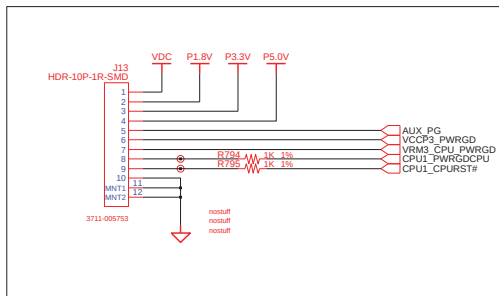


Add for EMI(2008.04.26)

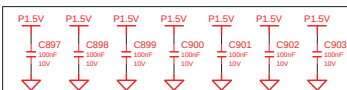
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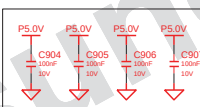
ICT PORT



Add for EMC ('08.04.22)

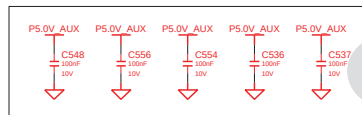
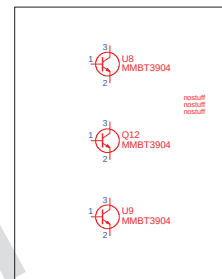


Add for EMC ('08.06.09)
 4th Layer

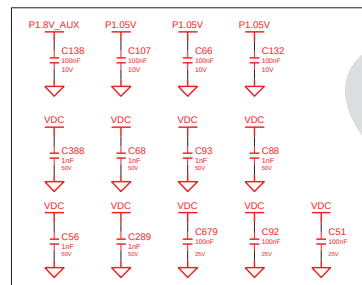


Add for EMC ('08.06.09)
 Bottom Side

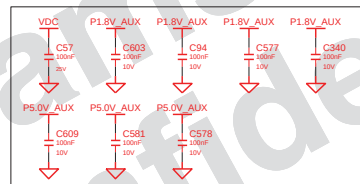
For Debugging



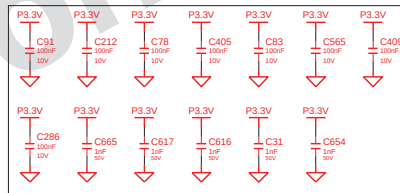
Add for EMC ('08.01.16)
 5th Layer



Add for EMC ('08.01.16)
 Top Side

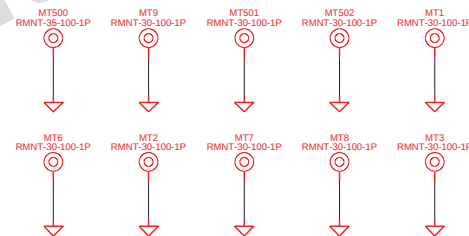


Add for EMC ('08.01.16)
 Bottom Side



Add for EMC ('08.03.05)

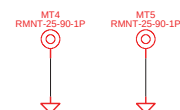
System



Board



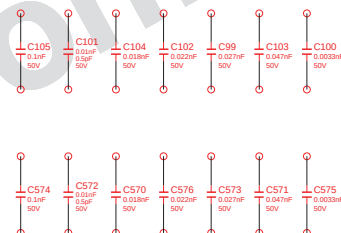
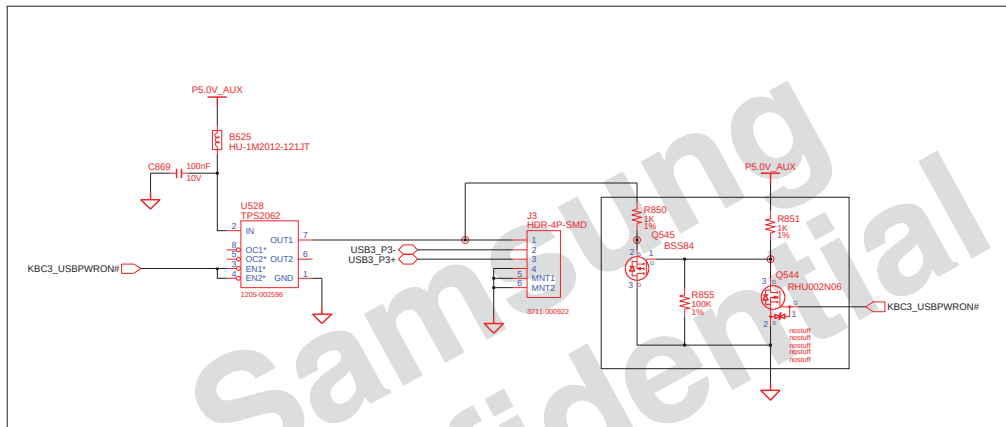
Keyboard



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DATE	1/10/2008	TITLE	ICT PORT	SAMSUNG ELECTRONICS
CHECK	HJ KIM	REV	PV	PART NO. BA411-00920A
APPROVAL	SJ PARK	REV	00	DATE
MODULE CODE	undef in ed	LAST EDIT	July 18, 2008 13:52:45 PM	PAGE 16 OF 19

Sub Board Connector (Istanbul Only)



PCB REVISION CONTROL (ICT)				
NO	CONNECTION	DATE(Y/M/MDD)	REVISION	STEP
1	N.C.			
2	1-2			
3	2-3			
4	3-1			
5	1-2-3			
6	N.C.			
7	1-2			
8	2-3			
9	3-1			
10	1-2-3			

REV1
1 O
2 O O3

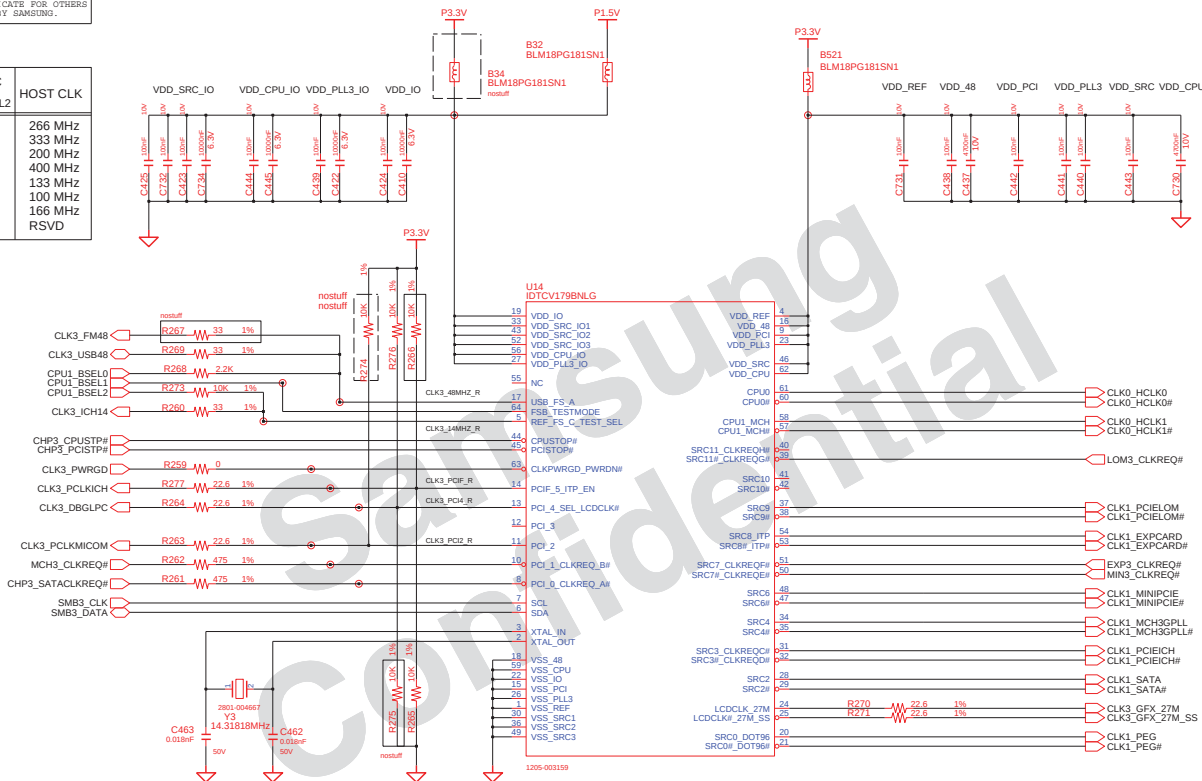
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[illegible][illegible][illegible][illegible]

- ☐ P1.2V_LAN
- ☐ P1.2V_LAN
- ☐ P1.2V_LAN
- ☐ P1.5V
- ☐ P1.5V
- ☐ P1.5V
- ☐ P1.5V_EXP
- ☐ P1.5V_EXP
- ☐ P1.5V_EXP
- ☐ P1.8V_AUX
- ☐ P1.8V_AUX
- ☐ P1.8V_AUX
- ☐ P3.3V_EXP
- ☐ P3.3V_MCD
- ☐ PS.0V_ALD
- ☐ PS.0V_ALD
- ☐ PS.0V_ALD
- ☐ PS.0V_AUX_X
- ☐ EXPFX_CORE
- ☐ EXPFX_CORE
- ☐ EXPFX_CORE
- ☐ P1.8V
- ☐ P1.8V
- ☐ P1.8V
- ☐ P1.8V

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FSA BSEL0	FSB BSEL1	FSC BSEL2	HOST CLK
0	0	0	266 MHz
0	0	1	333 MHz
0	1	0	200 MHz
0	1	1	400 MHz
1	0	0	133 MHz
1	0	1	100 MHz
1	1	0	166 MHz
1	1	1	RSVD



CLK REQ	DEVICE	SRC PORT
CLK REQ A	SATA	SRC2
CLK REQ B	GMCH	SRC4
CLK REQ E	MINI CARD	SRC6
CLK REQ F	LOM3_CLKREQ#	SRC8

SEL_LCDCLK*	Pin 20/21	Pin 24/25
LOW	DOT_96/DOT_96#	PEG_CLK/PEG_CLK#
HIGH	SBC_0/SBC_0#	27M & 27M_SS

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IDT : 1205-003159
Silego : 1205-003156
Spectra Linear : 1205-003533

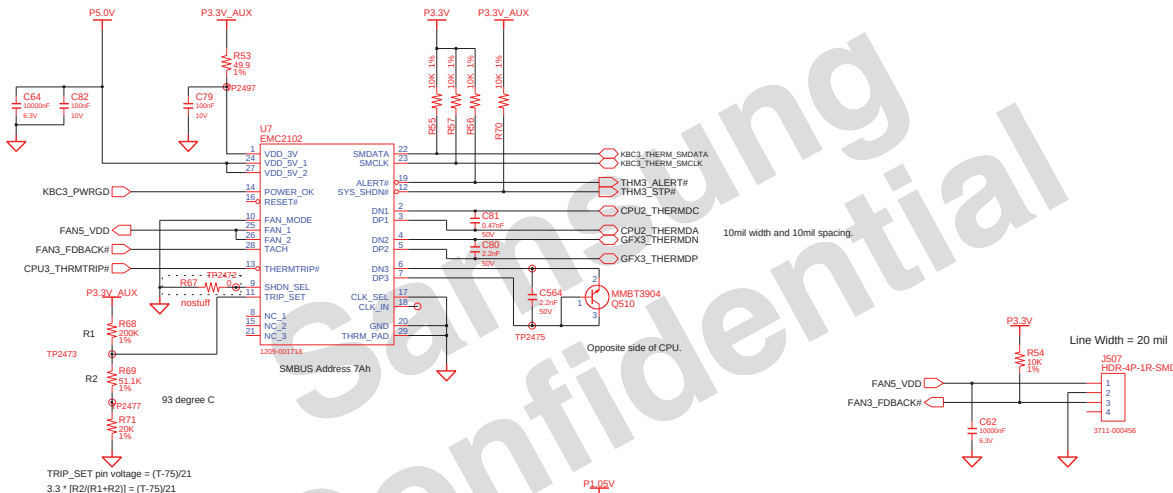
This part is 64pin QFN package.

DRAW	TERM1	DATE	1/10/2008	TITLE	ISTANBUL Main_Clock_Circuit CK_Clock_505M	SAMSUNG ELECTRONICS	
CHECK	HJ KJM	REV.	STEP	PV		PART NO.	BA41-00920A
APPROVAL	SJ PARK	REV		00			
MODULE CODE	undefined	LAST EDIT	Jul 18, 2008 13:52:45 PM			PAGE	21 OF 64

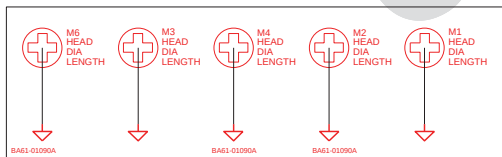
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THERMAL SENSOR & FAN CONTROL



RHE Support (Top)

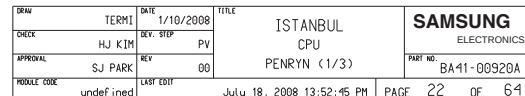


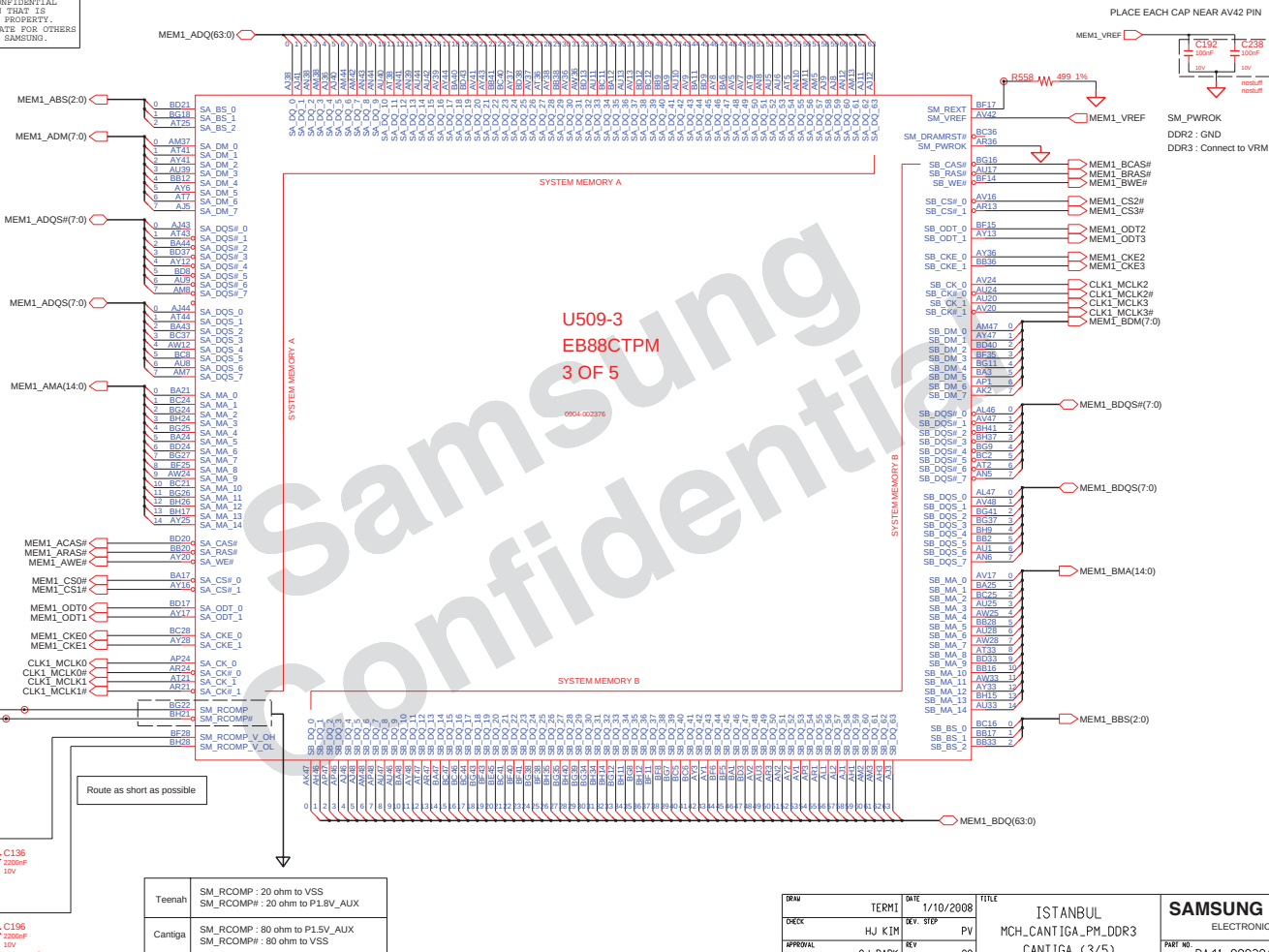
M6
 Internal : nostuff
 External : stuff

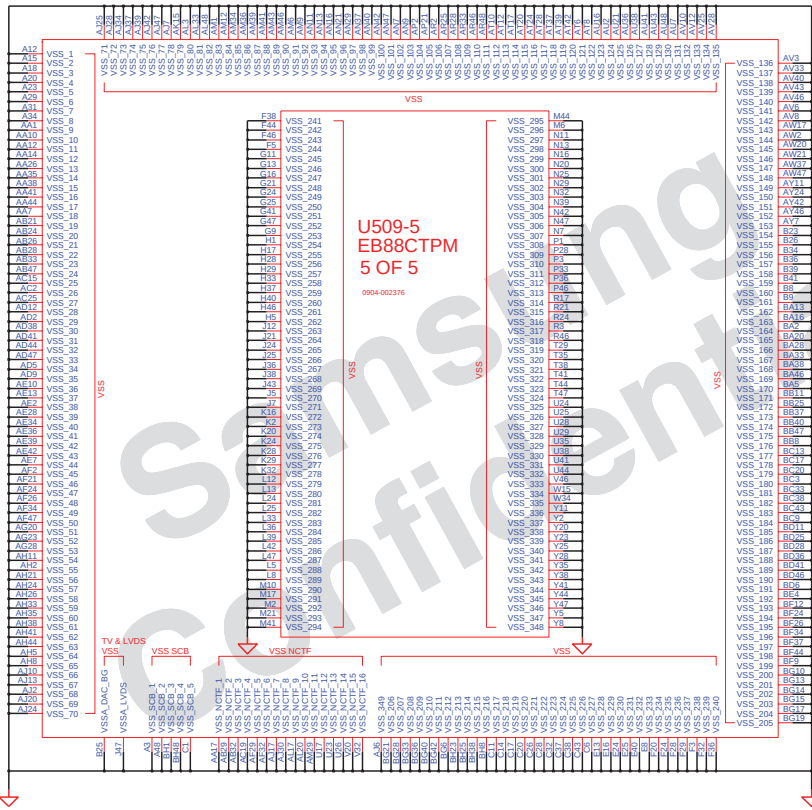
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DATE	1/10/2008	FILE	ISTANBUL	SAMSUNG ELECTRONICS
TERM	REV: 10P	PY	thermal_Sensor_SMSC_Enc2102	
CHECK	HJ KIM	REV	00	thermal_Sensor_SMSC_Enc2102
APPROVAL	SJ PARK	REV	00	BA41-00920A
MODULE CODE	LAST EDIT	July 18, 2008 13:52:45 PM	PAGE	61 OF 64

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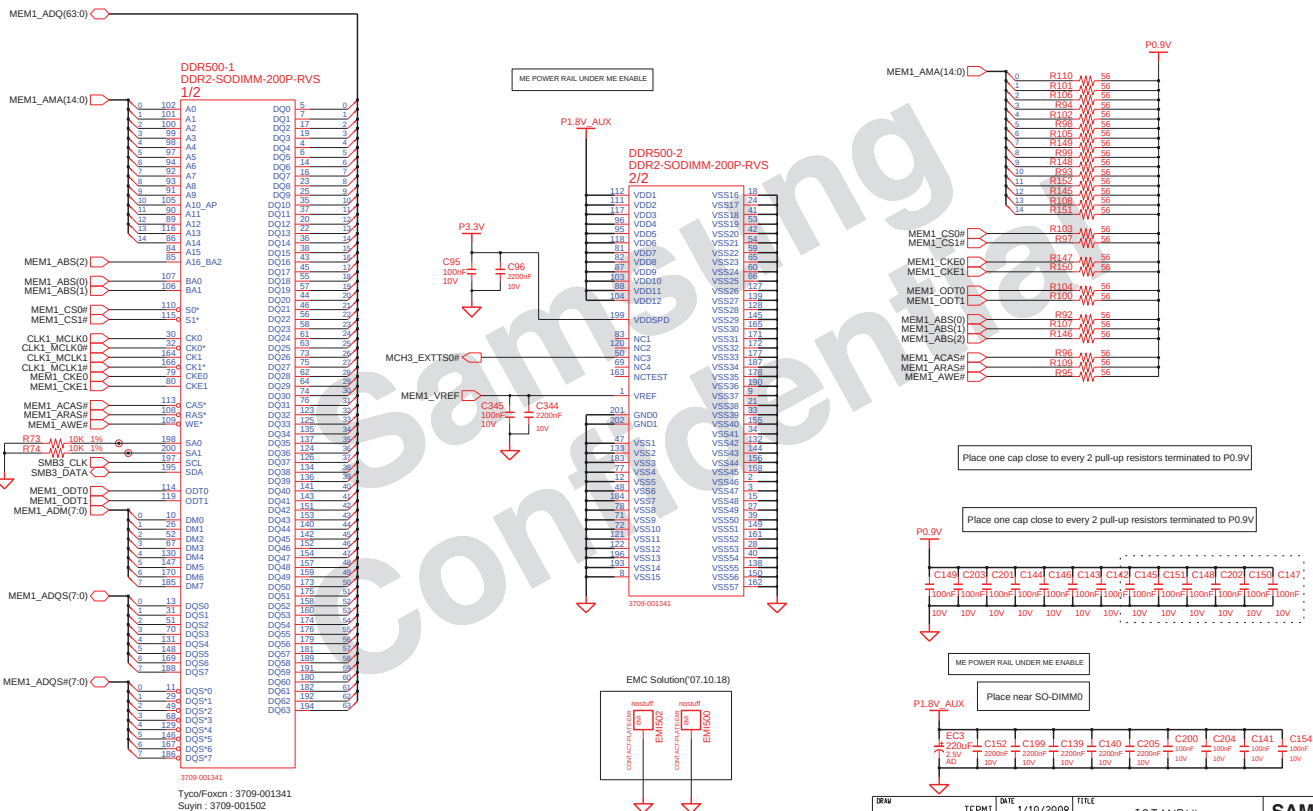


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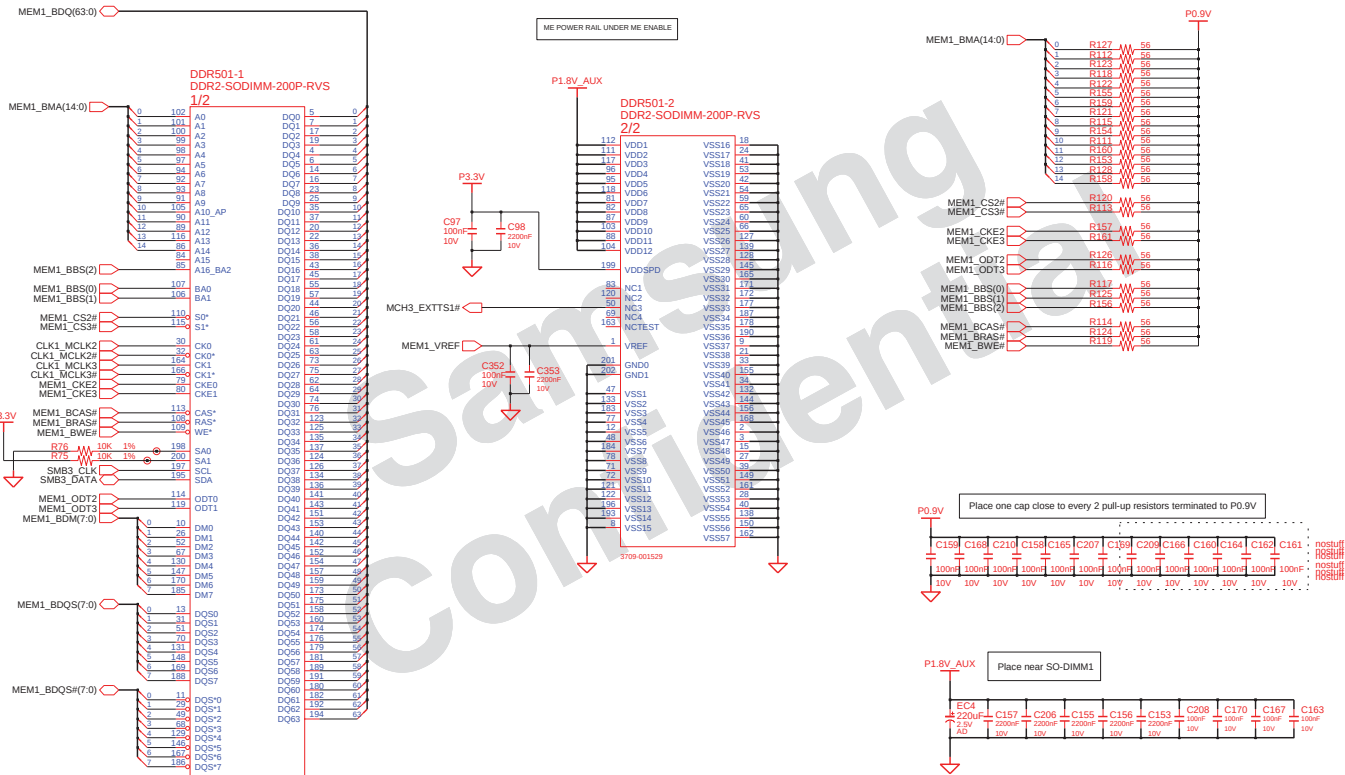
DDR SO-DIMM #0

Height : 5.2mm (Reverse)



DDR SO-DIMM #1

Height : 9.2mm (Reverse)

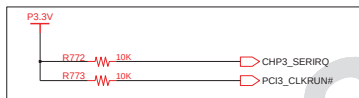


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U517-2 NH82801IBM 2/5

P3.3V



LPC option ; These are used with LPC

P3.3V

R772 10K
 R780 10K
 R782 10K

PEX1_MINIRXN1
 PEX1_MINIRXP1
 PEX1_MINITXN1
 PEX1_MINITXP1

PEX1_EXPCARDRXN3
 PEX1_EXPCARDXP3
 PEX1_EXPCARDTXN3
 PEX1_EXPCARDTXP3

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

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 PEX1_GLAN_TXP4

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 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
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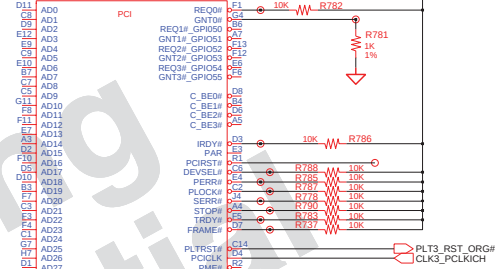
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 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
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PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4

PEX1_GLAN_RXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXN4
 PEX1_GLAN_TXP4



Boot BIOS Select

BIOS	PCI3_GNT0#	SPI3_CS1#
LPC	HIGH	HIGH
SPI	LOW	HIGH
PCI	HIGH	LOW

P3.3V

R844 1K

ITPM Disable

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

R844 1K

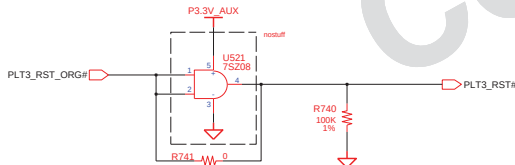
R844 1K

R844 1K

R844 1K

R844 1K

AC caps : PCIe need to be within 250mils of the driver
 Resistor for Test : Place Stufing Option to minimize stubs

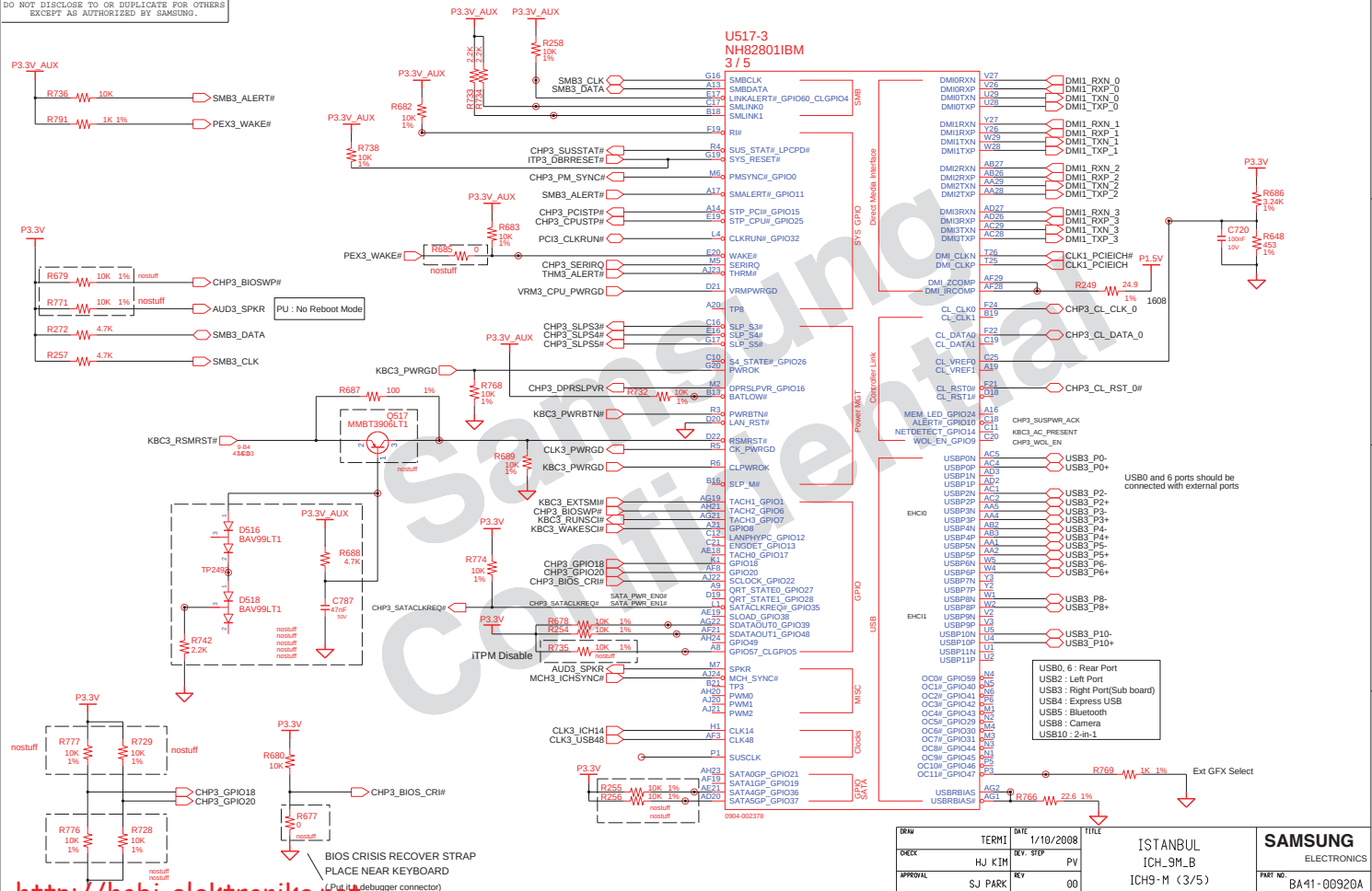


DATE	1/10/2008	TERM	1/10/2008	FILE	ISTANBUL	SAMSUNG
USER	HJ KIN	REV	1/10/2008	PV	ICH9_M_B	ELECTRONICS
APPROVAL	SJ PARK	REV	00	ICH9-M (2/5)	PART NO.	BA41-00920A
MODEL CODE	undef ined	LAST EDIT	July 18, 2008 13:52:45 PM	PAGE	35	OF 64

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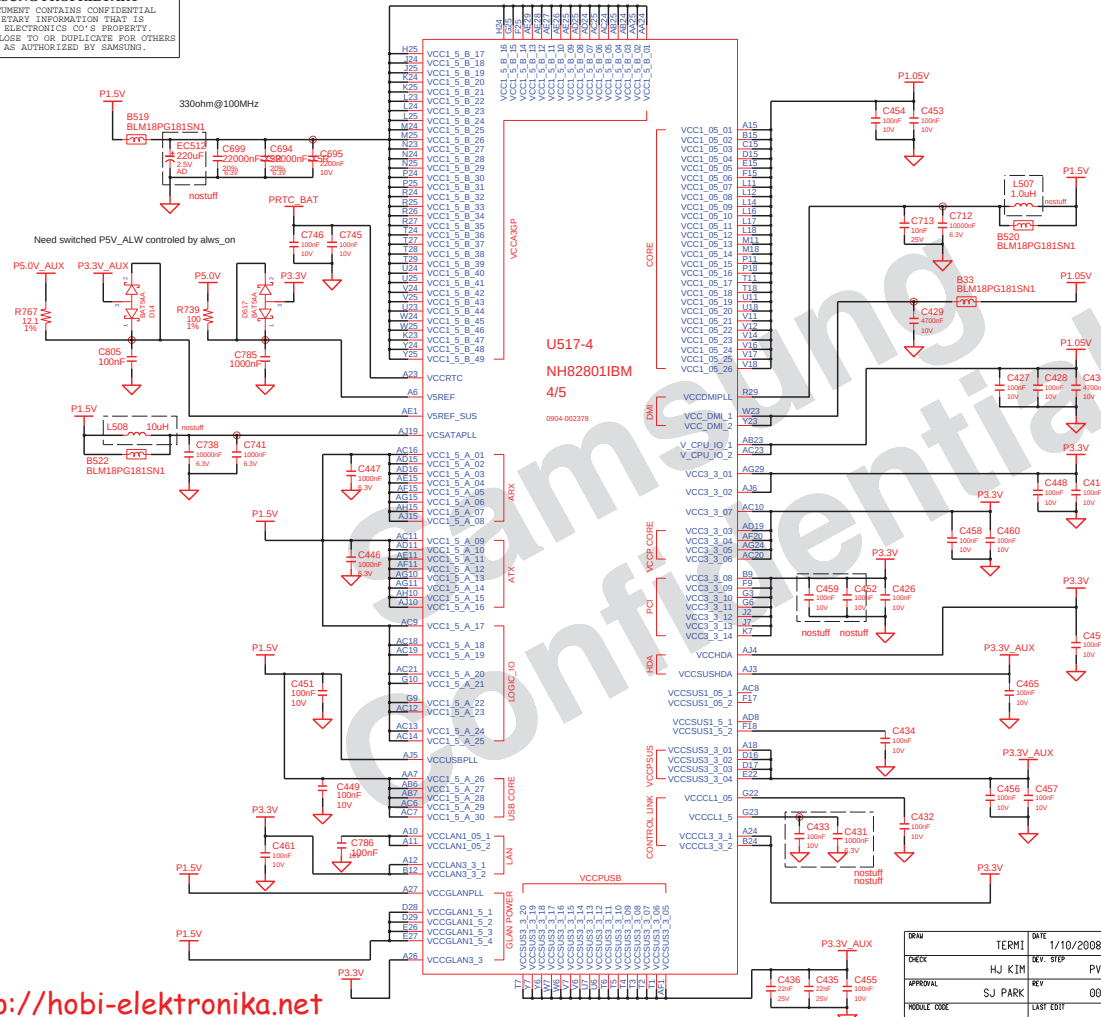
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DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG
DESIGN	HJ KIN	REV	ICH9M.B	ELECTRONICS
APPROVAL	SJ PARK	REV	ICH9-M (3/5)	PART NO.
MODEL CODE		LAST EDIT	July 18, 2008 13:52:45 PM	BA41-00920A
		PAGE	36	OF 64

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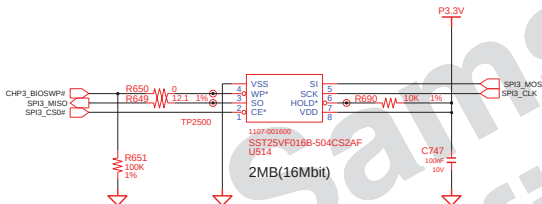
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DATE	1/10/2008	FILE	ISTANBUL	SAMSUNG ELECTRONICS
TERM	REV: TYP	PV	ICH9M_B	
CHECK	HJ KIN	REV	00	ICH9-M (3/5)
APPROVAL	SJ PARK	LAST EDIT	Jul 18, 2008 13:52:45 PM	PAGE 37
MODEL CODE				OF 64

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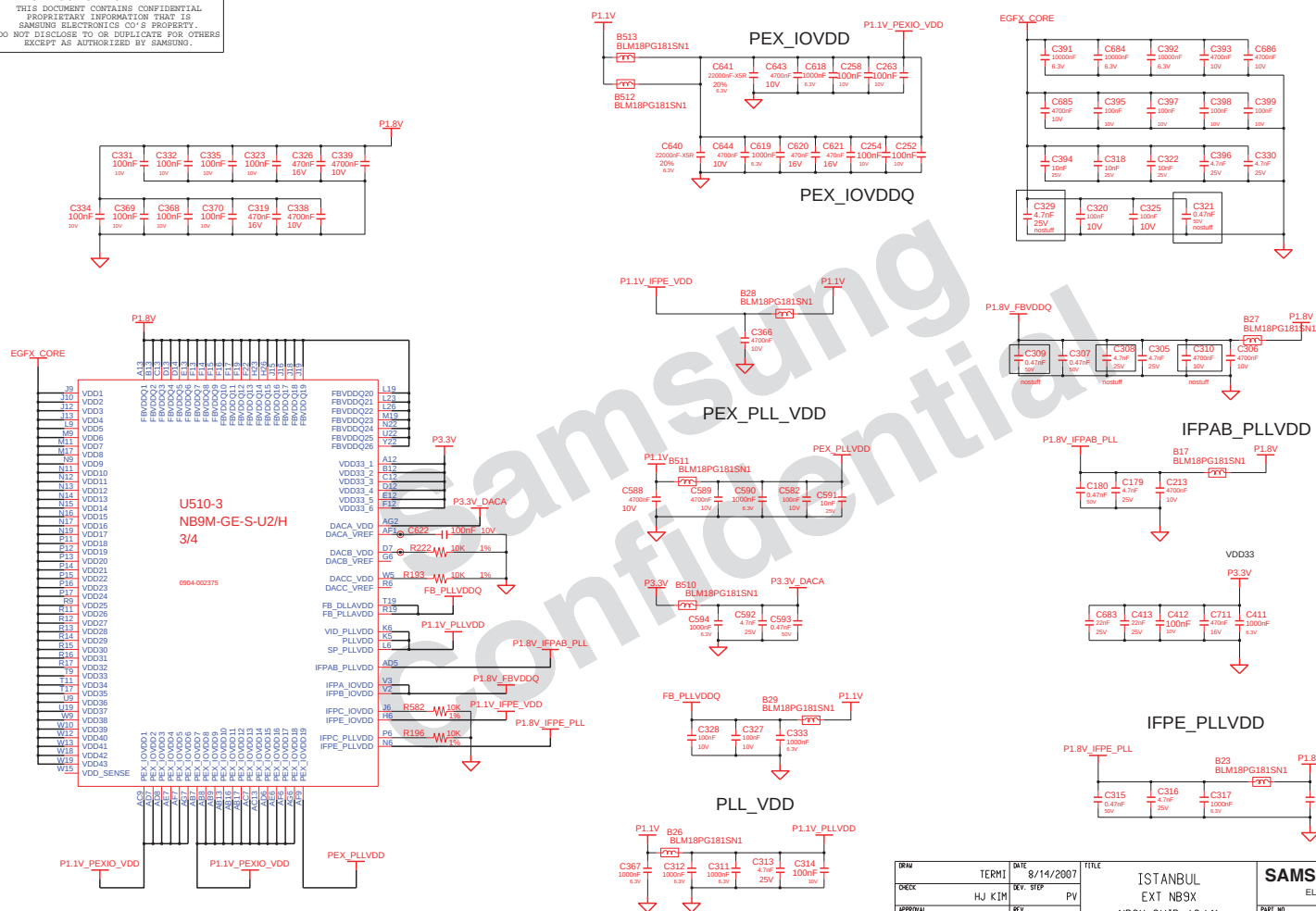
- SPI ROM LIST -



02	VERIFY REAL MODE	66	CONFIGURE ADVANCE CACHE REG.
03	DISABLE NMI	6A	DISPLAY EXTERNAL CACHE SIZE
04	GET CPU TYPE	6C	DISPLAY SHADOW MESSAGE
06	INIT. SYSTEM HW	6E	DISPLAY NON-DISPOSABLE SEGMENT
08	INIT. CHIPSET REG.	70	DISPLAY ERROR MESSAGE
09	SET IN POST FLAG	72	CHECK FOR CONFIGURATION ERROR
0A	INIT CPU REG.	74	TEST REAL-TIME CLOCK
0B	CPU CACHE ON	76	CHECK FOR KEYBOARD EERROR
0C	INIT.CACHE TO POST	7C	SETUP HARDWARE INTERRUPT VECTOR
0E	INIT. IO VALUE	7E	TEST COPROCESSOR IF PRESENT
0F	ENABLE THE L-BUS IDE	80	DISABLE ON-BOARD I/O PORT
10	INIT. POWER MANAGER	82	DETECT AND INSTALL EXT.RS232C
11	LOAD ALTERNATE REG.	84	DETECT AND INSTALL EXT.PARALLEL
13	PCI BUS MASTER RESET	86	RE-INIT. ON-BOARD I/O PORT
	WITH INITIAL POST VALUE	88	INIT. BIOS DATA ROM
14	INIT. KEYBOARD CONTROLLER	8A	INIT.EXTENDED BIOS DATA AREA
16	CHECK CHECKSUM	8C	INIT. FDD CONTROLLER
18	8254 TIMER INIT.	9A	SHADOW OPTION ROMS
1A	8237 DMA CONTROLLER INIT.	9C	SETUP POWER MANAGEMENT
1C	RESET INTERRUPT CONTROLLER	9E	ENABLE HW INTERRUPT
20	TEST DRAM REFRESH	A0	SET TIME OF DAY
22	TEST 8742 KEYBOARD CONTROLLER	A4	INIT. TYPEMATIC RATE
24	SET ES SEGMENT REG. TO 4GB	A8	ERASE F2 PROMPT
26	ENABLE A20	AA	SCAN FOR F2 KEY STROKE
28	AUTO SIZING DRAM	AC	ENTER SETUP
32	COMPUTE THE CPU SPEED	AE	CLEAR IN POST FLAG
34	TEST CMOS RAM	B0	CHECK FOR ERRORS
38	SHADOW SYSTEM BIOS ROM	B2	POST DONE-PREPARE TO BOOT O/S
3A	AUTO SIZING CACHE	B4	ONE BEEP
3C	CONFIGURE ADVANCED CHIPSET REG.	B6	CHECK PASSWORD (OPTION)
3D	LOAD ALTER REG. WITH CMOS VALUE	B7	ACPI INIT
42	INIT. INTERRUPT VECTOR	BA	DMI INIT
44	INIT. BIOS INTERRUPT	BE	CLEAR SCREEN
46	CHECK ROM COPYRIGHT NOTICE	C0	TRY BOOT WITH INT19
47	INIT. I20 SUPPORT IF INSTALLED	DO	INTERRUPT HANDLER ERROR
48	CHECK VIDEO CONFIGURE AGAINST CMOS	D2	UNKNOWN INTERRUPT ERROR
49	INIT. PCI BUS AND DEVICE	D4	PENDING INTERRUPT ERROR
4A	INIT. ALL VIDEO BIOS ROM	D6	SHUTDOWN 5
4C	SHADOW VIDEO BIOS ROM	D8	SHUTDOWN ERROR
50	DISPLAY CPU TYPE AND SPEED	DA	EXTENDED BLOCK MOVE
52	TEST KEYBOARD	DC	SHUTDOWN 10
54	SET KEYCLICK IF ENABLED	D9	ENABLE NMI
56	ENABLE KEYBOARD	90	INIT. HDD CONTROLLER
58	TEST FOR UNEXPECTED INTERRUPTS	91	INIT. LOCAL BUS HDD CONTROLLER
5A	DISPLAY "PRESS SETUP"	92	JUMP TO USER PATCH 2
5C	TEST RAM BETWEEN 512K AND 640K	94	DISABLE A20 ADDRESS LINE
60	TEST EXTENDED MEMORY	96	CLEAR HUGE ES SEGMENT REG.
62	TEST EXTENDED MEMORY ADDRESS LINE	98	SEARCH FOR OPTION ROMS
64	JUMP TO USER PATCH 1		

DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG ELECTRONICS
TERM	HJ KIN	REV	PV	
CHECK		REV	PV	
APPROVAL	SJ PARK	REV	00	
MODEL CODE		LAST EDIT		

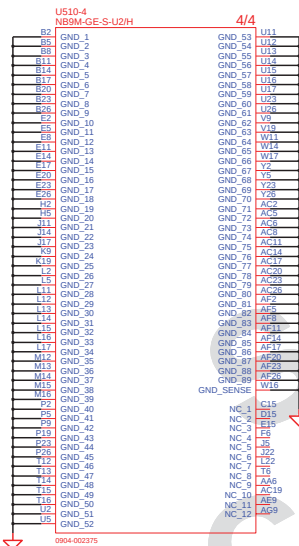
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DRAW	TERM1	DATE	8/14/2007	TITLE	ISTANBUL EXT NB9X NB9X CHIP (3/4)	SAMSUNG ELECTRONICS
CHECK	HJ KIM	DEV. STEP	PV			
APPROVAL	SJ PARK	REV	00			
MODULE CODE	LAST EDIT		8.14.2007 12:52:45 PM			
PART NO.						BA41-00920A

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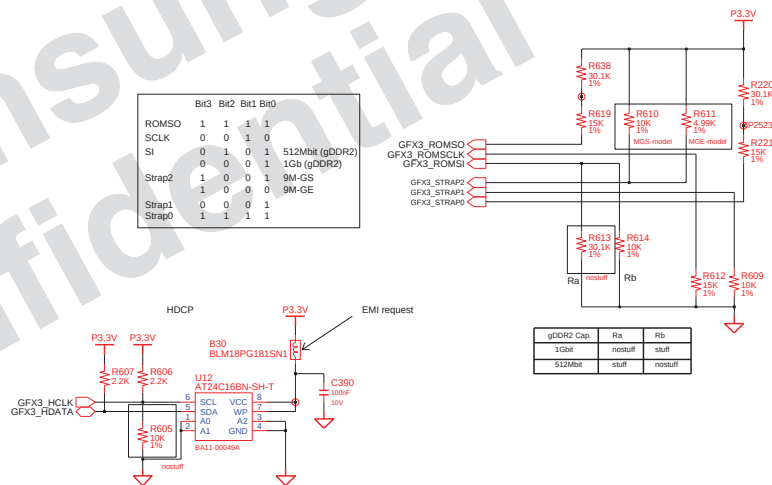


Pin	Description	Activate
GPIO0	DVI detect	High
GPIO1	TV detect	Low
GPIO2	PWM Brightness control	High
GPIO3	LCD VDD on enable	High
GPIO4	Backlight on/off	High
GPIO5	GPU VDD	High + 1.0V, Low = 1.5V
GPIO6	GPU VDD	NC
GPIO7	GPU VDD2 or Mem VDD	NC
GPIO8	Thermal diode Alert	LOW
GPIO9	Fan control	NC
GPIO10	Memory vREF switch	NC
GPIO11	SLI Rater sync	HIGH
GPIO12	AC Power detection input	NC
GPIO13	Power supply control	NC
GPIO14	Power supply control	NC
GPIO15	HDMI Detect	High
GPIO16	DP Detect	High

Strap option	Br 3	Br 2	Br 1	Br 0
ROM_S0	XCLK_27	TVMODE[2]	TVMODE[1]	TVMODE[0]
ROM_SCLK	DEVID[4]	VENID	CLK_FG	PLL_TMR
ROM_S1	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]
STRAP2	DEVID[3]	DEVID[2]	DEVID[1]	DEVID[0]
STRAP1	PADCFG[3]	PADCFG[2]	PADCFG[1]	PADCFG[0]
STRAP0	USER0	USER2	USER1	USER[0]

Resistor value	PU to VDD	PU to GND	CLKX27 = 127MHz
5K ohm	1000	0000	RAM CFG (SEC)
10 Kohm	1001	0001	0011 512mHz 0111 1Gbit
15K ohm	1010	0010	PADCFG3[0:11] for EDID
20K ohm	1011	0011	USER3[0:9]-0001 of NPD
25K ohm	1100	0100	NB9M-GS-0069 (0100)
30K ohm	1101	0101	NB9M-GS-0070 (0101)
35K ohm	1110	0110	NB9P-GS-0069 (0110)
45K ohm	1111	0111	NB9P-GS-0060 (0111)

NB9P-GE2-0040 (0000)
 NB9P-GE2-0040 (0001)



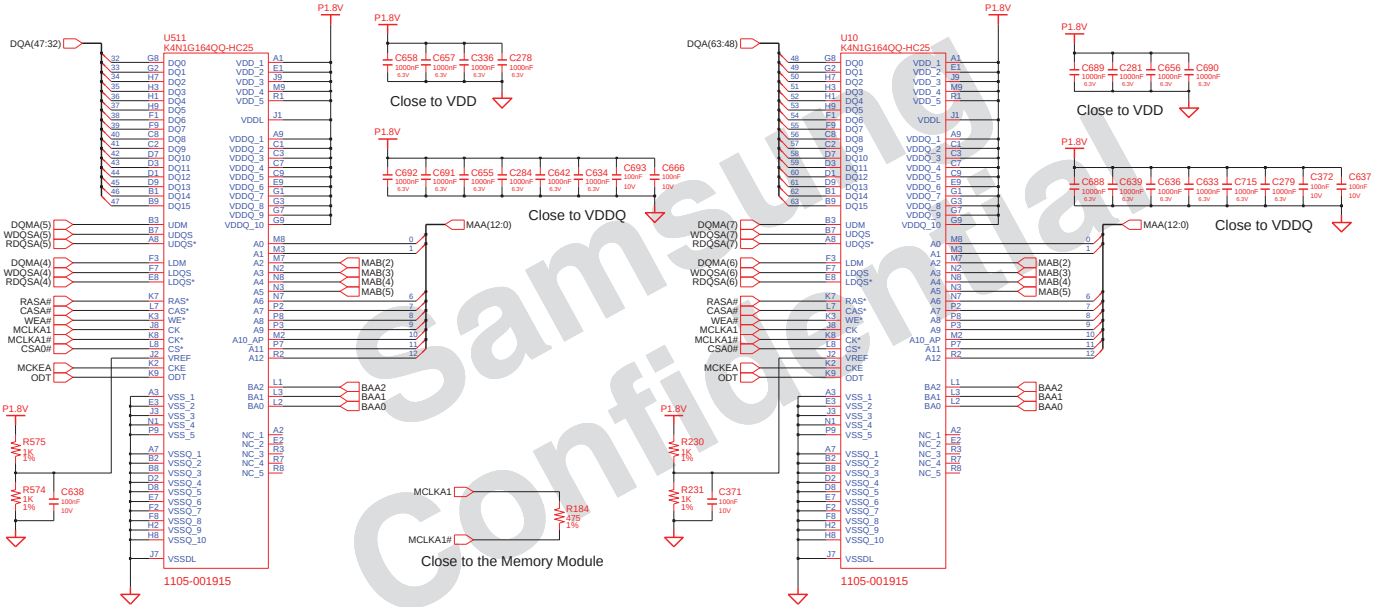
PU is for I2C ROM (AT24C16B, A0~A2 = 000)
PD is for Crypto ROM

DRAM	TERM1	DATE	8/14/2007	TITLE ISTANBUL EXT NB9X NB9X CHIP (4/4)	SAMSUNG ELECTRONICS PART NO. BA11-00920A		
CHECK	HJ KIM	DEV. STEP	PV				
APPROVAL	SJ PARK	REV	00				
MODULE CODE	LAST EDIT		JUL 18 2008 12:52:45 PM				
				DATE	4	05	4

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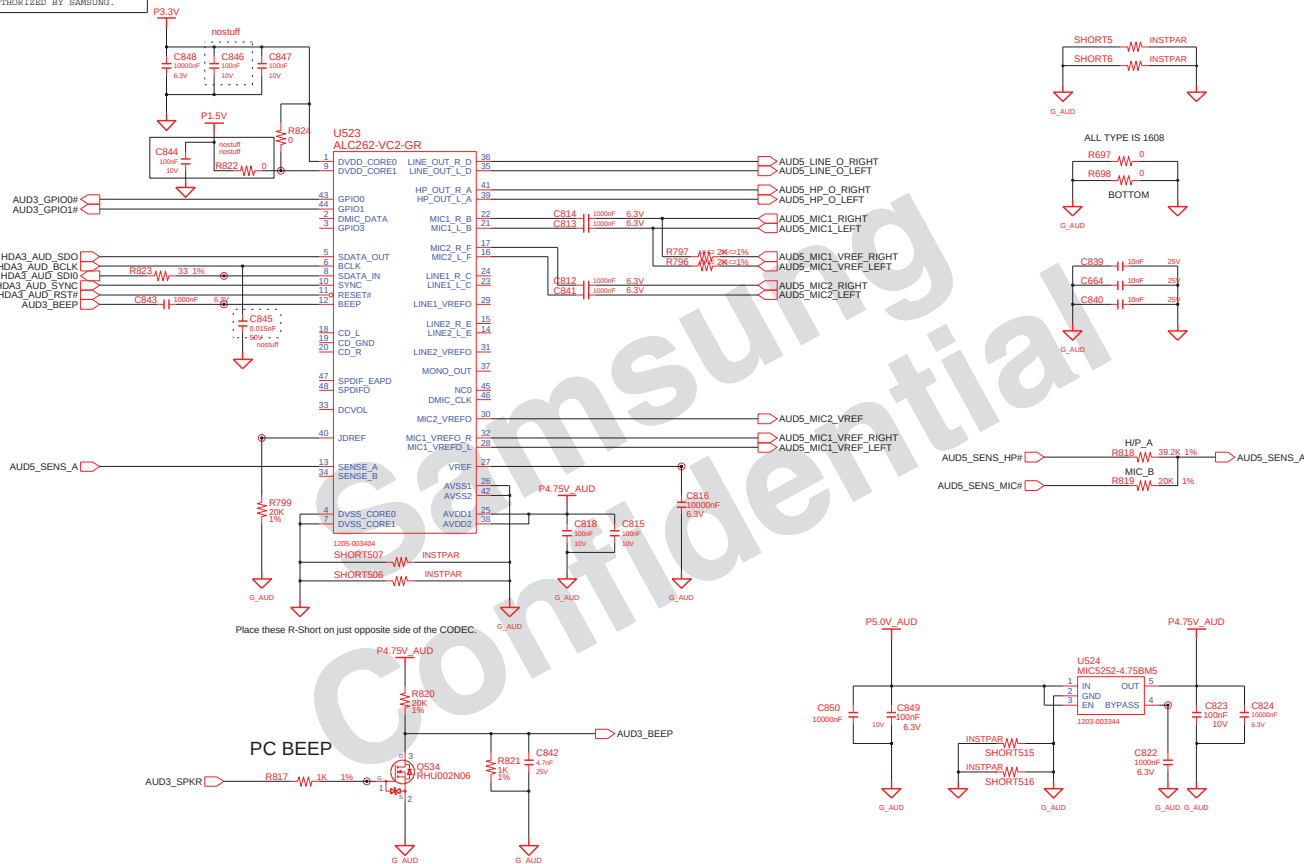
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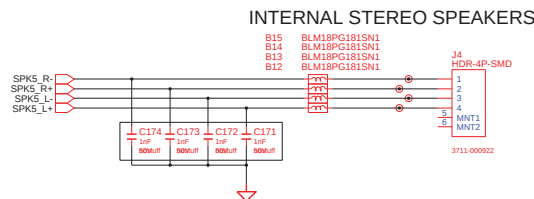
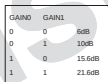
UPPER SUB PARTITION



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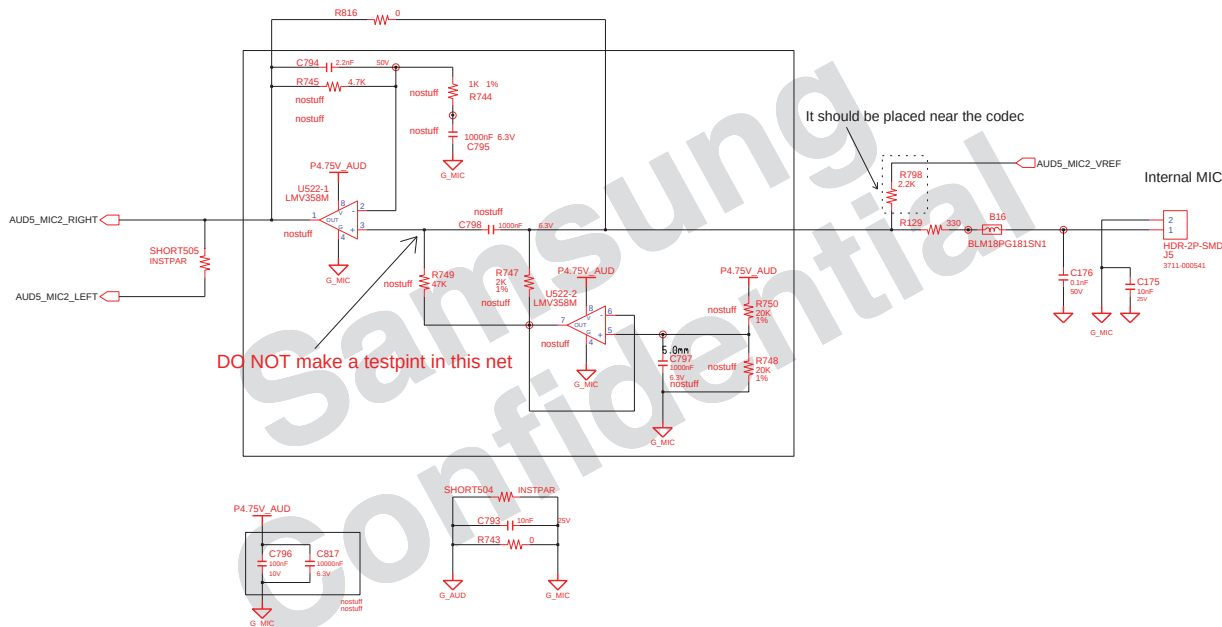




DRAW	TERMI	DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG ELECTRONICS		
CHECK	HJ KIM	REV.	STEP				PV	PART NO.
APPROVAL	SJ PARK	REV	00				HDA_Codec_Alc262 #2	
MODULE CODE	LAST EDIT		JAN 19 2008 12:52:45 PM				PAGE 29 OF 64	

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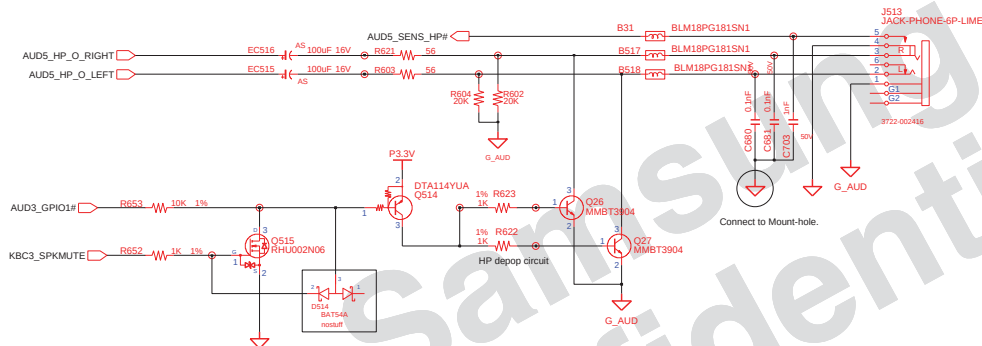


DATE	1/10/2008	FILE	ISTANBUL	PART NO.	BA41-00920A
REV	ST0P	PV	HDA_Codec_A1c262		
CHECK	HJ KIN	APPROVAL	SJ PARK	LAST EDIT	Jul 18, 2008 13:52:45 PM
MODULE CODE	undef ined	DATE	Jul 18, 2008 13:52:45 PM	PAGE	30 OF 64

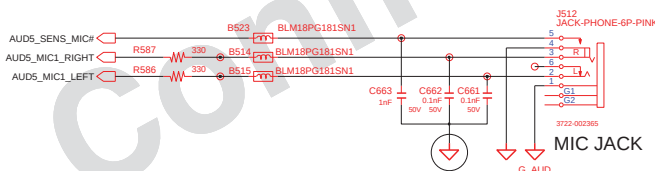
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HEADPHONE



Connect to Mount-hole.



The traces led to Audio Jacks have the width over 10mil

SWW	TERM1	DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG
CHECK	HJ K1N	REV	010	PV	HDA_Codec_A1 c262	ELECTRONICS
APPROVAL	SJ PARK	REV	00		HDA_Codec_A1 c262 #4	PART NO
MODEL CODE	LAST EDIT					BA41-00920A
				Jul 18, 2008 13:52:45 PM	PAGE	31 OF 64

Condition: P90=P91=P92=High(MICOM_P3V)
MD0=MD1=Low(0V)
Serial Port: P84 & P85

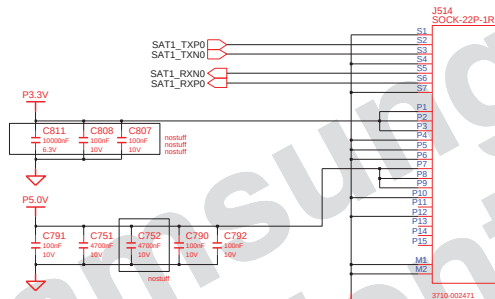
<http://hobi-elektronika.net>

DRAM	TERM1	1/10/2008	TITLE		ISTANBUL		SAMSUNG	
CHECK	HJ KIM	DEV. STEP	PV	MICOM_Renesas2110_100p		ELECTRONICS		
APPROVAL	SJ PARK	REV	00	MICOM_Renesas2110_100p		PART NO.	BA11-00920A	
MODULE CODE	LAST EDIT		JAN 10 2008 12:52:45 PM		PAGE	47	OF	64

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Main to HDD

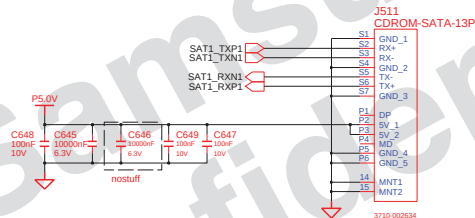


DRW	TERM	DATE	TITLE	SAMSUNG ELECTRONICS
CHECK	HJ KIN	1/10/2008	ISTANBUL	
APPROVAL	SJ PARK	REV	HDD_IF_Conn	PART NO.
MODULE CODE	LAB EDIT	00	HDD_IF_Conn	BA41-00920A
Jul 18, 2008 13:52:45 PM				PAGE 33 OF 64

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SATA ODD

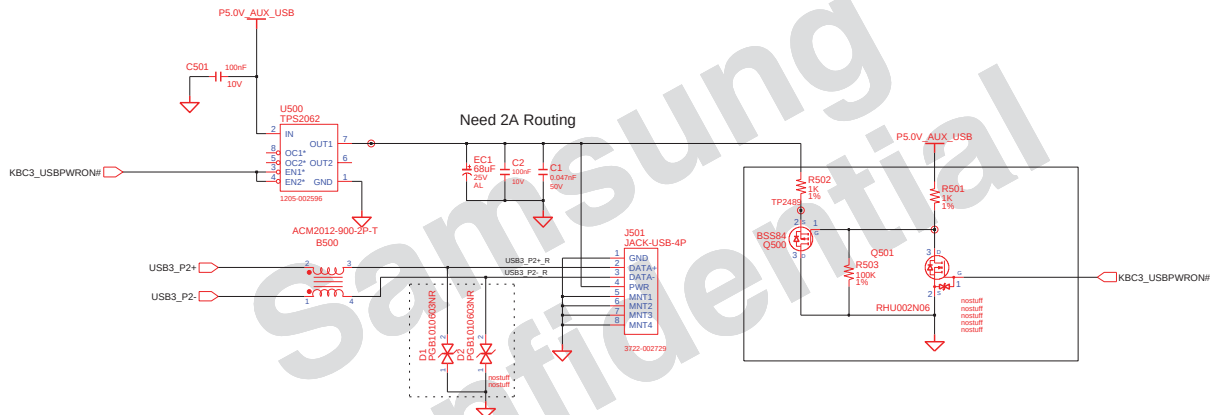


3710-002634

DRAW	TERM1	DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG
CHECK	HJ KIM	REV.	STEP	ODD_IF_Conn	ODD_IF_Conn	ELECTRONICS
APPROVAL	SJ PARK	REV.	00	ODD_IF_Conn	ODD_IF_Conn	PART NO.
MODULE CODE	LAST EDIT	July 18, 2008 13:52:45 PM	PAGE	49	OF	64

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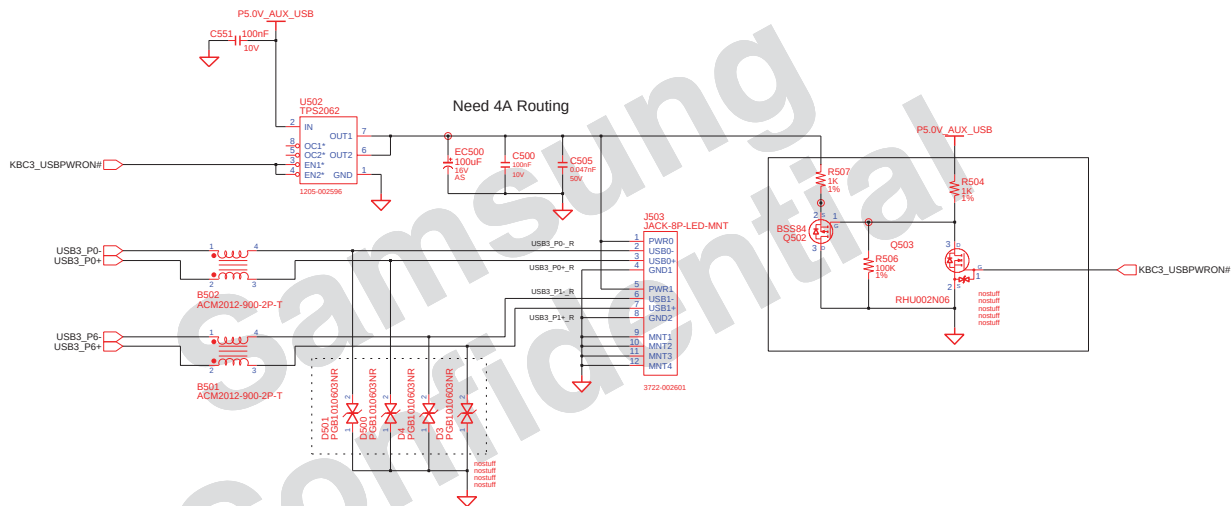
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1 PORT USB CONNECTOR

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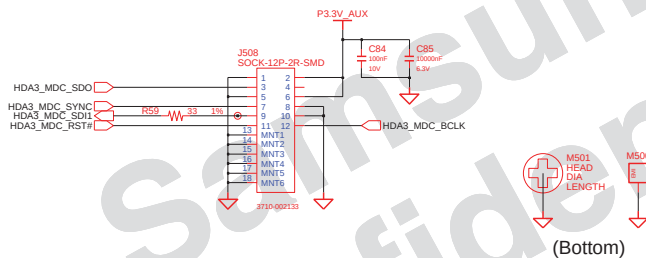
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2 PORT USB CONNECTOR



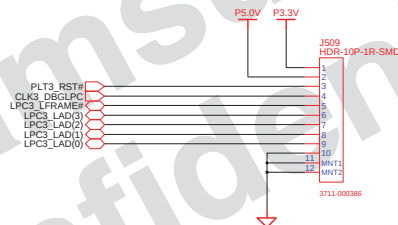
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MDC Connector**(Bottom)**

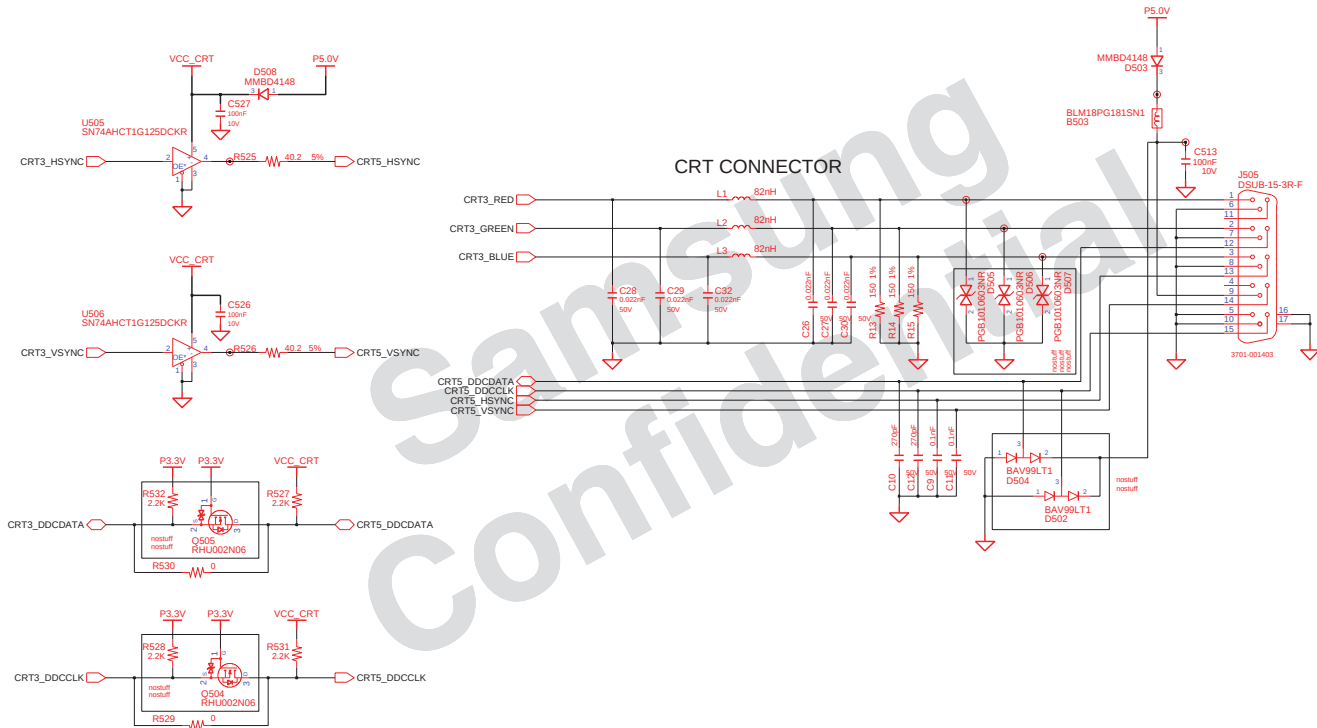
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80H DECODER CONNECTOR

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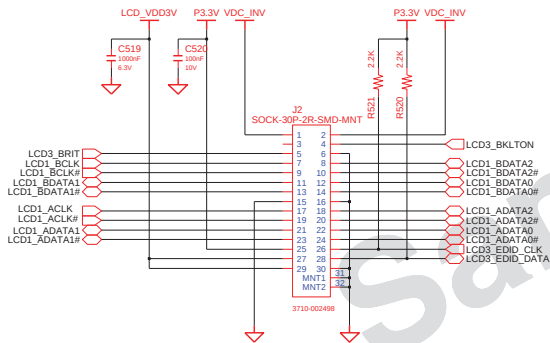
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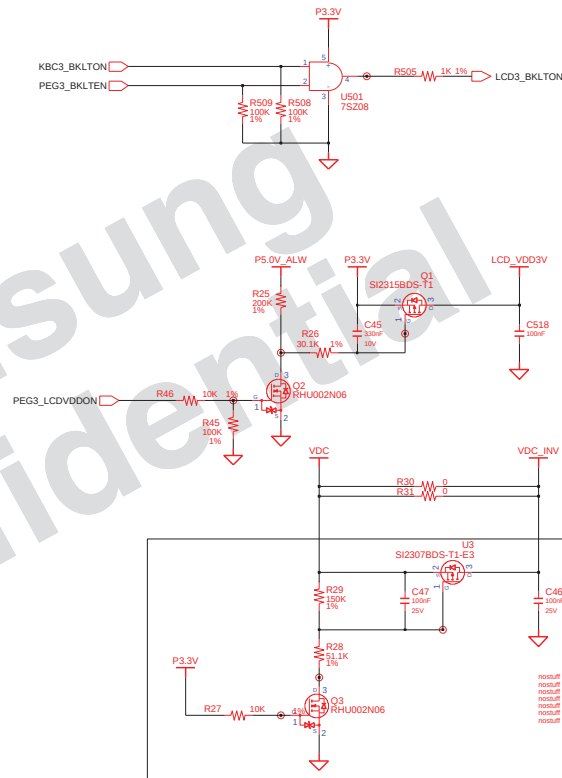
DATE	1/10/2008	FILE	ISTANBUL	PART NO.	BA41-00920A
TERM	REV: 910P	PV	Interface_Circuit		
CHECK	HJ KIM	REV	00	Graphics_IF_CRT	
APPROVAL	SJ PARK	REV	00		
MODULE CODE	LAST EDIT	July 18, 2008 13:52:45 PM	PAGE	25	OF 64

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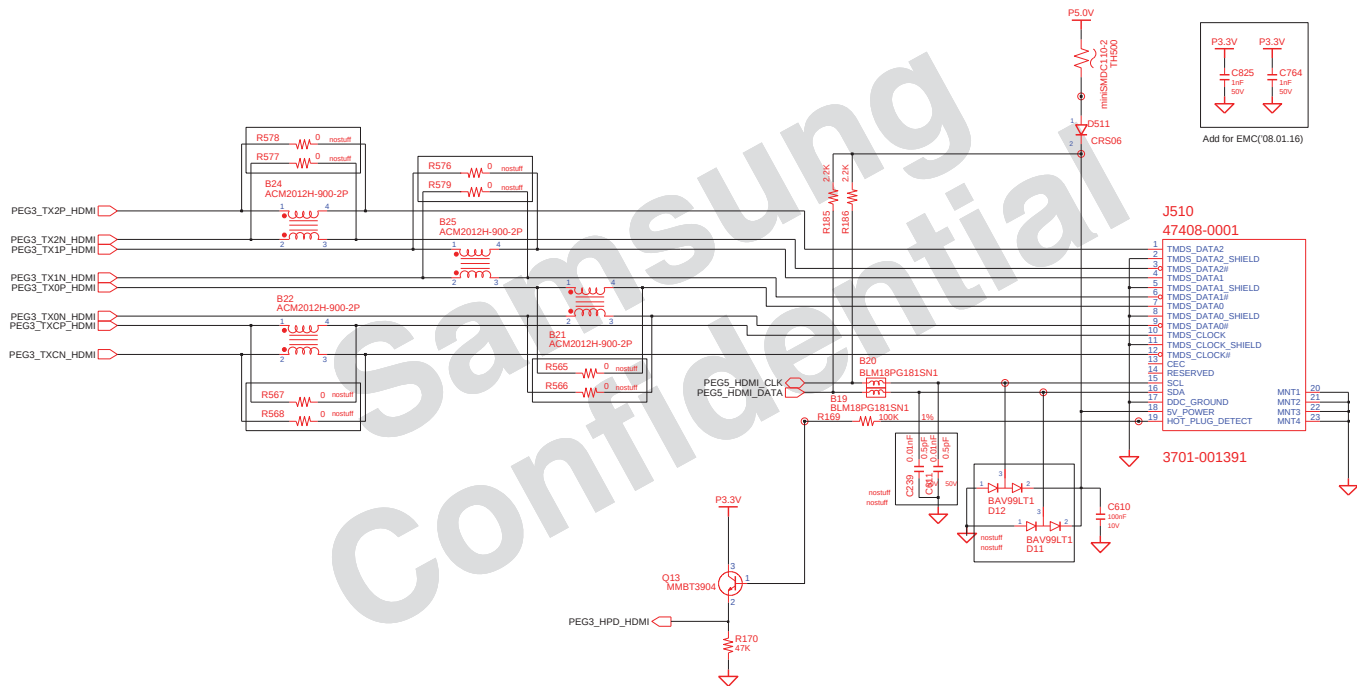


Lyon : 1channel
 Istanbul : 2channel



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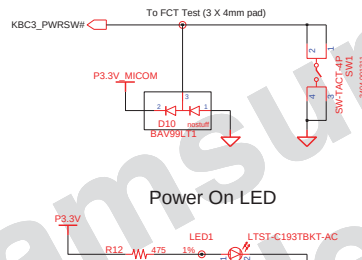
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Put this LED located near PWRSW from ME recommendation
Power LED, Blue color

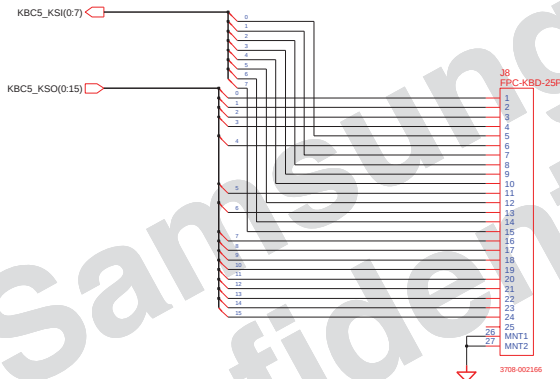


DRAW	TERM1	DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG
CHECK	HJ KIM	REV. STEP	PV	MIO_Switch		ELECTRONICS
APPROVAL	SJ PARK	REV	00	MIO_Switch	PART NO.	BA41-00920A
MODULE CODE	LAST EDIT	July 18, 2008 13:52:45 PM			PAGE	48 OF 64

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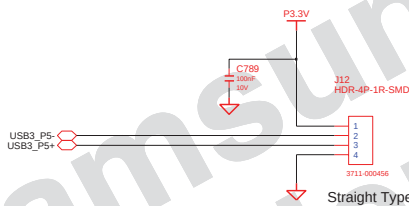
KEYBOARD



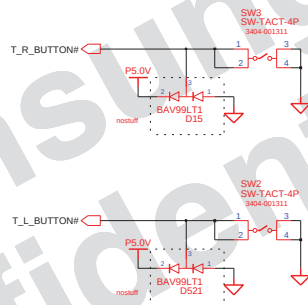
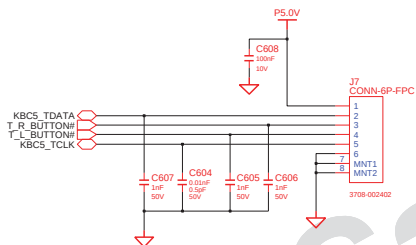
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Bluetooth Interface



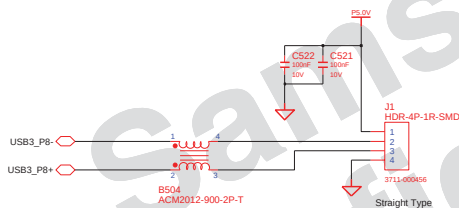
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CHECK	HJ KIM	REV. STEP	PV	Bluetooth_IF_Conn		ELECTRONICS
APPROVAL	SJ PARK	REV	00	Bluetooth_IF_Conn	PART NO.	BA41-00920A
MODULE CODE		LAST EDIT	July 18, 2008 13:52:45 PM	PAGE	19	OF 64



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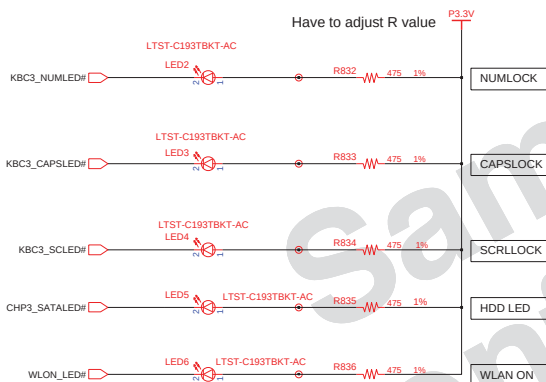
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CAMERA

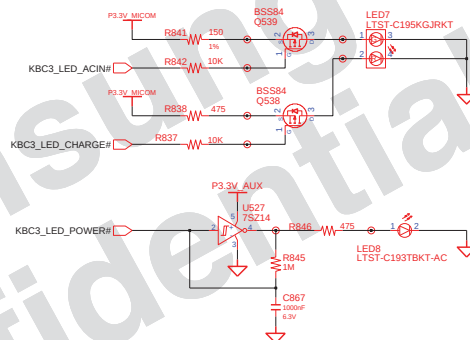


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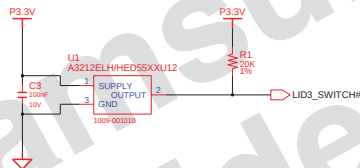


ADAPTERIN/CHARGING LED



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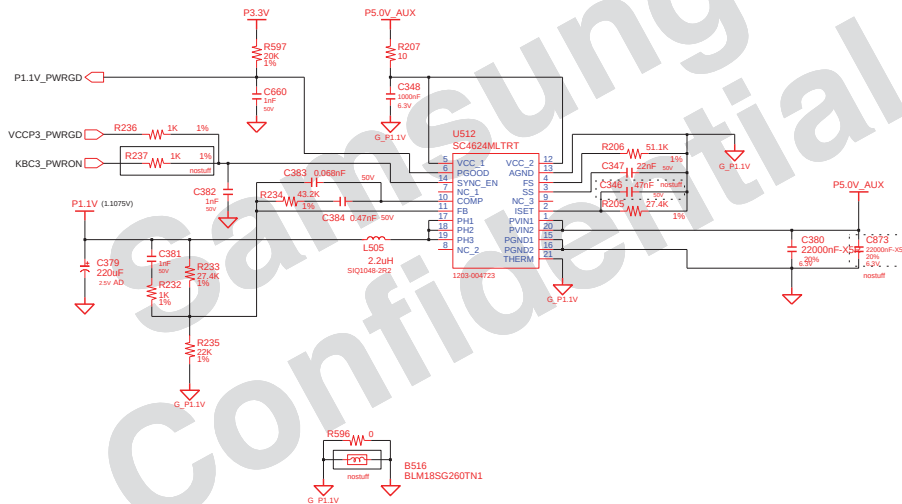
LID_SWITCH

DATE	1/10/2008	FILE	ISTANBUL	SAMSUNG ELECTRONICS
TERM	REV. STEP	PV	LID_Switch	
CHECK	HJ KIM	PV	LID_Switch	PART NO.
APPROVAL	SJ PARK	00	LID_Switch	BA41-00920A
MODULE CODE	LAST EDIT	July 18, 2008 13:52:45 PM	PAGE	41 OF 64

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P1.1V

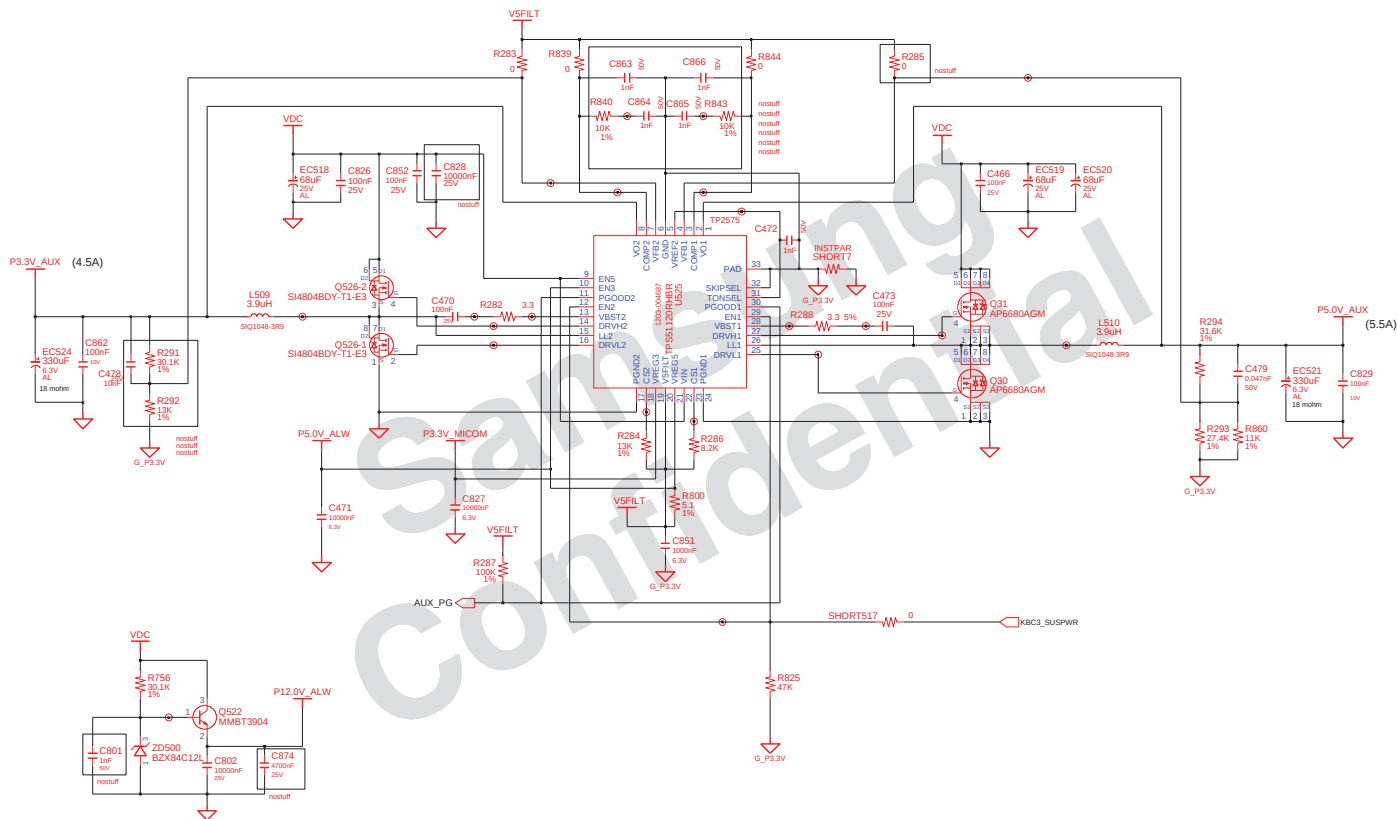


DRAW	TERM1	DATE	8/14/2007	FILE	ISTANBUL	SAMSUNG ELECTRONICS
CHECK	HJ KIM	REV. STEP	PV		PWR_Gfx_MV_Ext	
APPROVAL	SJ PARK	REV	00		Ext Gfx Memory(SC486)	PART NO.
						BA41-00920A
MODULE CODE		LAST EDIT	Jul 18, 2008 13:52:45 PM		PAGE	1 OF 2

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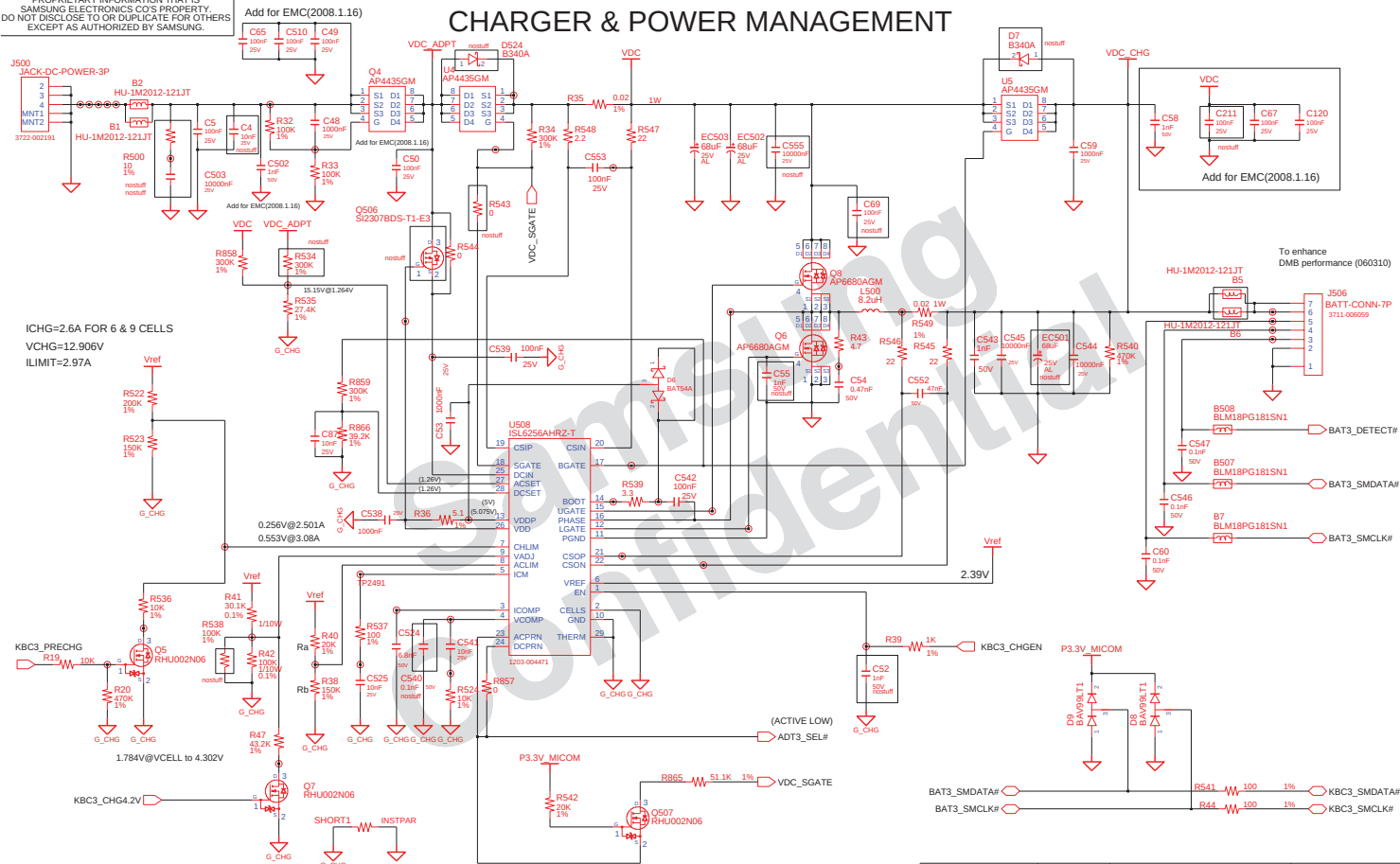
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P3.3V_AUX & P5.0V_AUX



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CHARGER & POWER MANAGEMENT



	60W	90W
Ra(R40)	150K(2007-007489)	20K(2007-007312)

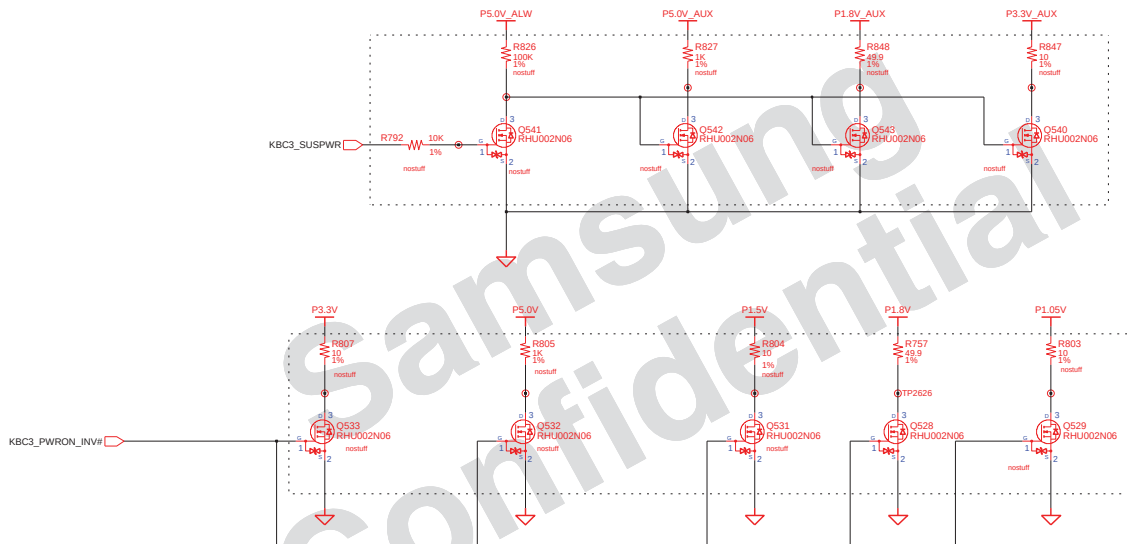
<http://hobi-elektronika.net>

DRAW	TERM1	DATE	1/10/2008	TITLE	ISTANBUL		SAMSUNG	
CHECK	HJ KJM	DEV. STEP	PV	PWR_MV_Charger		ELECTRONICS		
APPROVAL	SJ PARK	REV	00	CHARGER		PART NO.	BA41-00920A	
MODULE CODE	undefined		LAST EDIT		Jul 18, 2008 13:52:45 PM	PAGE	55	OF 64

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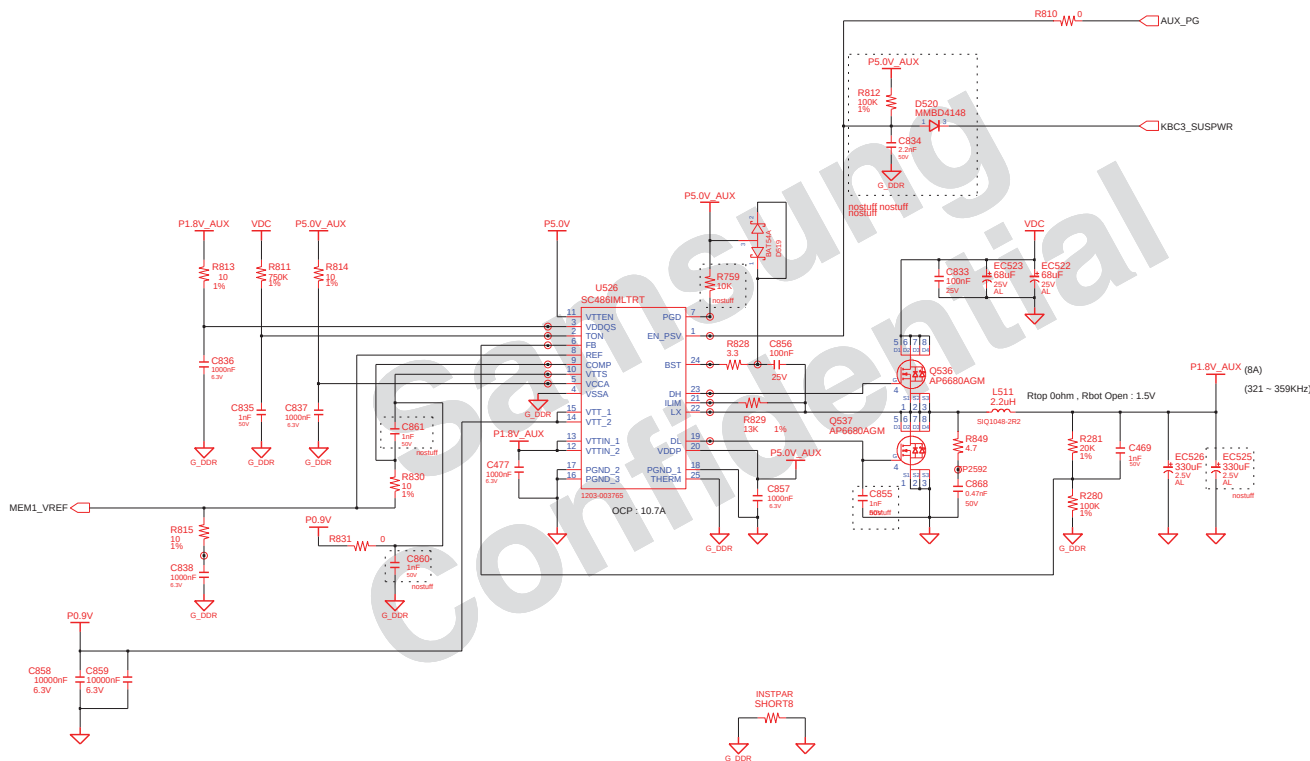
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POWER DISCHARGER



DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG ELECTRONICS
REV.	STEP	PV	PWR_MV_DisCharger	
CHECK	HJ KJN	REV	00	DISCHARGING LOGIC
APPROVAL	SJ PARK	LIBR EDIT		PART NO. BA41-00920A
MODULE CODE	undef ined		July 18, 2008 13:52:45 PM	PAGE 56 OF 64

DDR2 Power

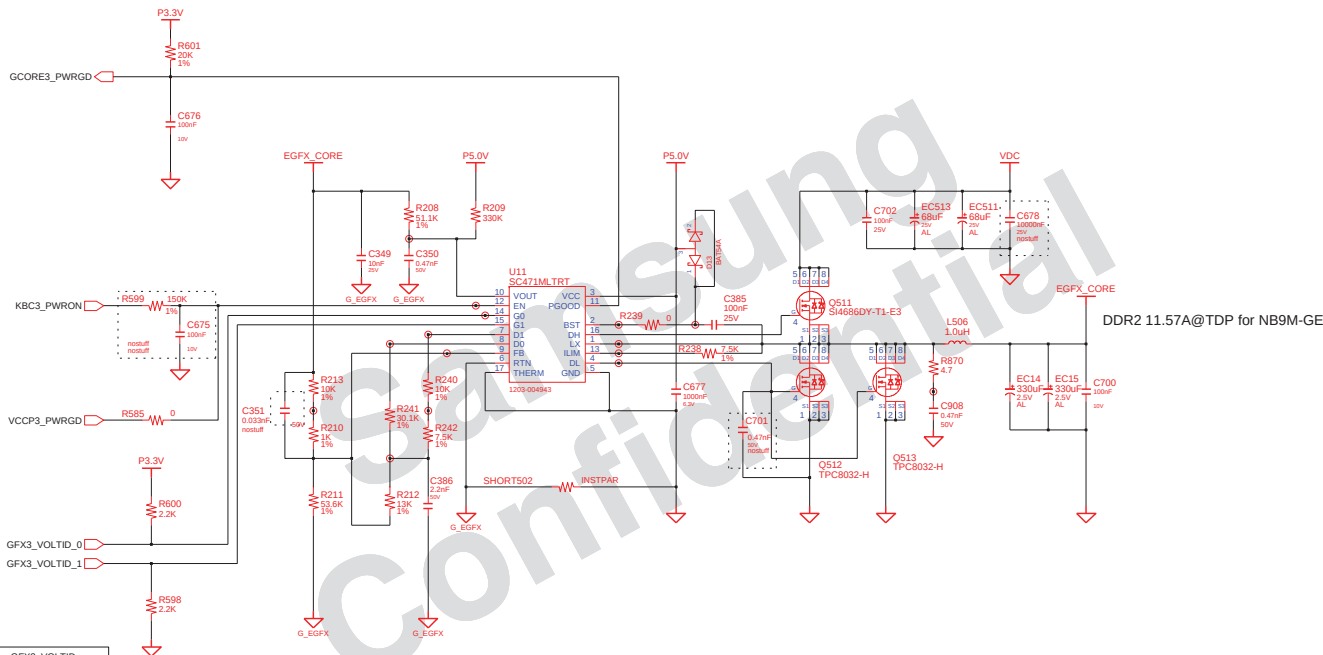


DATE	1/10/2008	TITLE	ISTANBUL	SAMSUNG
DESIGN	HJ KIN	REV	00	ELECTRONICS
APPROVAL	SJ PARK	REV	00	DDR3 POWER (P1.5V_AUX)
MODULE CODE	undefined	LAST EDIT	July 18, 2008 13:52:45 PM	PAGE 57 OF 64

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Graphic Core Power



GFX3_VOLDID	
G0	C1
0	0
1	0
0	1
1	1