

KN2/MA3 BLOCK DIAGRAM

Dothan / Montara-GM+

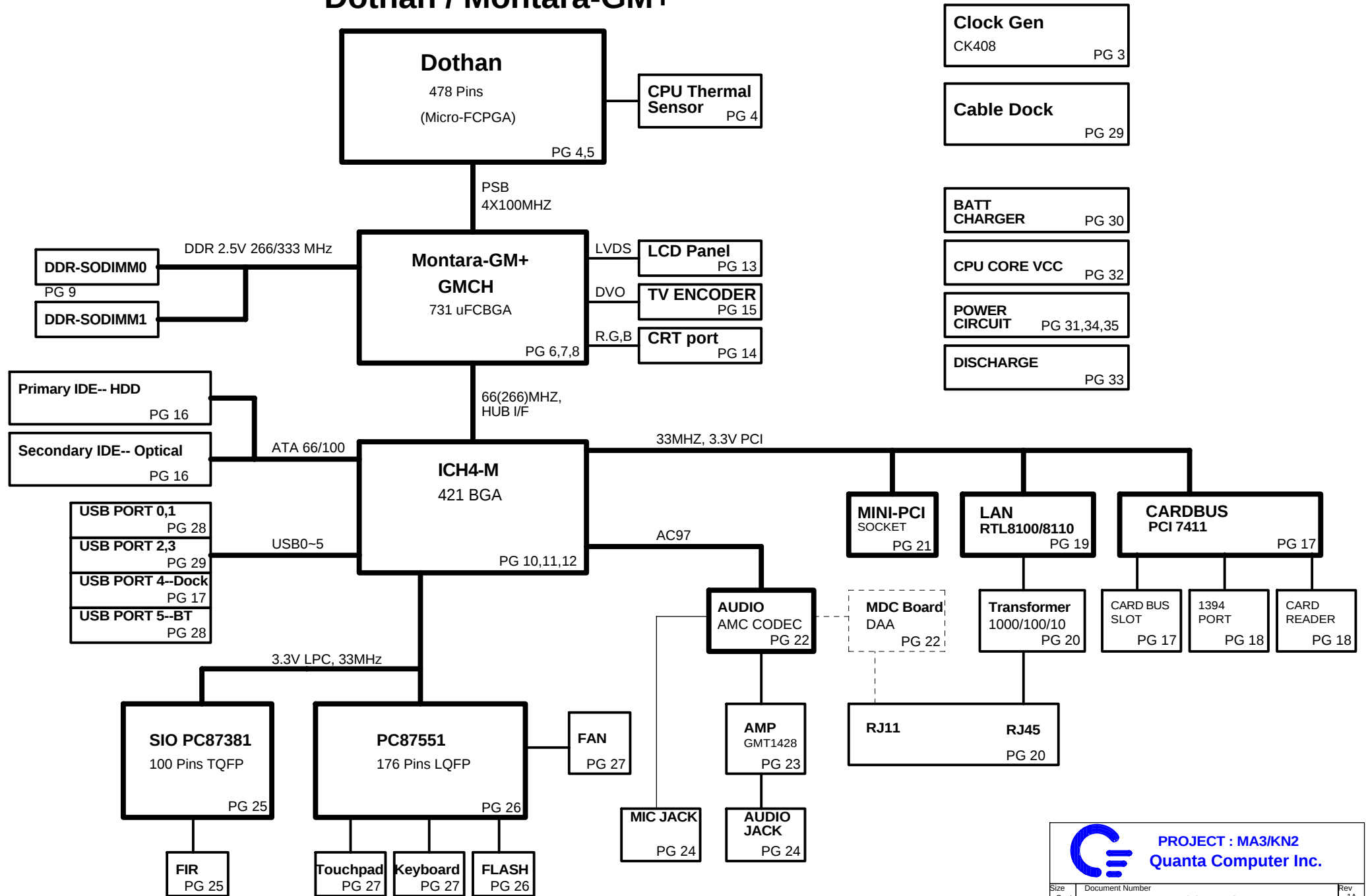


TABLE OF CONTENTS

- P01: BLOCK DIAGRAM
- P02: TABLE OF CONTENTS
- P03: CLOCK GENERATOR
- P04: DOTHAN (HOST BUS)
- P05: DOTHAN (POWER/NC)
- P06: GMCH-GM+ (HOST, HUB, APG)
- P07: GMCH-GM+ (DDR I/F)
- P08: GMCH-GM+ (VCC, GND)
- P09: DDR SO-DIMM(200P)
- P10: ICH4-M (CPU, PCI, IDE)
- P11: ICH4-M (USB, HUB, LPC)
- P12: ICH4-M (POWER, GND)
- P13: LCD CONNECTOR
- P14: CRT CONNECTOR
- P15: TV ENCODER
- P16: HDD & CDROM
- P17: CARDBUS (TI PCI7411)
- P18: CARD READER & IEEE1394
- P19: LAN RTL8100/8110
- P20: LAN TR & RJ45
- P21: MINI PCI
- P22: AUDIO AMC CODEC
- P23: AUDIO AMP
- P24: AUDIO JACK
- P25: SUPER I/O PC87381
- P26: PCU 87551 & FLASH ROM
- P27: KEYBOARD & FAN
- P28: USB & LED CONNECTOR
- P29: PORT REPLICATOR
- P30: BATTERY CHARGER
- P31: 5V & 3.3V SYSTEM POWER
- P32: CPU VCC CORE POWER
- P33: DISCHARGER
- P34: 2.5V & 1.5V
- P35: 1.35GMCH, VCCP, 1.8V, & 1.25V

Dothan VID CODE

D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
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1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
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1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V

I2C / SMBUS ADDRESSING

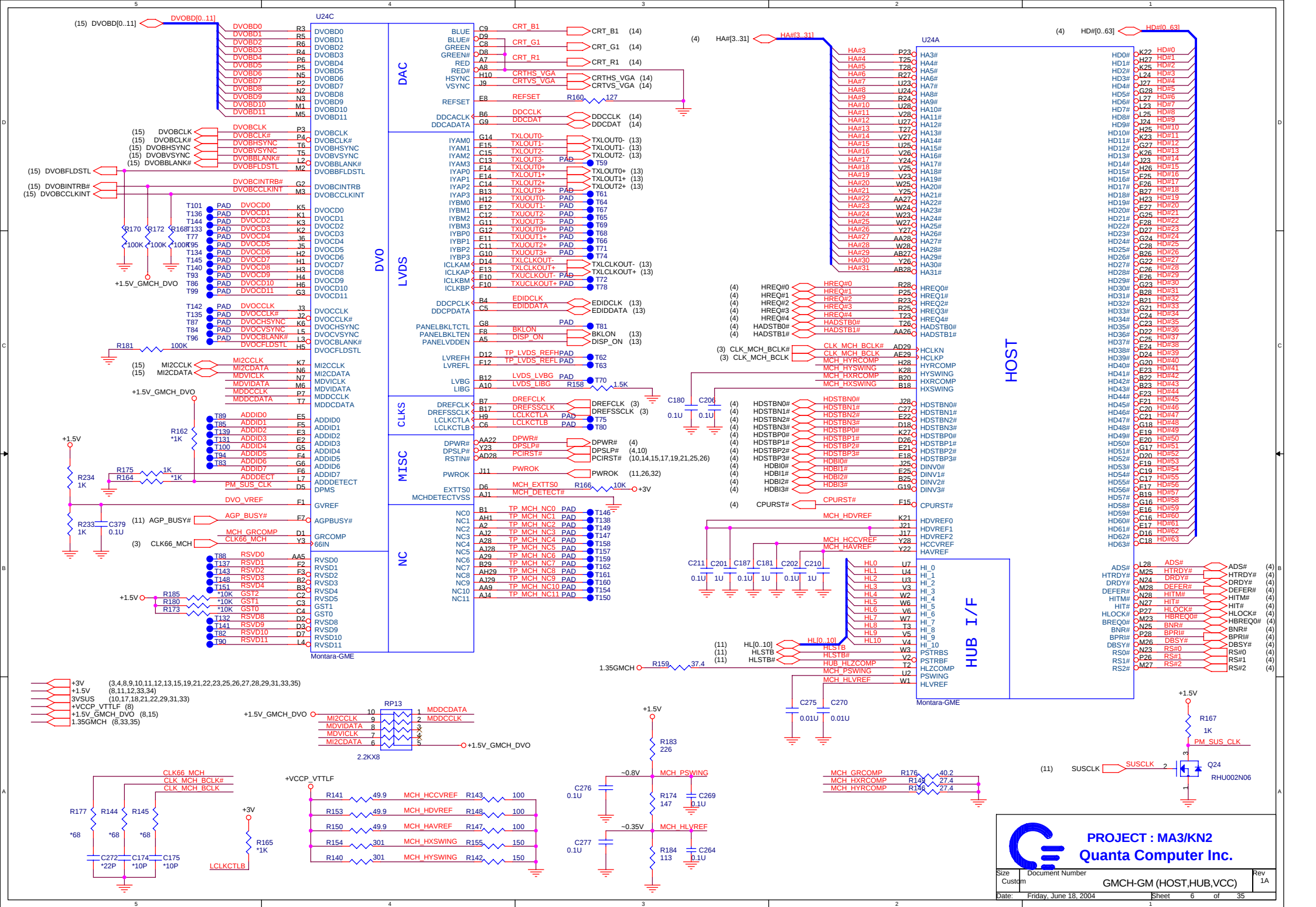
DEVICE	ADDRESS
DDR SO-DIMM 0	A0H
DDR SO-DIMM 1	A1H
CLOCK GEN (ICS950810BGT)	D2H
THERMAL MONITOR	90H
BATTERY CHARGER	12H
SMART BATTERY	16H
SPEREAD SPECTRUM (Option)	D4H

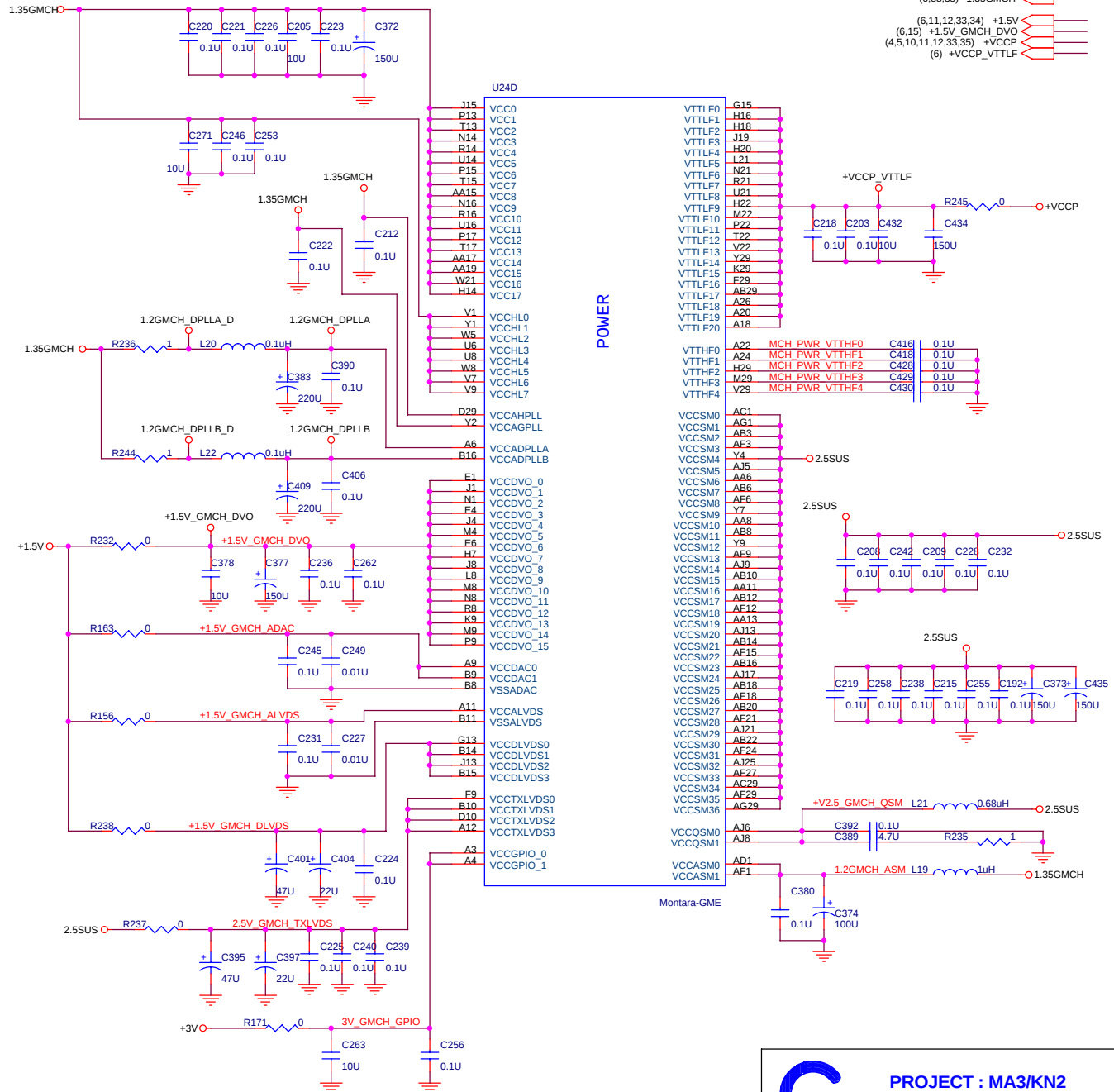
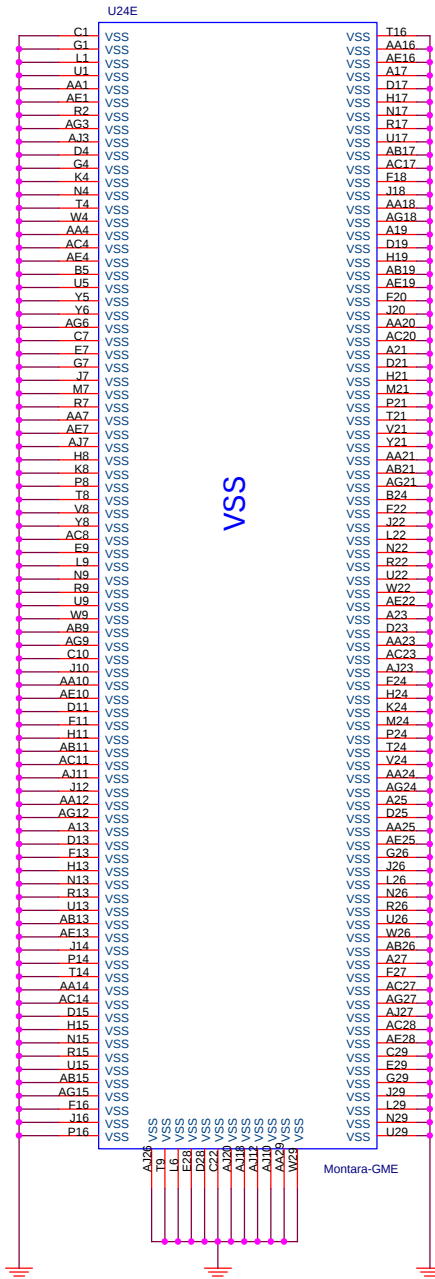
PCI DEVICES

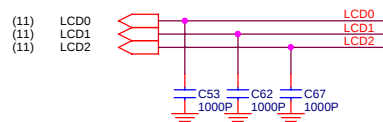
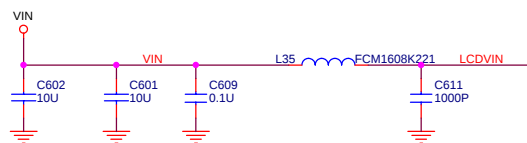
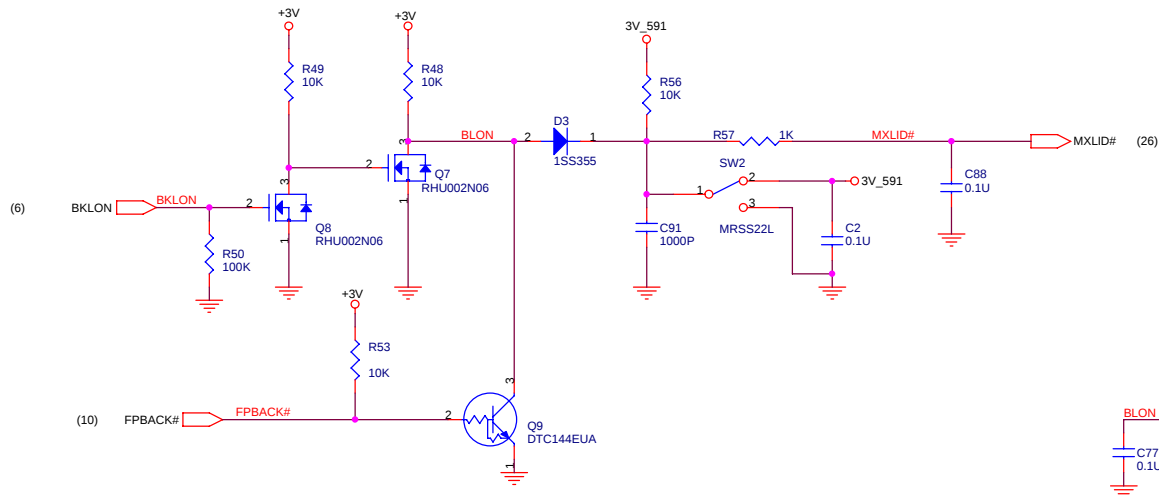
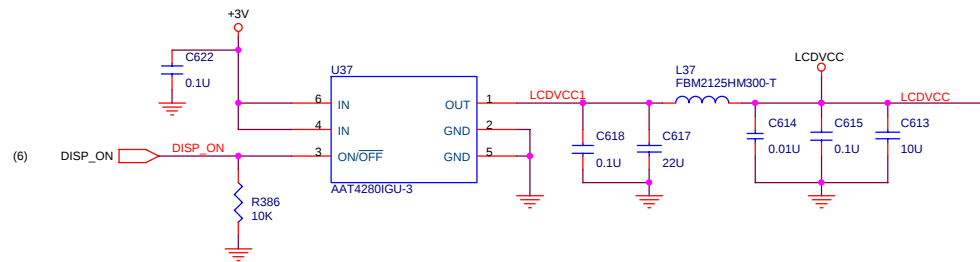
DEVICE	IDSEL #	REQ/GNT #	PIRQ
CARDBUS TI PCI7411	AD18	0	A,B,C
MINI PCI	AD20	1	E,F
LAN	AD16	2	D



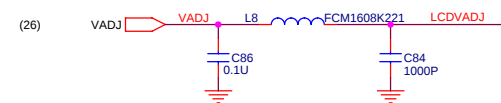
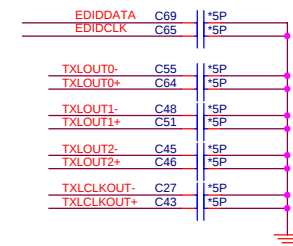
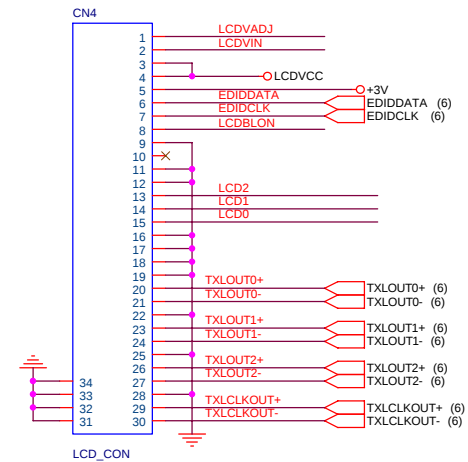
PROJECT : MA3/KN2
Quanta Computer Inc.

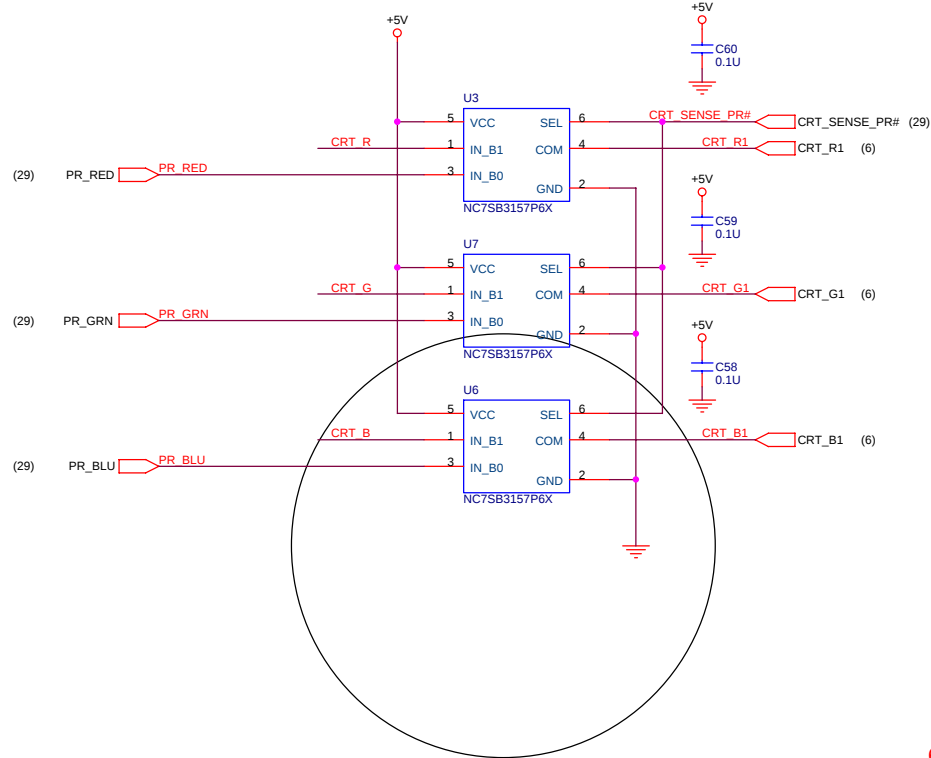




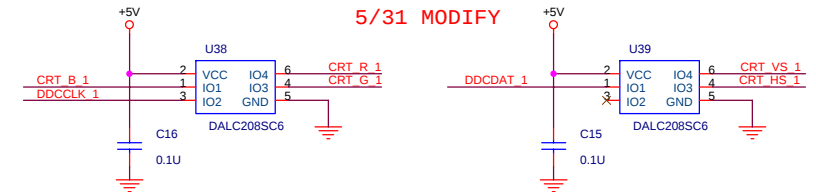
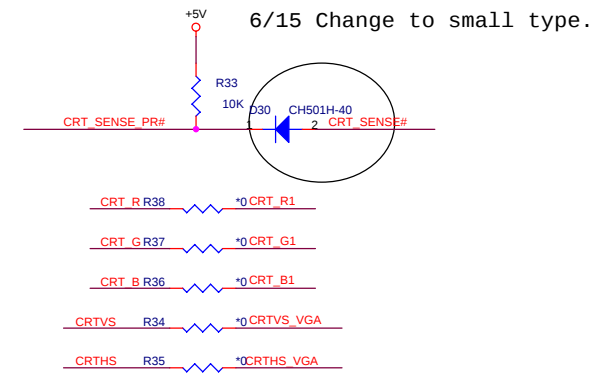


3V_591 (11,26,30,31)
 +3V (3,4,6,8,9,10,11,12,15,19,21,22,23,25,26,27,28,29,31,33,35)
 VIN (30,31,32,33,34)





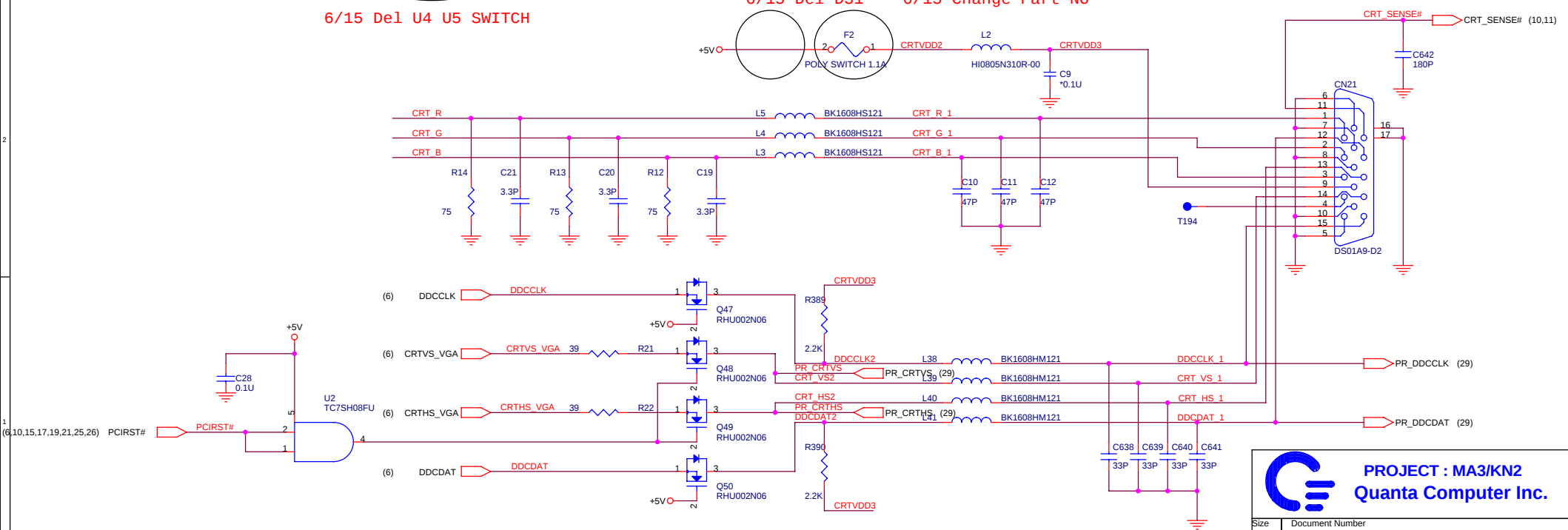
SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



6/15 Del U4 U5 SWITCH

6/15 Del D31 6/15 Change Part-NO

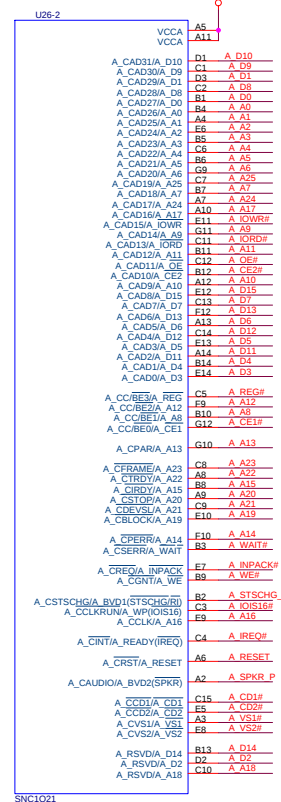
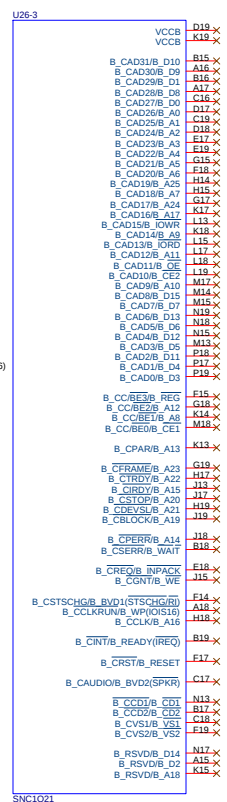
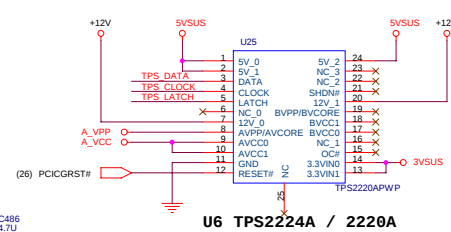
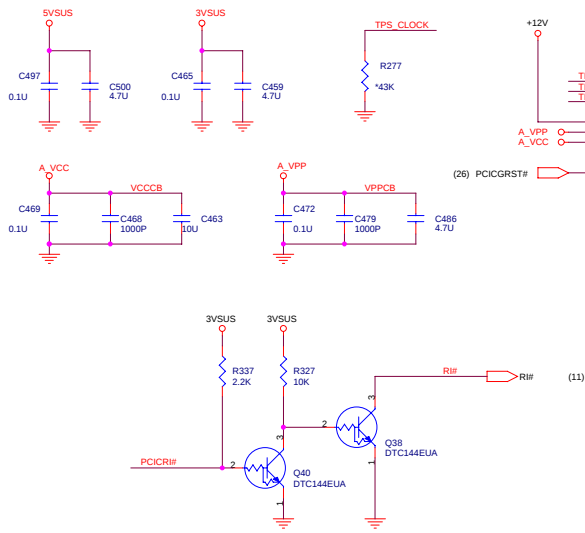
CRT PORT




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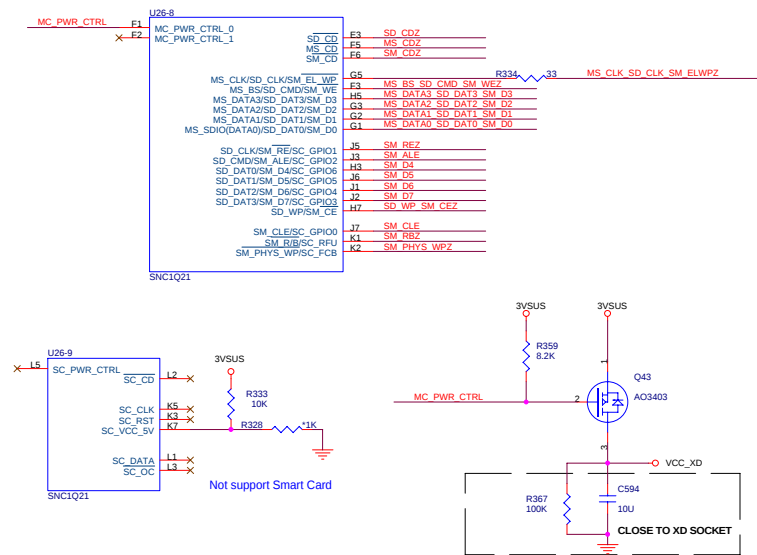
ID Select      : AD17
Interrupt Pin   : INTB#,  INTC#
Request indicates : REQ0#
Grant indicates  : GNT0#

```

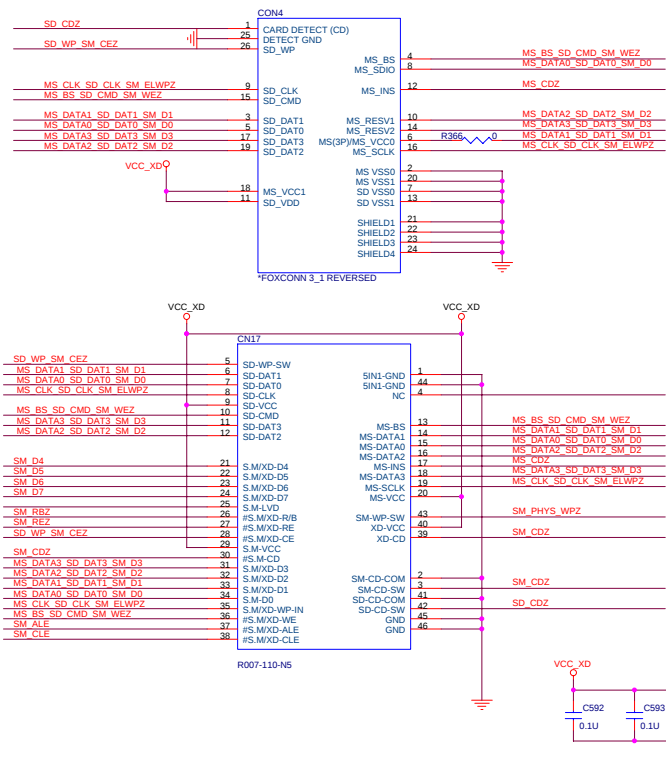
[illegible]

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Memory Card Reader

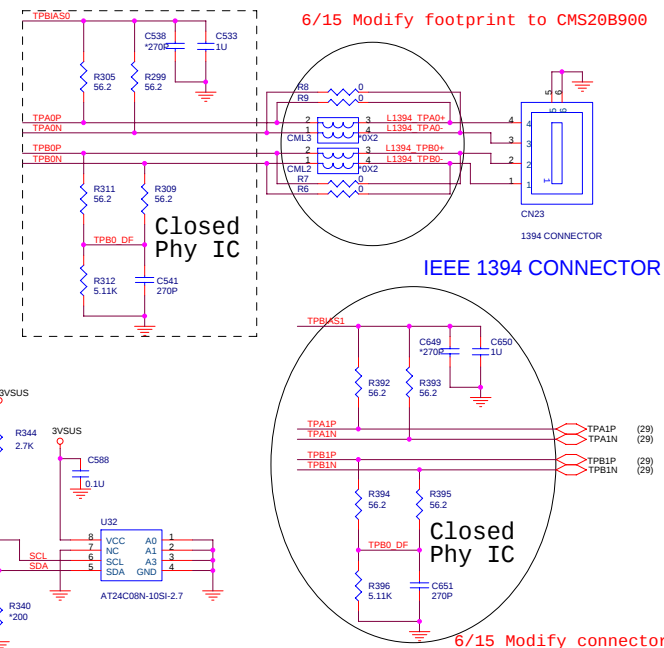
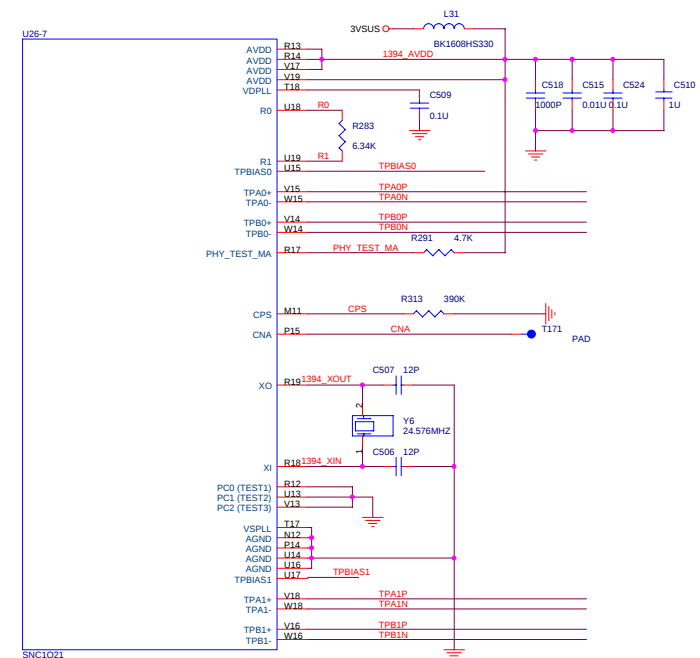


Supporting MMC/SD/MS Cards



Place 0.1u bypass capacitor to each VCC Pin.

IEEE 1394a



6/15 Modify connector to PR

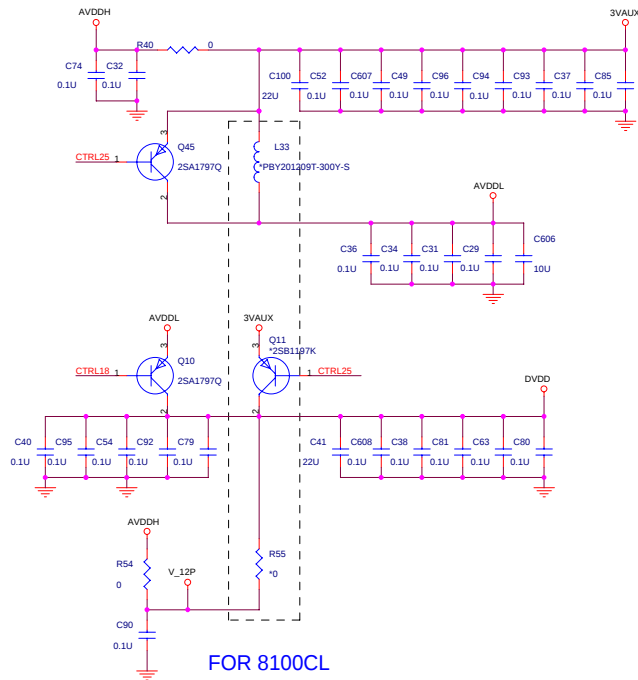


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Size C	Document Number CARD READER & IEEE1394	Rev 1A
Date: Friday, June 18, 2004	Sheet 18 of 35	

ID Select : AD16
 Interrupt Pin : INTD#
 Request indicates : REQ2#
 Grant indicates : GNT2#

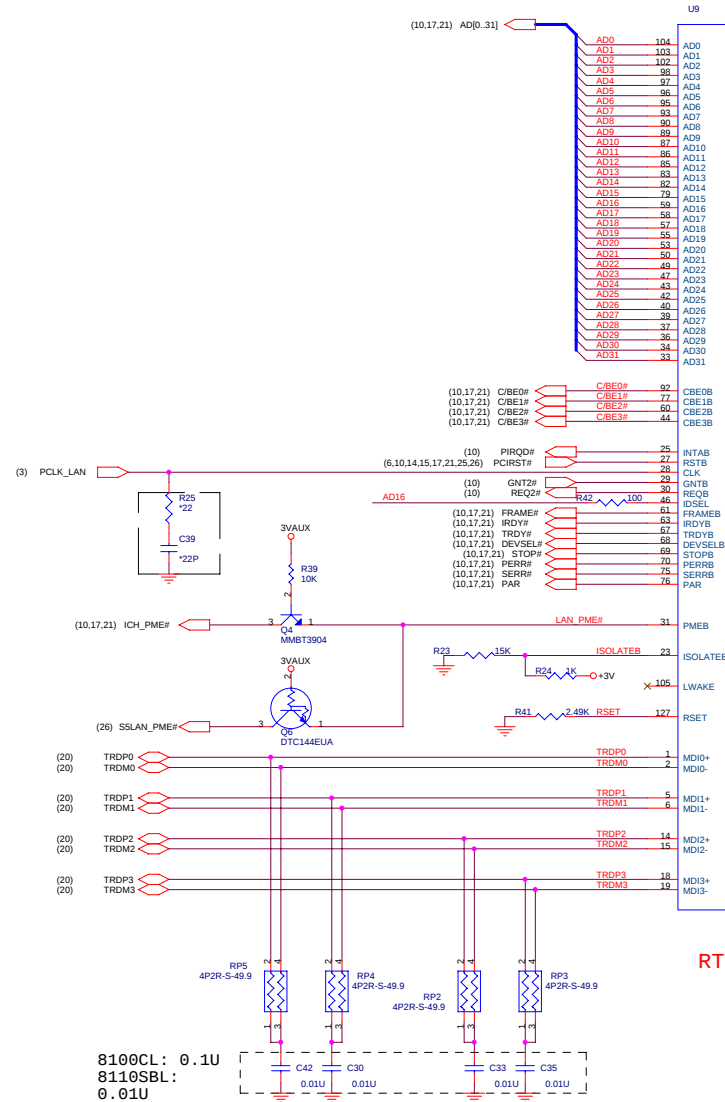
FOR 8110SB



FOR 8100CL

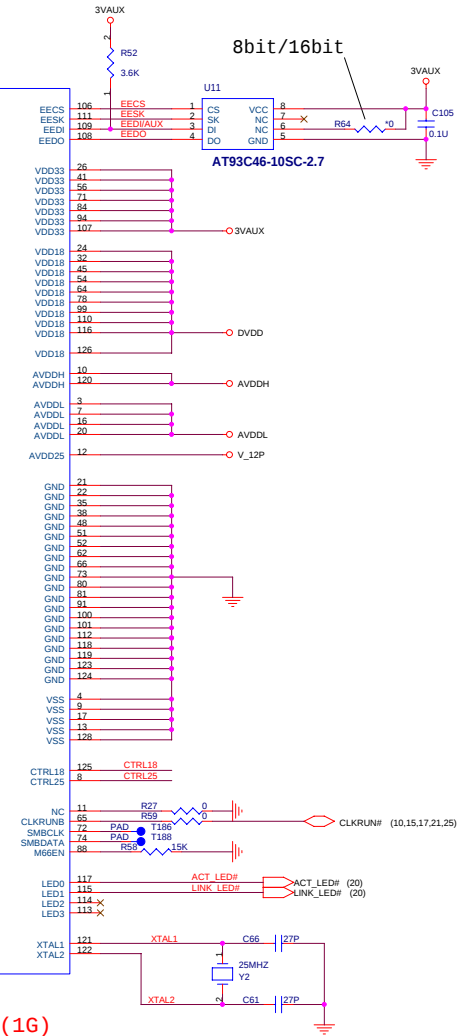
	8100CL(10/100M)	8110SB(1G)
DVDD33	3.3VD 26, 41, 56, 71, 84, 94, 107	3.3VD 26, 41, 56, 71, 84, 94, 107
AVDDL	3.3VA 3, 7, 20	2.5VA 3, 7, 20, 16
DVDD	2.5VD 32, 54, 78, 99	1.8VD 32, 54, 78, 99, 24, 45, 64, 110, 116, 126
AVDD25	2.5VA 12	NC
AVDDH	NC	3.3VA 10, 120
Pin 127	5.6k (1%) pull-low	2.49k (1%) pull-low
Pin 88	NC	15K pull-low

8100CL: 0.1U
 8110SBL:
 0.01U



RTL8110SB (1G)

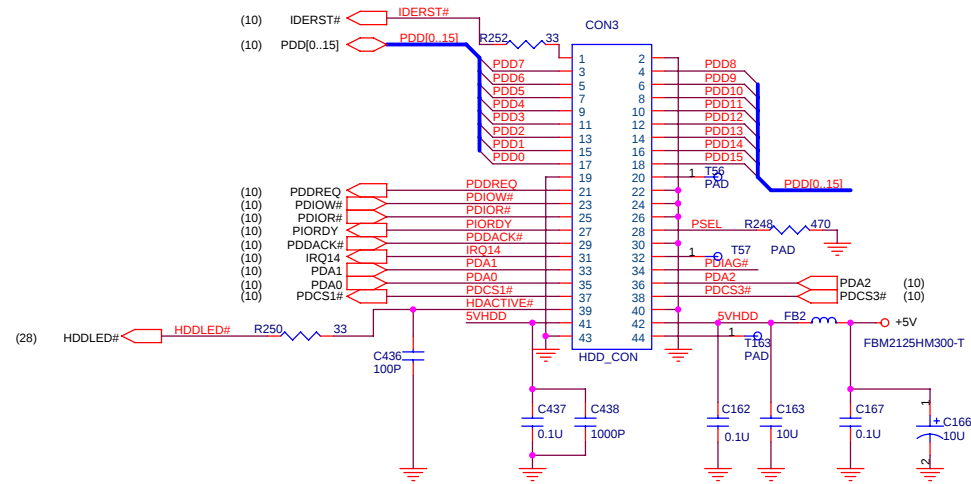
RTL8100C(L)



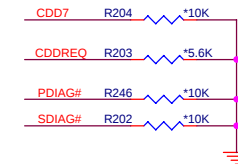
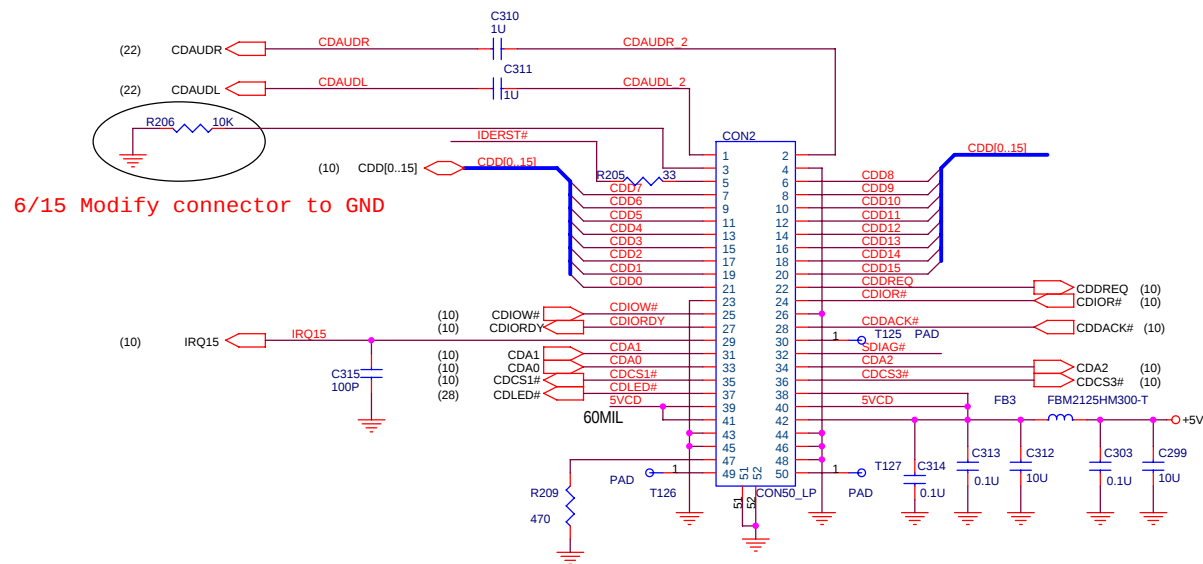
PROJECT : MA3/KN2
 Quanta Computer Inc.

Size	Document Number	Rev
C	LAN RTL8100CL	1A
Date	Friday, June 18, 2004	Sheet 19 of 35

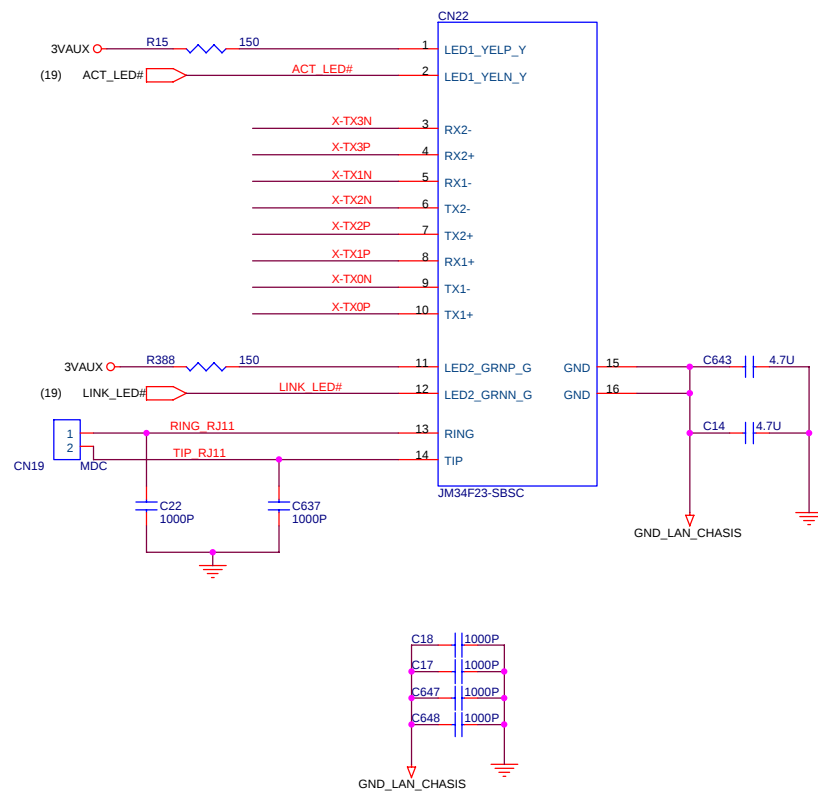
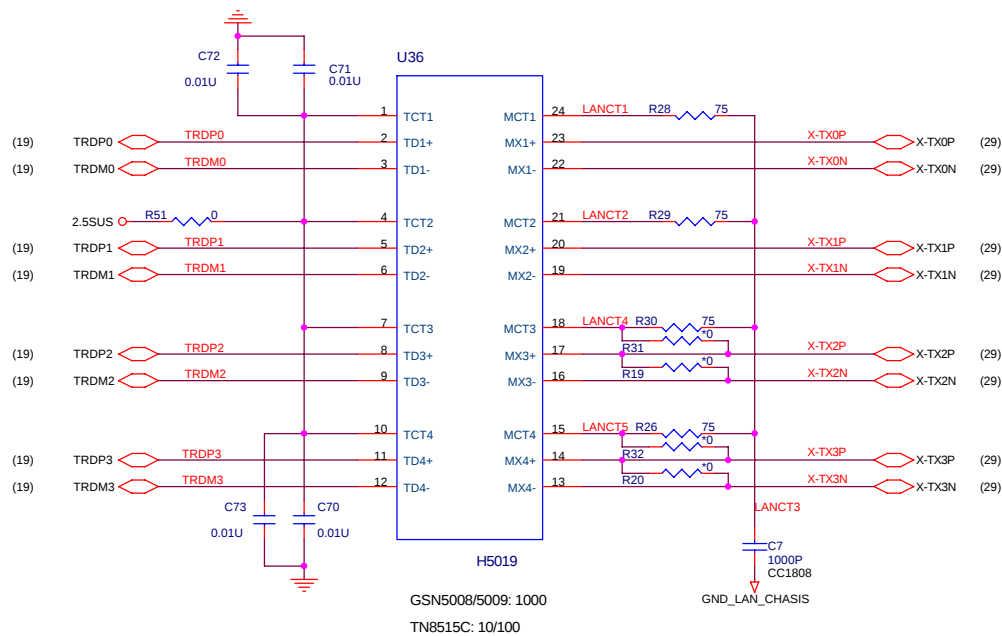
HDD connector

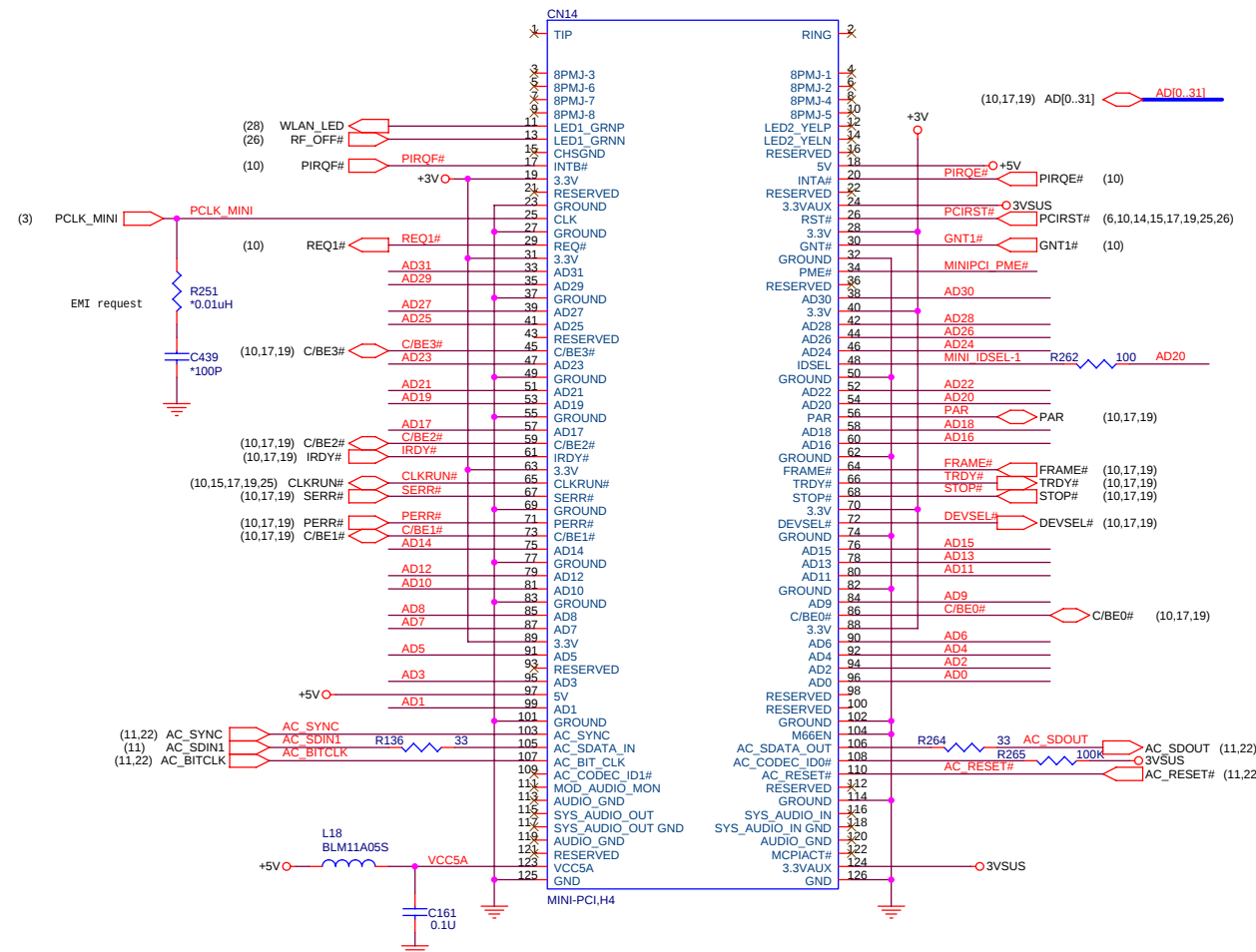


CD-ROM connector

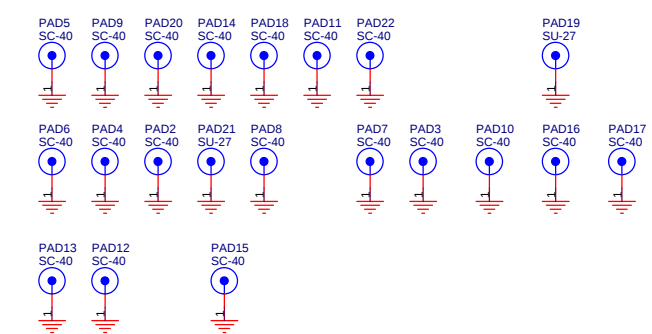
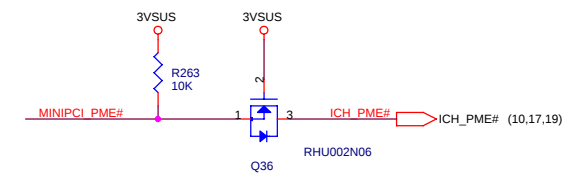
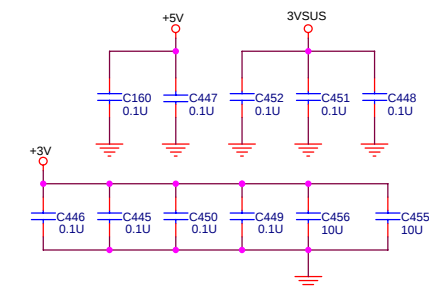


6/15 Modify connector to GND





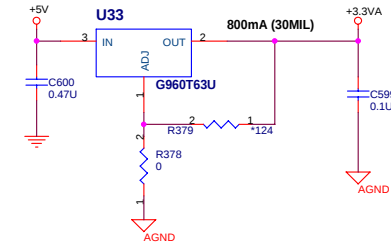
ID Select : AD20
Interrupt Pin : PIRQE# , PIRQF#
Request indicates : REQ1#
Grant indicates : GNT1#



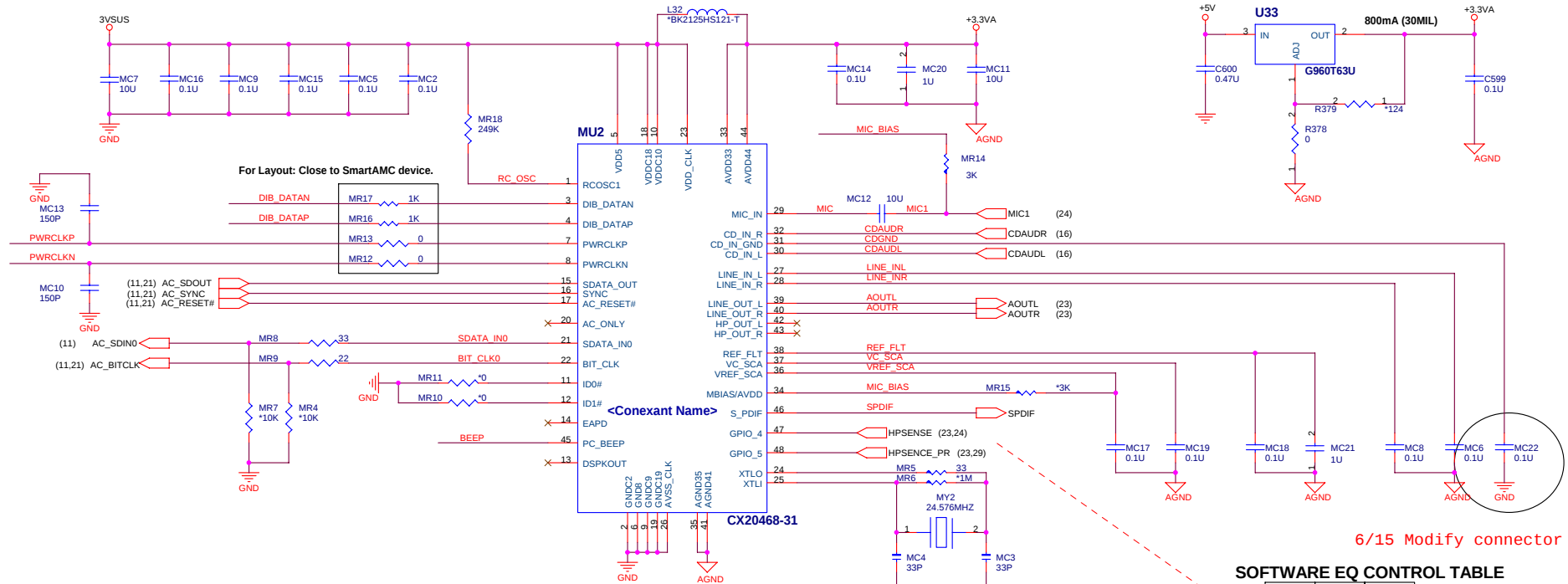
For Layout:
Place decoupling caps near the power pins of SmartAMC device.

15:5:15 routing , 50 mil
to other signal

CODEC POWER



For Layout: Close to SmartAMC device.

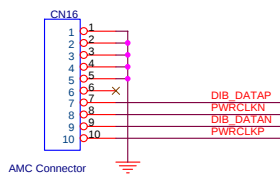


6/15 Modify connector to AGND

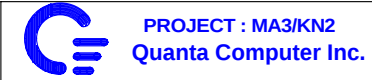
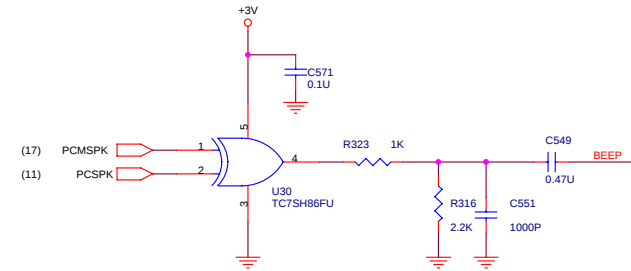
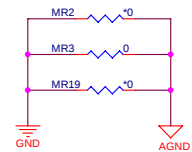
SOFTWARE EQ CONTROL TABLE

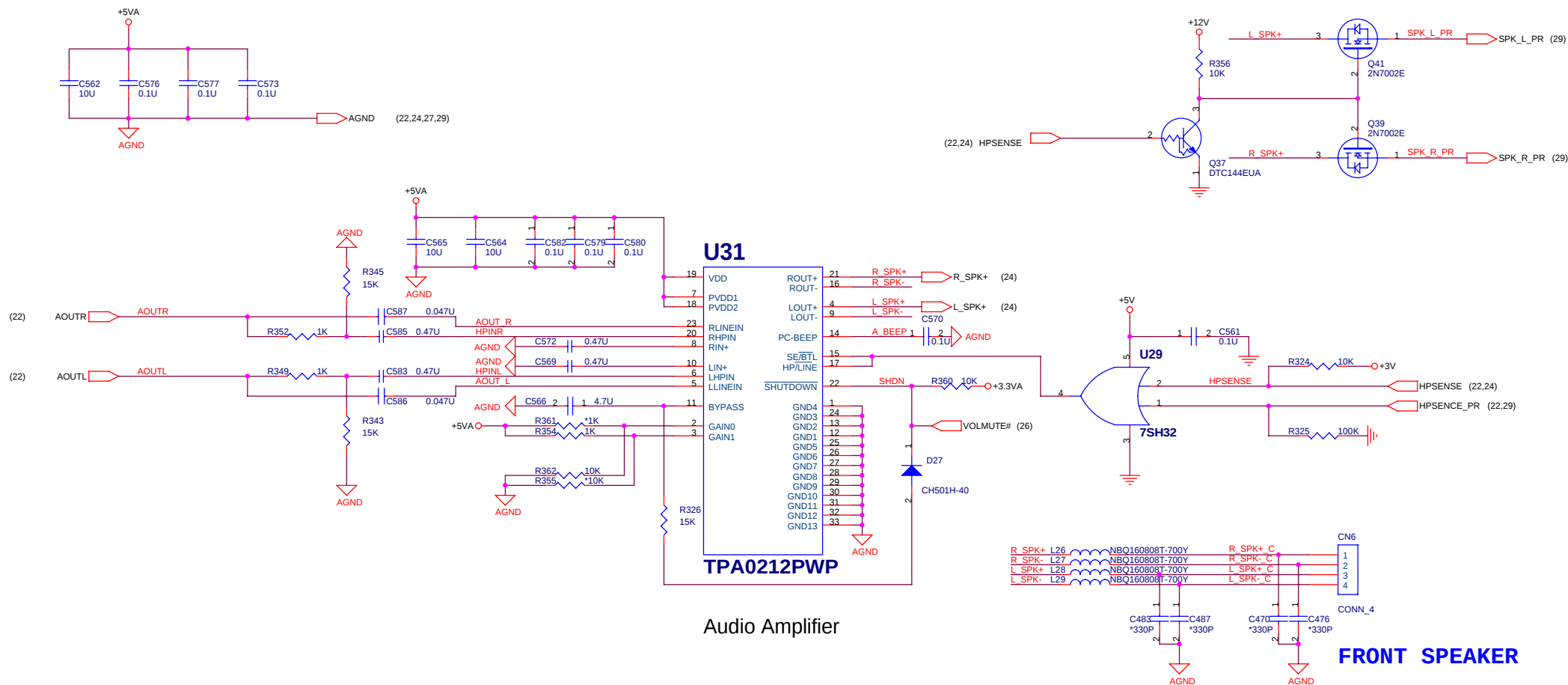
GPIO4	GOIO5	SW EQ
LOW	LOW	ON
LOW	HIGH	OFF
HIGH	LOW	OFF
HIGH	HIGH	OFF

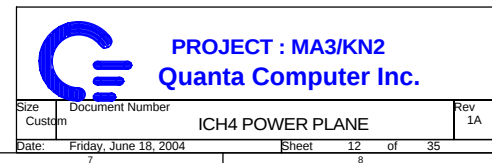
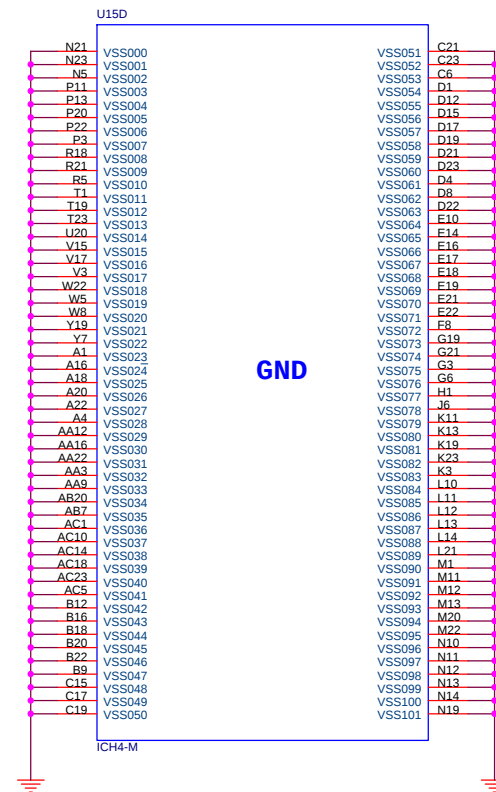
For Layout:
Place crystal and associated
circuitry very near
SmartAMC Device.



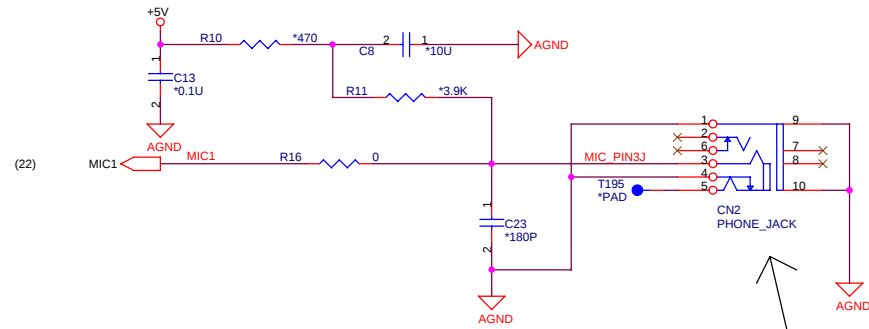
Ground Tie



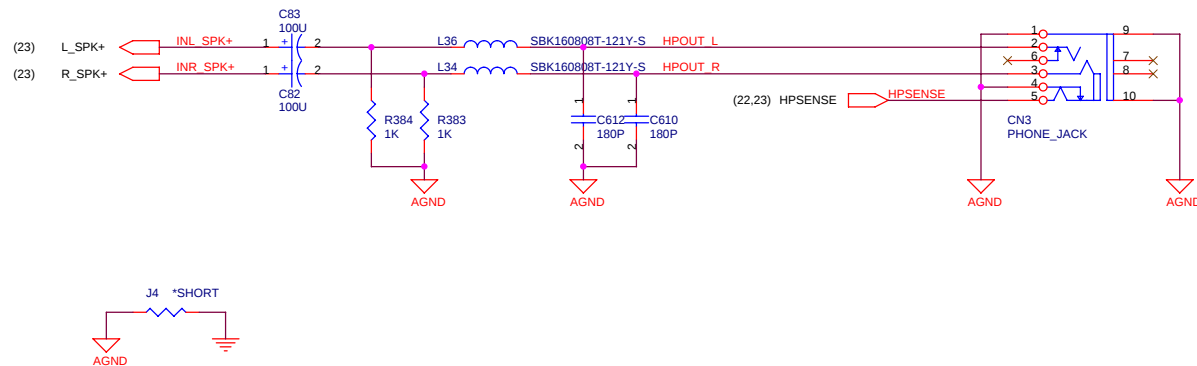




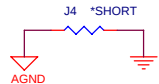
EXTERNAL MIC



HEAD PHONE



JA6333L -1S0-UA1



ICH4_M

USB

HUB LINK

LPC&FWH

AC97&RTC

SM

PM

MISC&GPIO

SpeedStep

LAN

USBOC0# R273 10K
USBOC1# R117 10K
USBOC2# R119 10K
USBOC4# R116 10K

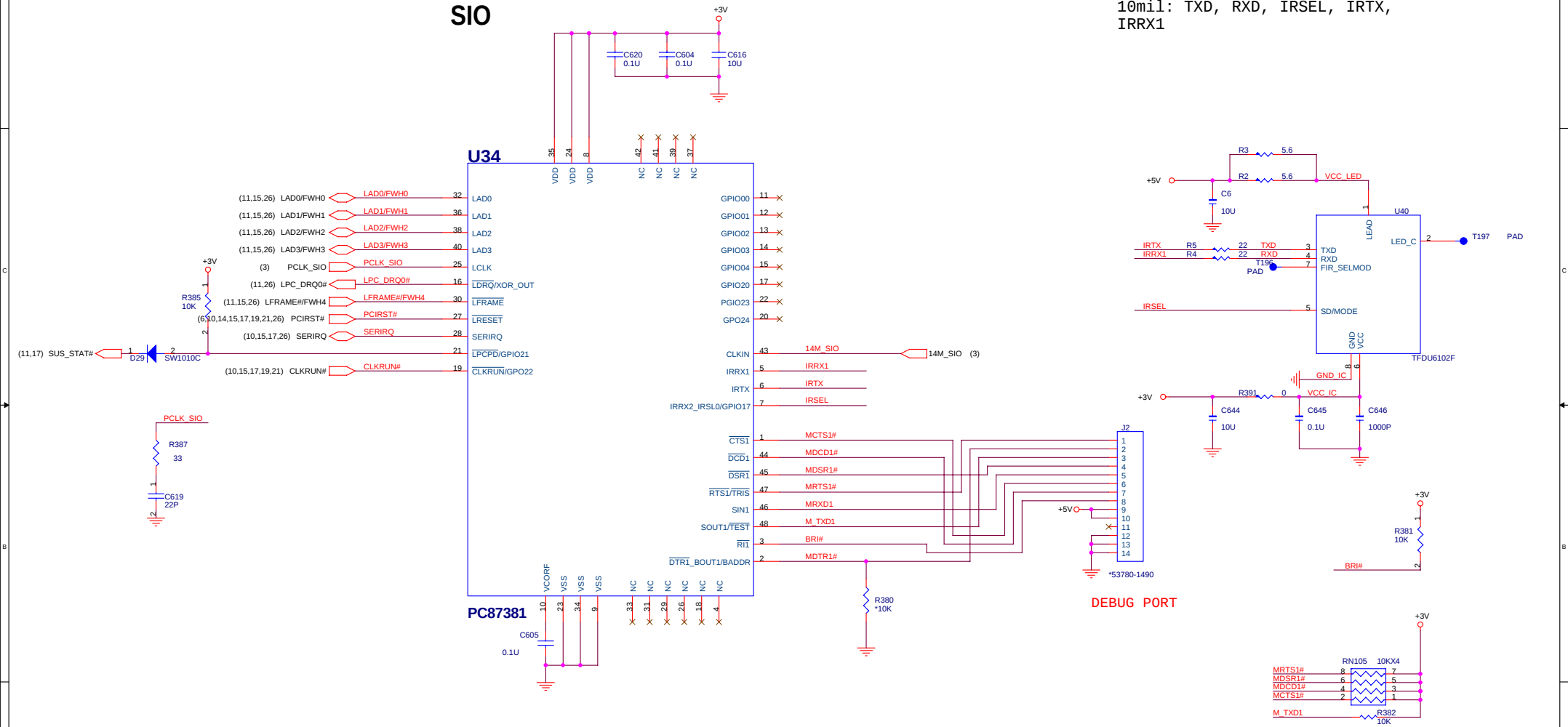
USBOC3# R274 10K
USBOC5# R276 10K

(13) LCD0# FDDSES J20
(15) TV_NTSC_PAL TV_NTSC_PAL G22
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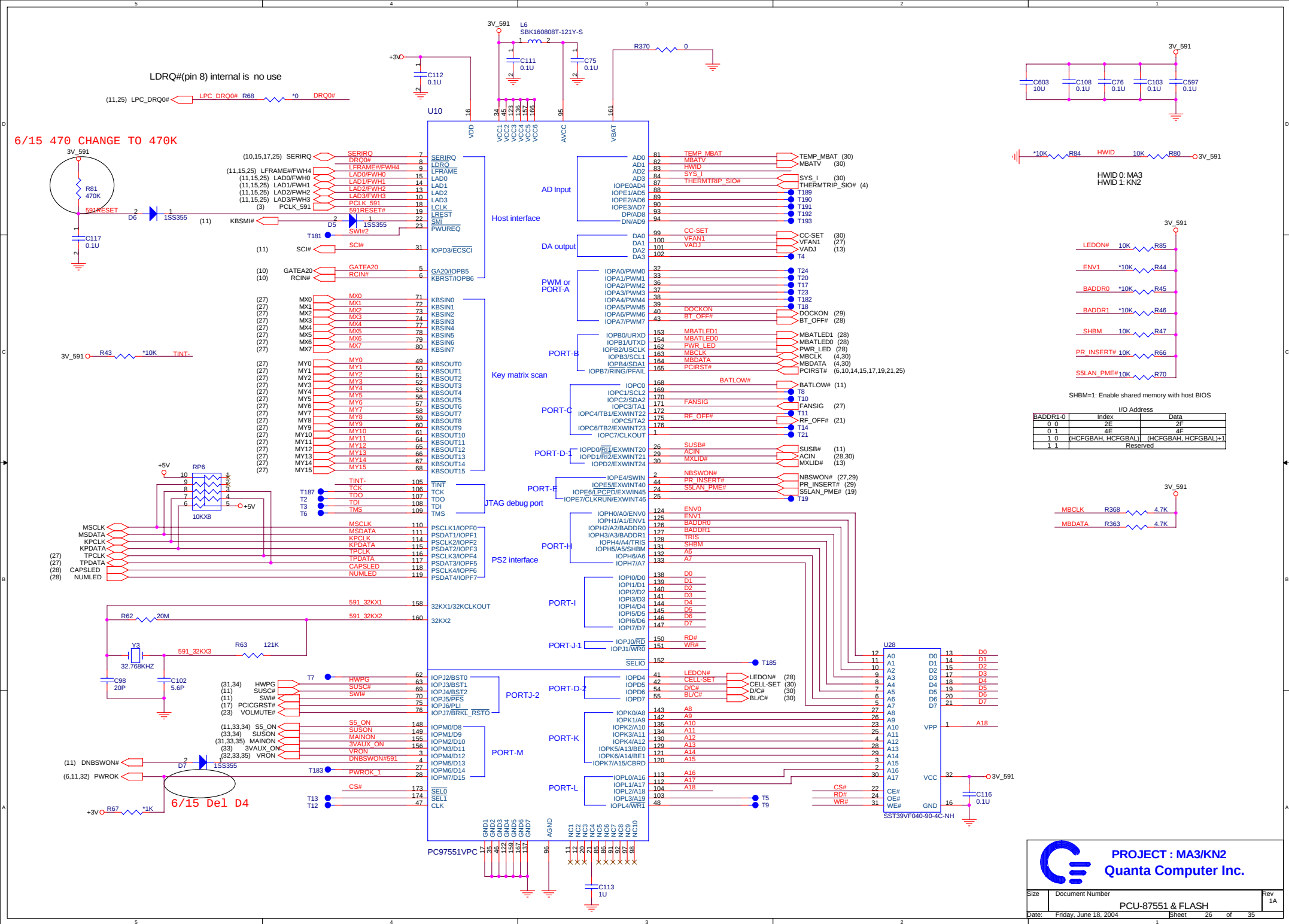
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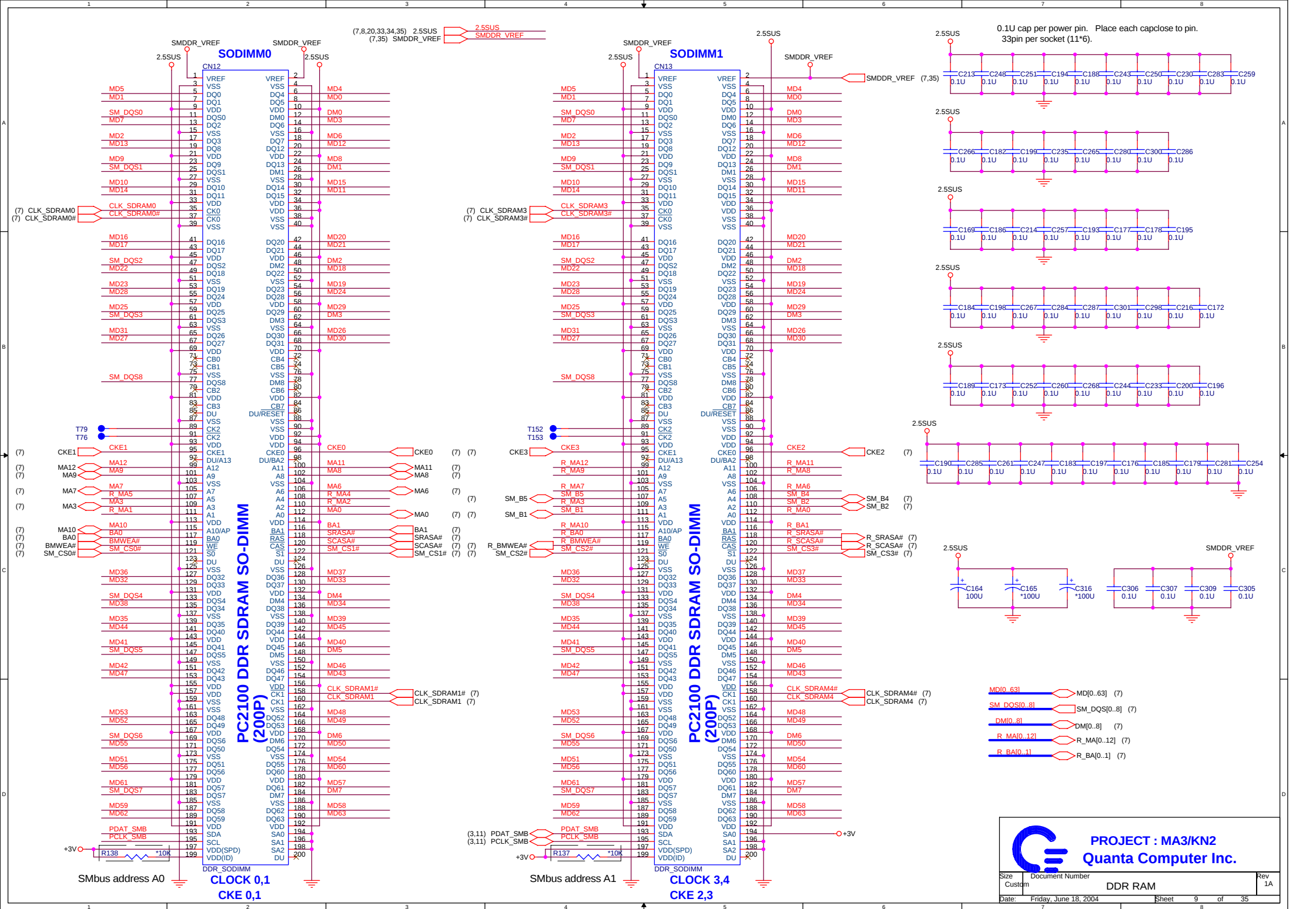
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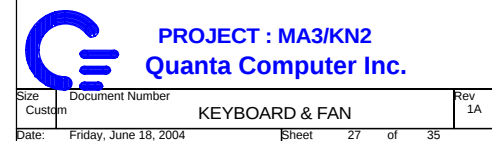
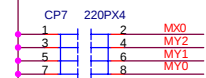
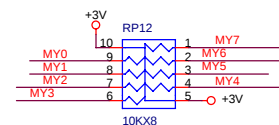


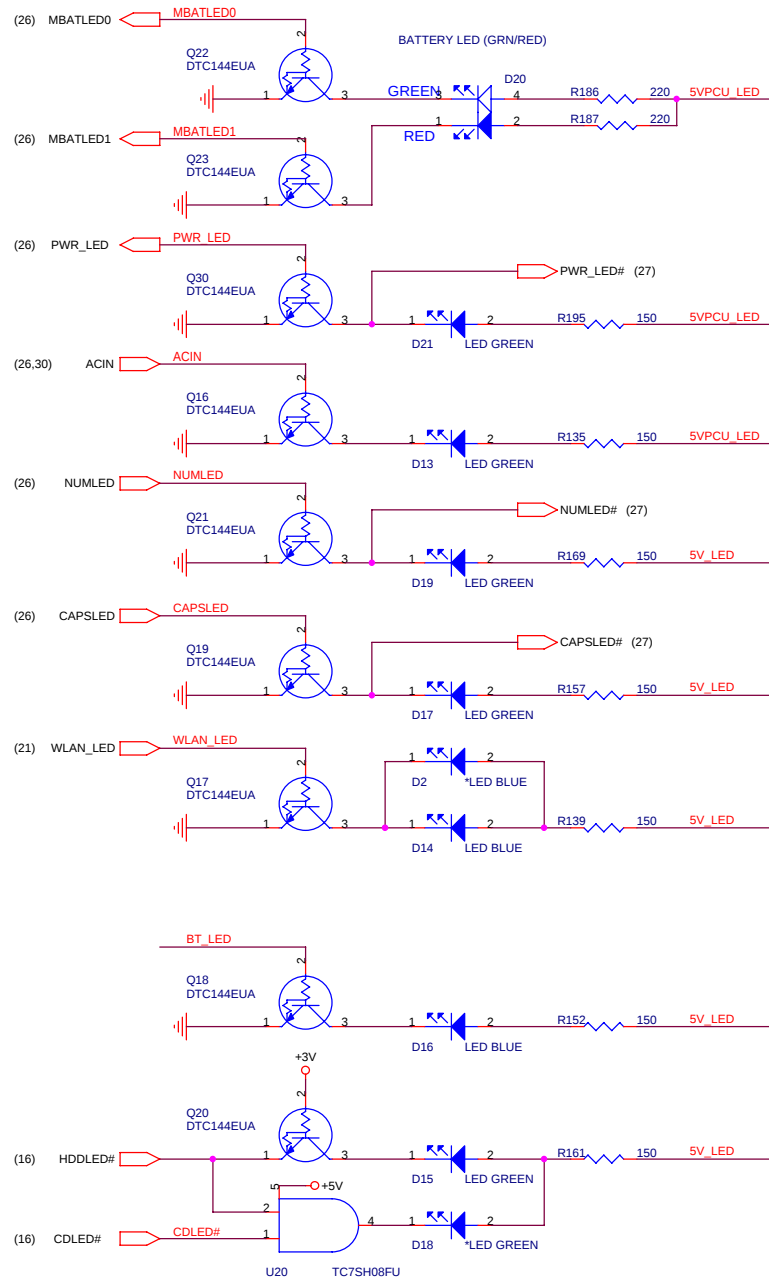
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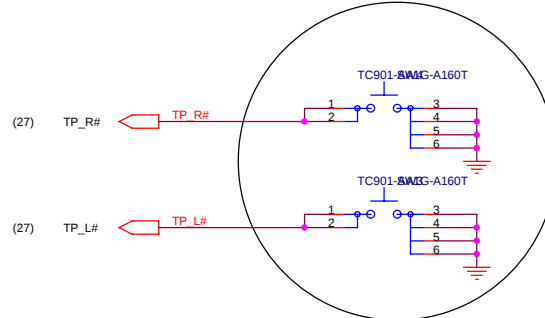
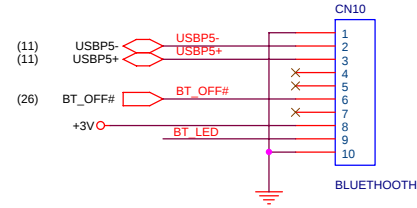






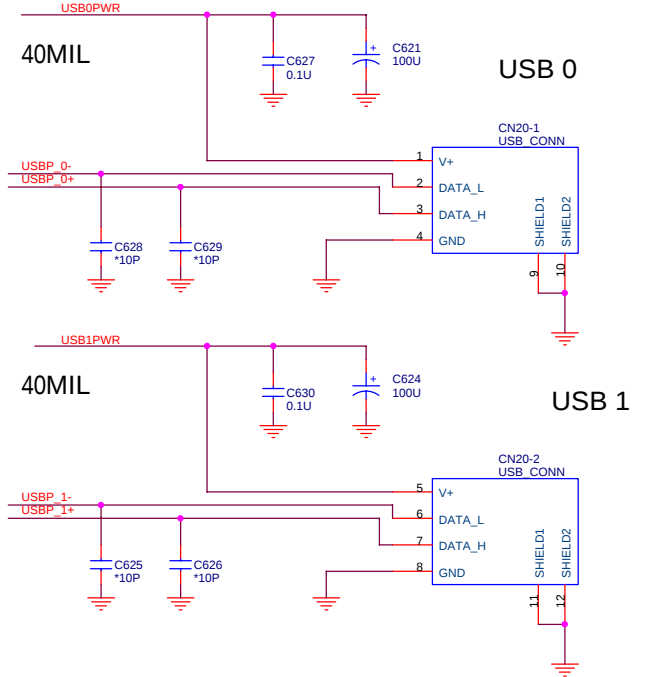
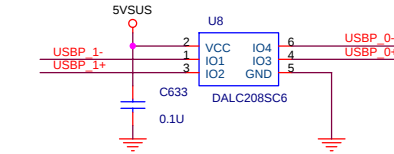
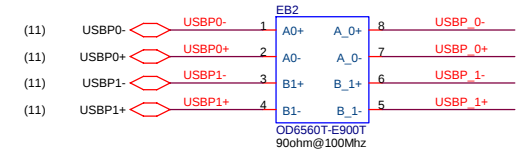
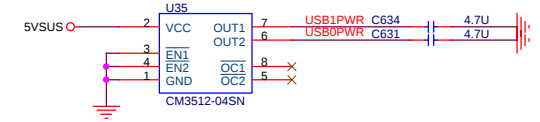
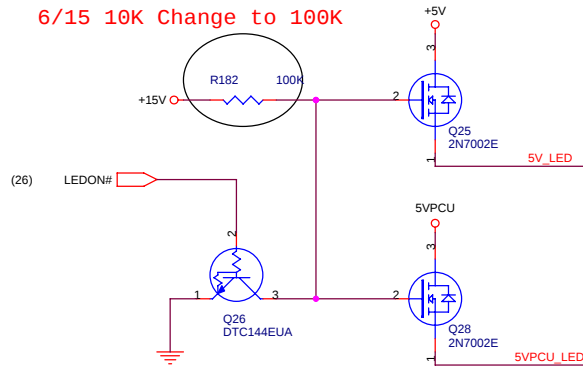
- +5V (12,14,16,21,22,23,24,25,26,27,31,33,35)
- +3V (3,4,6,8,9,10,11,12,13,15,19,21,22,23,25,26,27,29,31,33,35)
- 5VSUS (17,27,29,31,33)

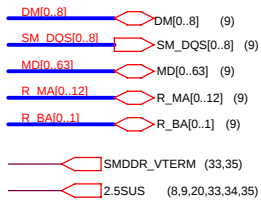
BLUETOOTH CONNECTOR



6/15 Change footprint to SWSTS-055-B-5P

6/15 10K Change to 100K



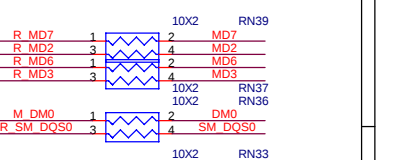
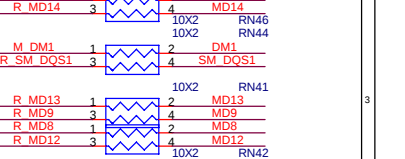
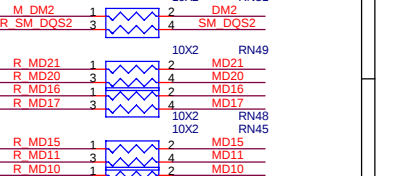
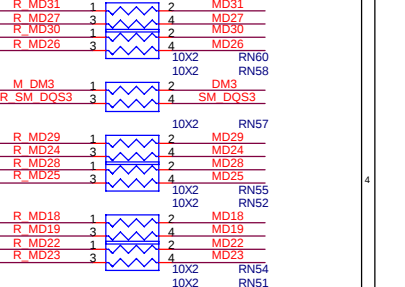
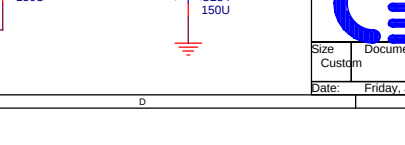
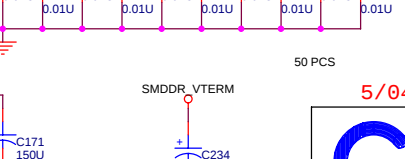
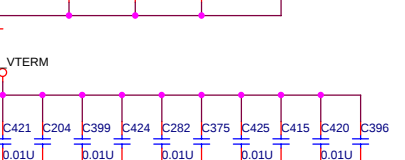
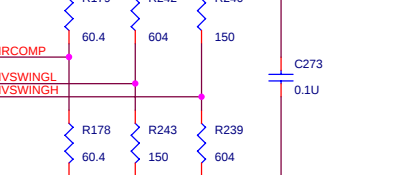
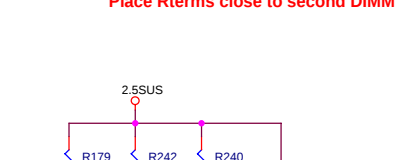
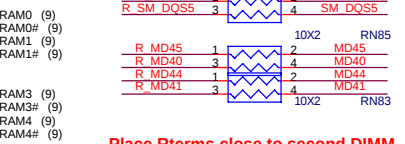
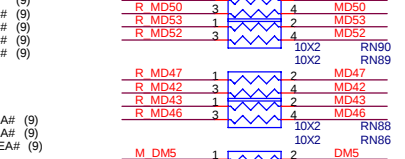
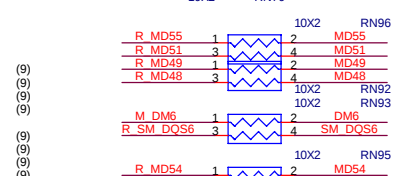
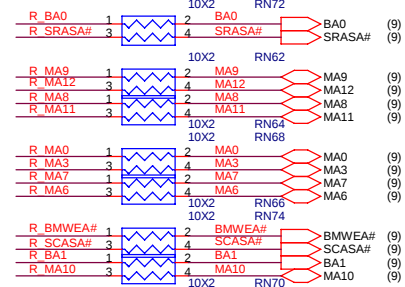
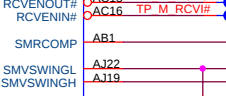
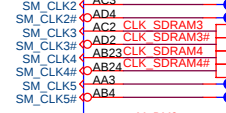
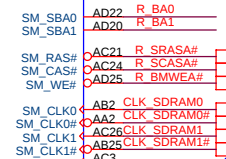
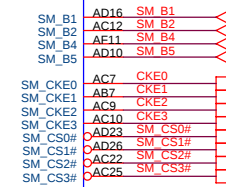
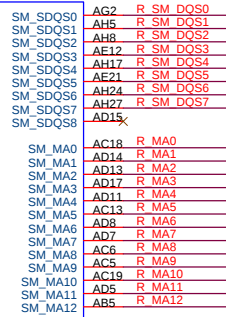
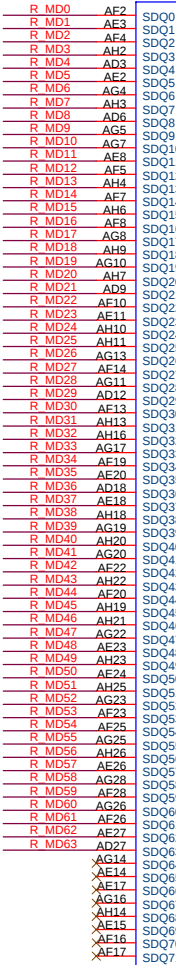


USE 8P4R-0402 package

5/04 Change to 4P2R package type

DDR SYSTEM MEMORY

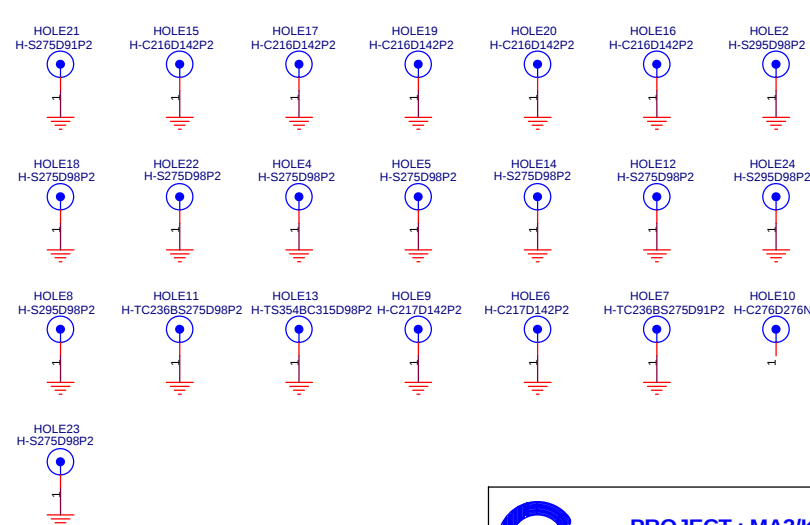
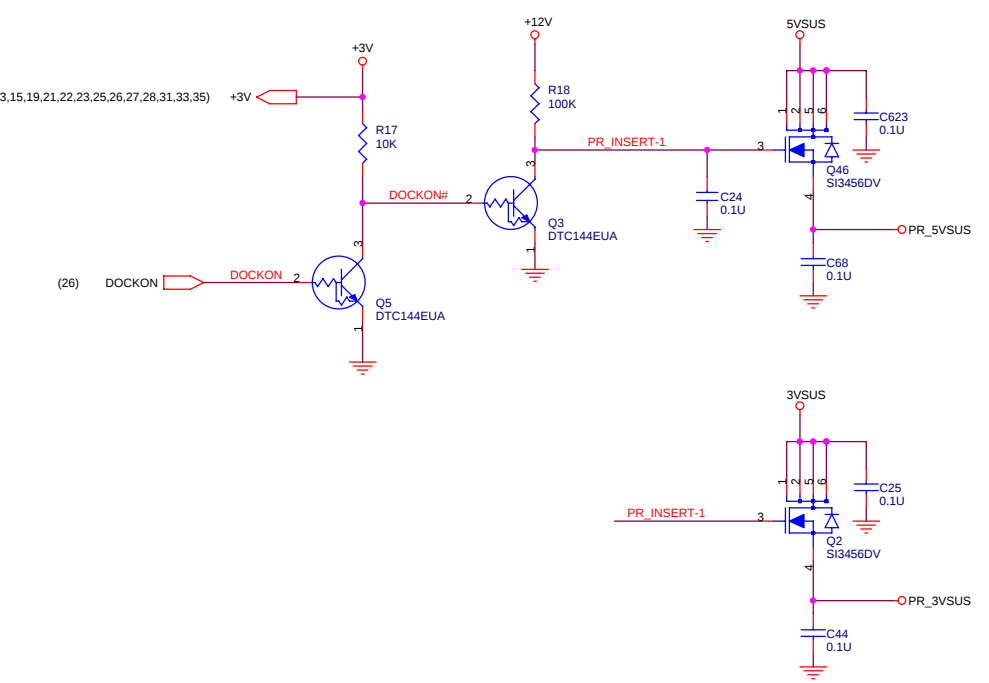
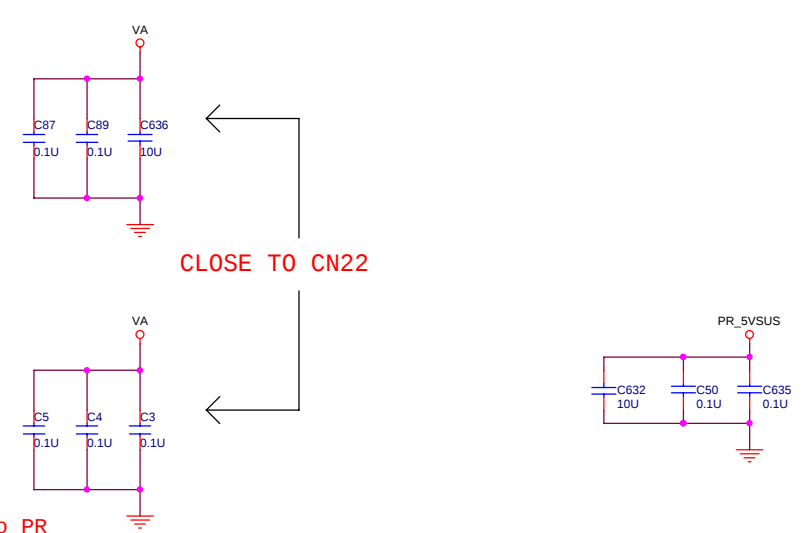
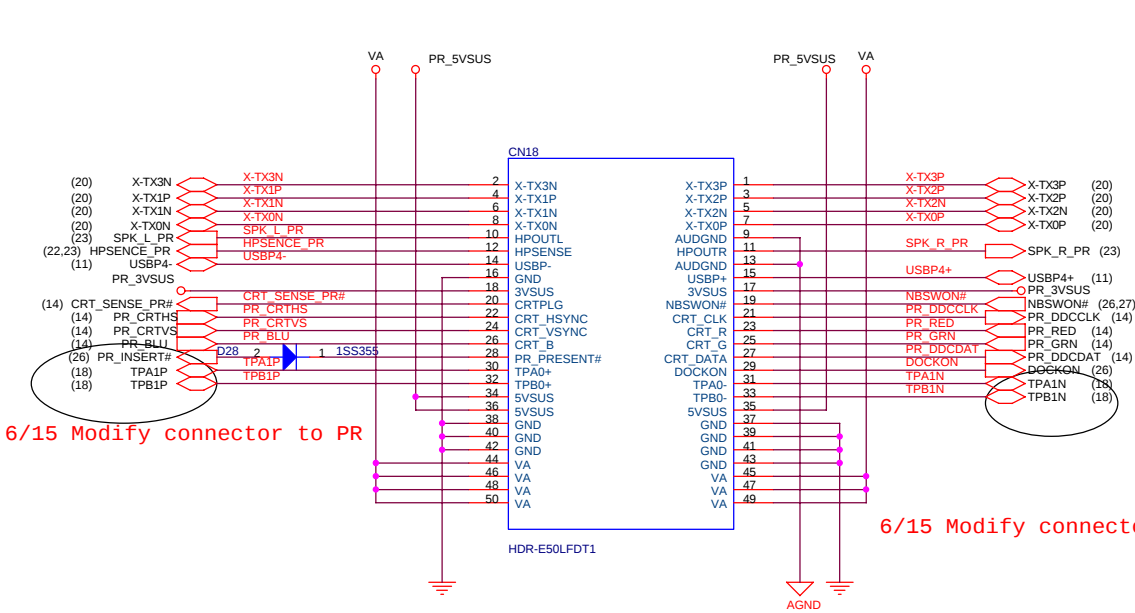
U24B

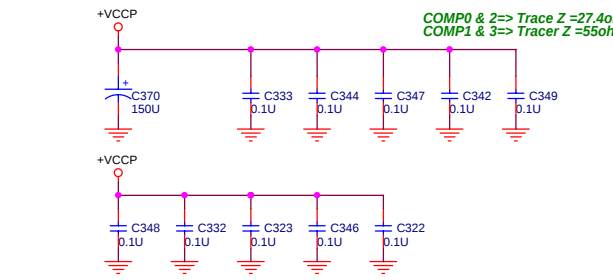


5/04 Change to 4P2R package type



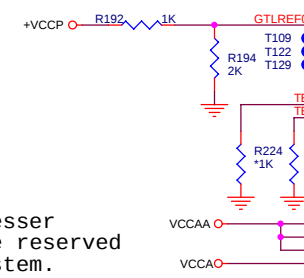
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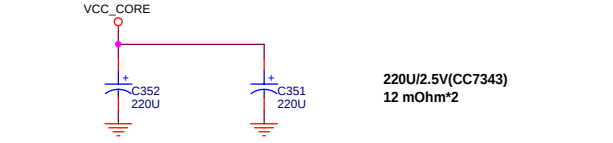
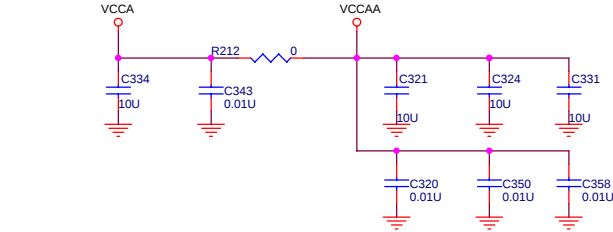


COMP0 & 2=> Trace Z =27.4ohm
COMP1 & 3=> Tracer Z =55ohm

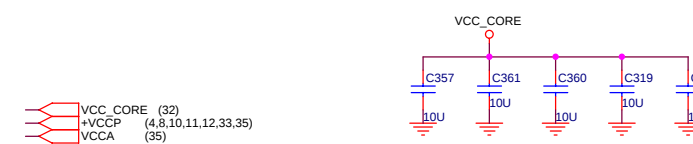
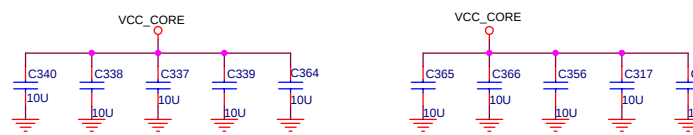
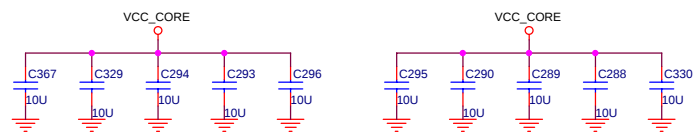
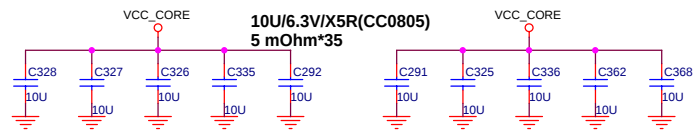
WITHIN 0.5" & SPACE 50 mil



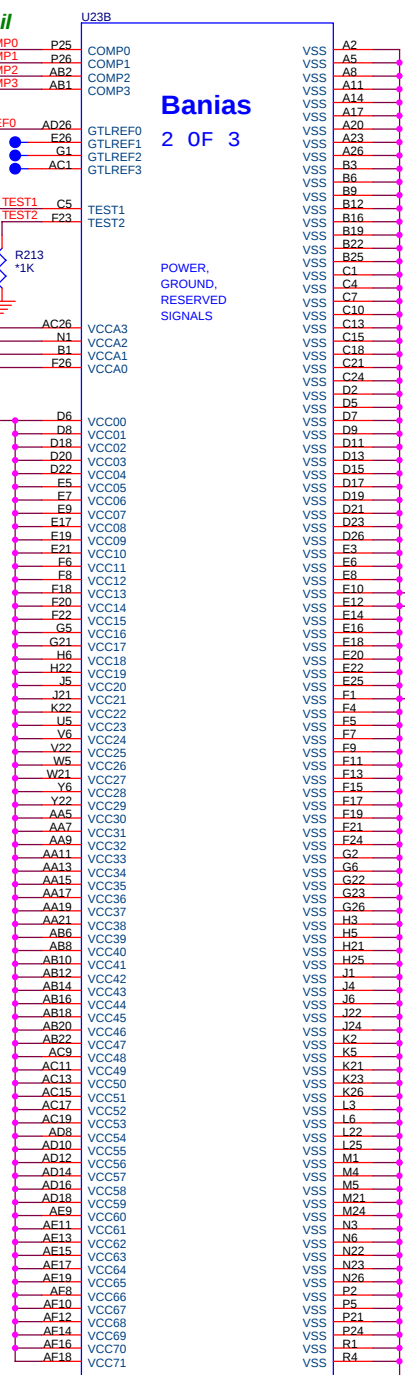
For Dothan processor
VCCA[3:1] can be reserved
as NC on one system.



220U/2.5V(CC7343)
12 mOhm*2



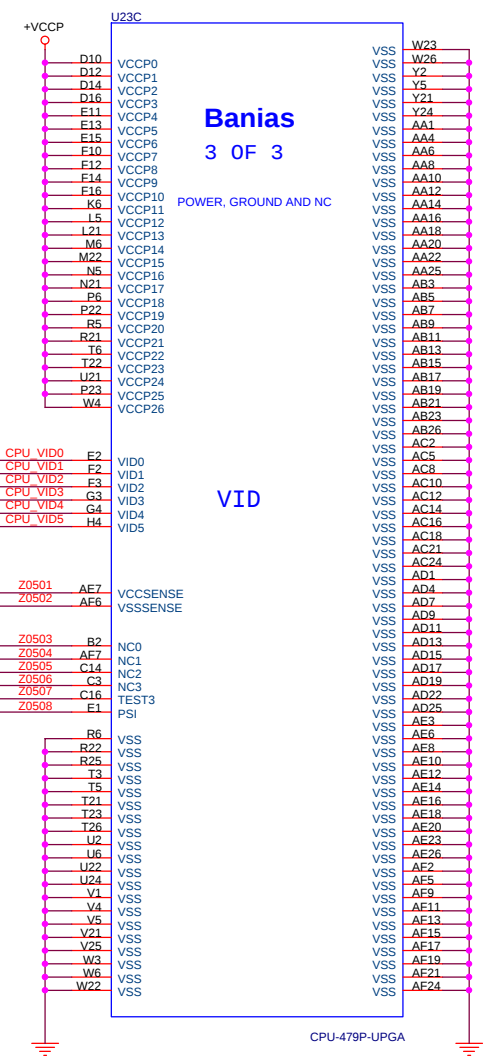
VCC_CORE (32)
+VCCP (4,8,10,11,12,33,35)
VCCA (35)



Banias
2 OF 3

POWER,
GROUND,
RESERVED
SIGNALS

CPU-479P-UPGA



Banias
3 OF 3

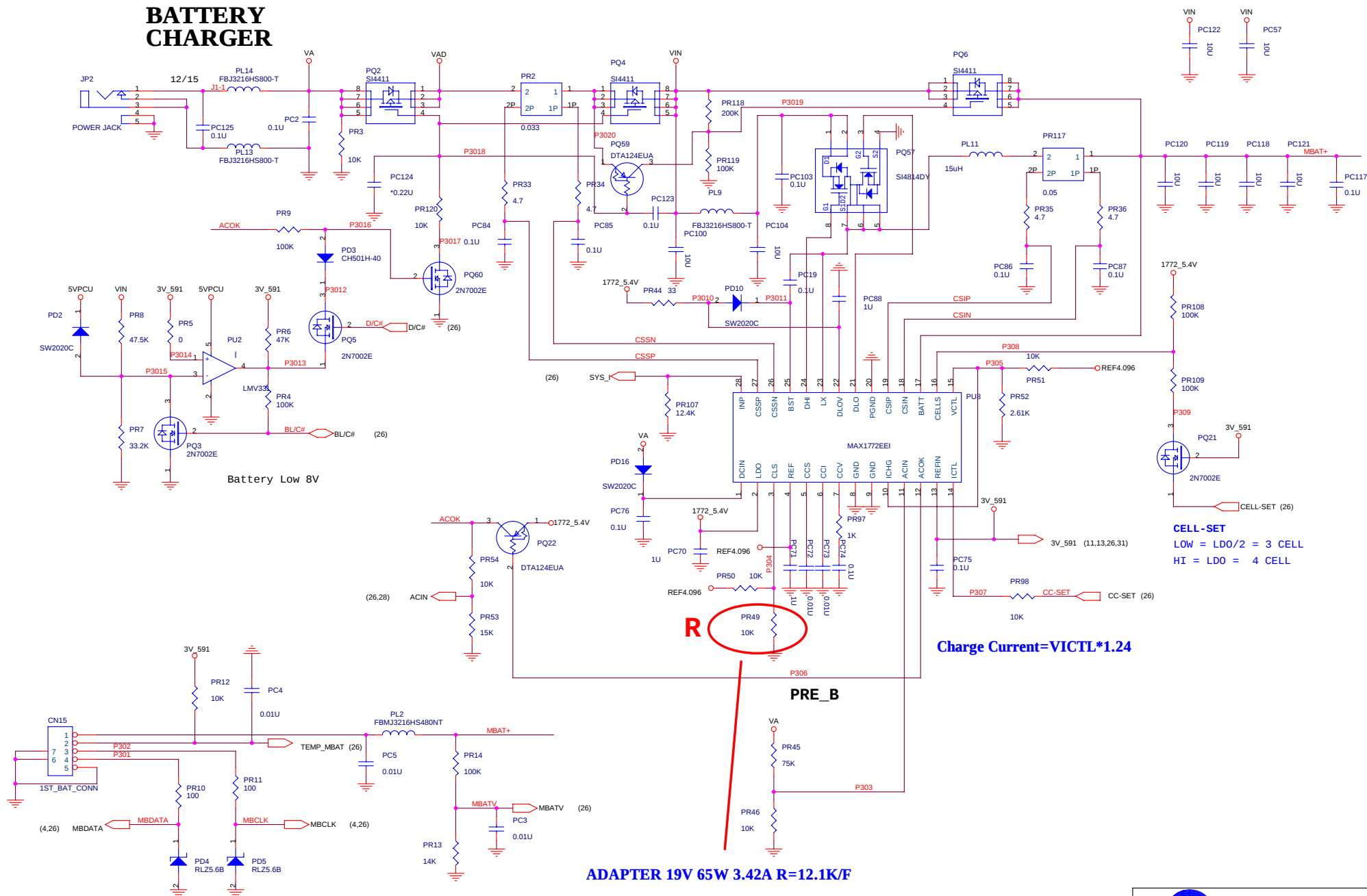
POWER, GROUND AND NC

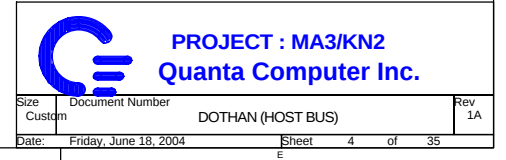
VID

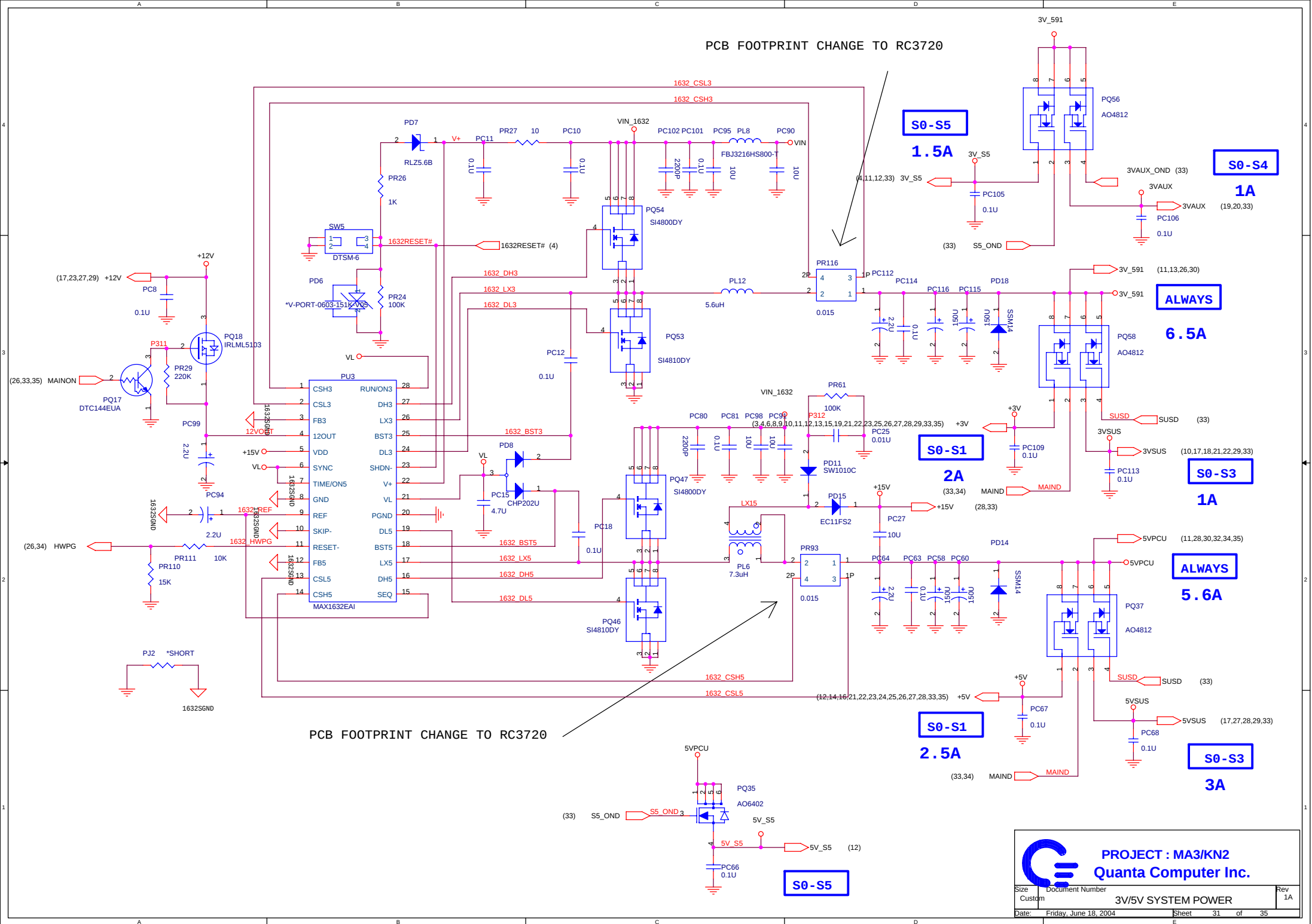
CPU-479P-UPGA

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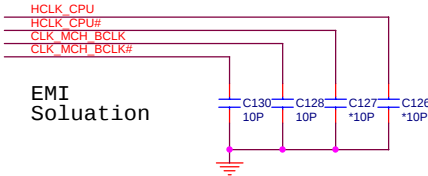
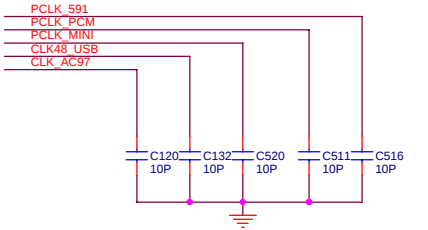
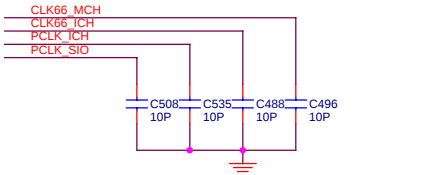
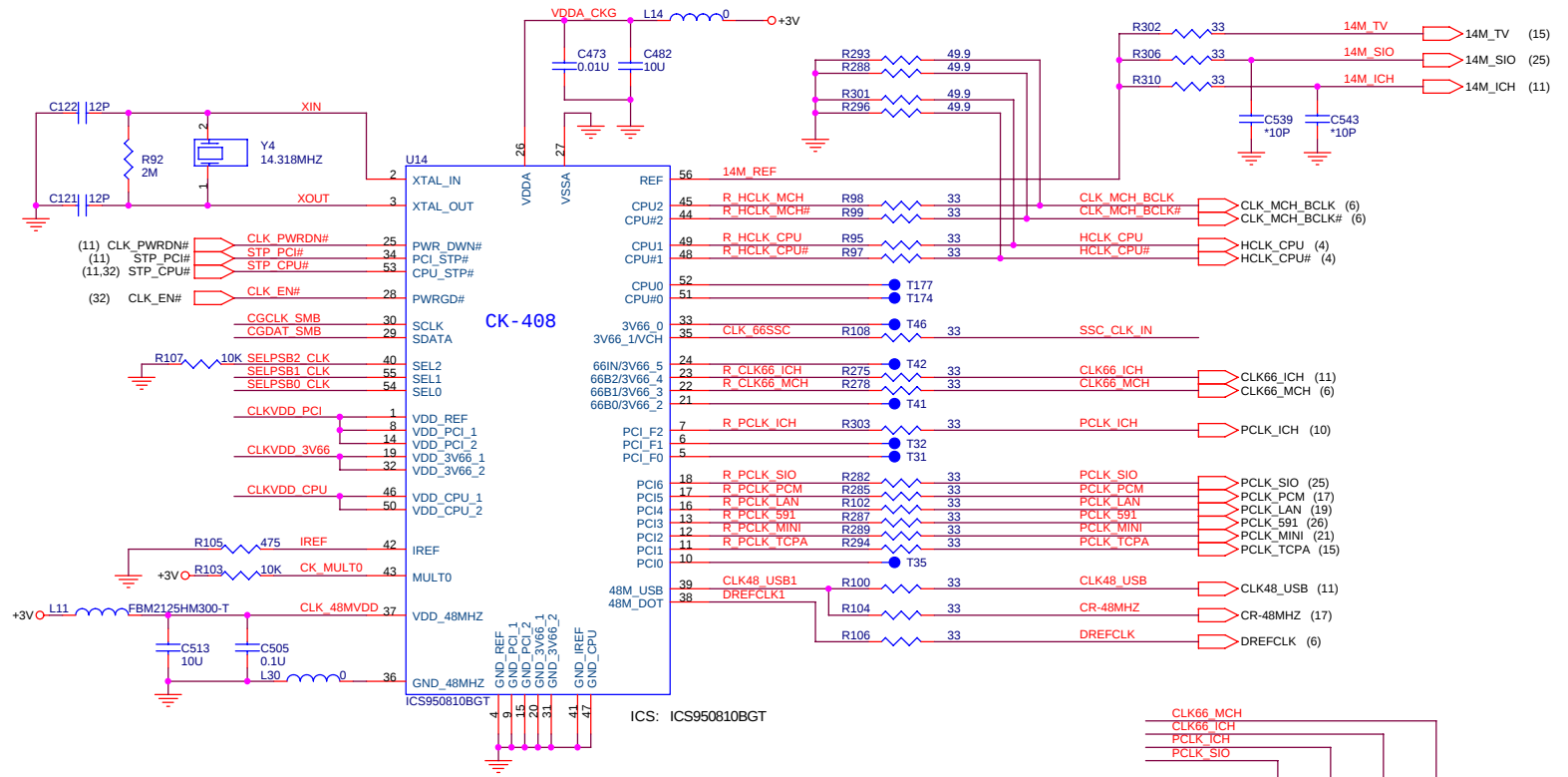
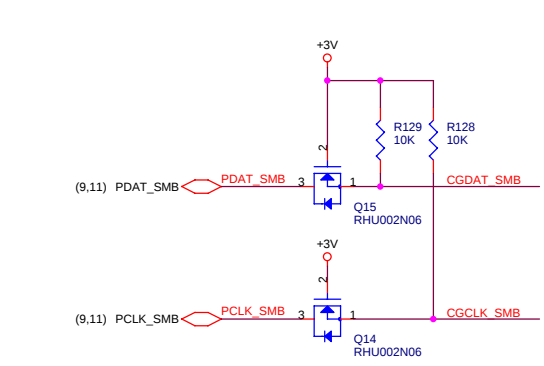
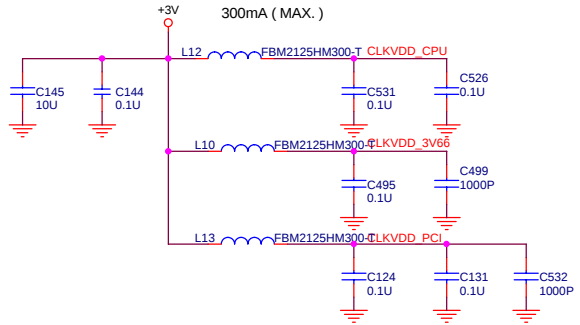
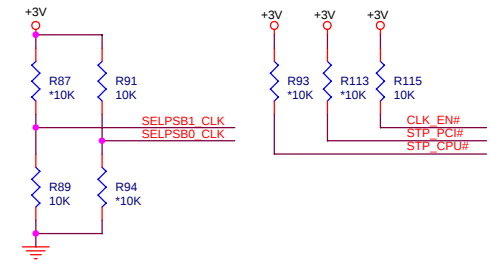
BATTERY CHARGER







S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input



PROJECT : MA3/KN2
Quanta Computer Inc.

