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HSF Property: ROHS or Halogen-Free(5L3?)

LEPUS

SODIMM_EDP

2011.10.13

LEPUS MB P/N: 6050A2501101

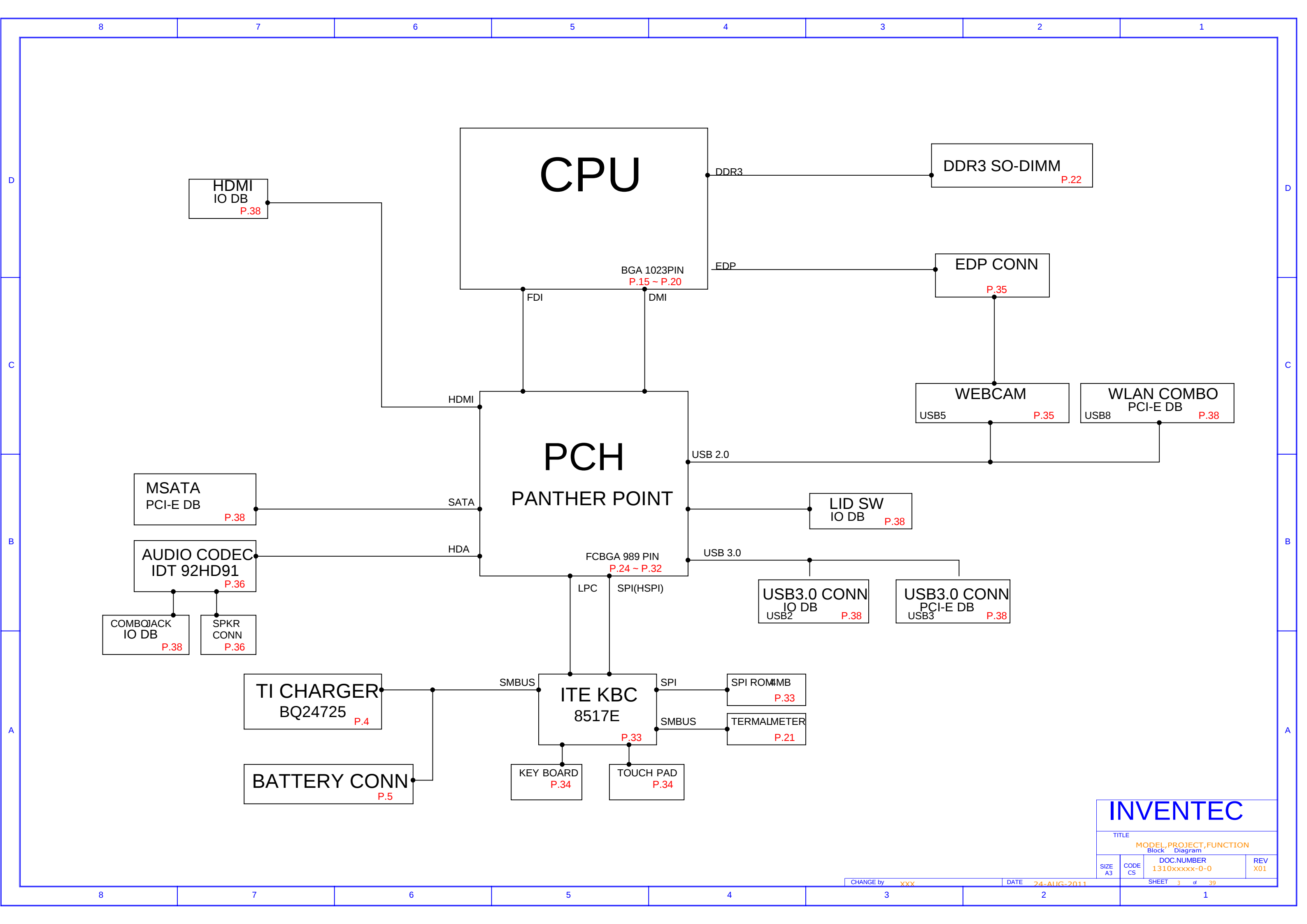
STAND OFF: CPU S4501 , S4502, S4503

STAND OFF: 6052B0205101

24-AUG-2011		
DATE	CHANGE NO.	REV

		EE	DATE	POWER	DATE	INVENTEC			
DRAWER						TITLE MODEL,PROJECT,FUNCTION LEPUS			
DESIGN									
CHECK						SIZE=			
RESPONSIBLE									
SIZE=					VER:	SIZE C	CODE CS	DOC.NUMBER	REV
FILE NAME:								1310xxxxx-0-0	X01
PIN	XXX								
					SHEET 1 of 39				

8		7		6		5		4		3		2		1	
Index															
01		Project Name										26		PCH-3	
02		Page Index										27		PCH-4	
03		Block Diagram										28		PCH-5	
04		Charger										29		PCH-6	
05		SELECTOR										30		PCH-7	
06		P3V3, P5V0										31		PCH-8	
07		P1V5										32		PCH-9	
08		P1V05S_VCCP										33		EC	
09		P1V8S										34		KB & TP & USB PWR SW	
10		PVSA										35		EDP	
11		PVCORE-1										36		AUDIO CODEC	
12		PVCORE-2										37		WWAN OPTION	
13		PORT										38		TO DB BTB CONN	
14		P3V3S, P5V0S										39		SCREW	
15		CPU-1													
16		CPU-2													
17		CPU-3													
18		CPU-4													
19		CPU-5													
20		CPU-6													
21		Thermal & Fan													
22		DDR3-1													
23		XDP													
24		PCH-1													
25		PCH-2													



INVENTEC

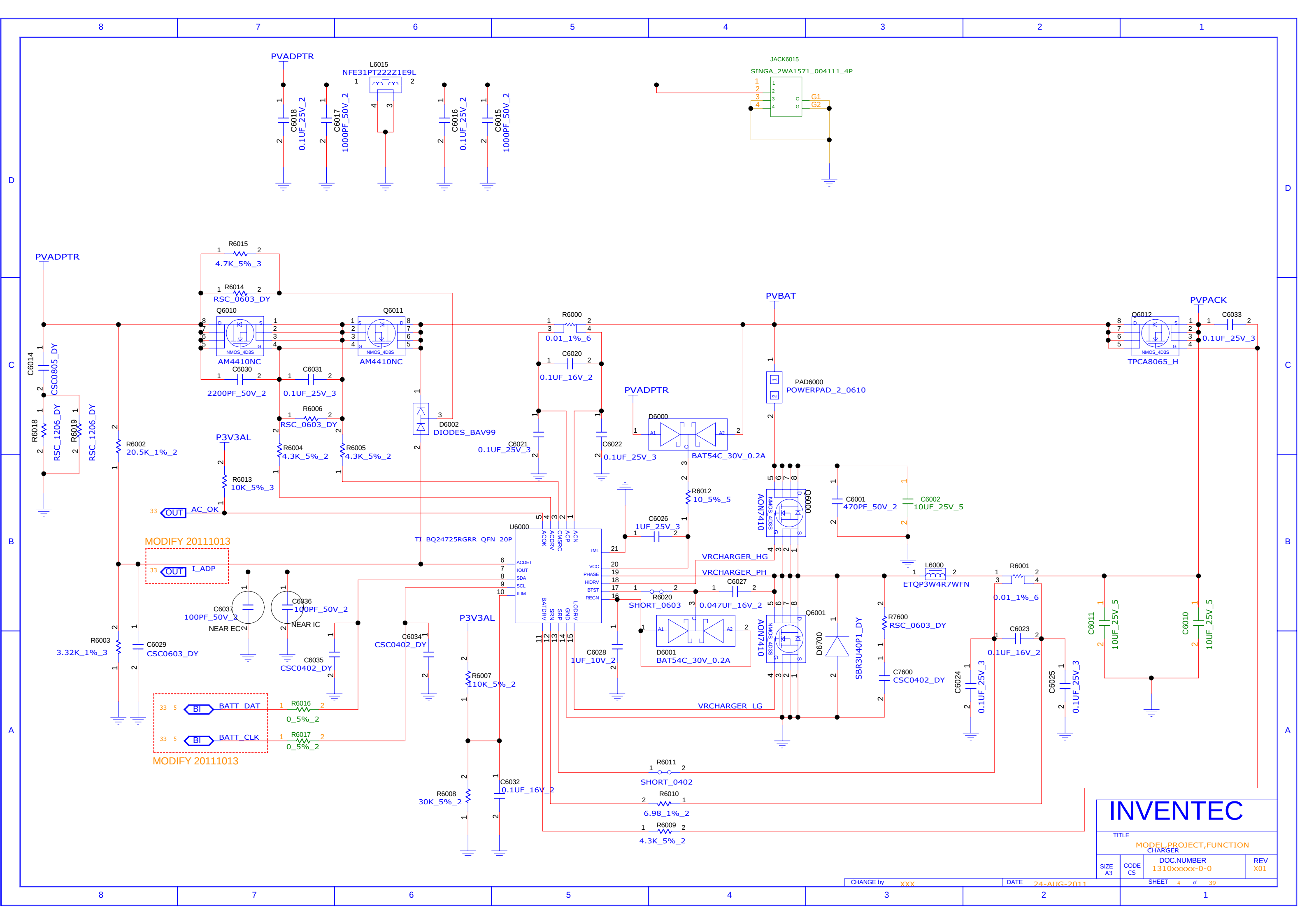
TITLE

MODEL,PROJECT,FUNCTION

SIZE A3 CODE CS DOC.NUMBER 1310xxxxxx-0-0 REV X01

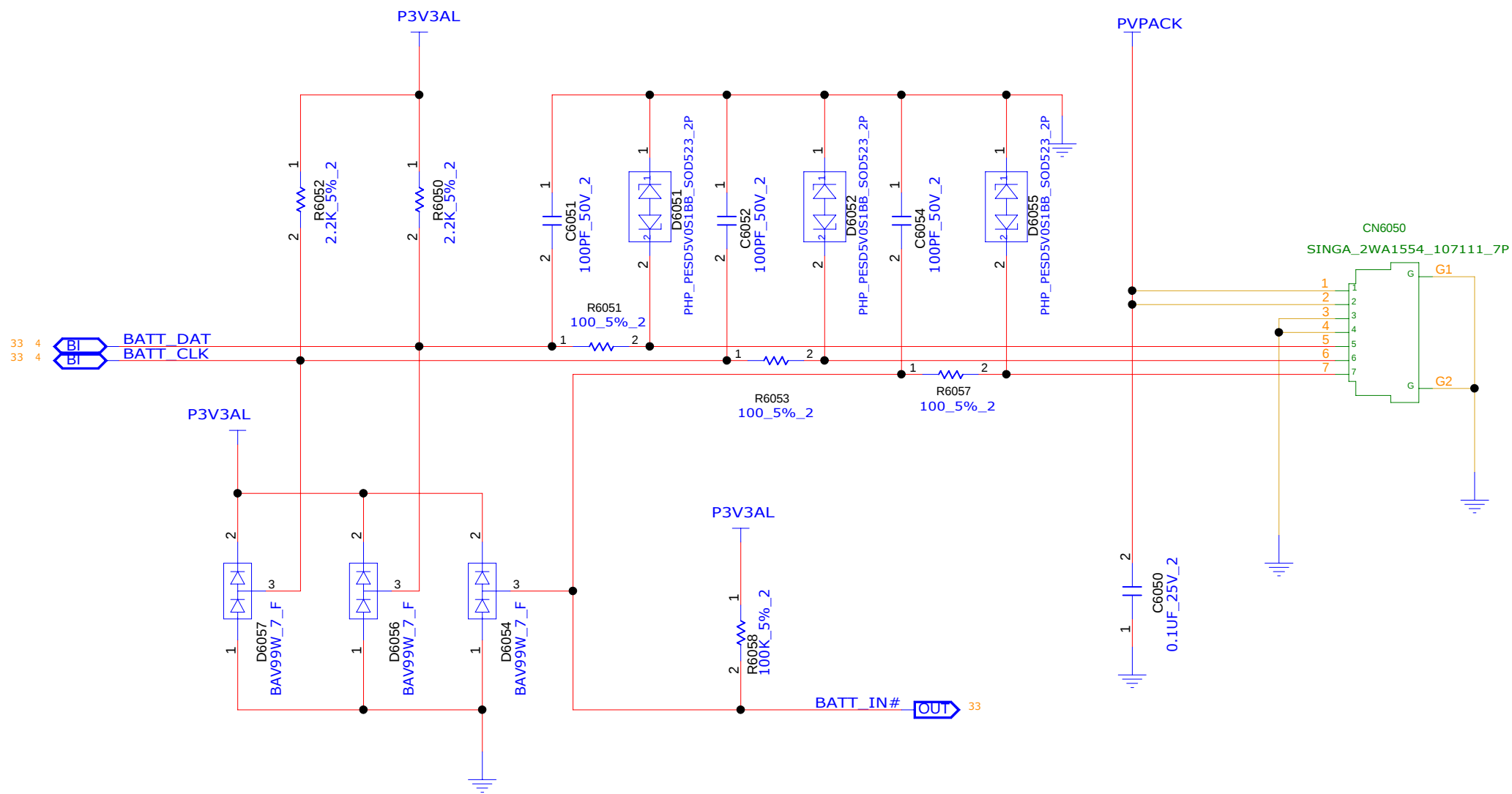
CHANGE by xxx DATE 24-AUG-2011

SHEET 3 of 39



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
CHARGER			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 4 of 39			

CHANGE by XXX DATE 24-AUG-2011



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION
SELECTOR

SIZE
A3

CODE
CS

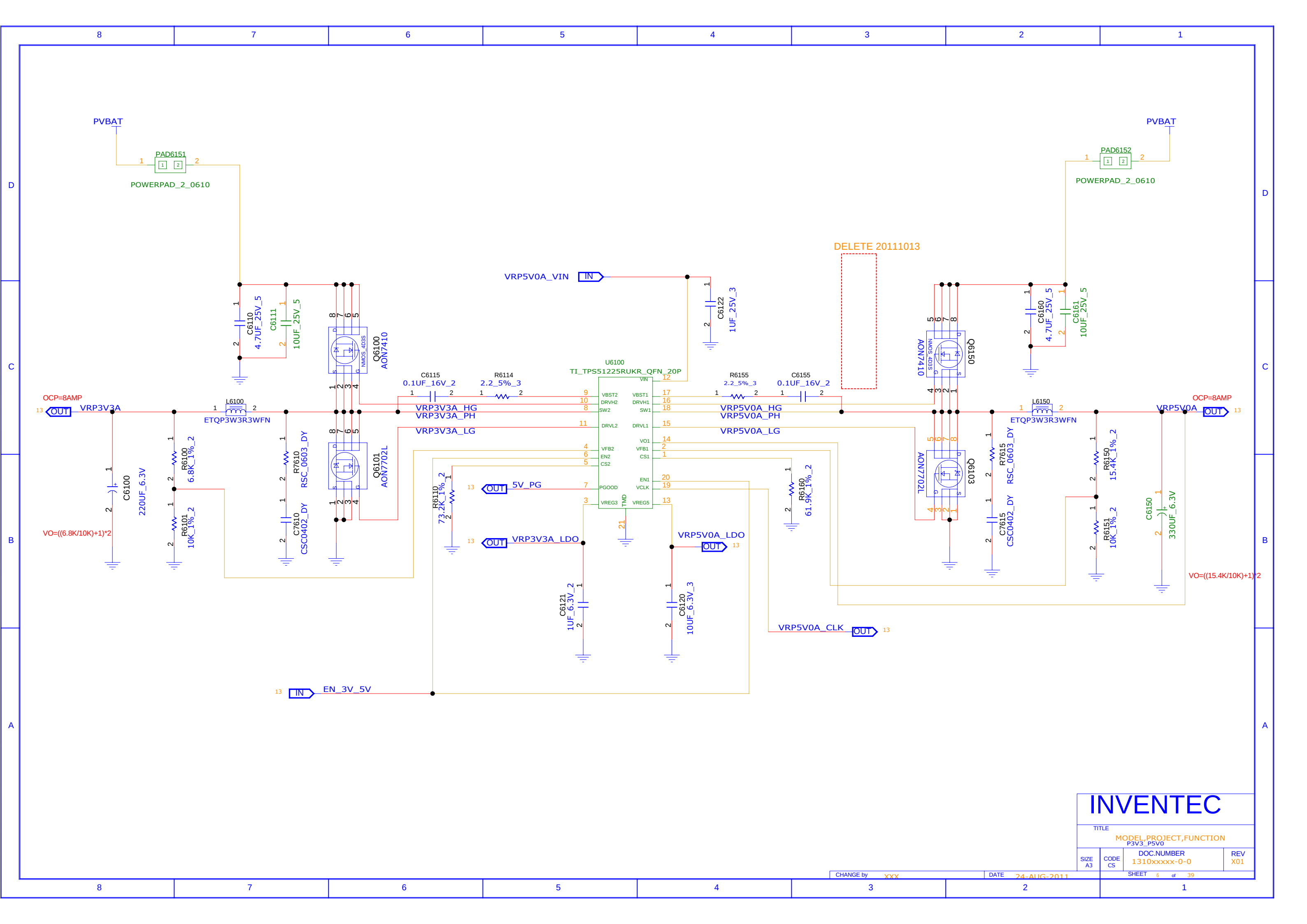
DOC.NUMBER
1310xxxxxx-0-0

REV
X01

CHANGE by XXX

DATE 24-AUG-2011

SHEET 5 of 39



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
P3V3_P5V0

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

SHEET 6 of 39

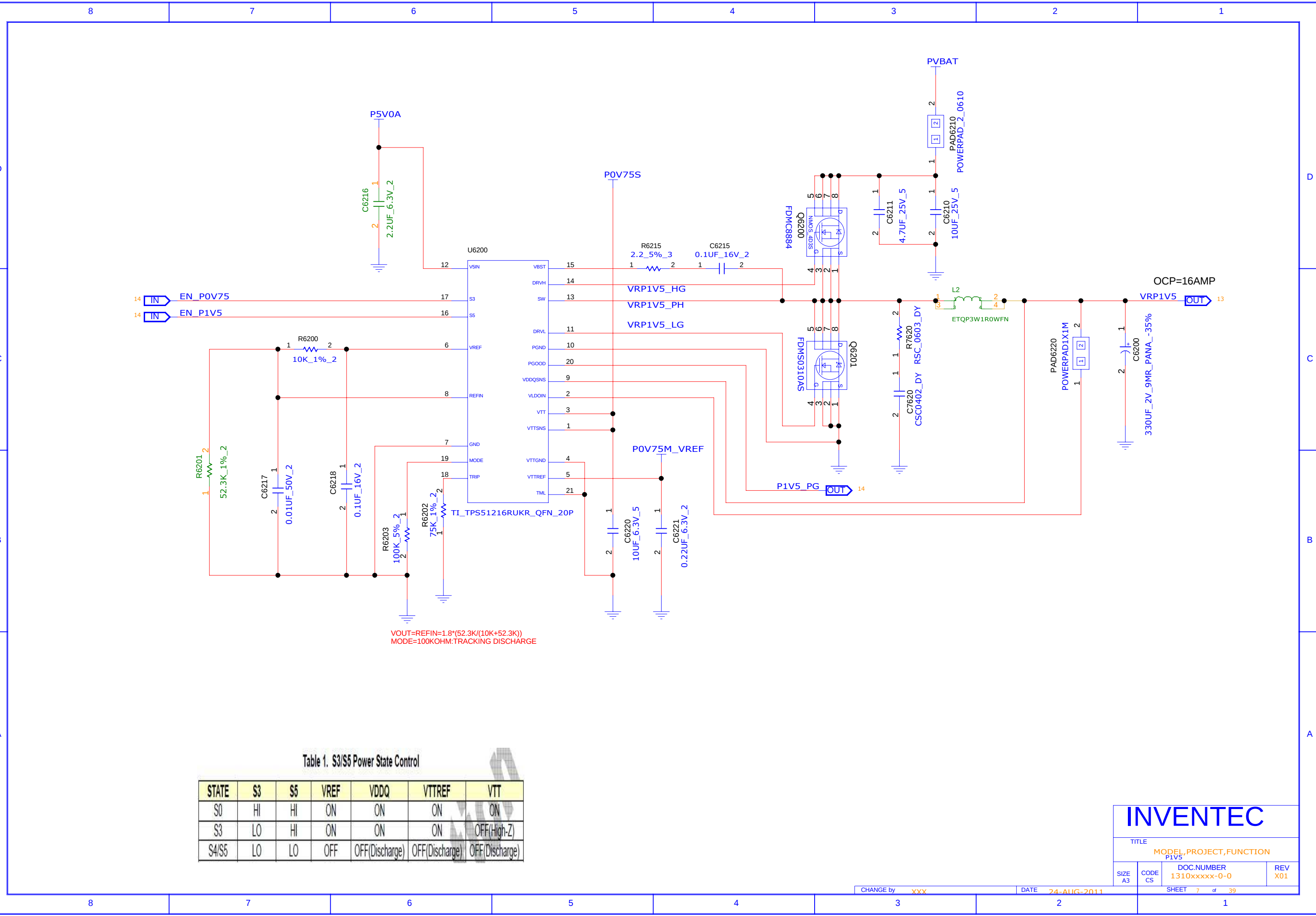


Table 1. S3/S5 Power State Control

STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

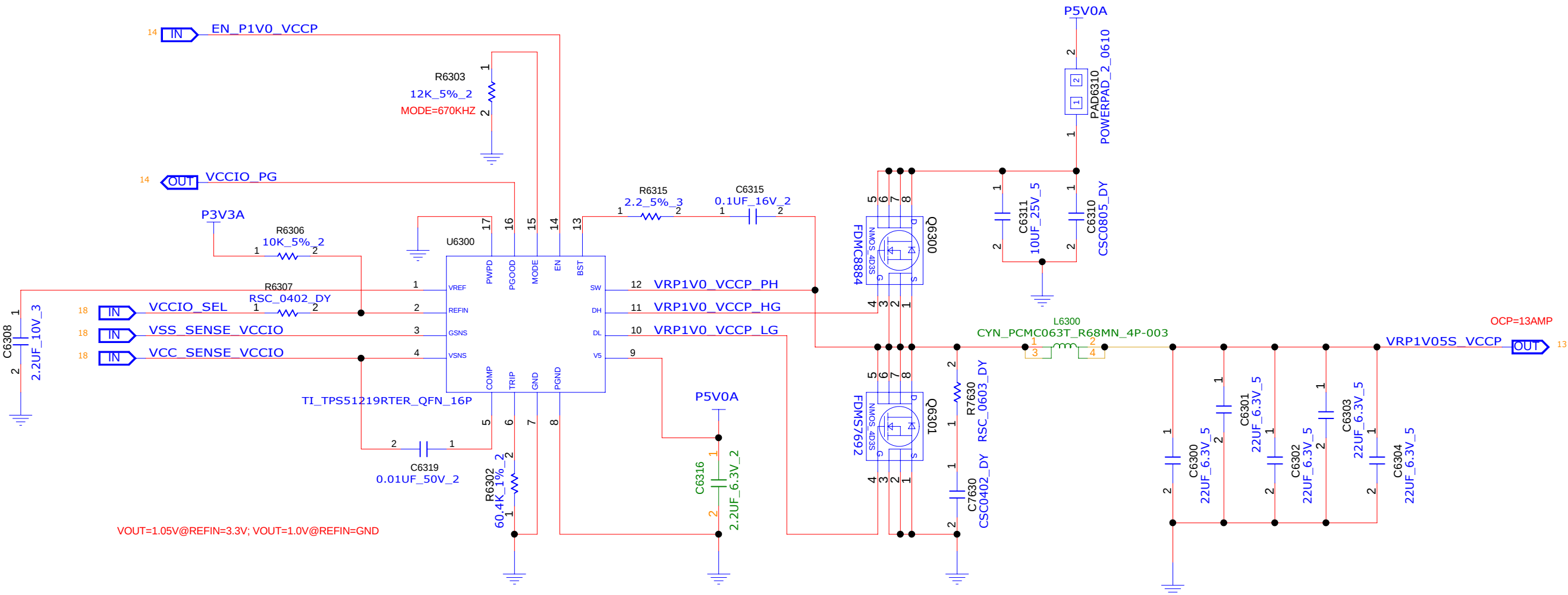
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
P1V5

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

SHEET 7 of 39



VOUT=1.05V@REFIN=3.3V; VOUT=1.0V@REFIN=GND

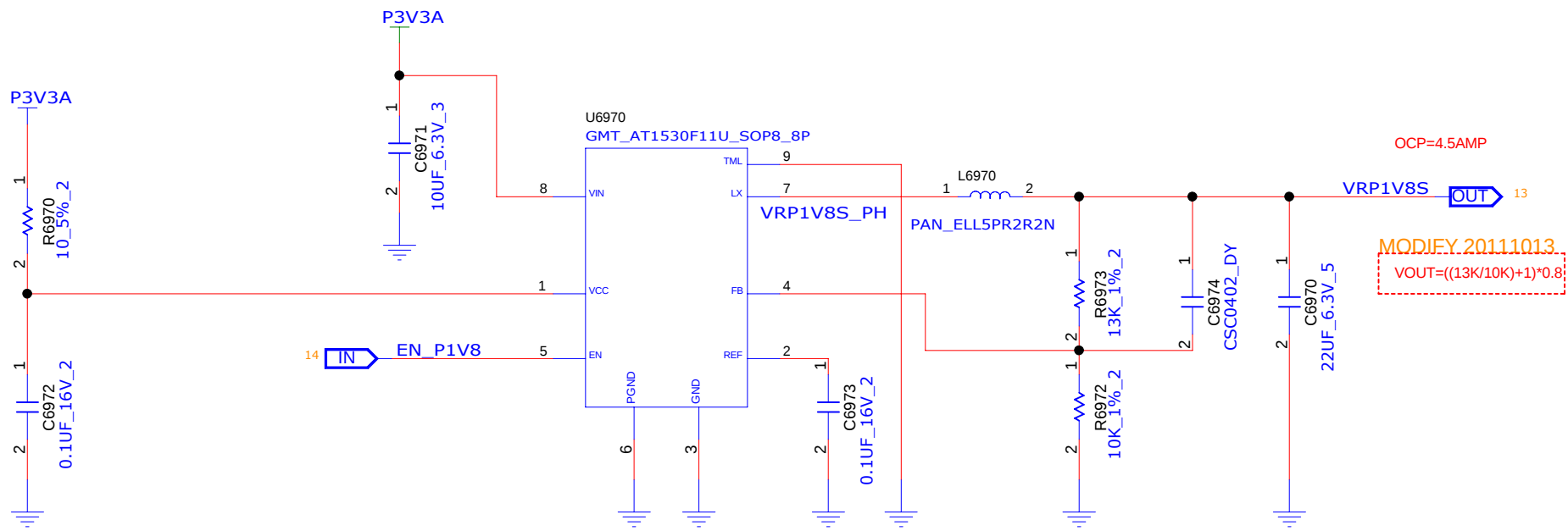
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
P1V055_VCCP

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

SHEET 8 of 39



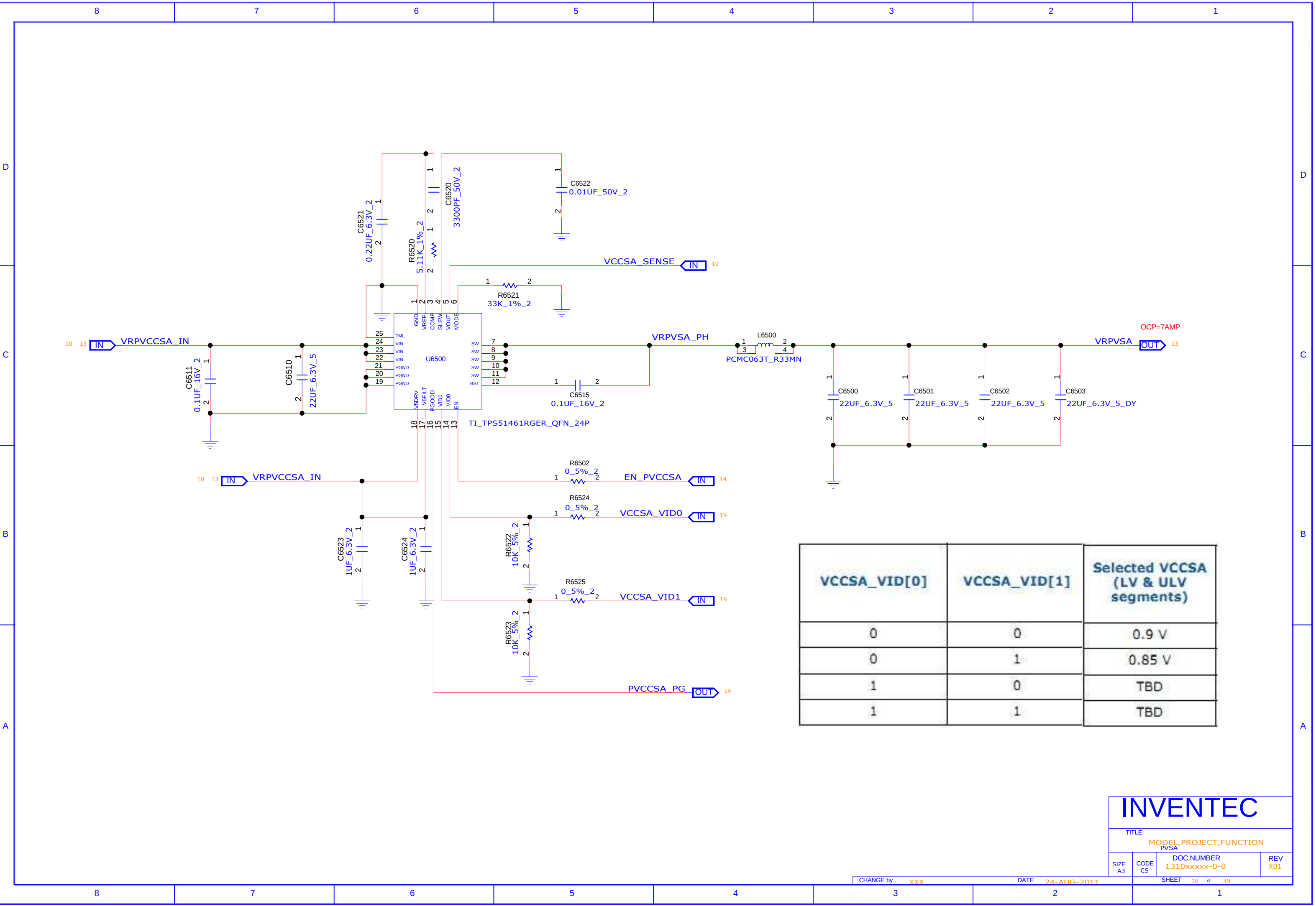
INVENTEC

TITLE
MODEL_PROJECT,FUNCTION
P1V8S

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

SHEET 9 of 39



VCCSA_VID[0]	VCCSA_VID[1]	Selected VCCSA (LV & ULV segments)
0	0	0.9 V
0	1	0.85 V
1	0	TBD
1	1	TBD

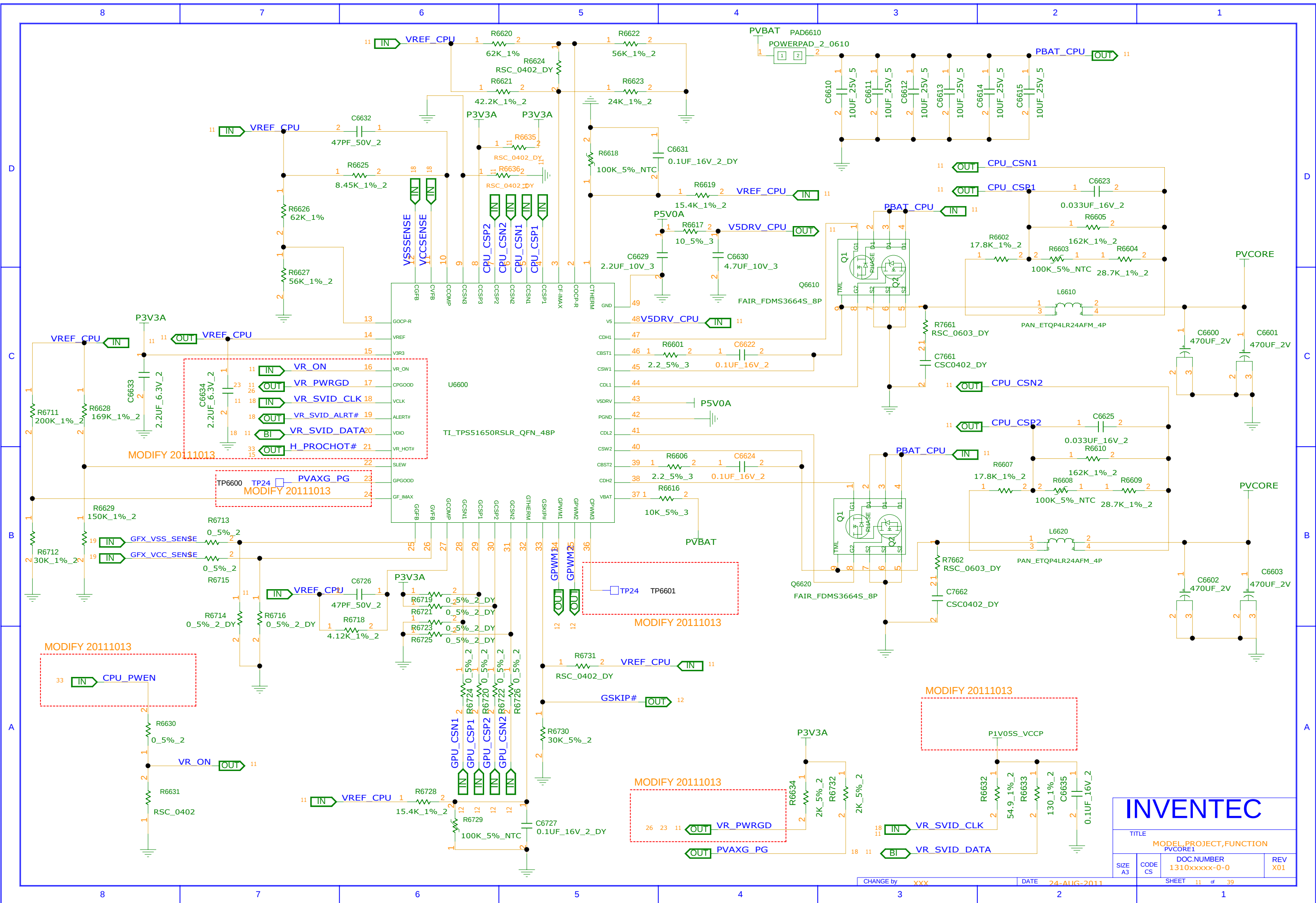
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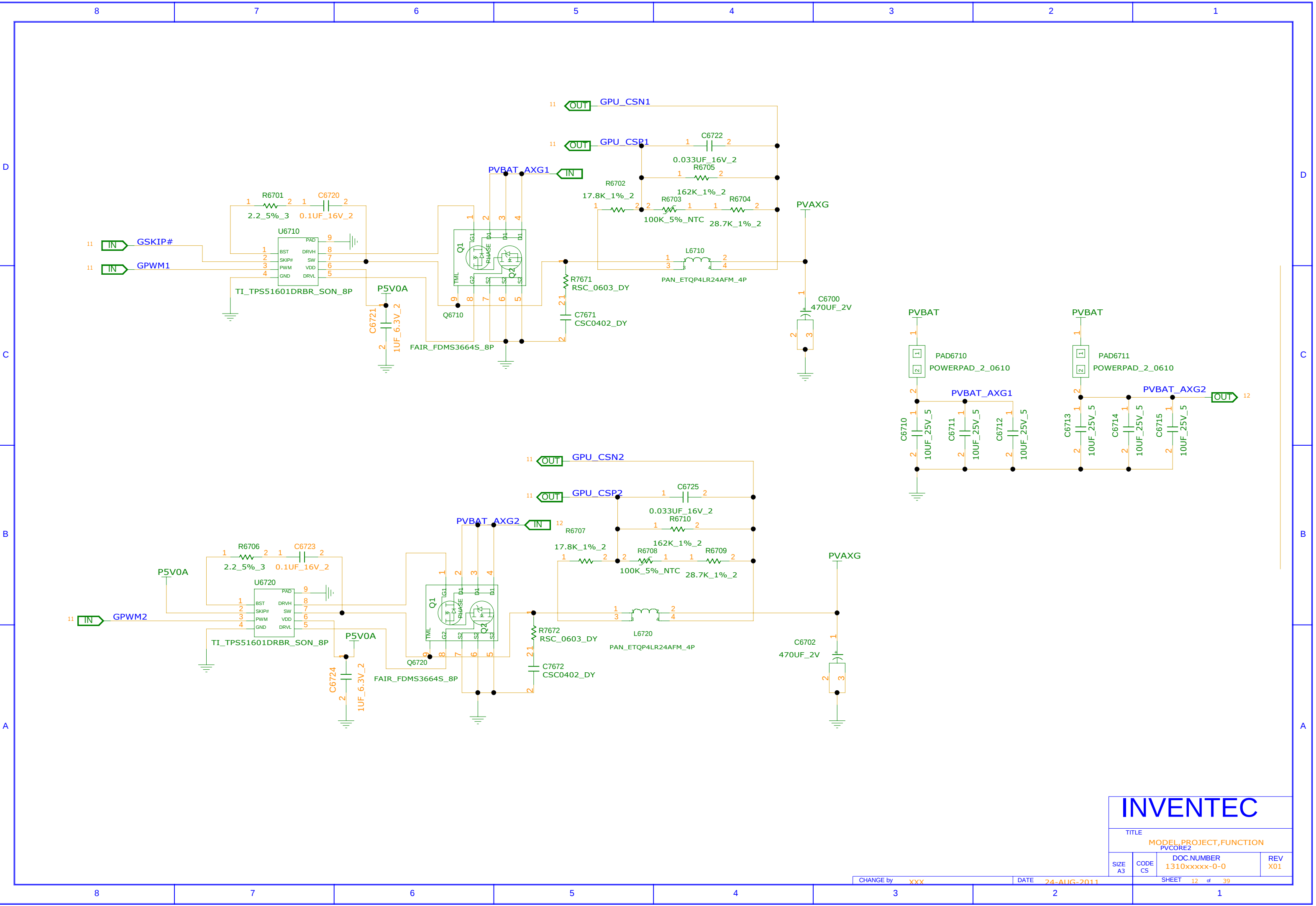
TITLE
MODEL,PROJECT,FUNCTION
PVSA

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

SHEET 10 of 39





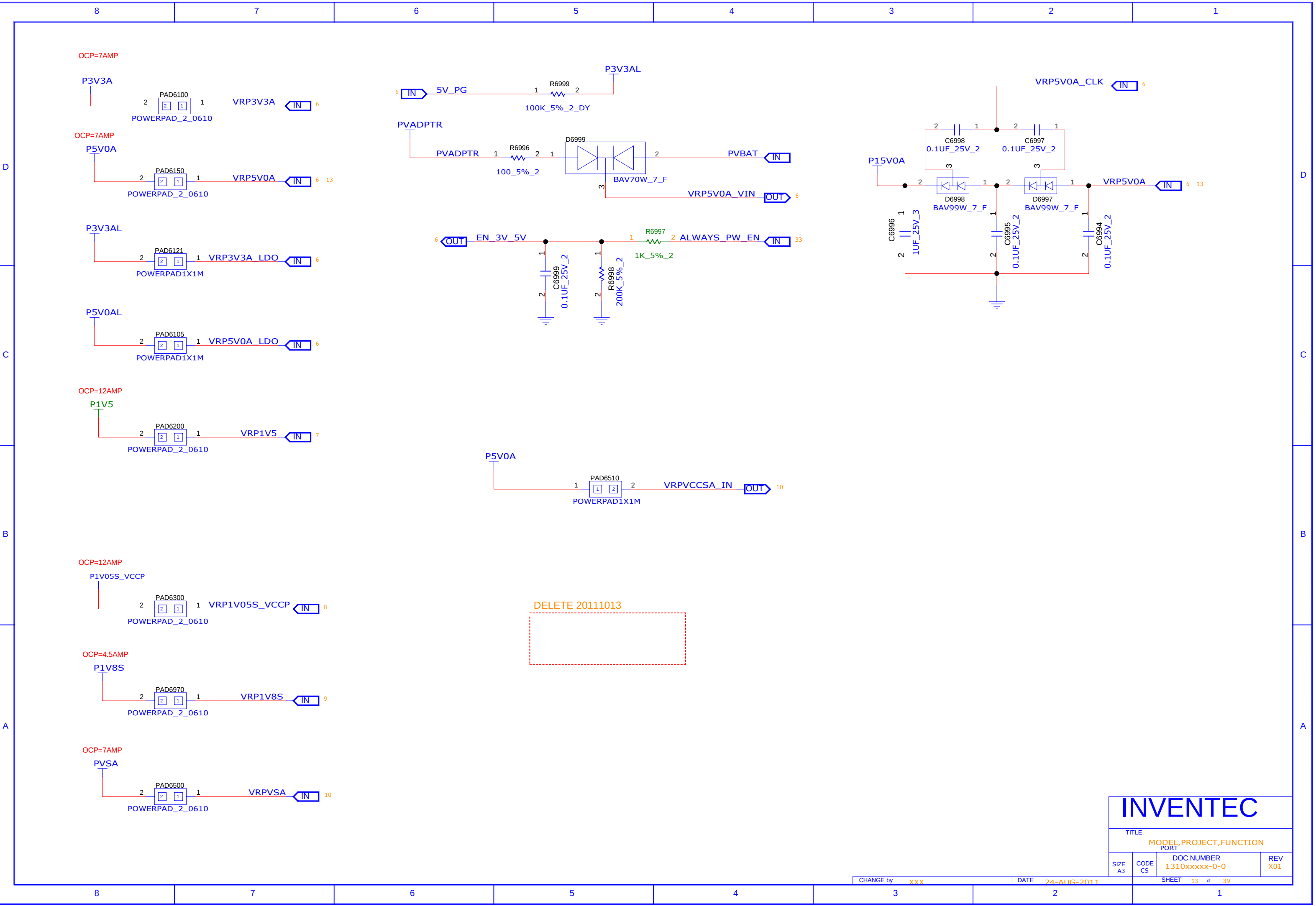
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PVCORE2

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxxx-0-0	X01

CHANGE by XXX DATE 24-AUG-2011

SHEET 12 of 39



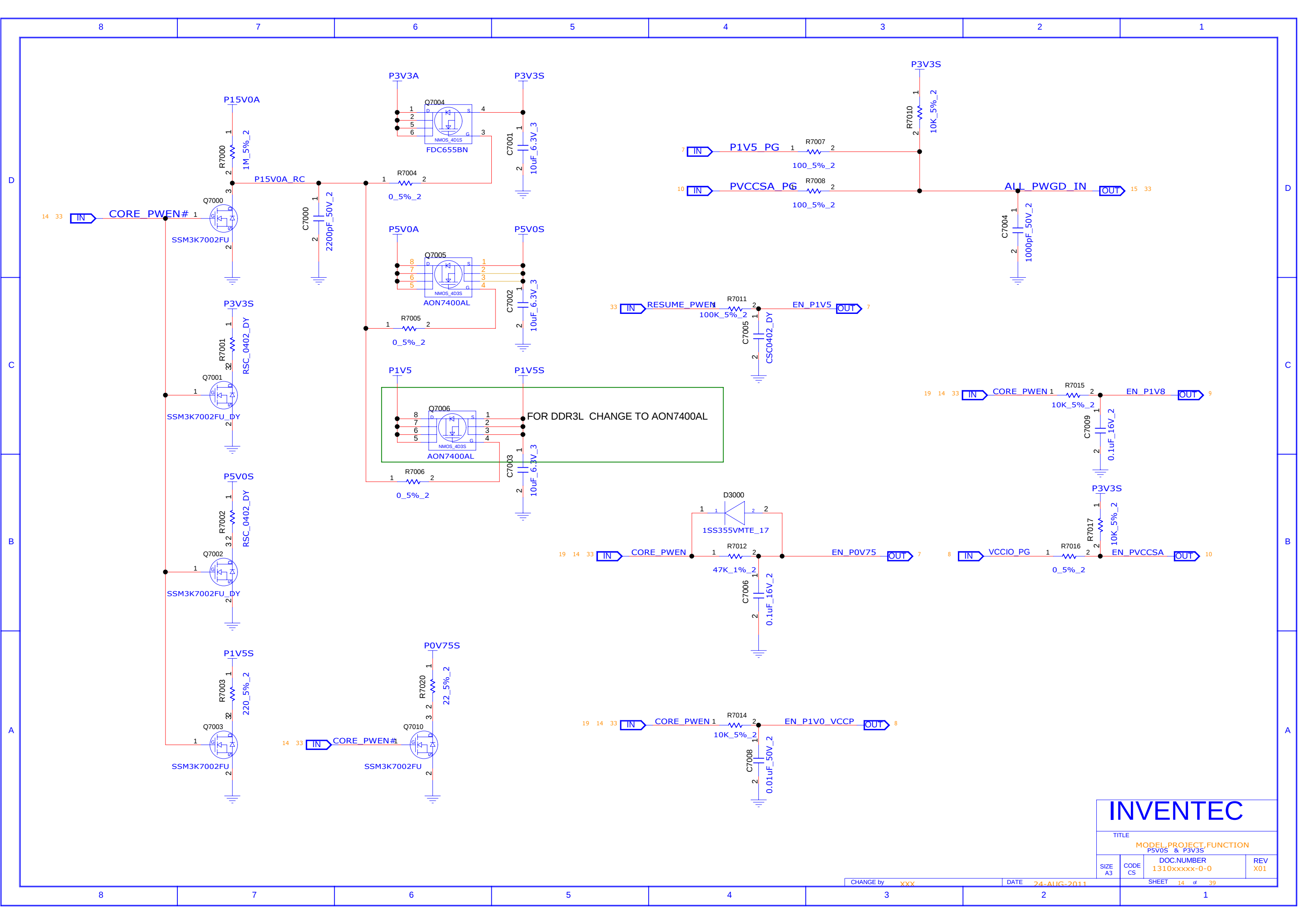
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PORT

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxxx-0-0	X01

CHANGE by XXX DATE 24-AUG-2011

SHEET 13 of 39

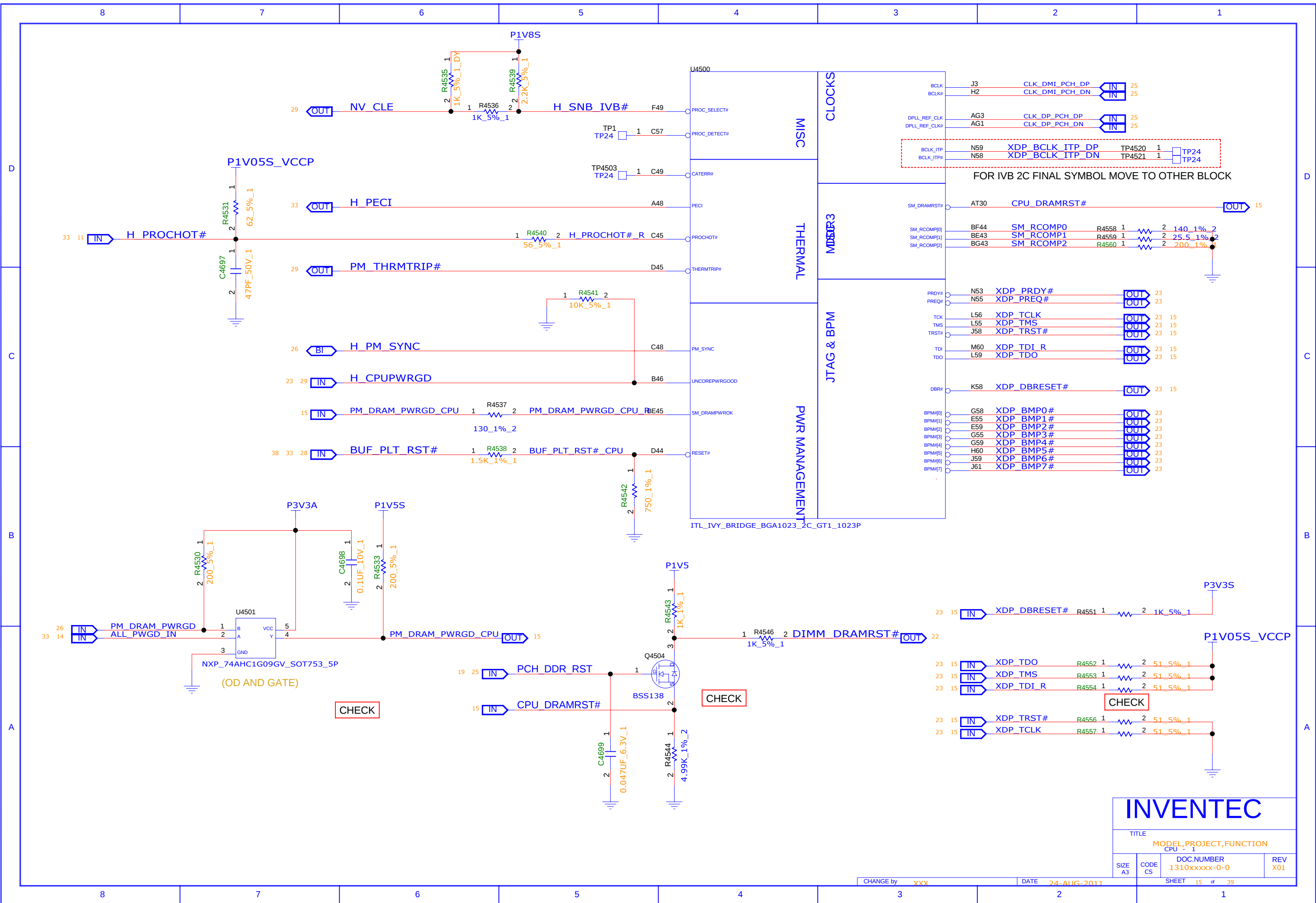


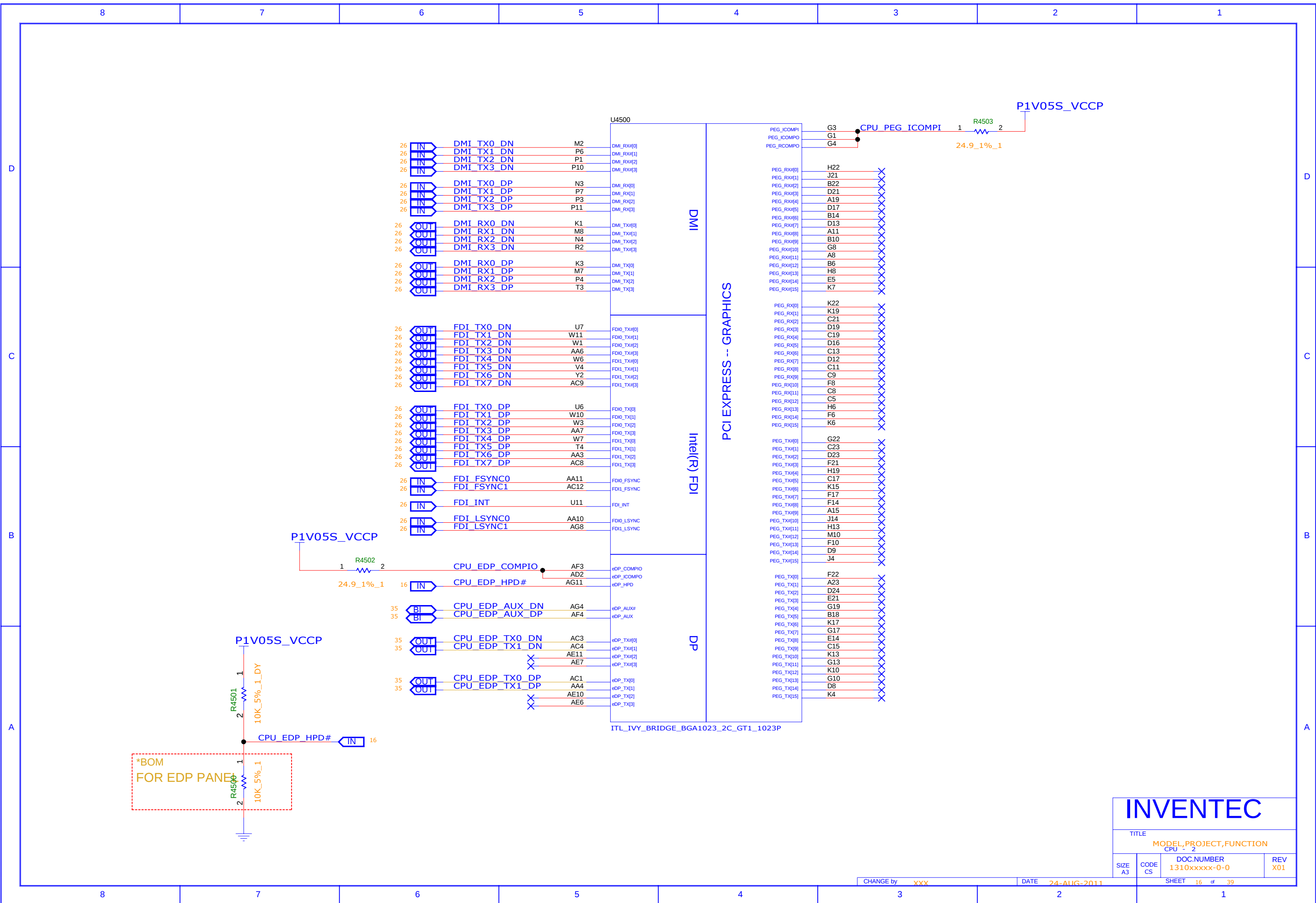
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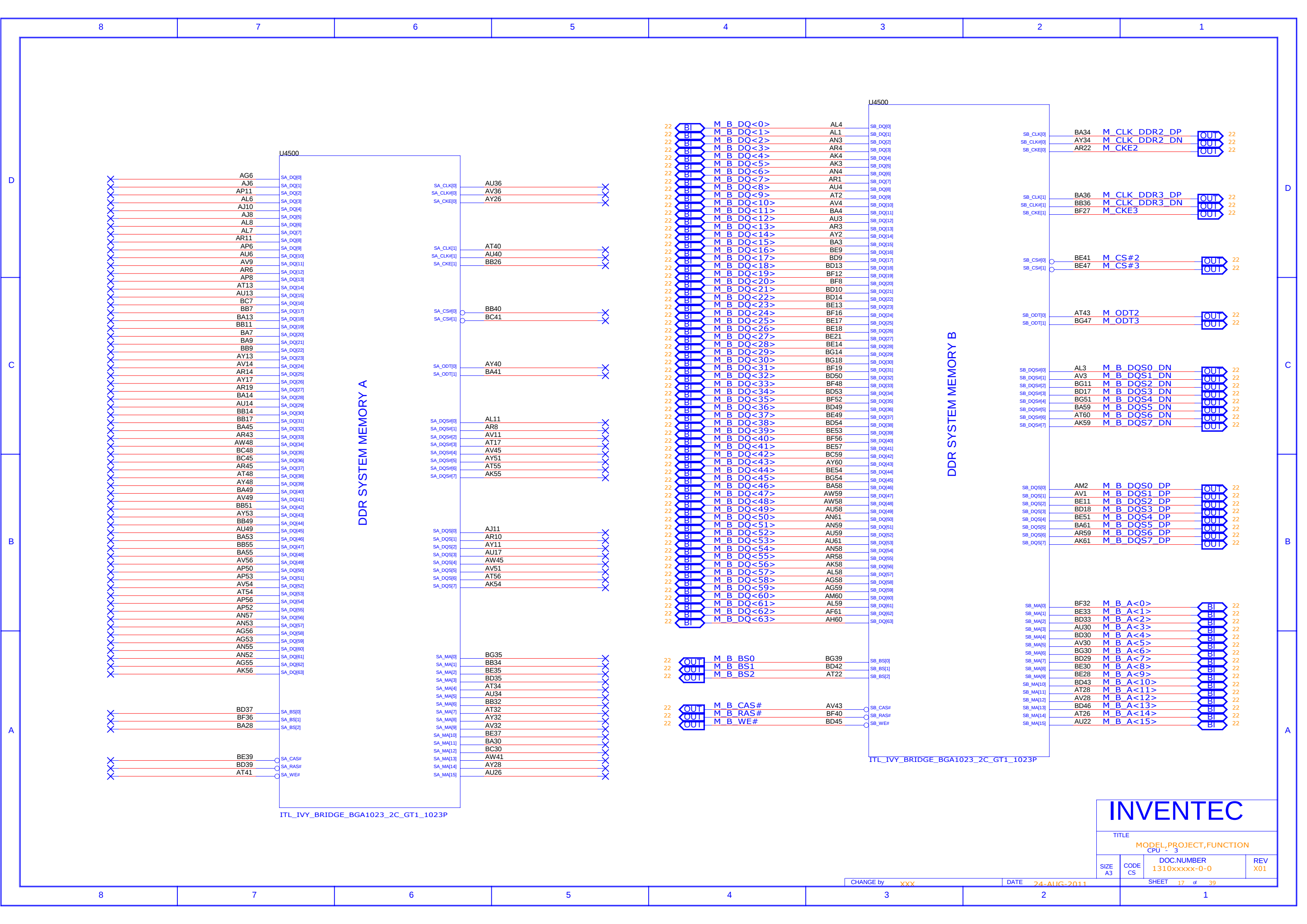
TITLE
MODEL,PROJECT,FUNCTION
P5V0S & P3V3S

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011 SHEET 14 of 39







INVENTEC

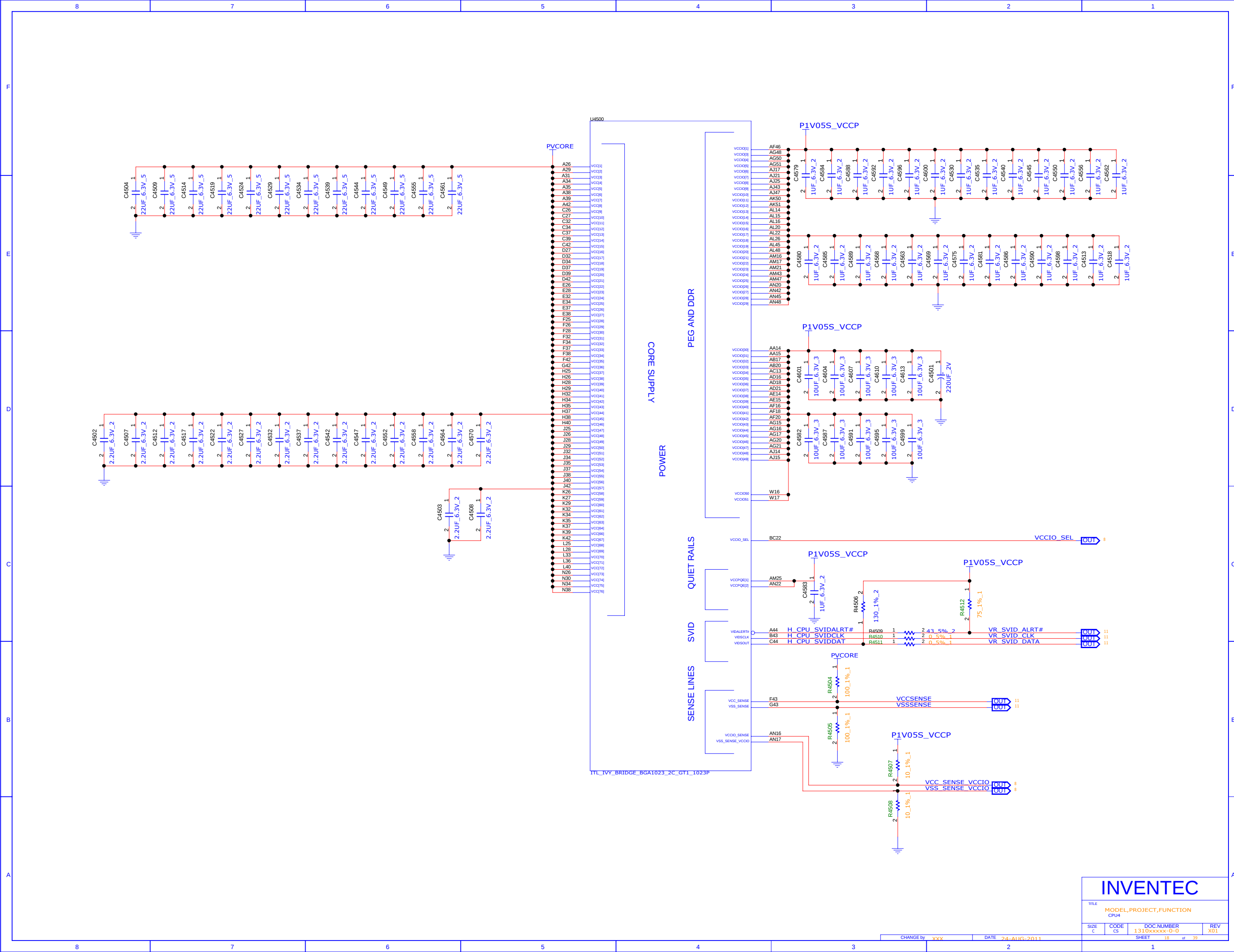
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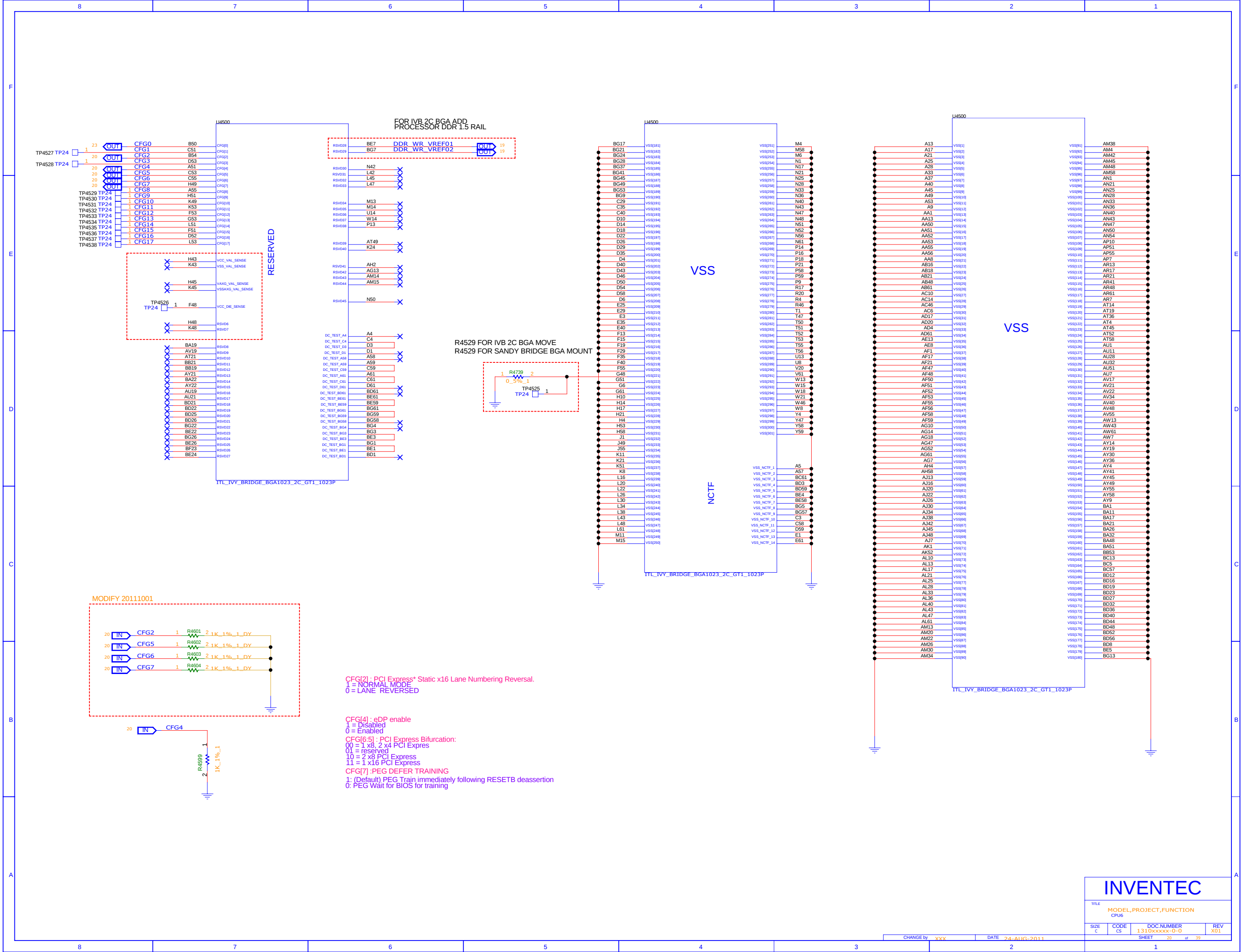
MODEL,PROJECT,FUNCTION
CPU - 3

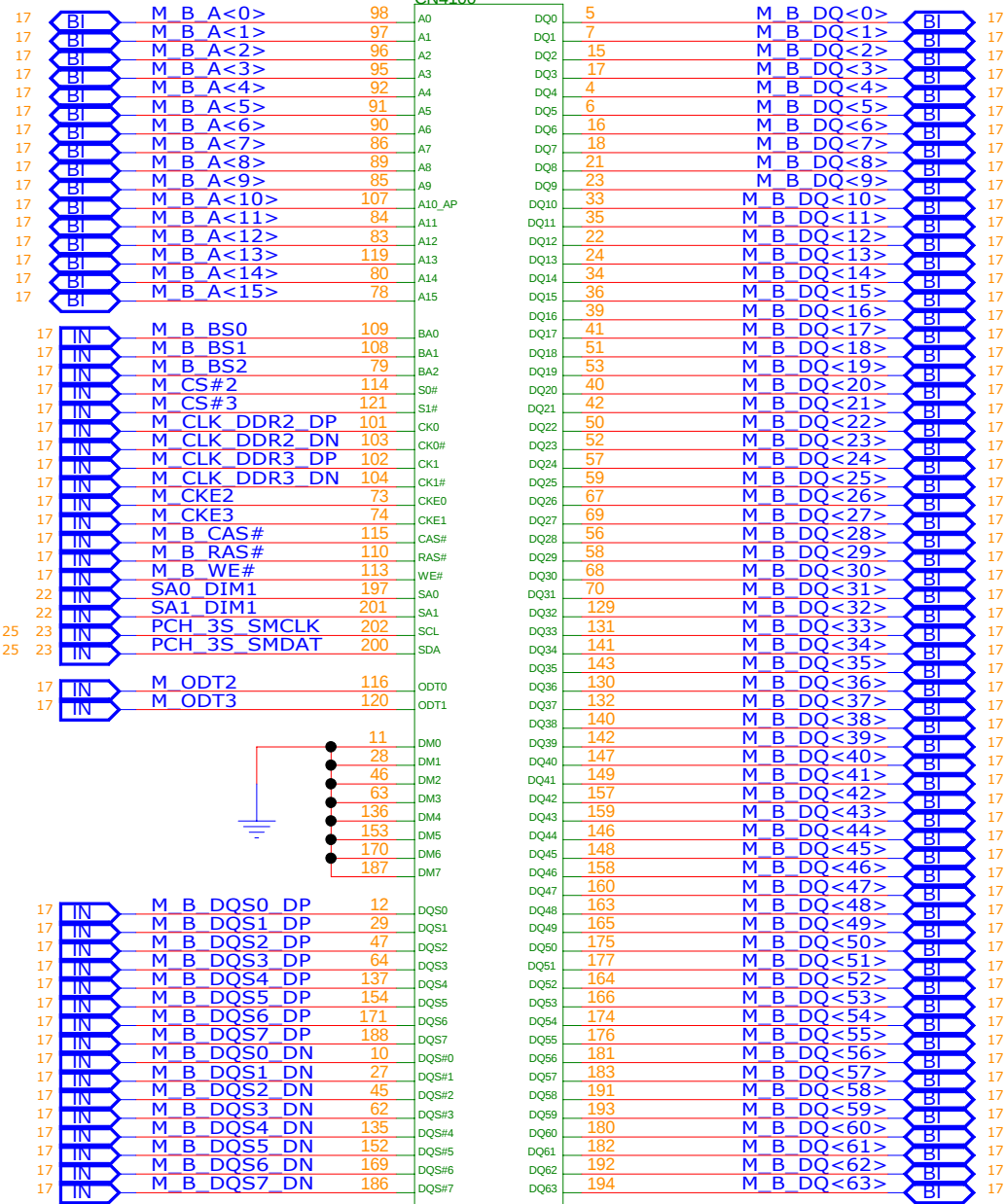
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CHANGE by XXX DATE 24-AUG-2011

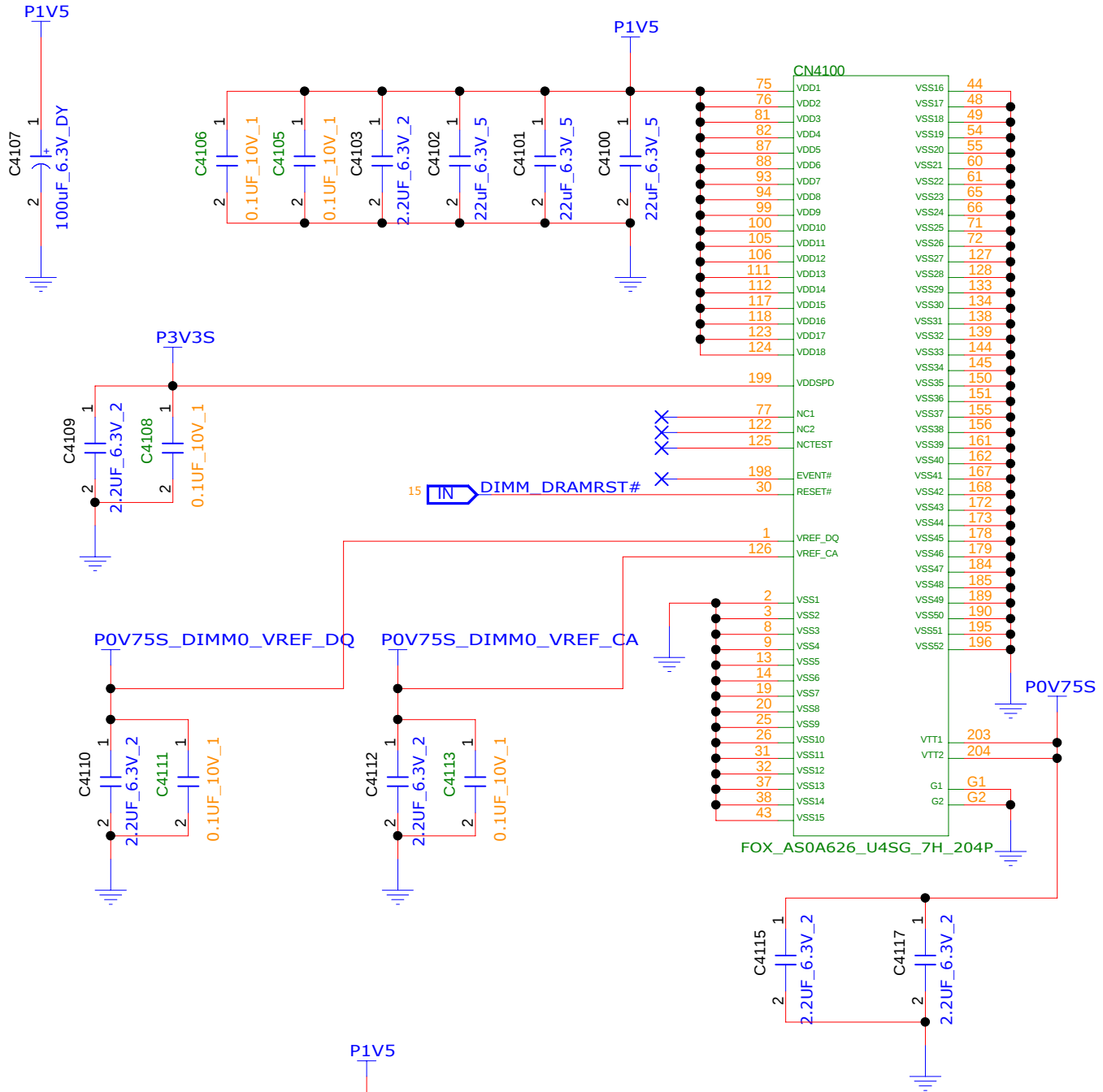
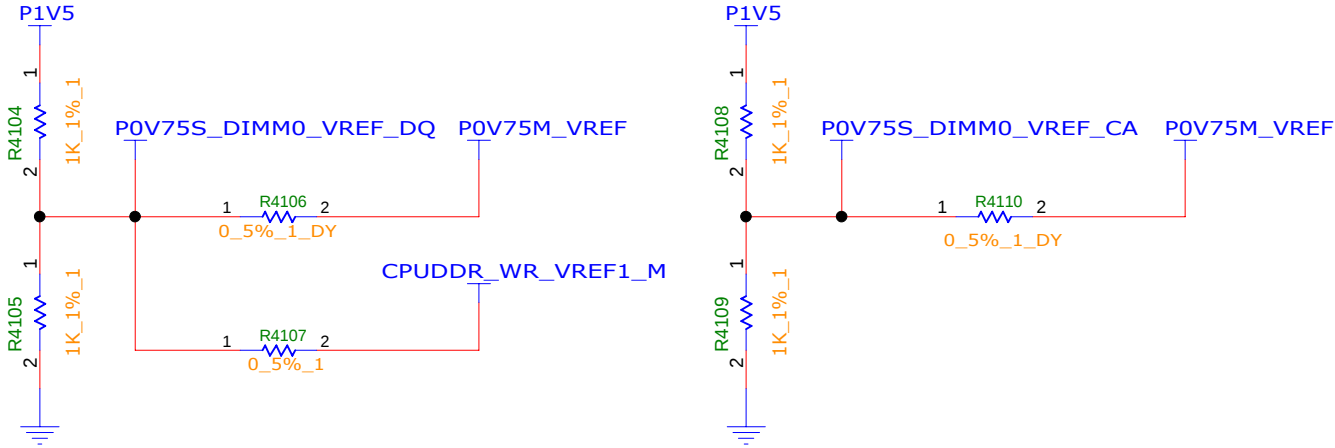
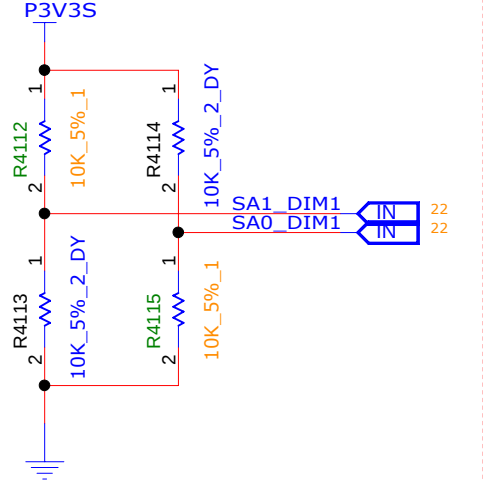
SHEET 17 of 39







Note :
SO-DIMMA SPD Address is 0xA4
SO-DIMMA TS Address is 0x34

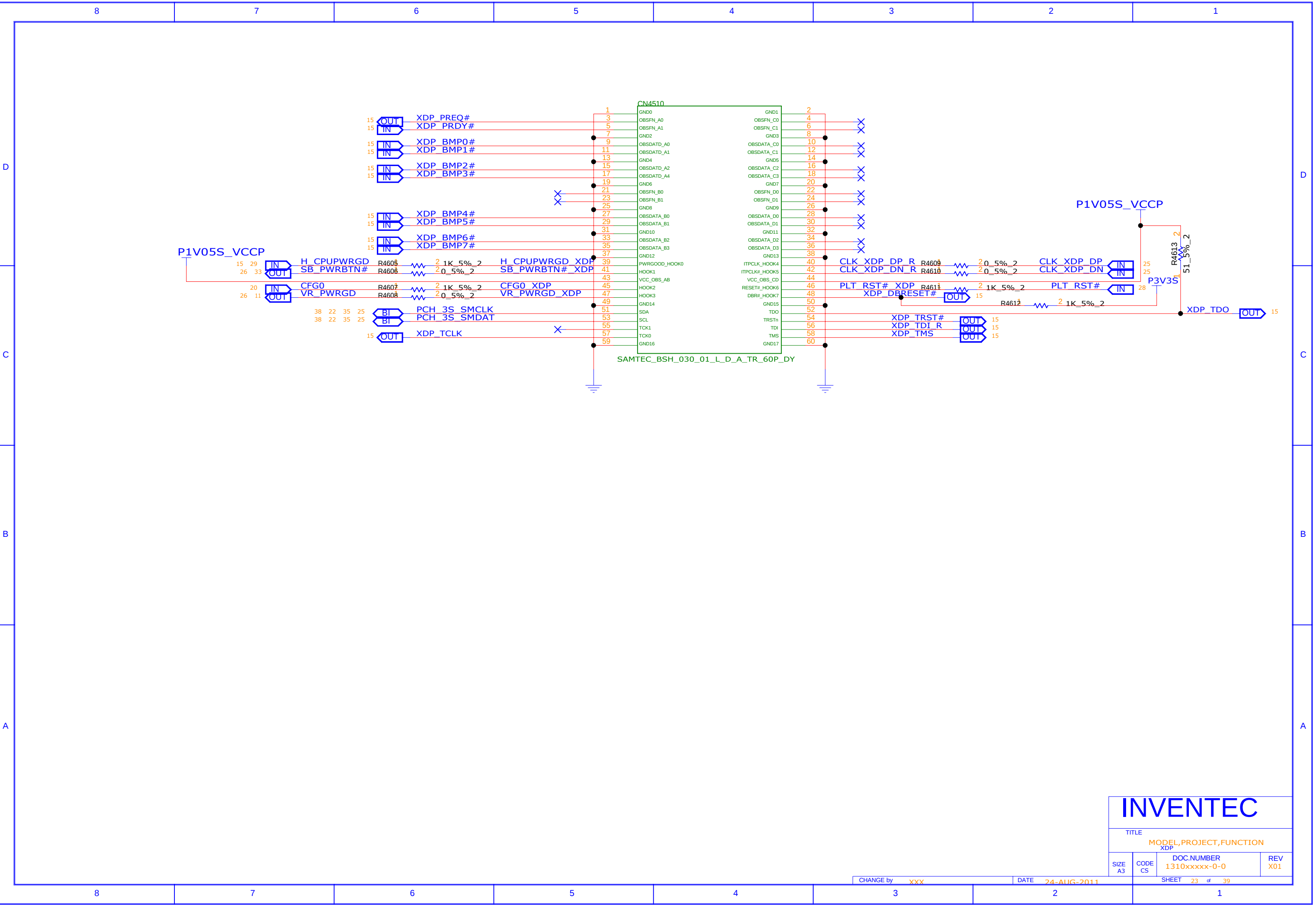


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
DDR3 - 1

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011 SHEET 22 of 39



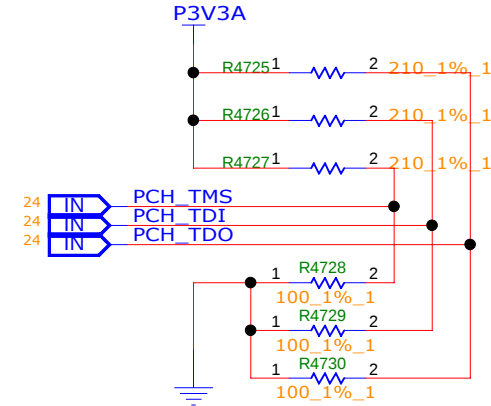
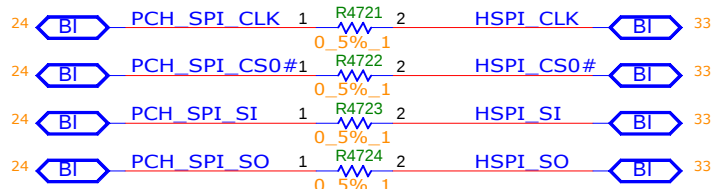
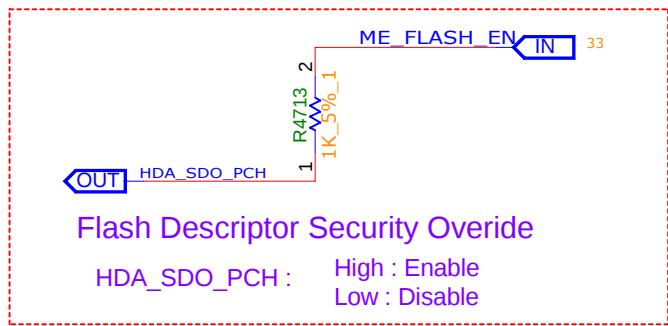
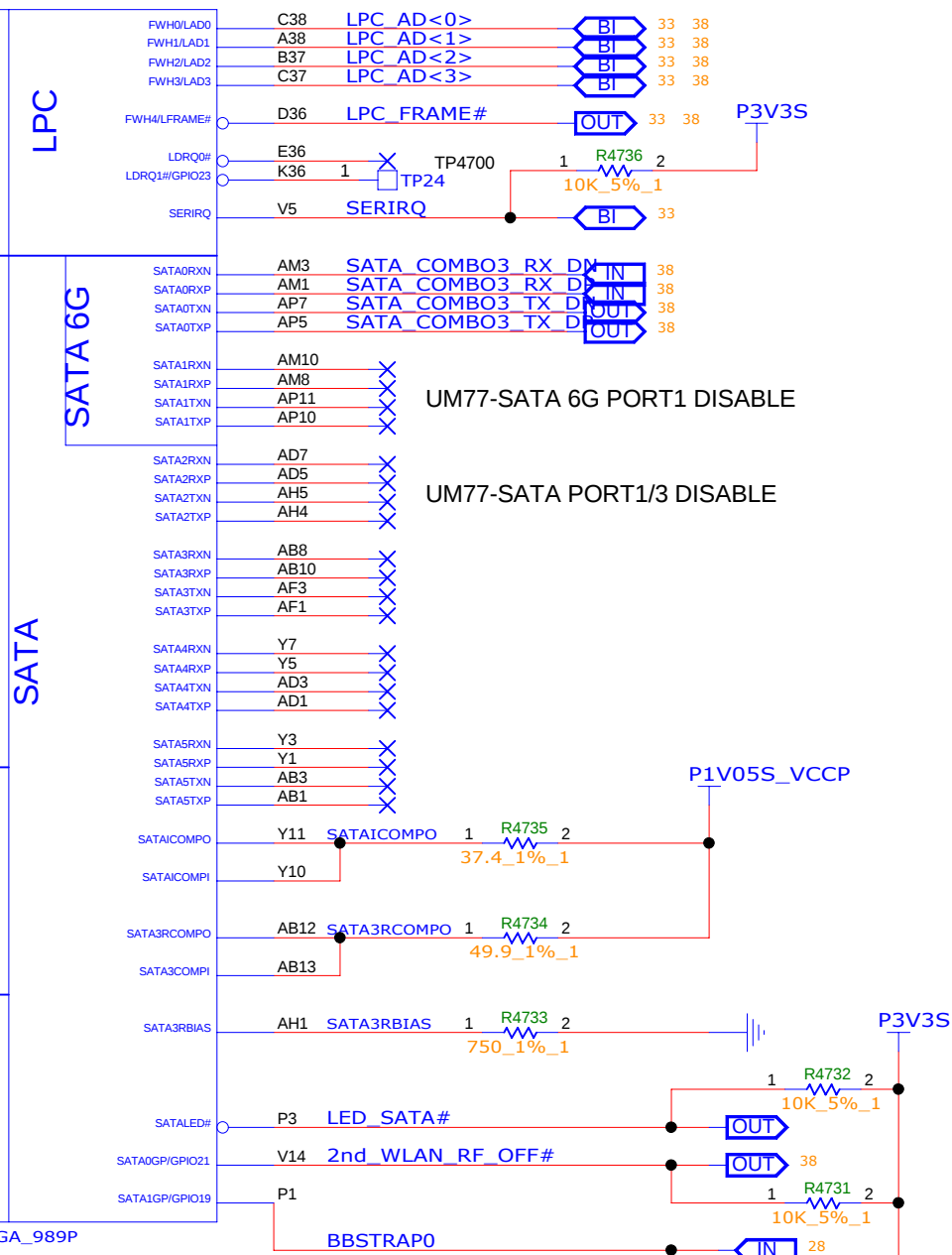
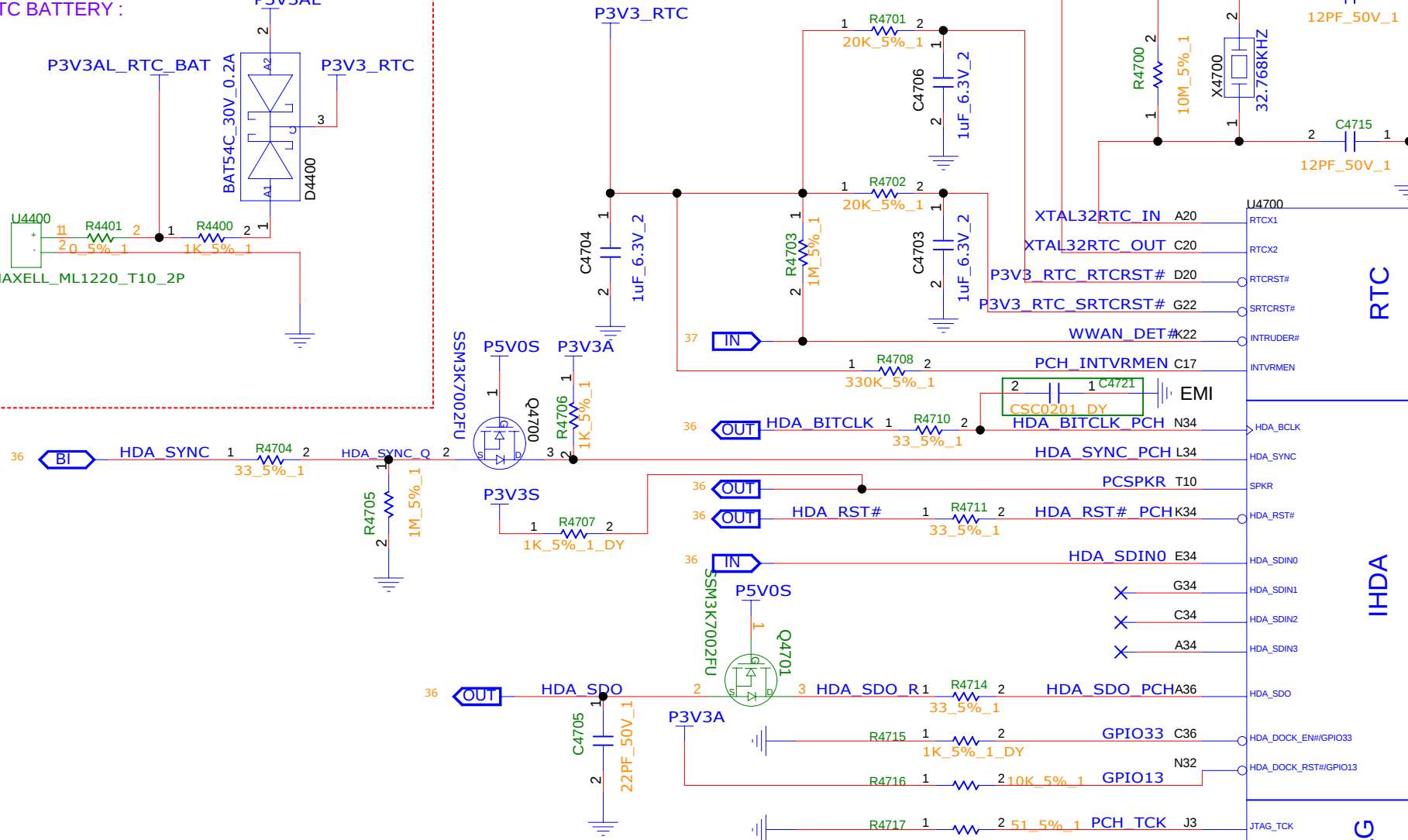
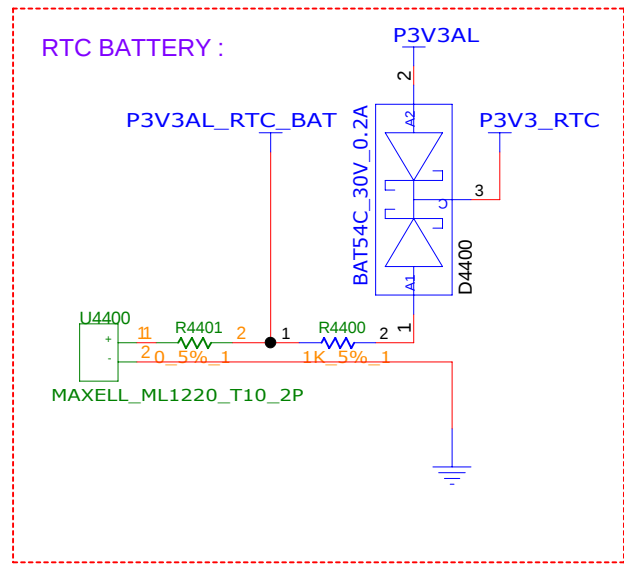
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
XDP

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

SHEET 23 of 39

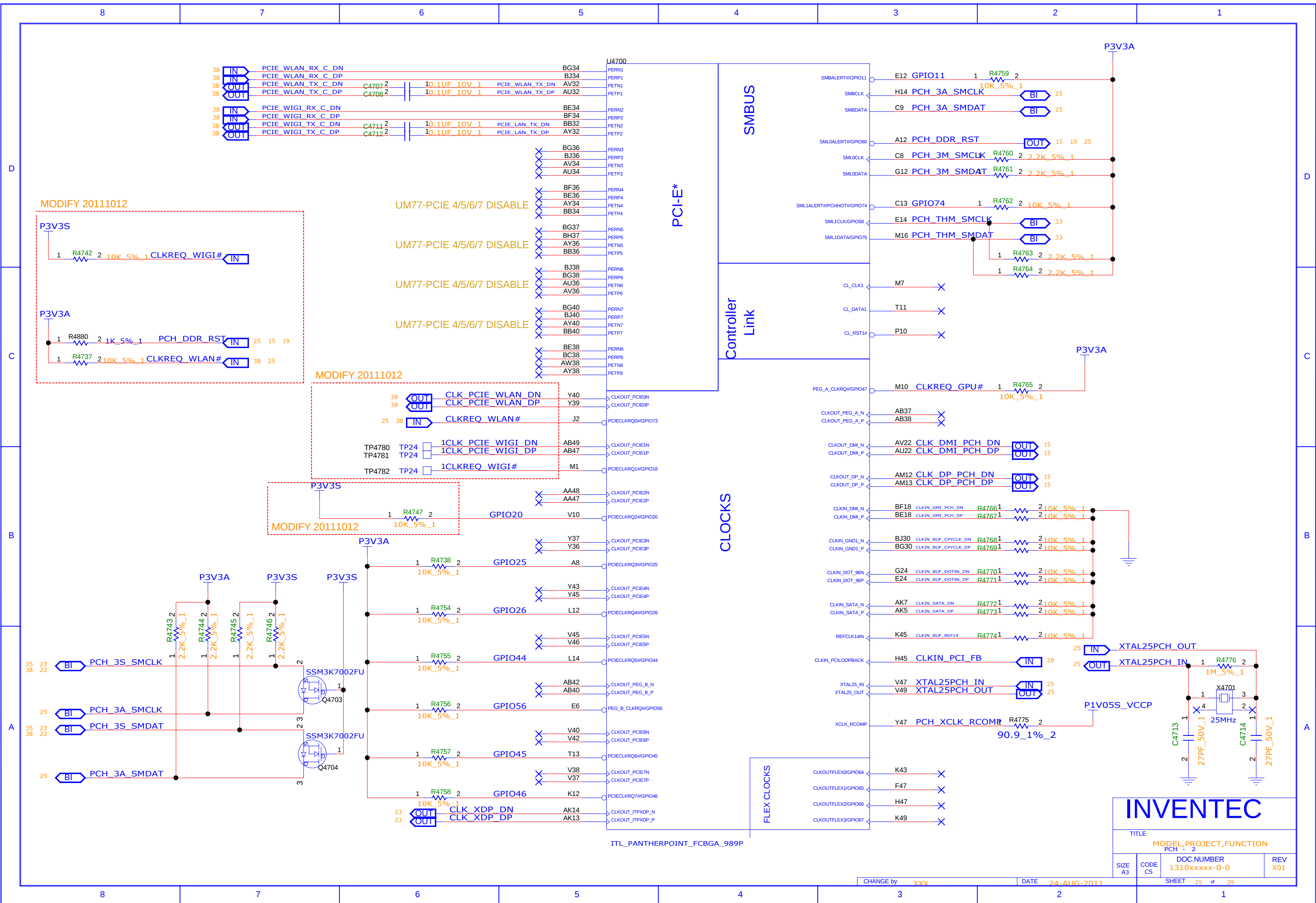


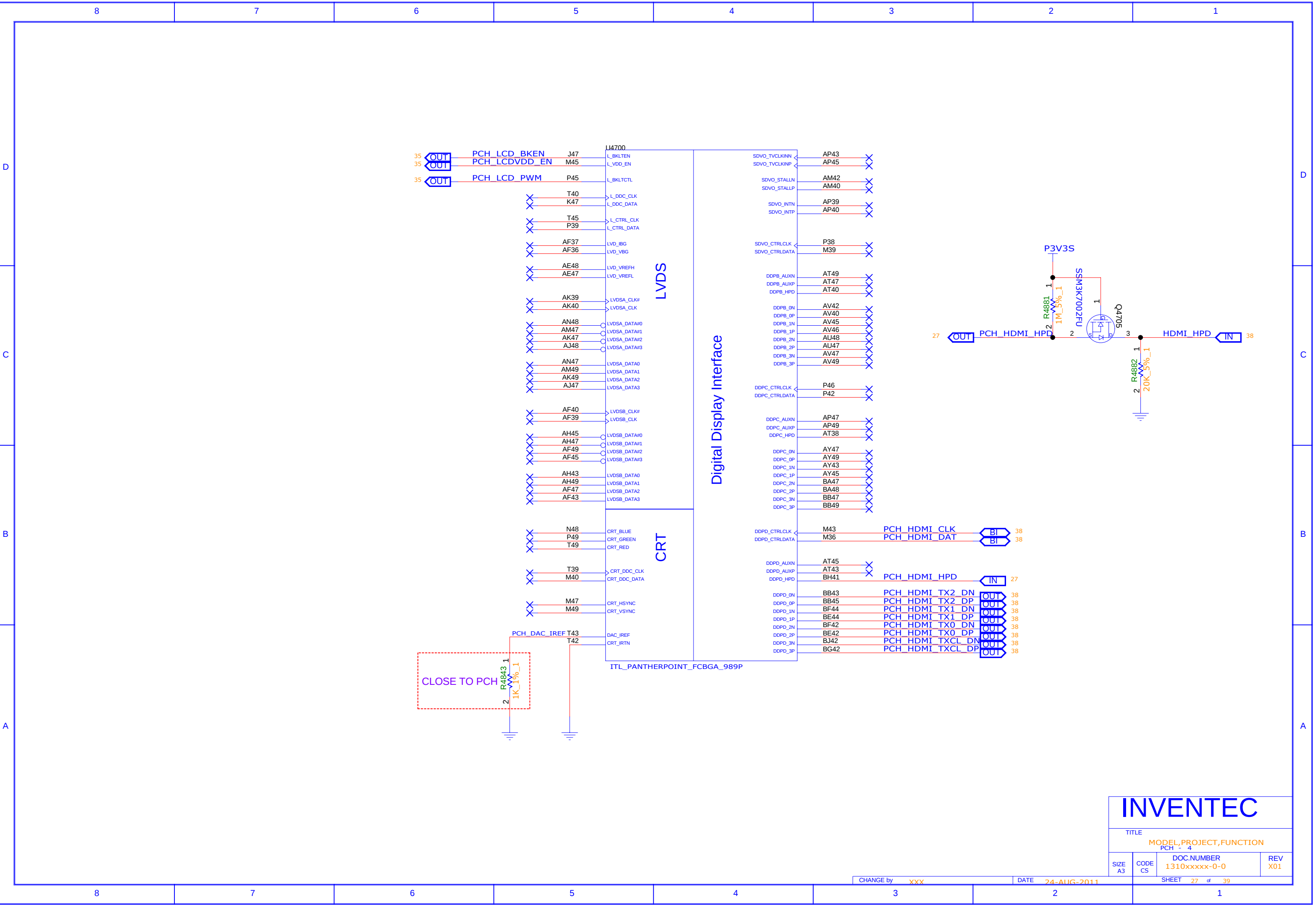
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 1

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011 SHEET 24 of 39





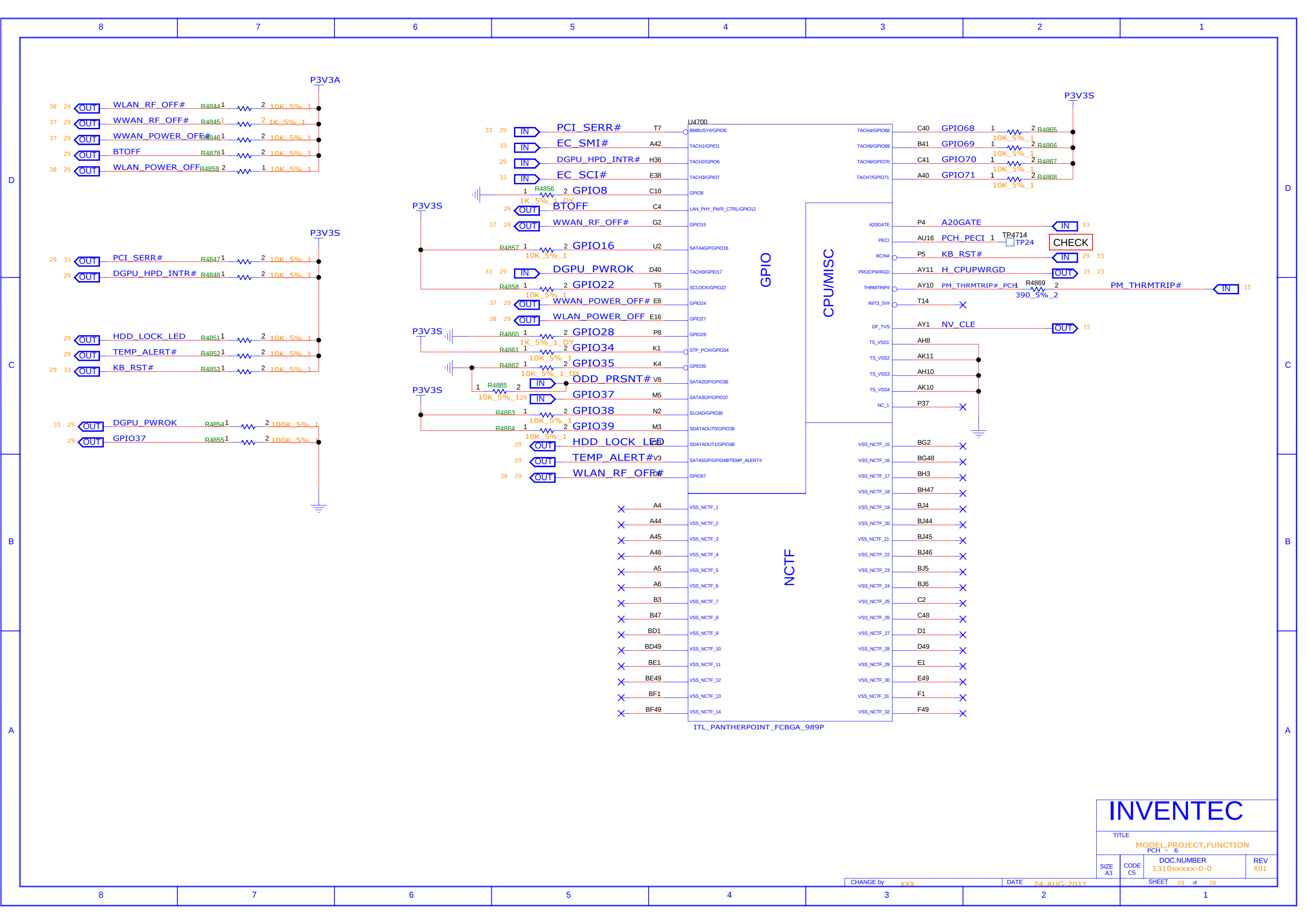
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 4

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CHANGE by XXX DATE 24-AUG-2011

SHEET 27 of 39

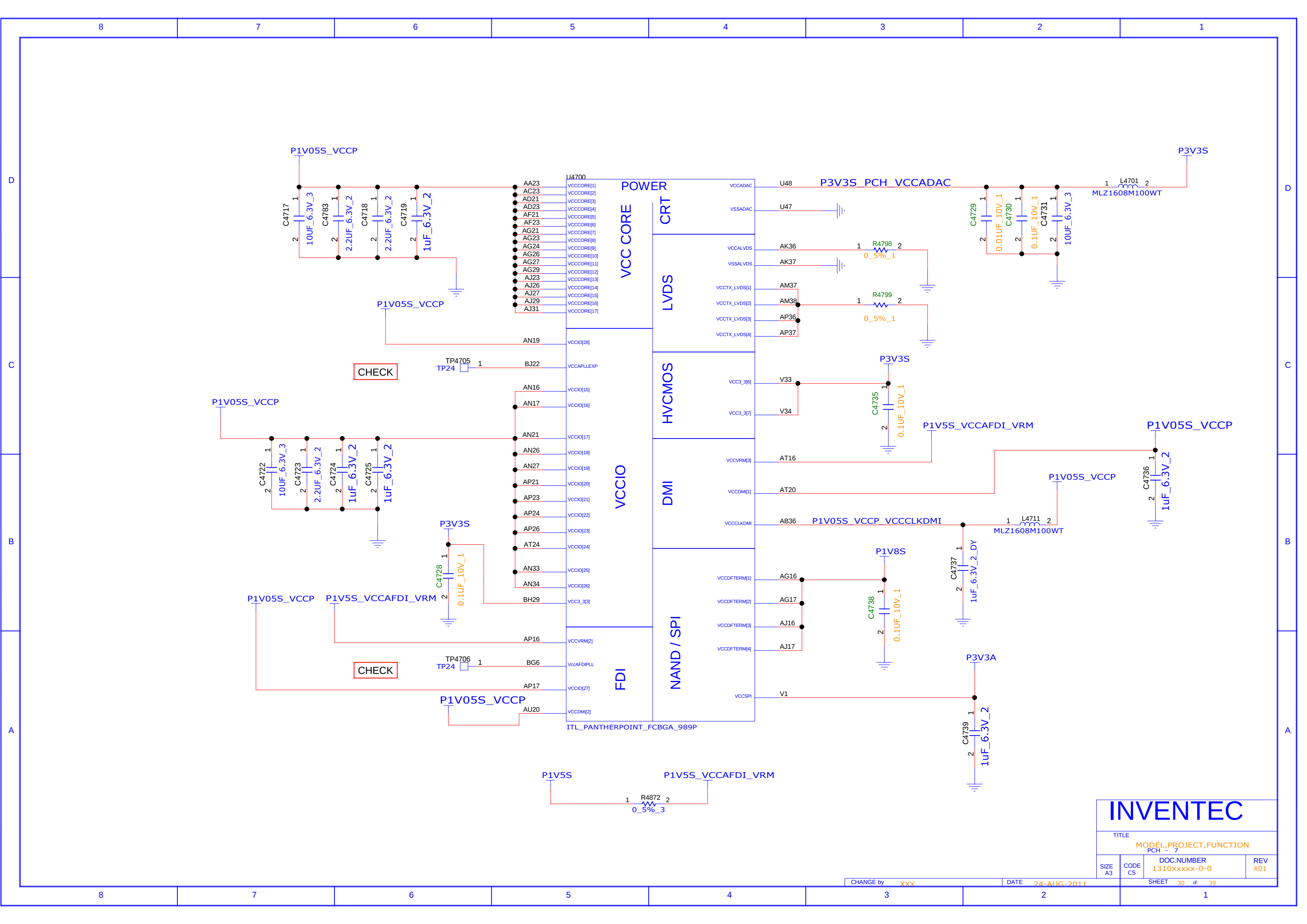


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 6

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011 SHEET 29 of 39

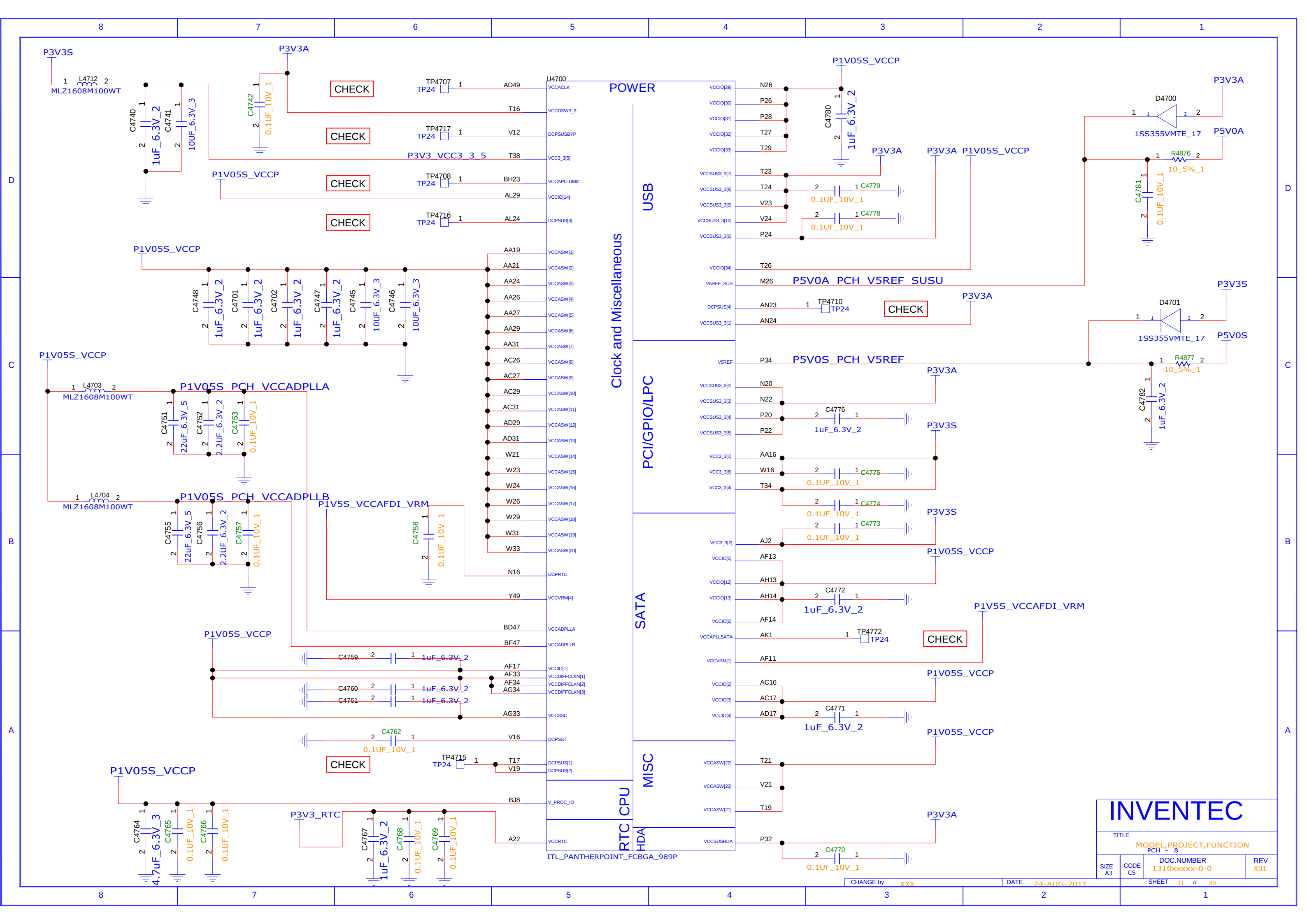


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 7

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 24-AUG-2011 SHEET 30 of 39

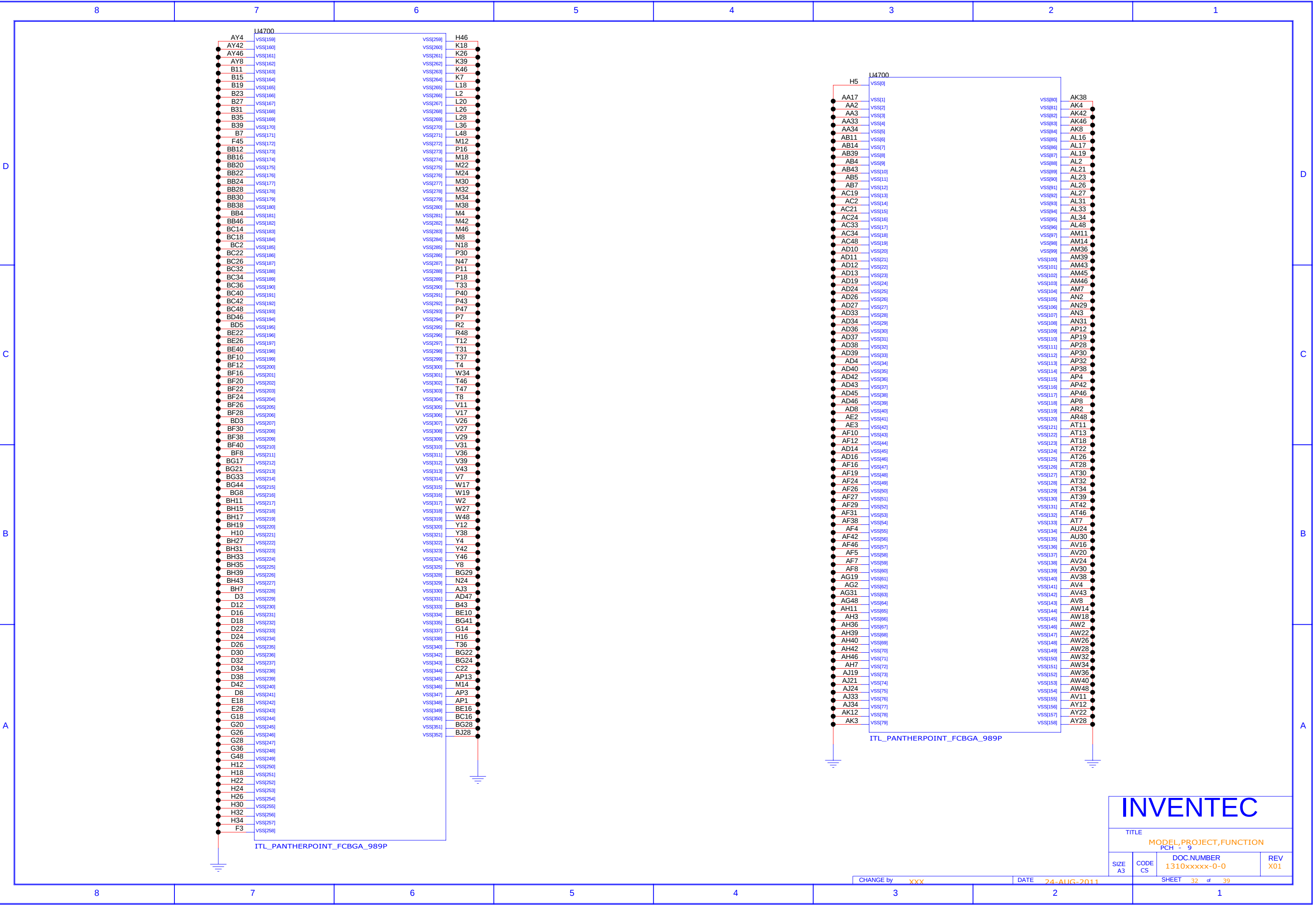


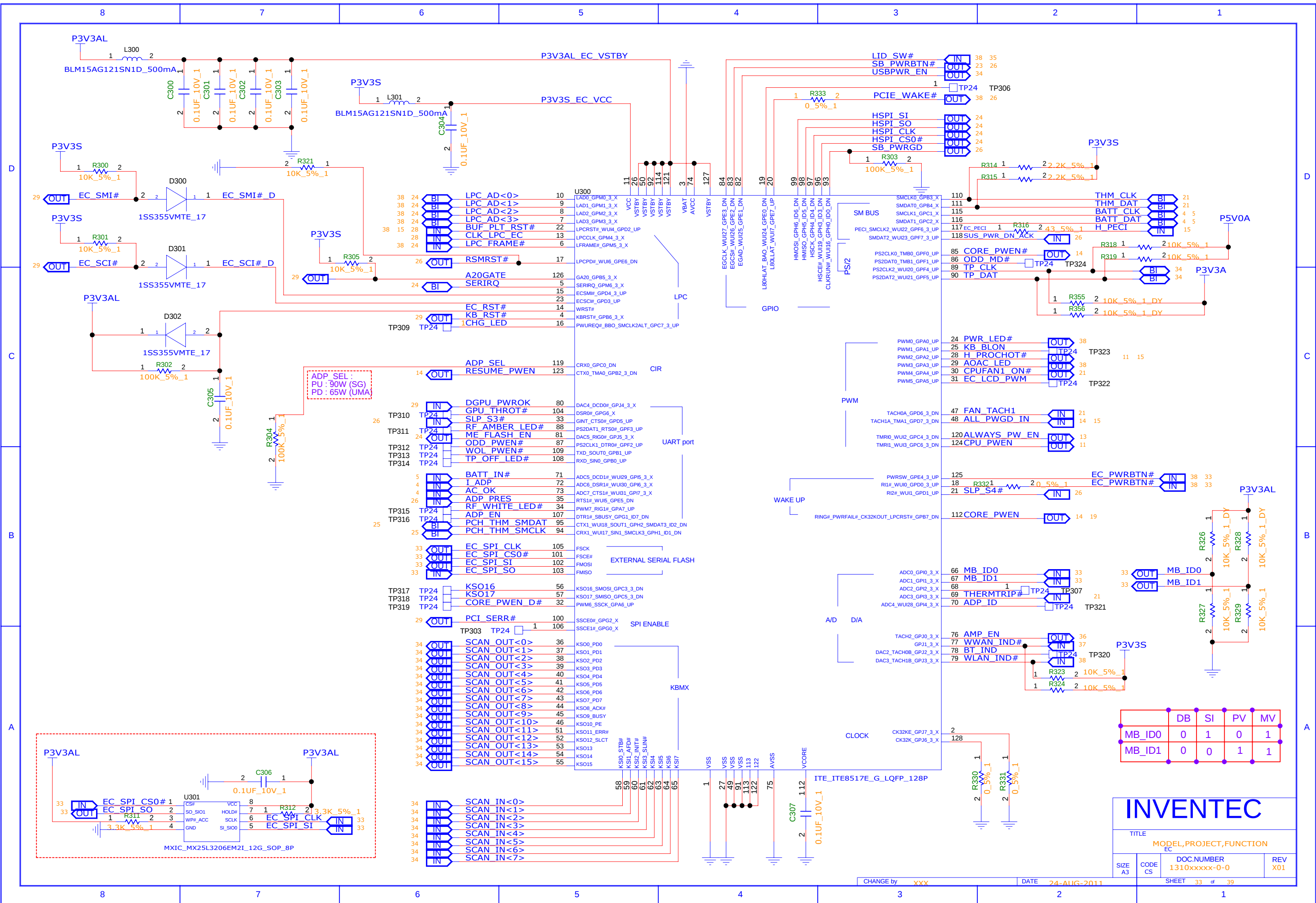
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PCH - 8

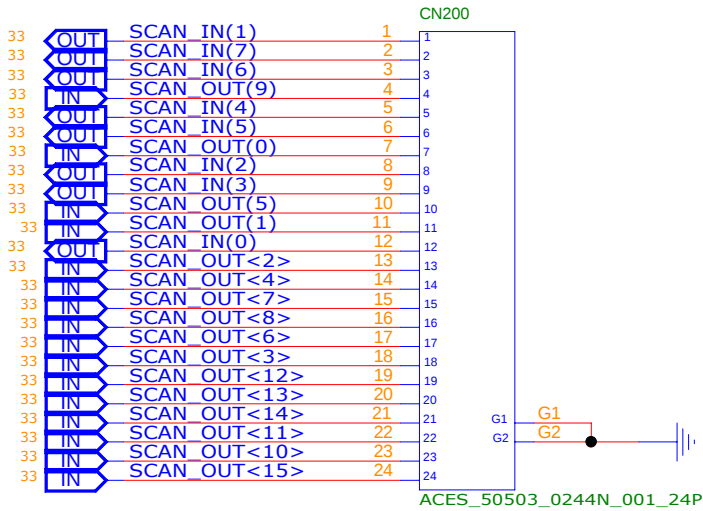
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CHANGE by XXX DATE 24-AUG-2011 SHEET 31 of 39

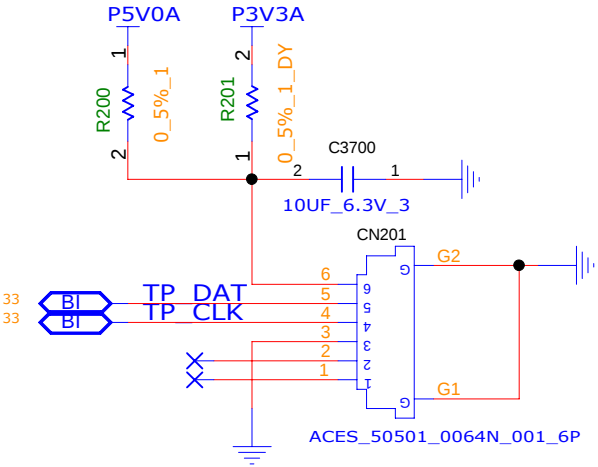




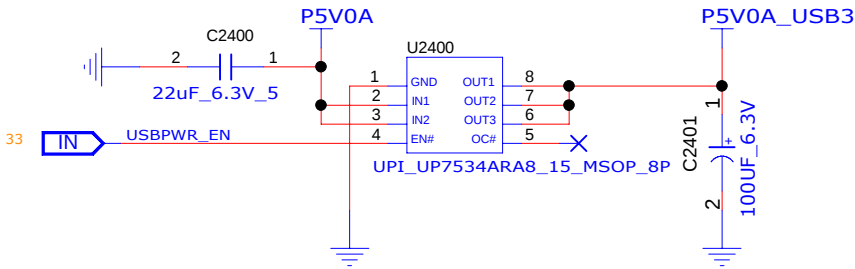
KEYBOARD CONN(26 PIN)



TouchPad Module CONN



USB 3.0 POWER SW



INVENTEC

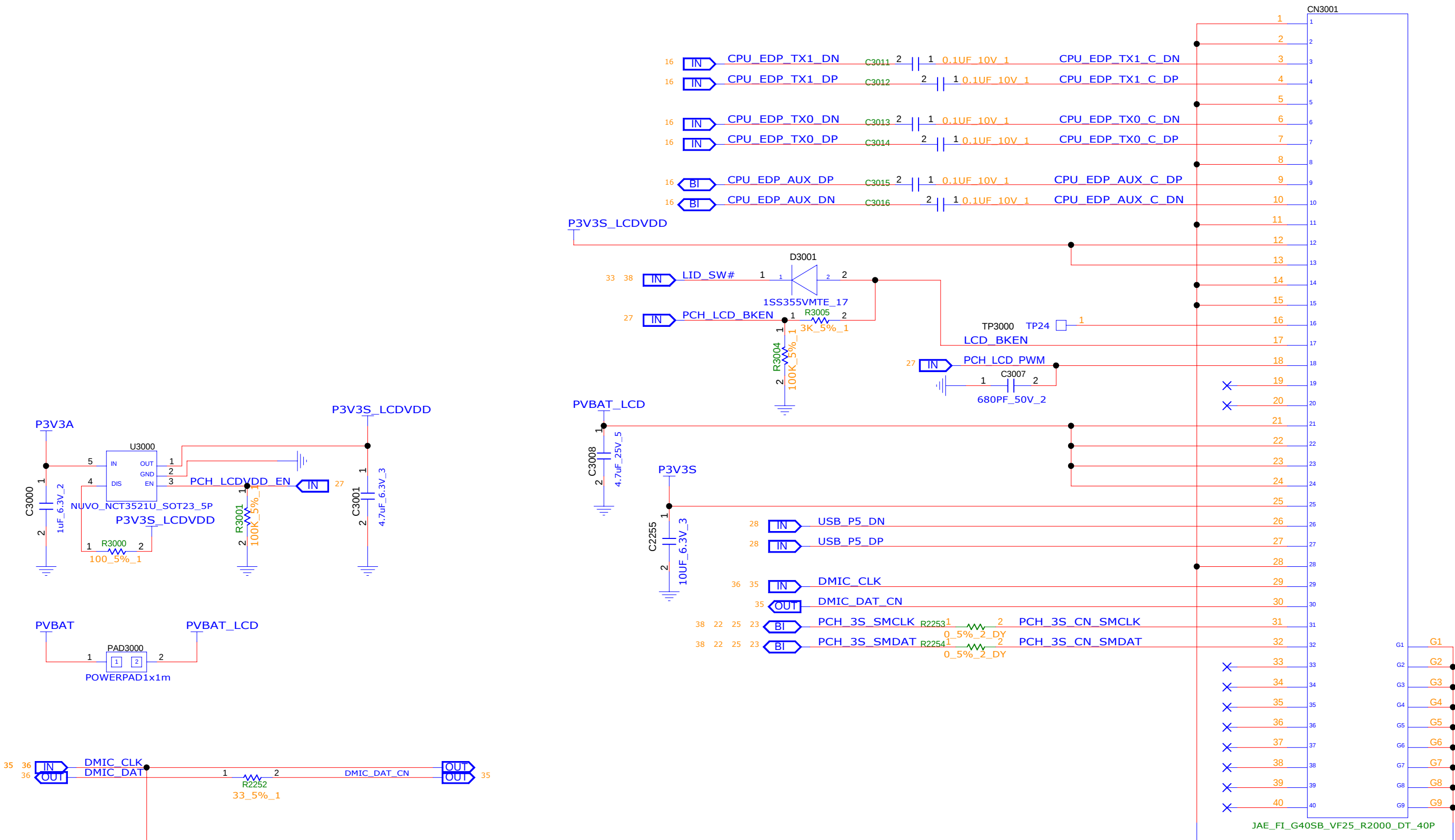
TITLE
MODEL,PROJECT,FUNCTION
KB & TP & USB PWR SW

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

SHEET 34 of 39

EDP CONN



INVENTEC

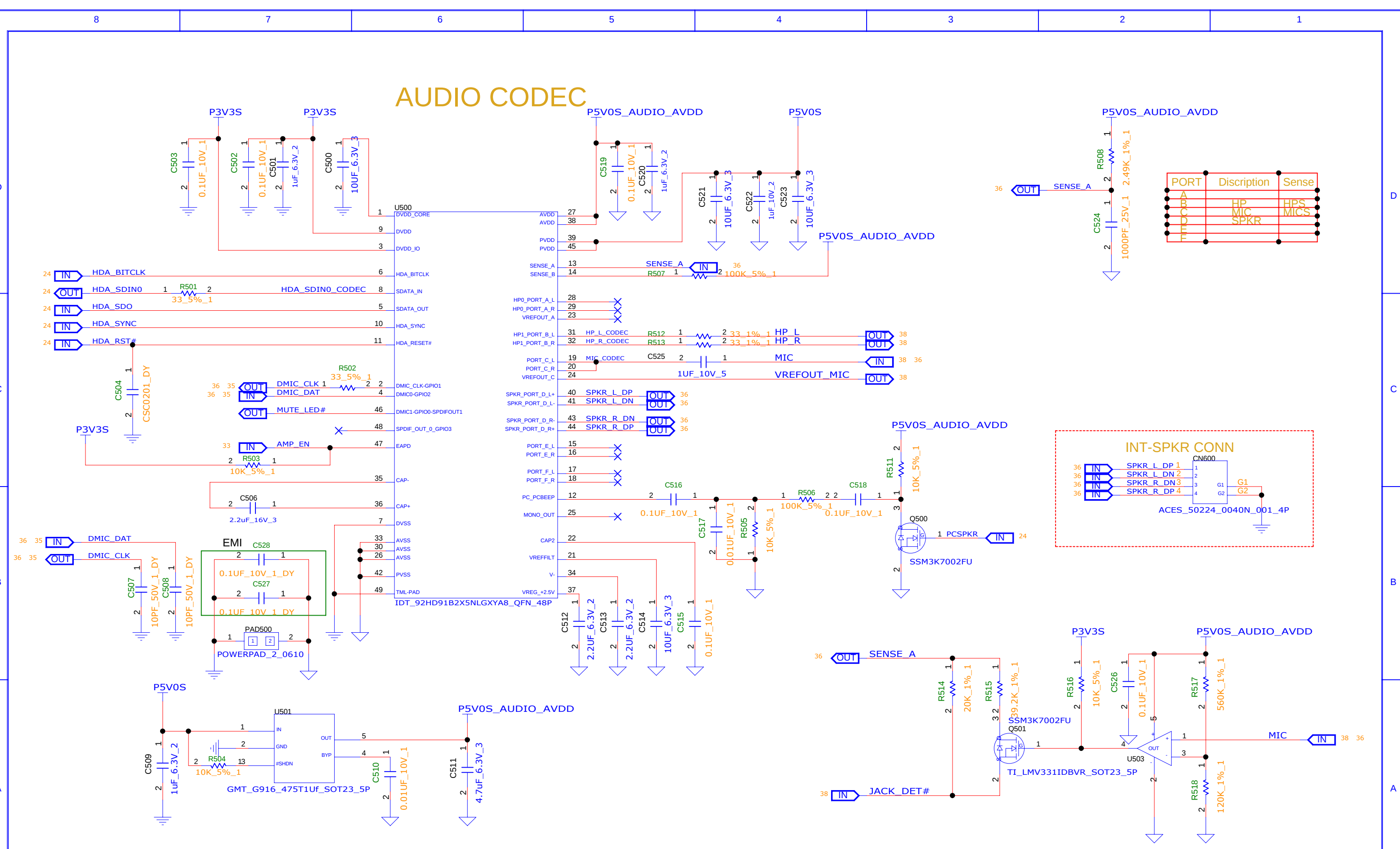
TITLE
MODEL,PROJECT,FUNCTION
EDP

SIZE	CODE	DOC.NUMBER	REV
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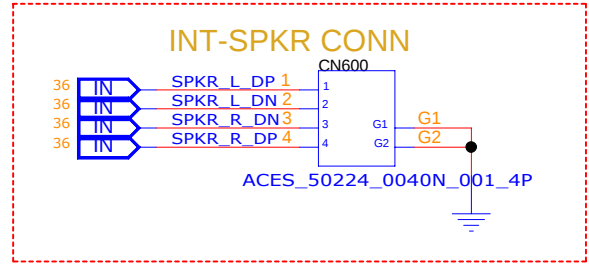
CHANGE by XXX DATE 24-AUG-2011

SHEET 35 of 39

AUDIO CODEC

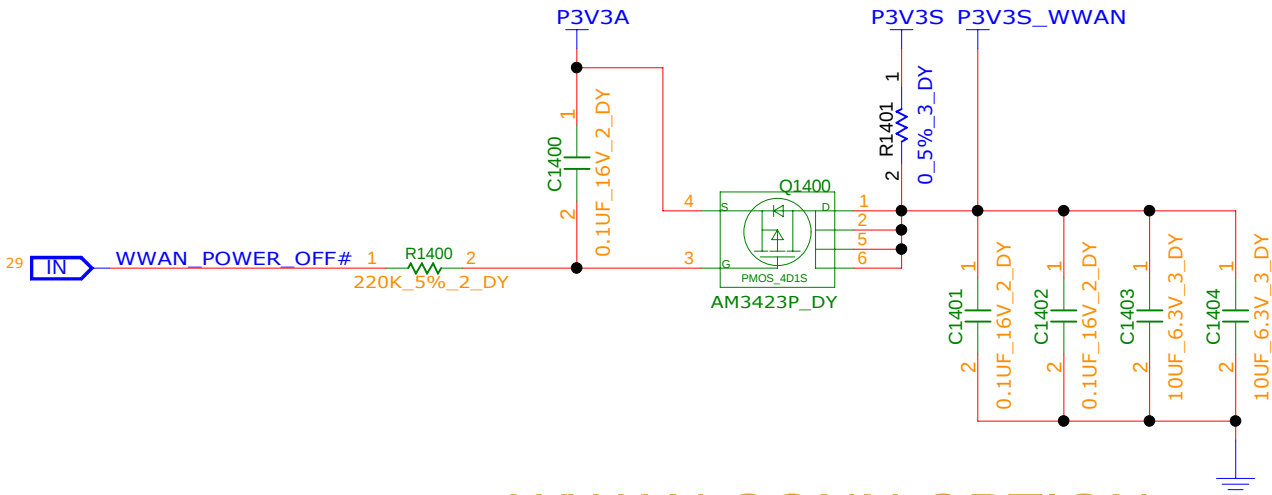


PORT	Discription	Sense
A	HP	HPS
B	MIC	MICS
C	SPKR	
D		

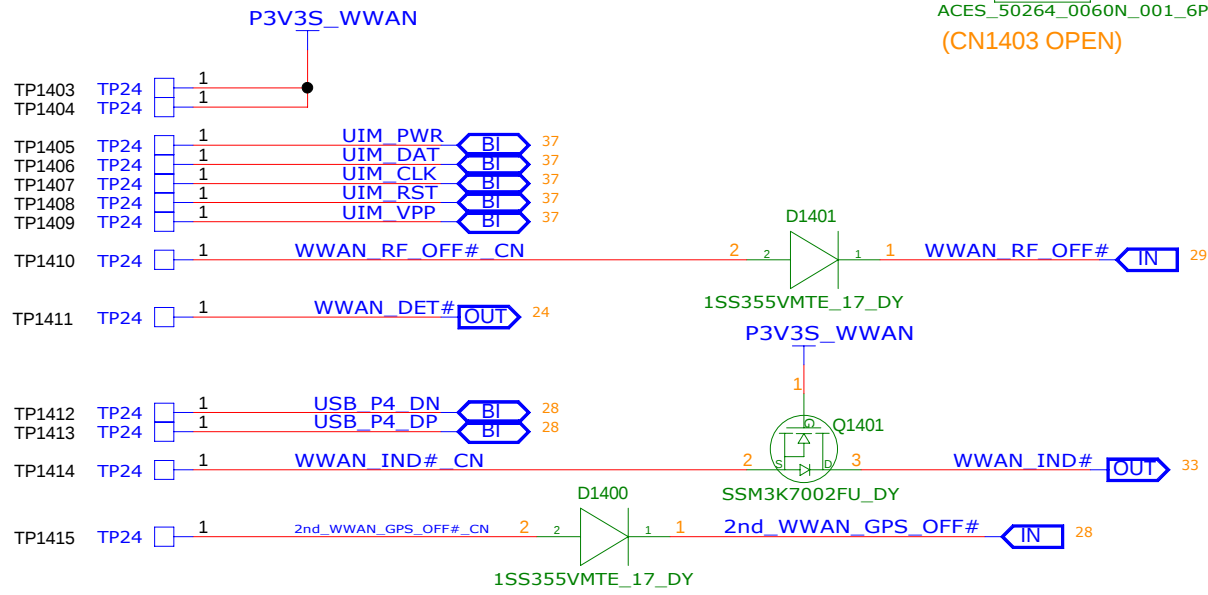
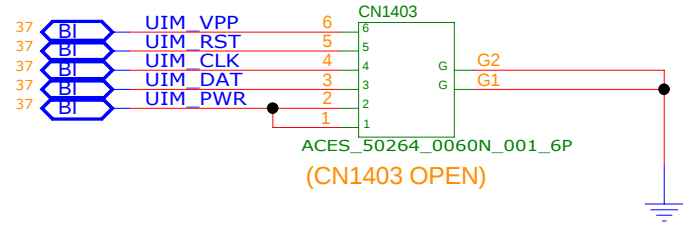


INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
AUDIO CODEC			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01



WWAN CONN OPTION



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
WWAN_OPTION

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 24-AUG-2011

SHEET 37 of 39

8

7

6

5

4

3

2

1

8

7

6

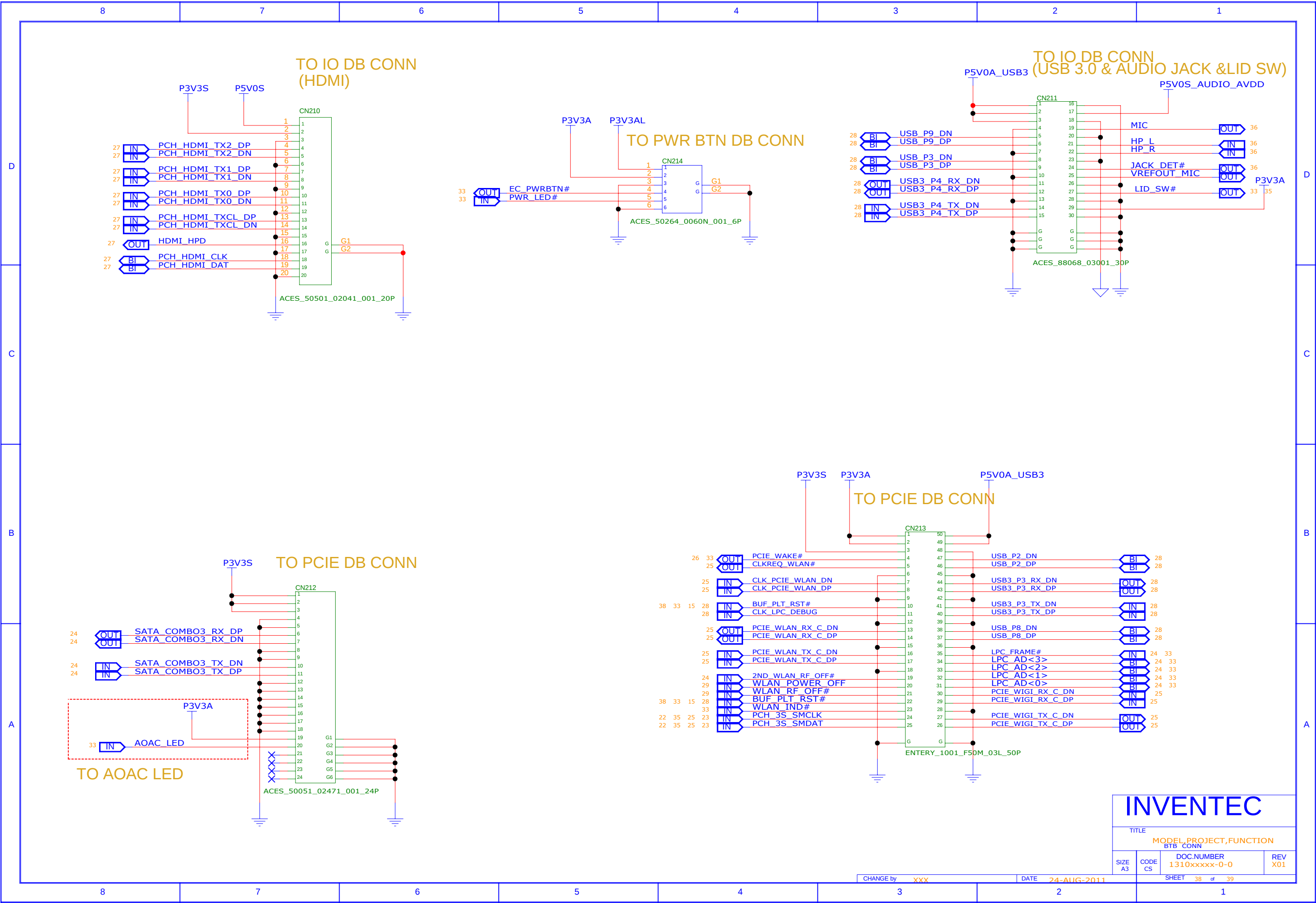
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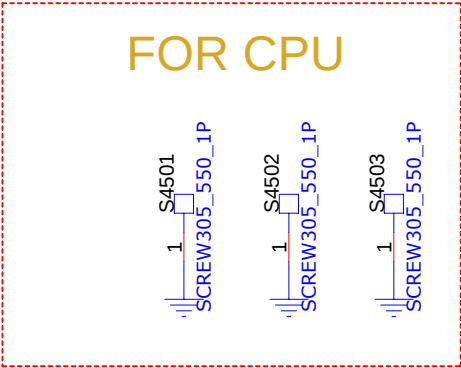
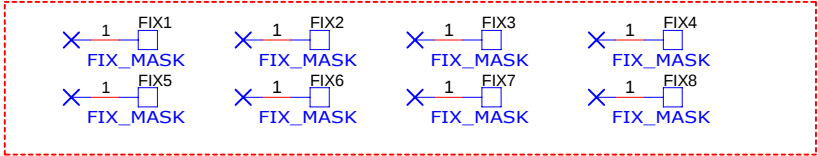
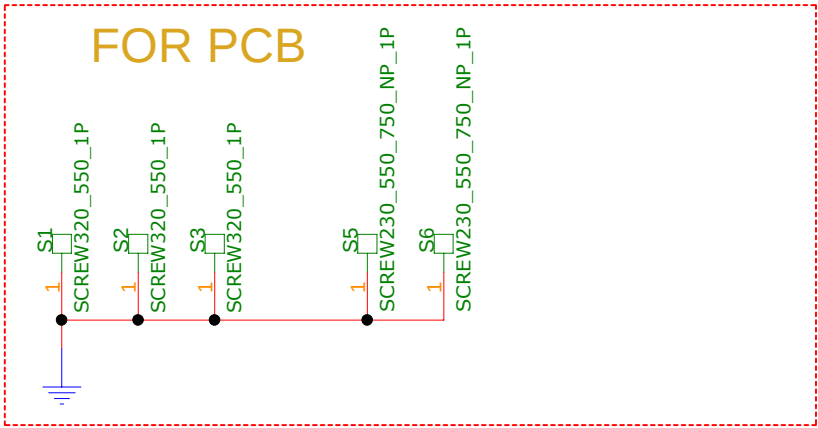
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2

1



8	7	6	5	4	3	2	1
D							
C							
B							
A							
8	7	6	5	4	3	2	1



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
SCREW

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxxx-0-0	REV X01
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CHANGE by XXX DATE 24-AUG-2011

SHEET 39 of 39