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38	PCI (PCI BUS)	0.1	12/04
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P. Leader	Check by	Design by

Project Code & Schematics Subject: MS72 Main Board

PCB P/N: (FUBAI) 1P-006B100-60SA
(NAN YA) 1P-006B200-60SA
(HANSTAR) 1P-006B500-60SA

FOXCONN HON HAI Precision Ind Co., Ltd CCPBG - R&D Division

Title: Index Page

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33	AUDIO(CODEC & POWER)	0.1	12/04
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49	CPU_Vcore ---MAX8771	0.1	12/04
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51	OVP protection	0.1	12/04
52	History (1)	0.1	12/04
53	History (2)	0.1	12/04
54			
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Project Code & Schematics Subject:	MS72 Main Board	PCB P/N:	(FUBAI) 1P-006B100-60SA (NAN YA) 1P-006B200-60SA (HANSTAR) 1P-006B500-60SA

FOXCONN HON HAI Precision Ind. Co., Ltd.
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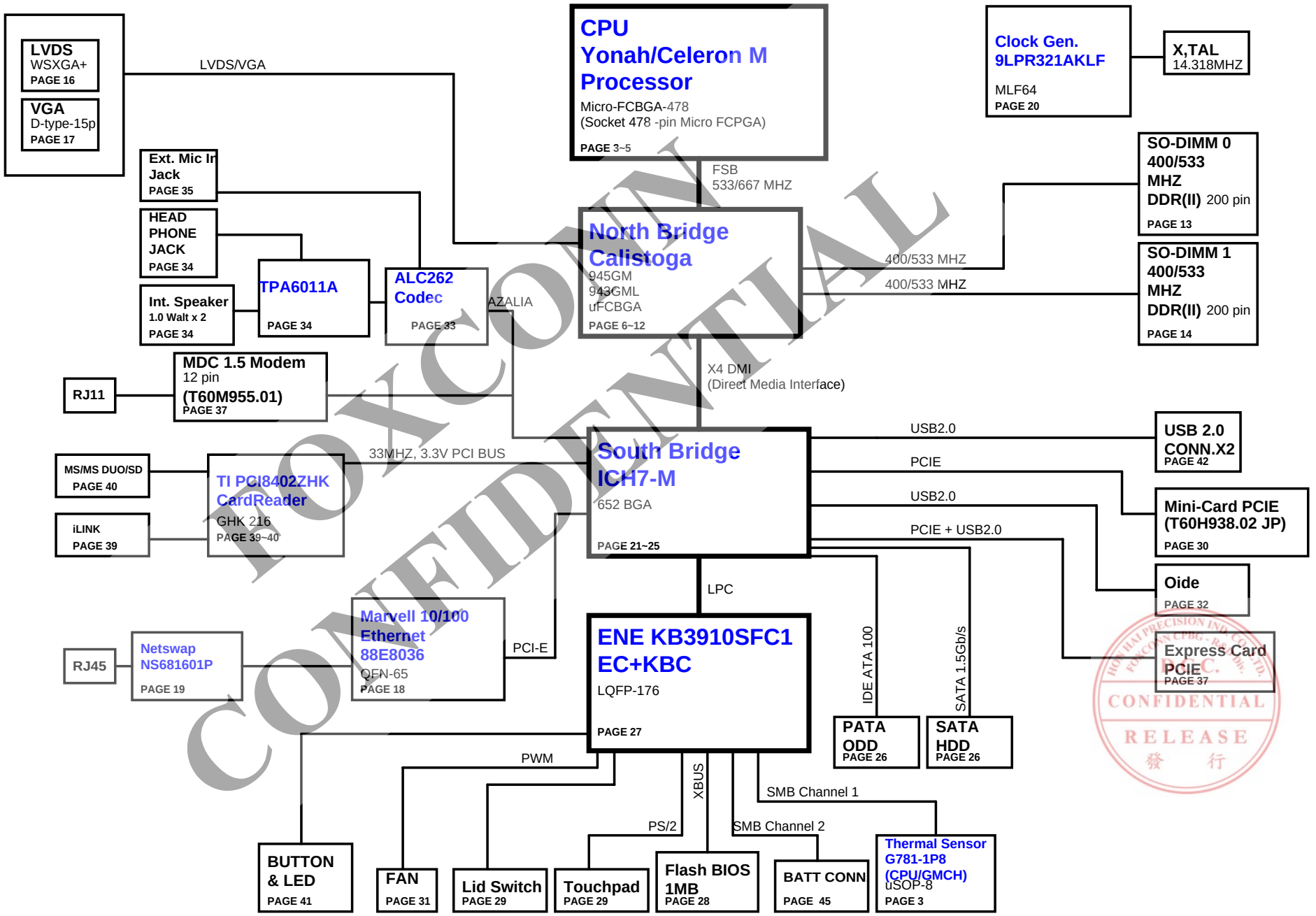
RAPTOR3/MS72(CALISTOGA GM/GML Block Diagram)



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Title: Block Diagram		
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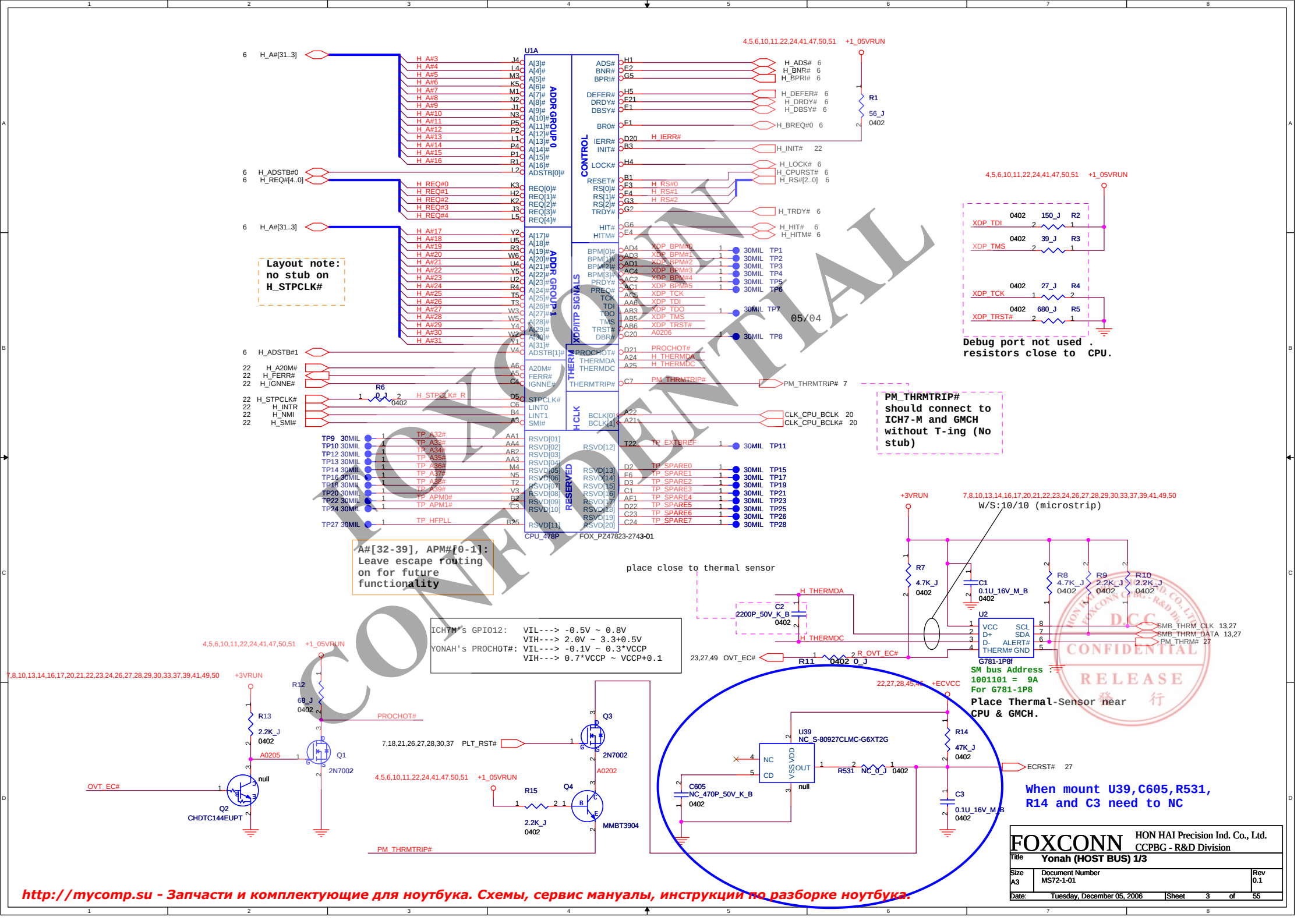

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division
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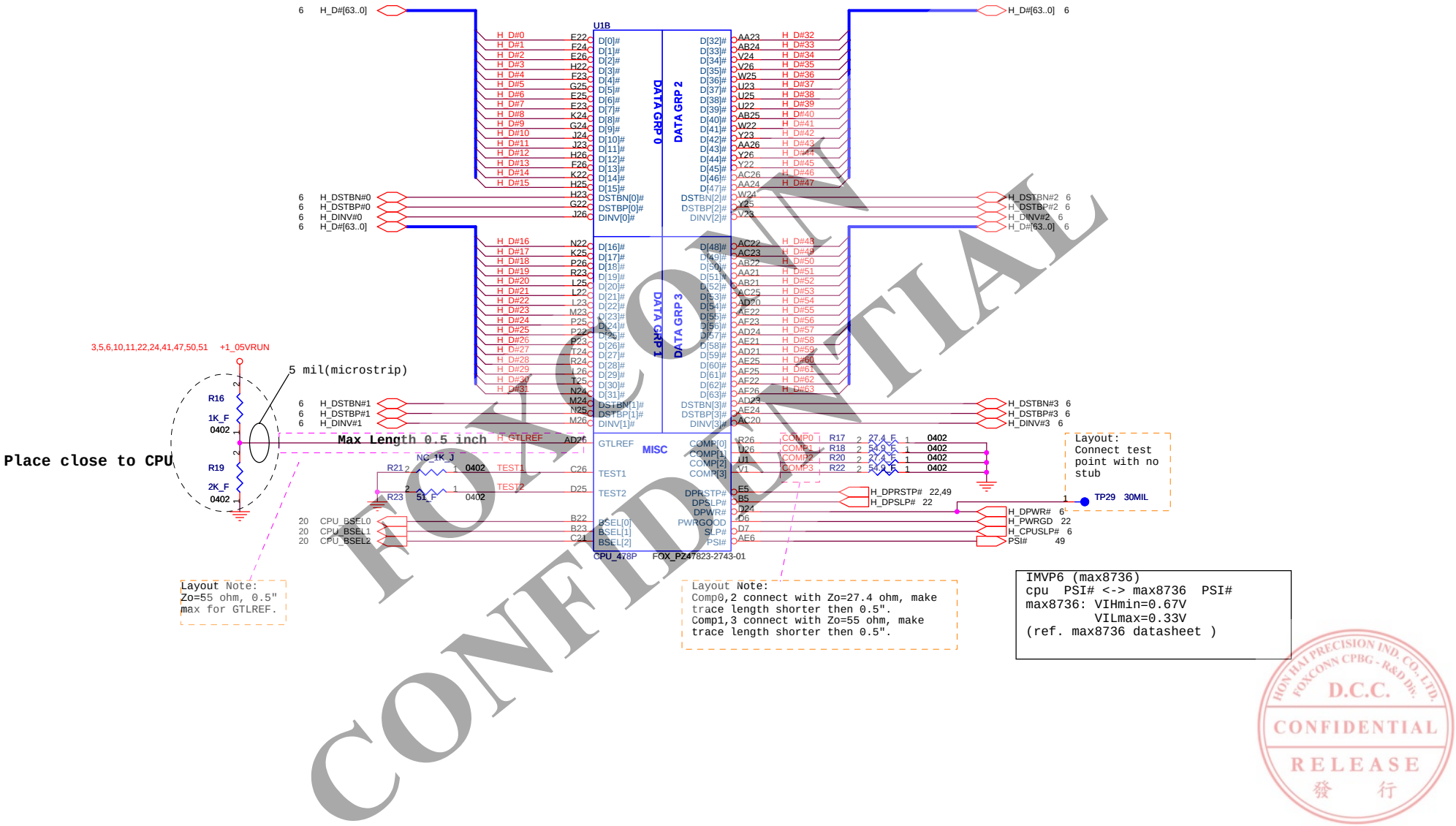

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	Yonah (HOST BUS) 2/3	
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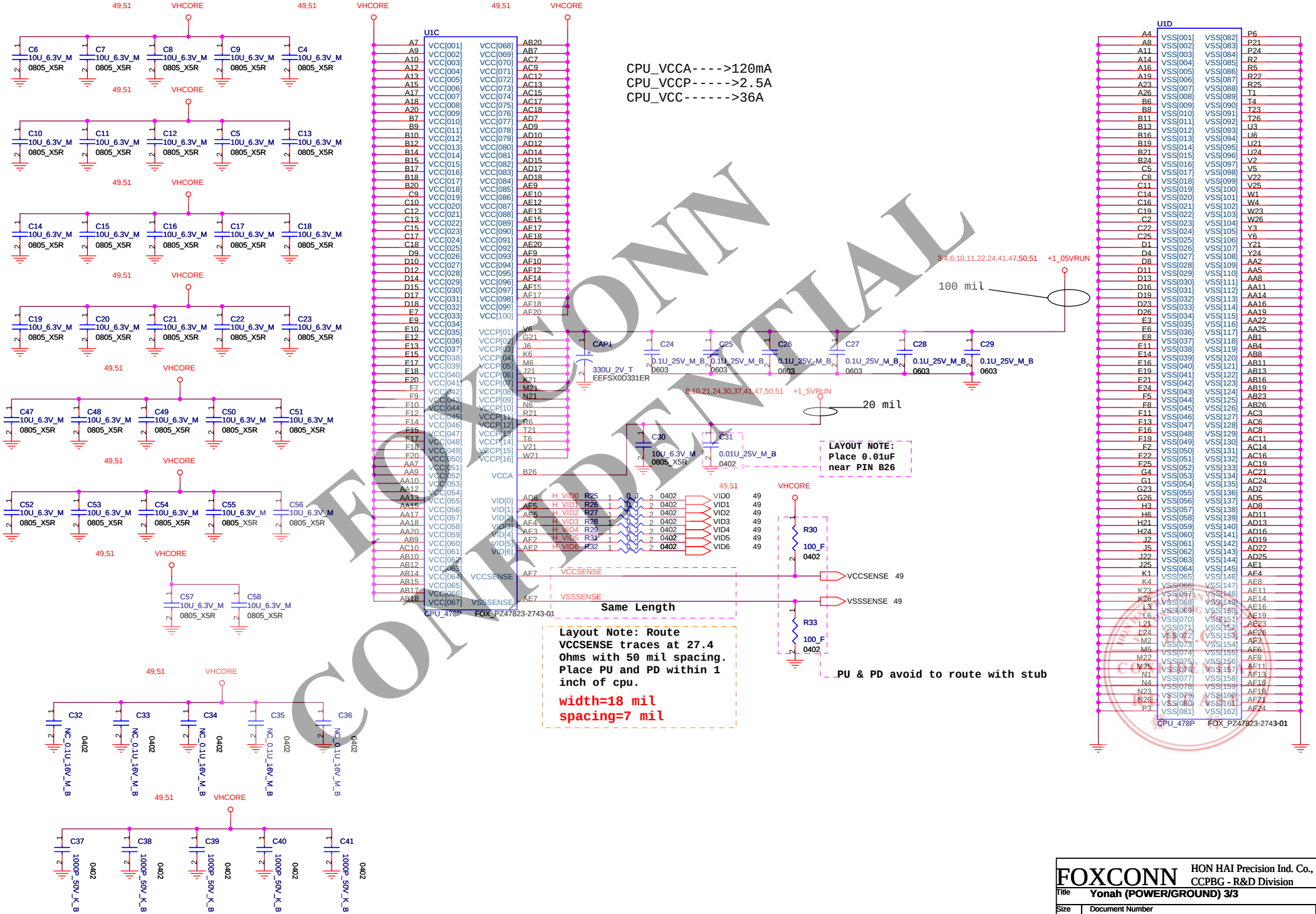

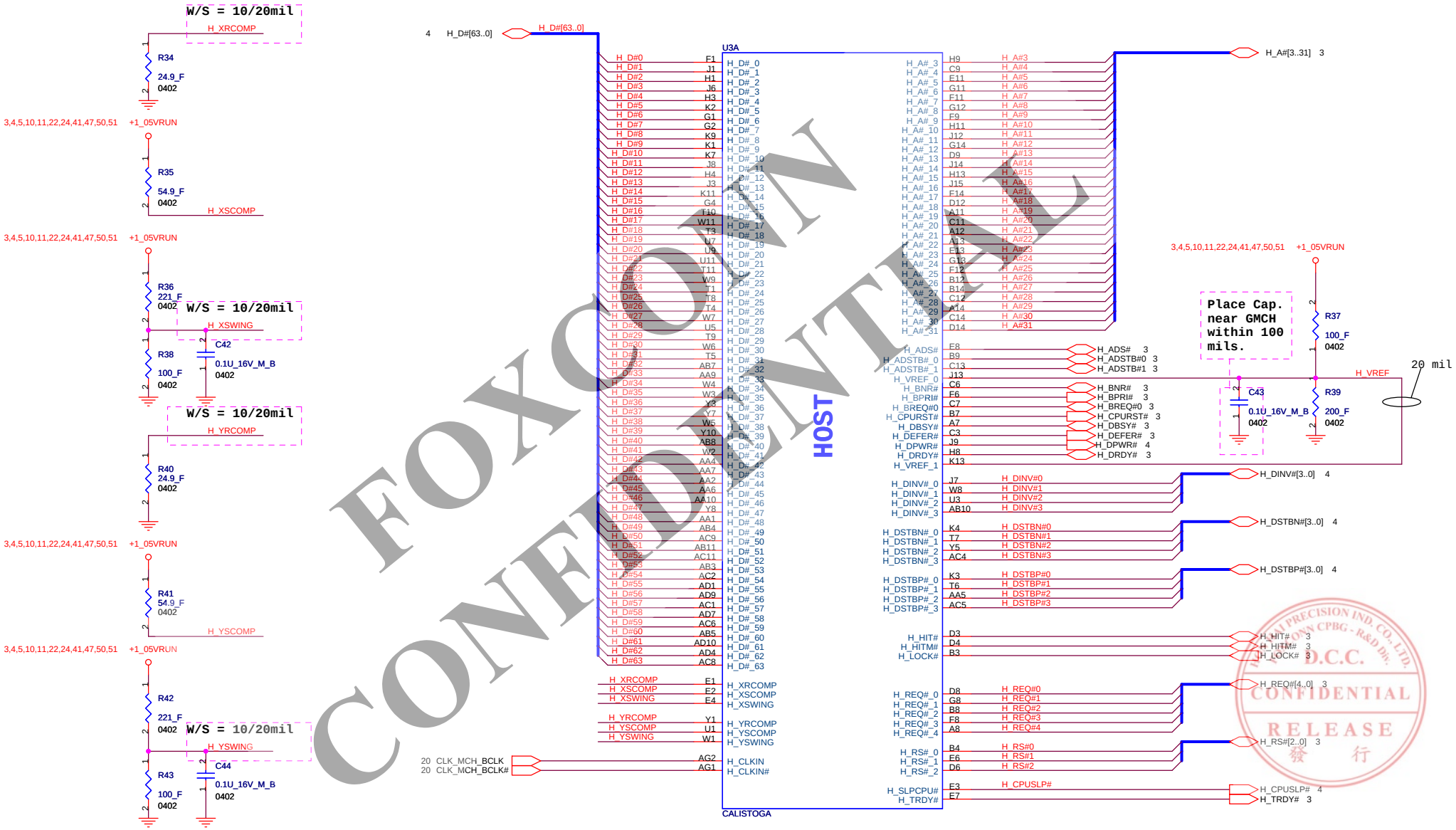

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	CALISTOGA (HOST) 1 of 7	
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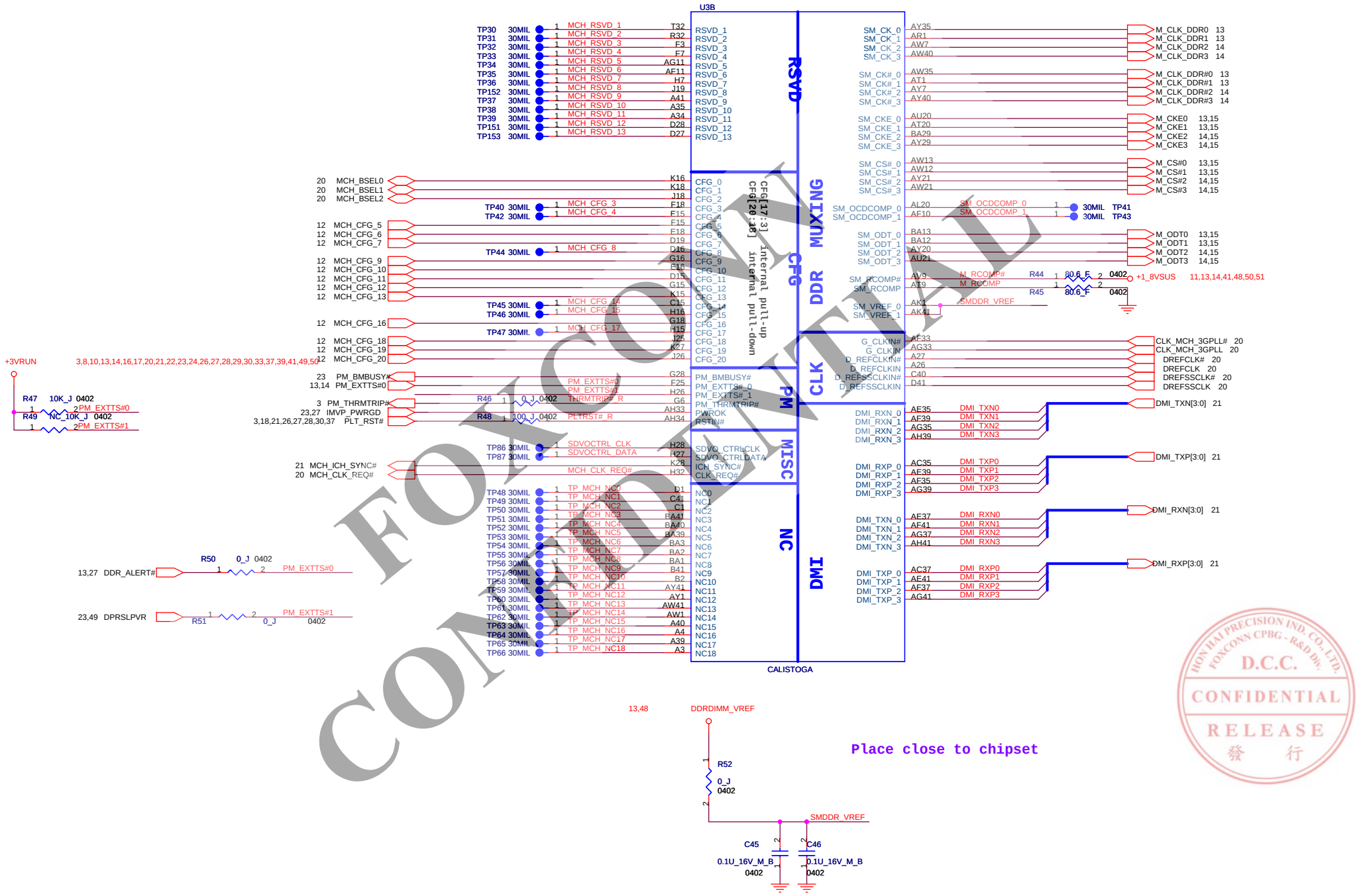

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	CALISTOGA (DMI) 2 of 7	
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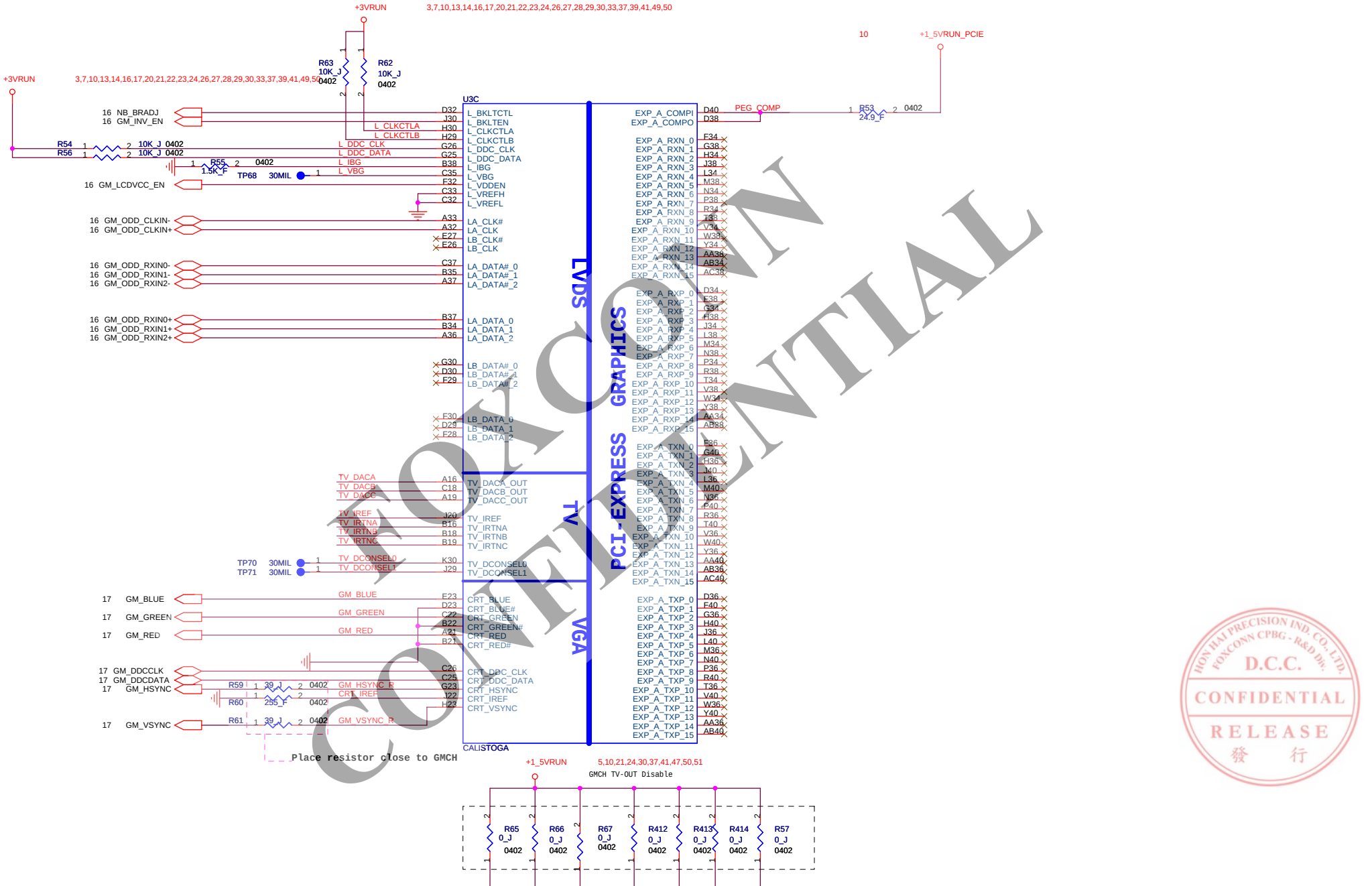

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	CALISTOGA (DMI) 3 of 7	
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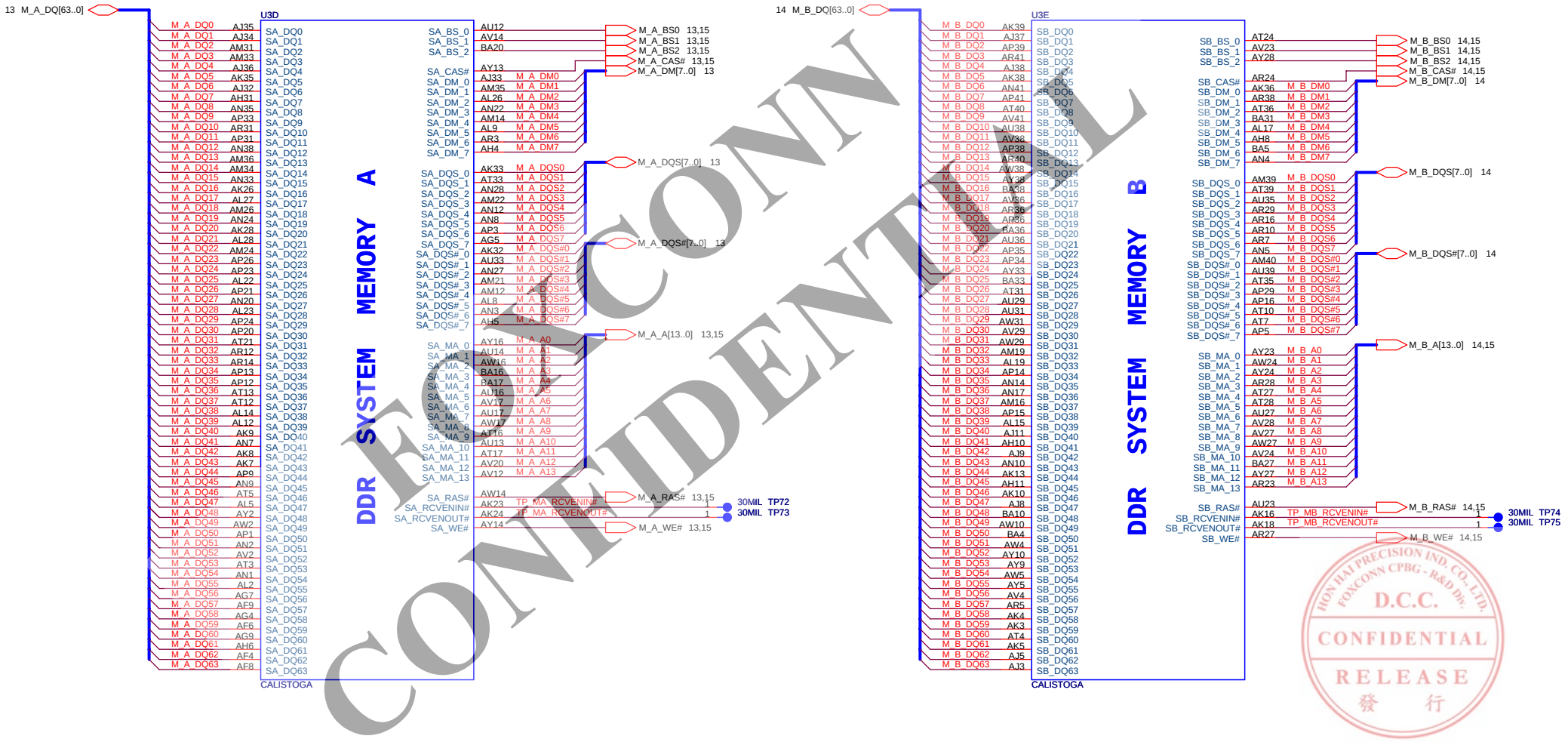

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	CALISTOGA (DDRII) 4 of 7	
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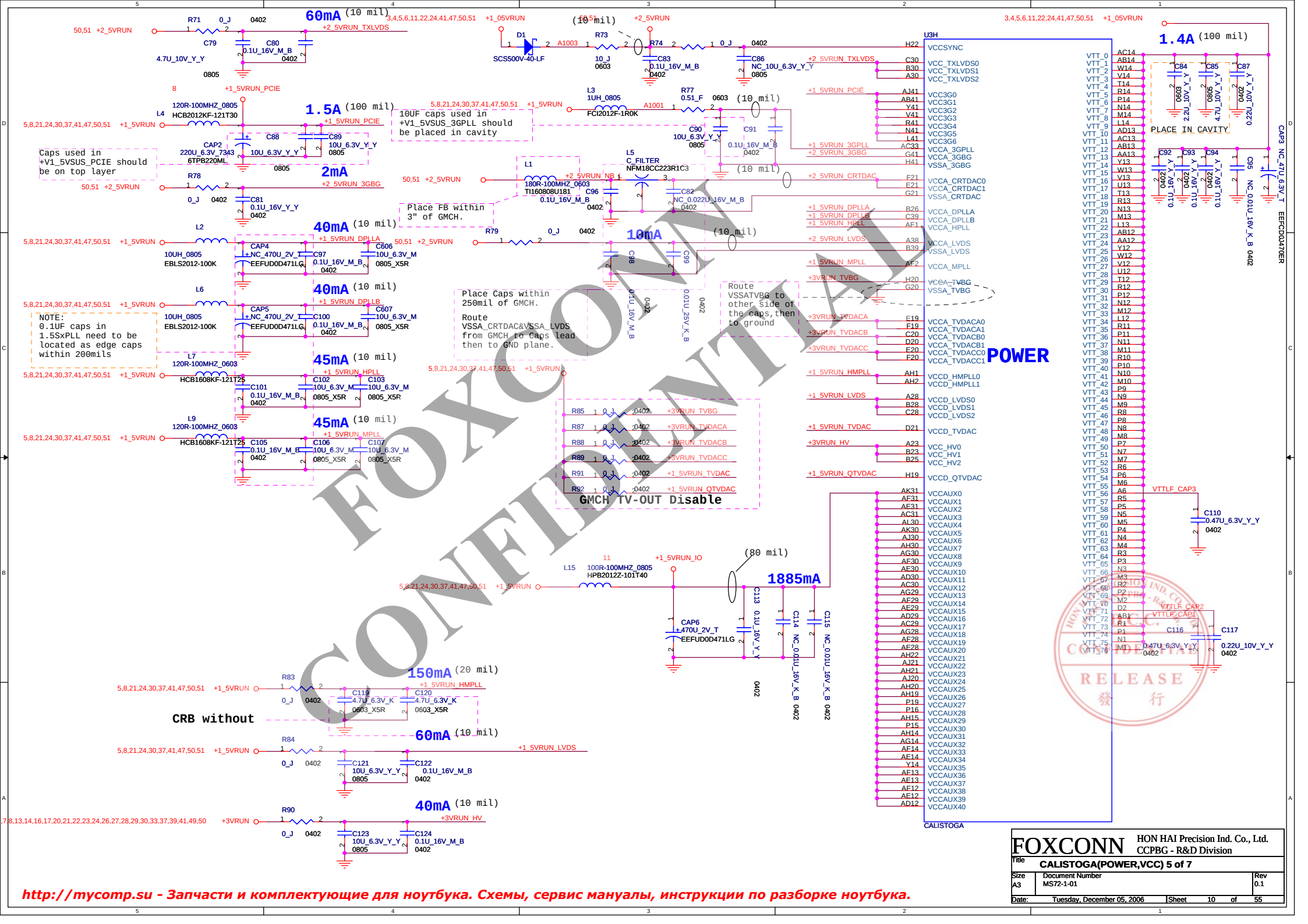
POWER
GMCH TV-OUT Disable
CRB without
CALISTOGA
60mA (10 mil)
1.5A (100 mil)
2mA
40mA (10 mil)
40mA (10 mil)
45mA (10 mil)
45mA (10 mil)
10mA
1885mA
150mA (20 mil)
60mA (10 mil)
40mA (10 mil)
1.4A (100 mil)
PLACE IN CAVITY
10UF caps used in +V1_5VSUS_3GPLL should be placed in cavity
Caps used in +V1_5VSUS_PCIE should be on top layer
Place FB within 3" of GMCH.
Place Caps within 250mil of GMCH.
Route VSSA_CRTDAC&VSSA_LVDS from GMCH to caps lead then to GND plane.
Route VSSATVBG to other side of the caps, then to ground
NOTE: 0.1UF caps in 1.5SxPLL need to be located as edge caps within 200mils
FOXCONN CONFIDENTIAL
RELEASE
http://mycomp.su - Запчасти и комплектующие для ноутбука. Схемы, сервис мануалы, инструкции по разборке ноутбука.
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HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
CALISTOGA(POWER,VCC) 5 of 7
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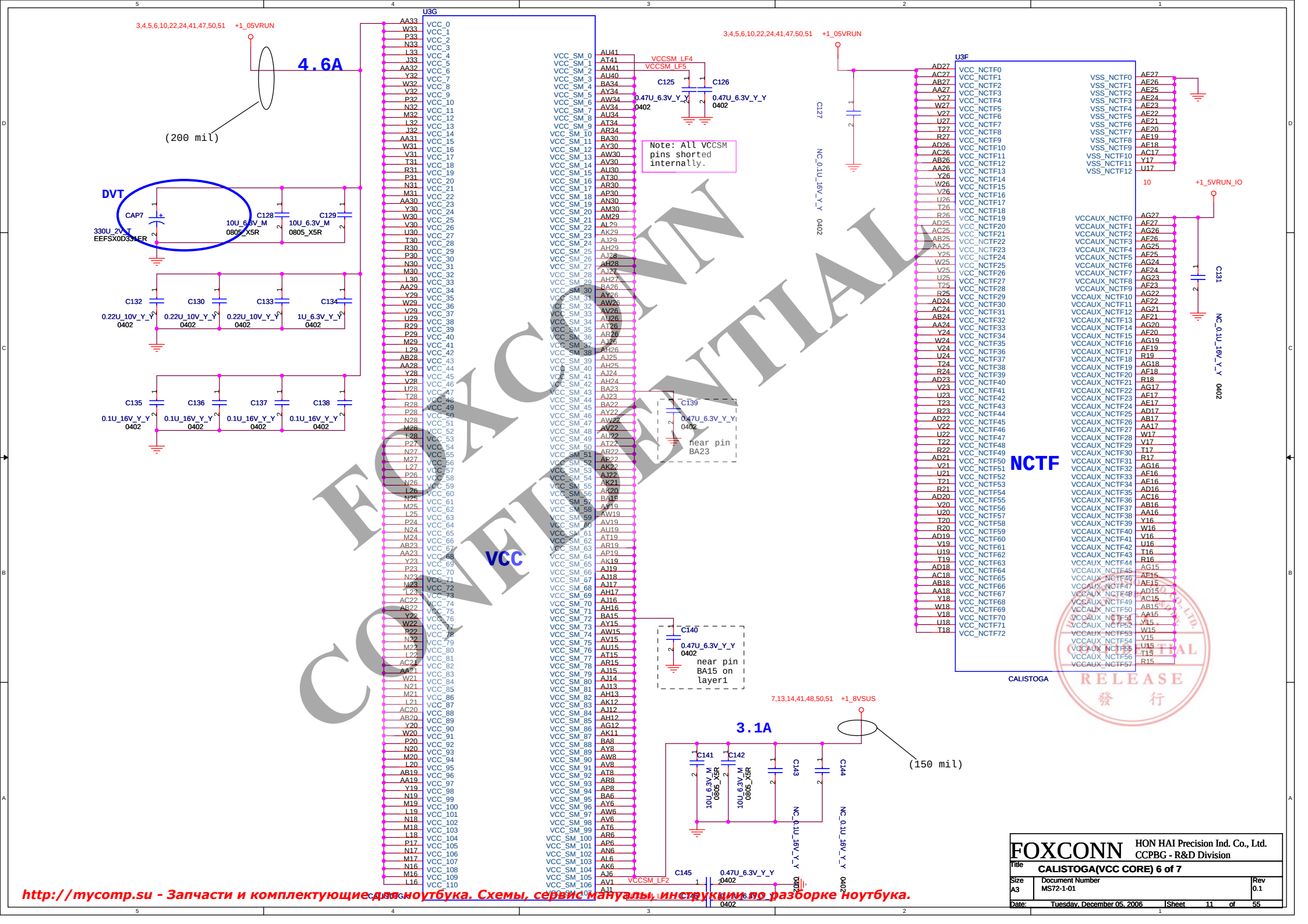

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	CALISTOGA(VCC CORE) 6 of 7	
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7 MCH_CFG_5 → 1 30MIL TP76

MCH_CFG_5	Low = DMIx2 High = DMIx4

7 MCH_CFG_6 → 1 30MIL TP77

MCH_CFG_6	Low = Moby Dick High = Calistoga

DDR2 select (default high)

7 MCH_CFG_7 → 1 30MIL TP78

MCH_CFG_7 (CPU Strap)	Low = RSVD High = Mobile Yonah processor

7 MCH_CFG_9 → 1 30MIL TP81

MCH_CFG_9 (PCIE Graphics Lane)	Low = Reverse Lane High = Normal operation

For layout convenience

7 MCH_CFG_10 → 1 30MIL TP82

MCH_CFG_10 (HOST PLL VCC SELECT)	Low = RESERVED High = MOBILITY

7 MCH_CFG_11 → R93 NC_2.2K_J 0402

MCH_CFG_11 (PSB 4x CLK ENABLE)	Low = Calistoga High = Reserved

7 MCH_CFG_12 → 1 30MIL TP84
7 MCH_CFG_13 → 1 30MIL TP85

MCH_CFG_[13:12] (XOR/ALLZ)	00=Partial Clock Gating Disable 01=XOR Mode Enable 10=All-Z Mode Enable 11=Normal Operation(Default)

7 MCH_CFG_16 → 1 30MIL TP160

MCH_CFG_16 (FSB Dynamic ODT)	Low = Dynamic ODT Disabled High = Dynamic ODT Enable

MCH_CFG_18 (VCC_CORE Select)	Low = 1.05V(default) High = 1.5V

7 MCH_CFG_18 → 1 30MIL TP79

MCH_CFG_19 (DMI LANE REVERSAL)	Low = Normal(default) High = LANES REVERSED

7 MCH_CFG_19 → 1 30MIL TP80

MCH_CFG_20 (PCIe Backward Interpoerability mode)	Low = Only SDVO or PCIE x1 is operational (defaults)) High = SDVO and PCIE x1 are operating simultaneously via the PEG port

7 MCH_CFG_20 → 1 30MIL TP83

Layout Noe:
Location of all MCH_CFG strap resistors needs to be close to trace to minimize stub

U3I

VSS

CALISTOGA

U3J

VSS

CALISTOGA

FOXCONN HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

Title: CALISTOGA(VSS) 7 of 7

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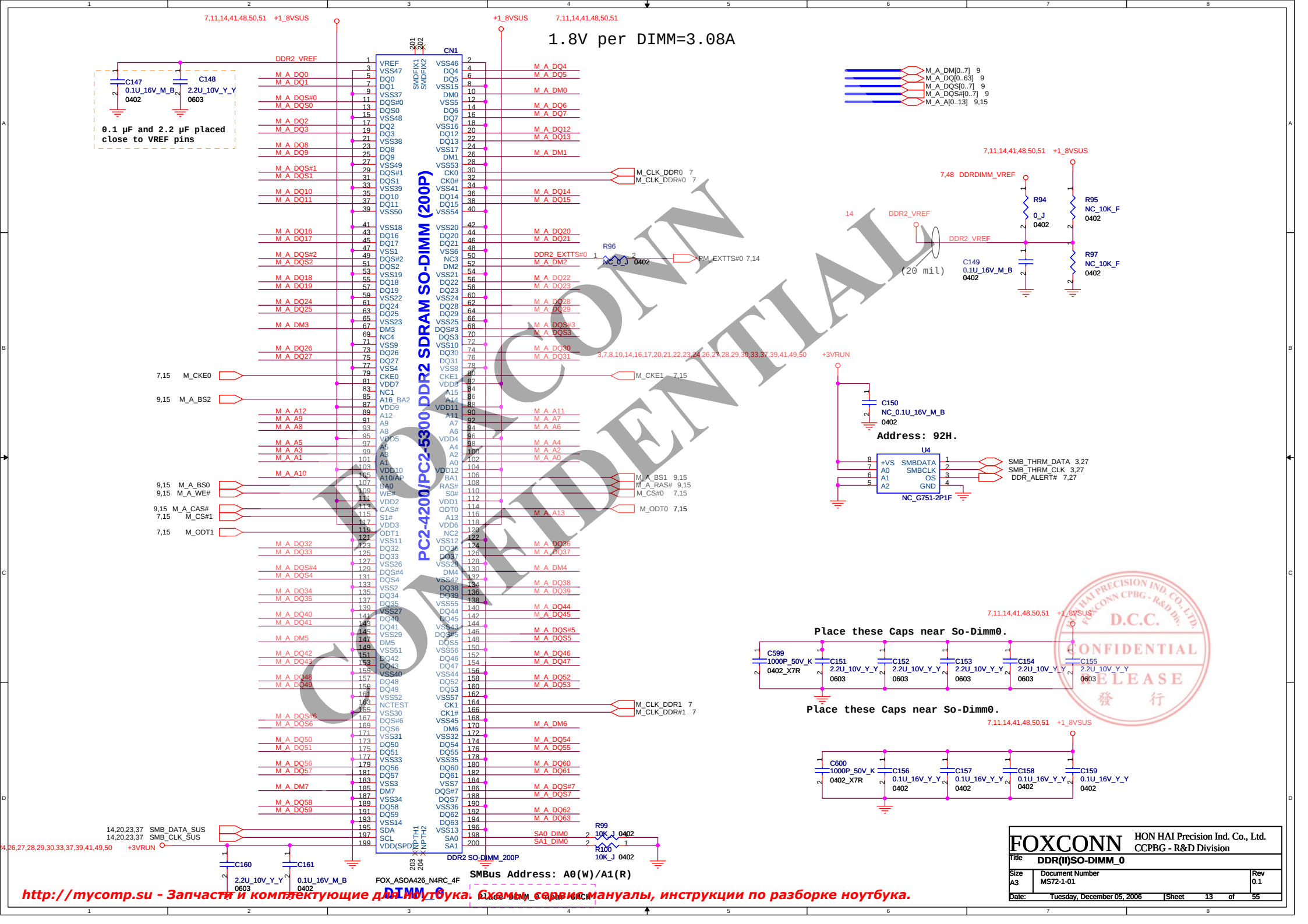

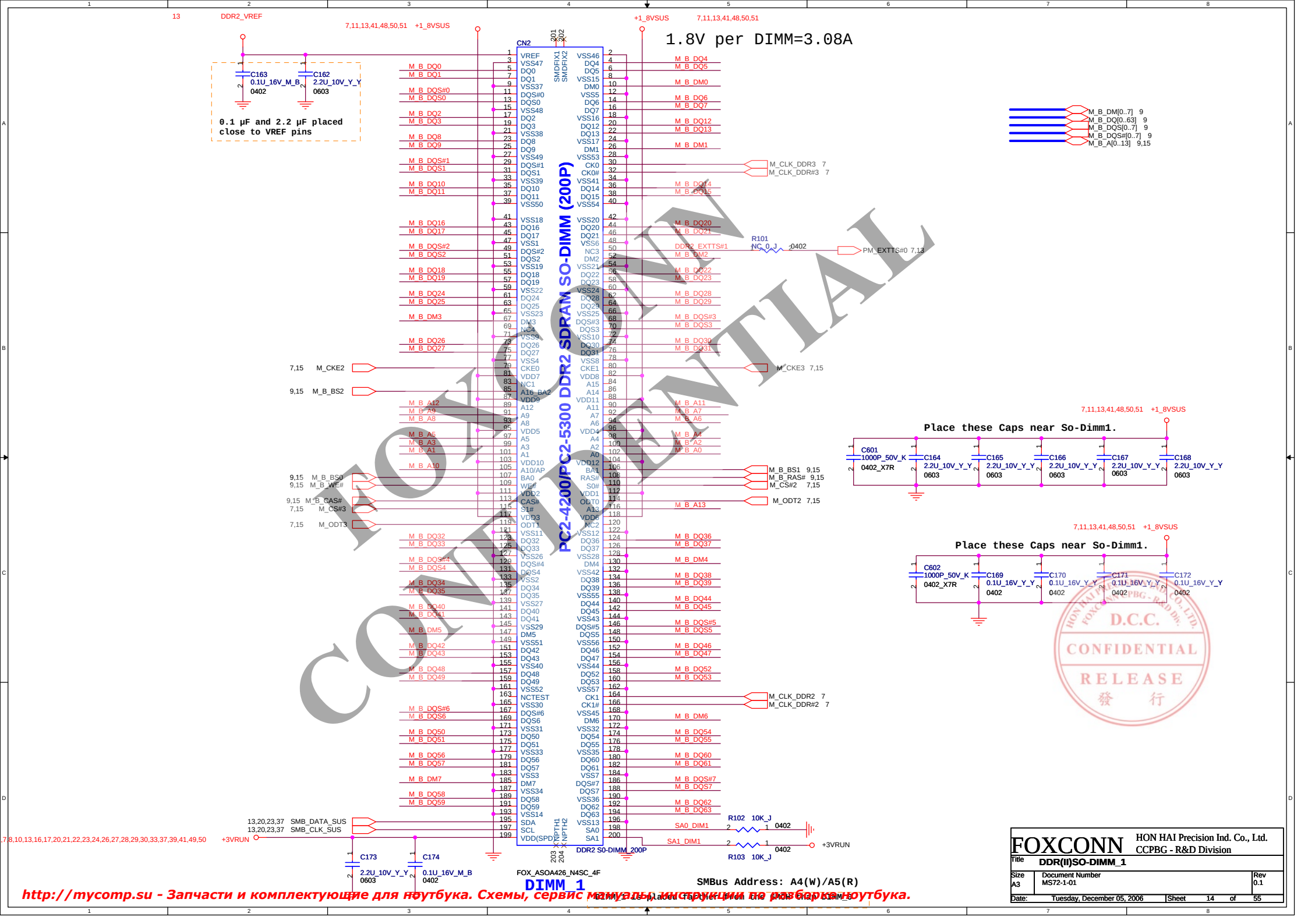

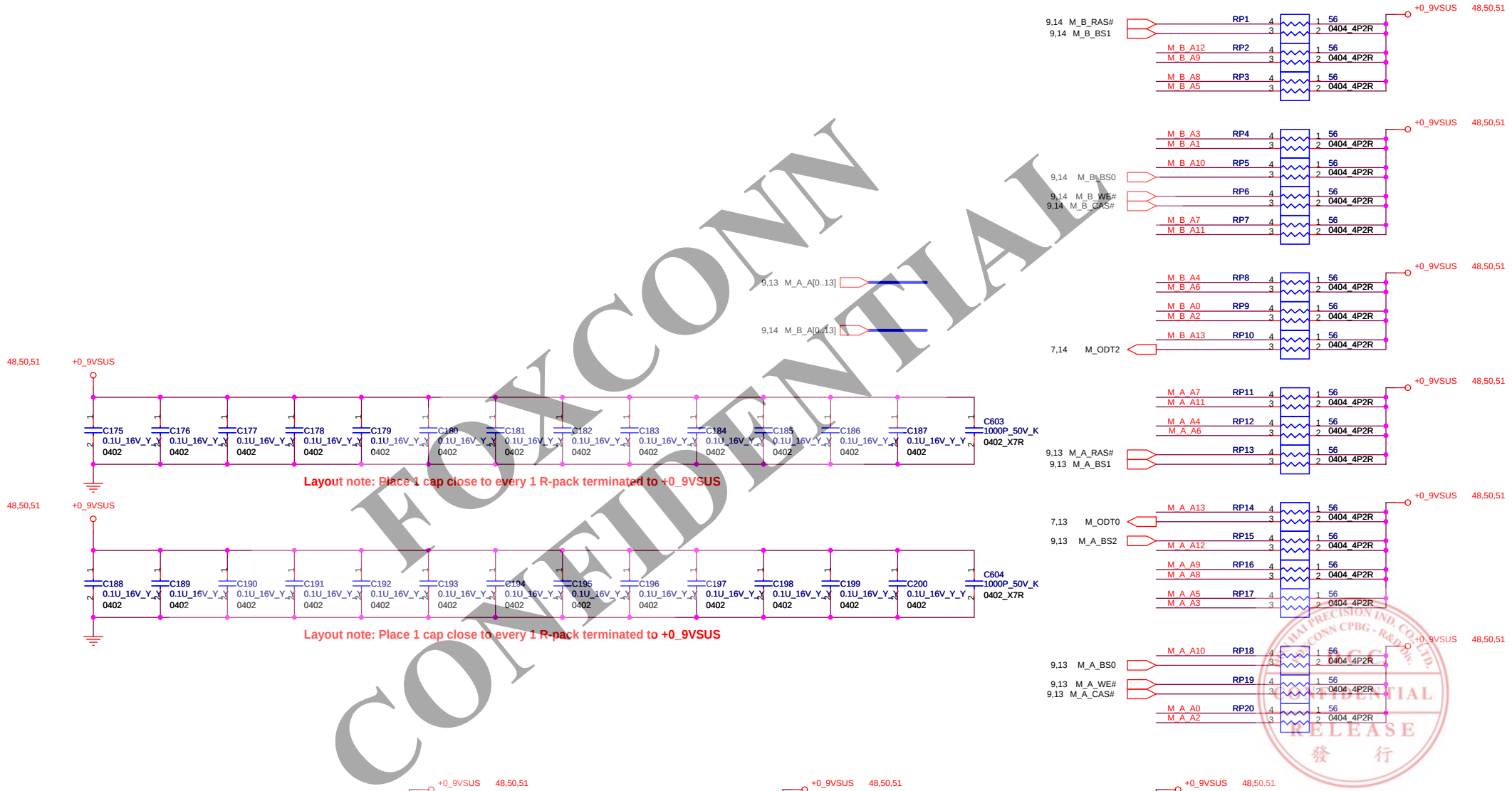

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: DDR(II)Termination		
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LVDS

ODD_CLKIN- GM_ODD_CLKIN- 8
ODD_CLKIN+ GM_ODD_CLKIN+ 8

ODD_RXIN0- GM_ODD_RXIN0- 8
ODD_RXIN0+ GM_ODD_RXIN0+ 8

ODD_RXIN1- GM_ODD_RXIN1- 8
ODD_RXIN1+ GM_ODD_RXIN1+ 8

ODD_RXIN2- GM_ODD_RXIN2- 8
ODD_RXIN2+ GM_ODD_RXIN2+ 8

41,45,46,47,48,49,50 DCBATOUT

C201 0.1U_50V_K_B 0603

2A

8 NB_BRADJ

INV_ENABLE
INV_BRADJ

CN4
FOX_HS8206E
HEADER CONN_6P

R117 10K_J 0402

R116 10K_J 0402

LVDS CONNECTOR

LCDVCC

C579 0.1U_16V_Y_Y 0402

21 LVDS_GPIO

R515 NC_0_J 0402

CN3

ODD_RXIN0-
ODD_RXIN0+
ODD_RXIN2-
ODD_RXIN2+
ODD_RXIN1-
ODD_RXIN1+
ODD_CLKIN-
ODD_CLKIN+

CTB_20P
FOX_GS12201-1011A-9F

INVERTER CONNECTOR

U5 +3VRUN 74AHC1G08GW

27 INV_EN_EC

INV_ENABLE

INV_ENABLE_1

U6 +3VRUN

27,41 LIDIN#

8 GM_INV_EN

74AHC1G08GW

R512 100K_J 0402

PANEL ID

SW1

HDS404-E_SW-SLIDE

R519 NC_0_J 0402

LCDID0 23
LCDID1 23
LCDID2 23
LCDID3 23

Type	WXGA	WXGA	WSXGA
Size	15.4" wide	15.4" wide	15.4" wide
Vender	CPT	QDI	InnoLux
Device Name	CLAA154WA05AN	QDI15TL0703	TBD
Panel ID Check[3..0]	1001	1010	1011

ON=0

3,7,8,10,13,14,17,20,21,22,23,24,26,27,28,29,30,33,37,39,41,49,50 +3VRUN

8 GM_LCDVCC_EN

R119 100K_J 0402

C206 4.7U_6.3V_K 0805_X5R

U7

GND OUT3
IN1 OUT2
IN2 OUT1
EN OC#

G548B1P1U_1.0

1.5A

LCDVCC

C207 4.7U_10V_Y_Y 0805

C208 0.1U_16V_Y_Y 0402

R120 200_J 0603

DISCHARGE

The R461 will consume about 0.054 Watt (3.3x3.3/200 = 0.054W). We changed resistor to 0603 size (1/8 Watt)

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Title: LVDS		
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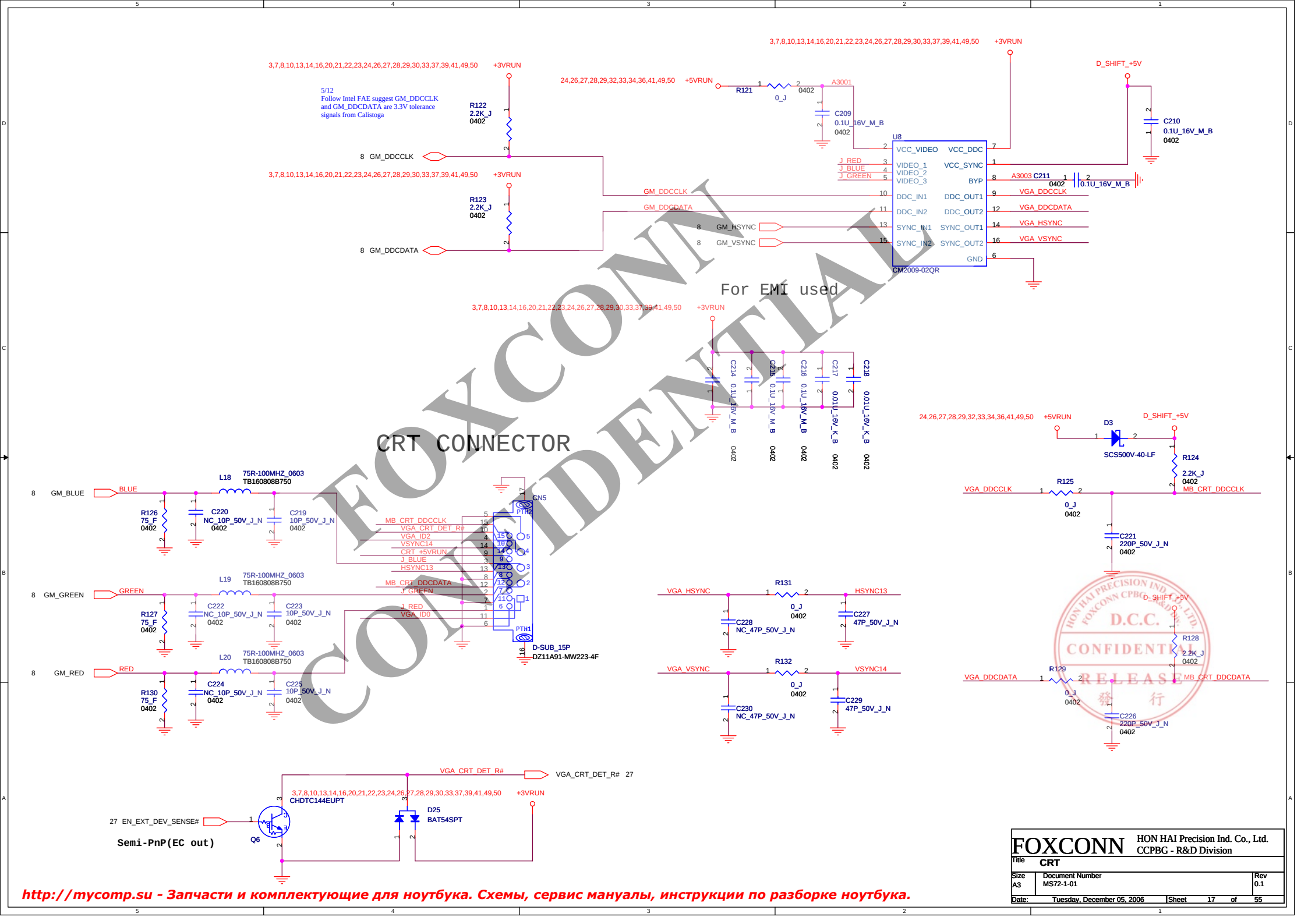

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: CRT		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
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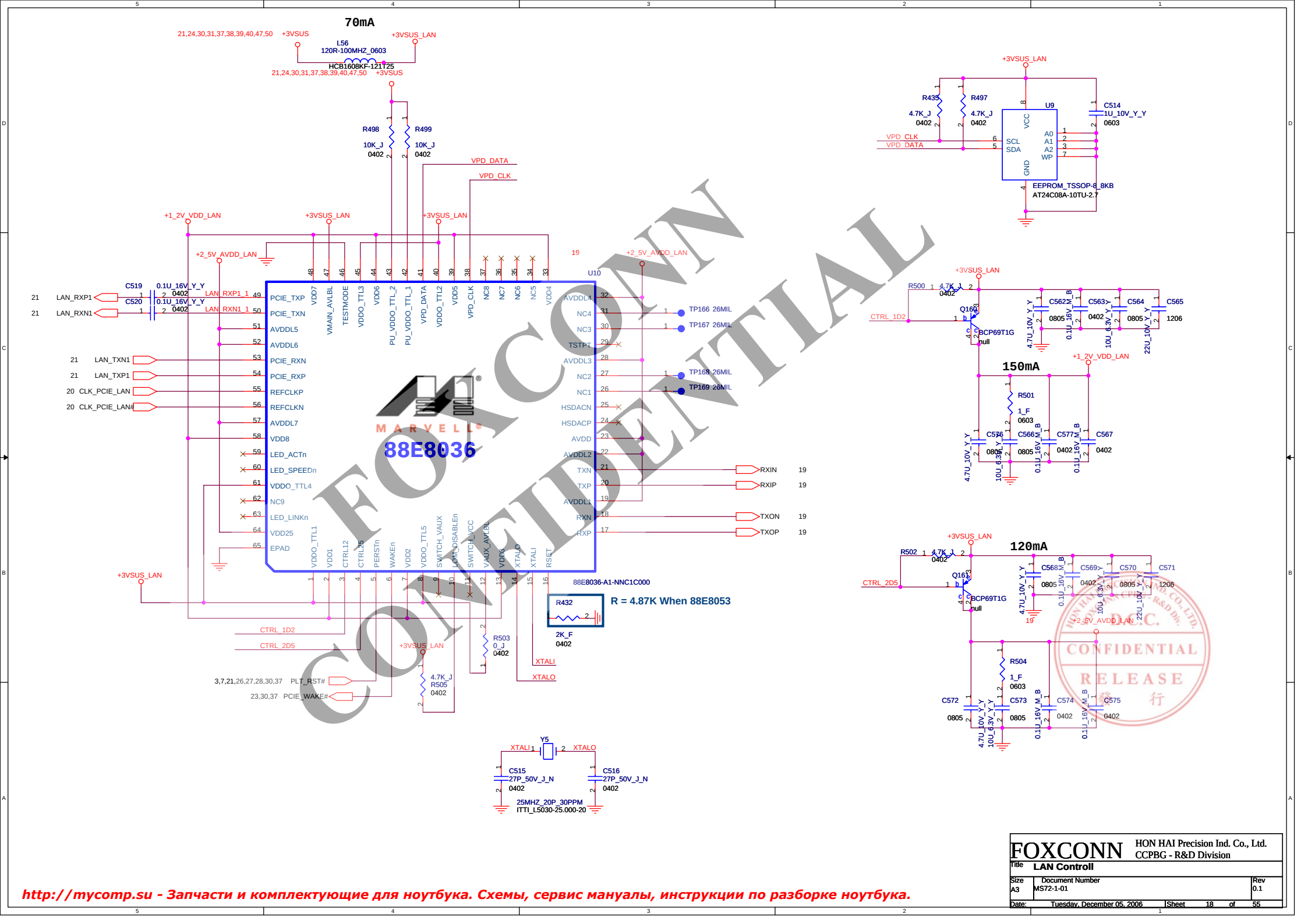

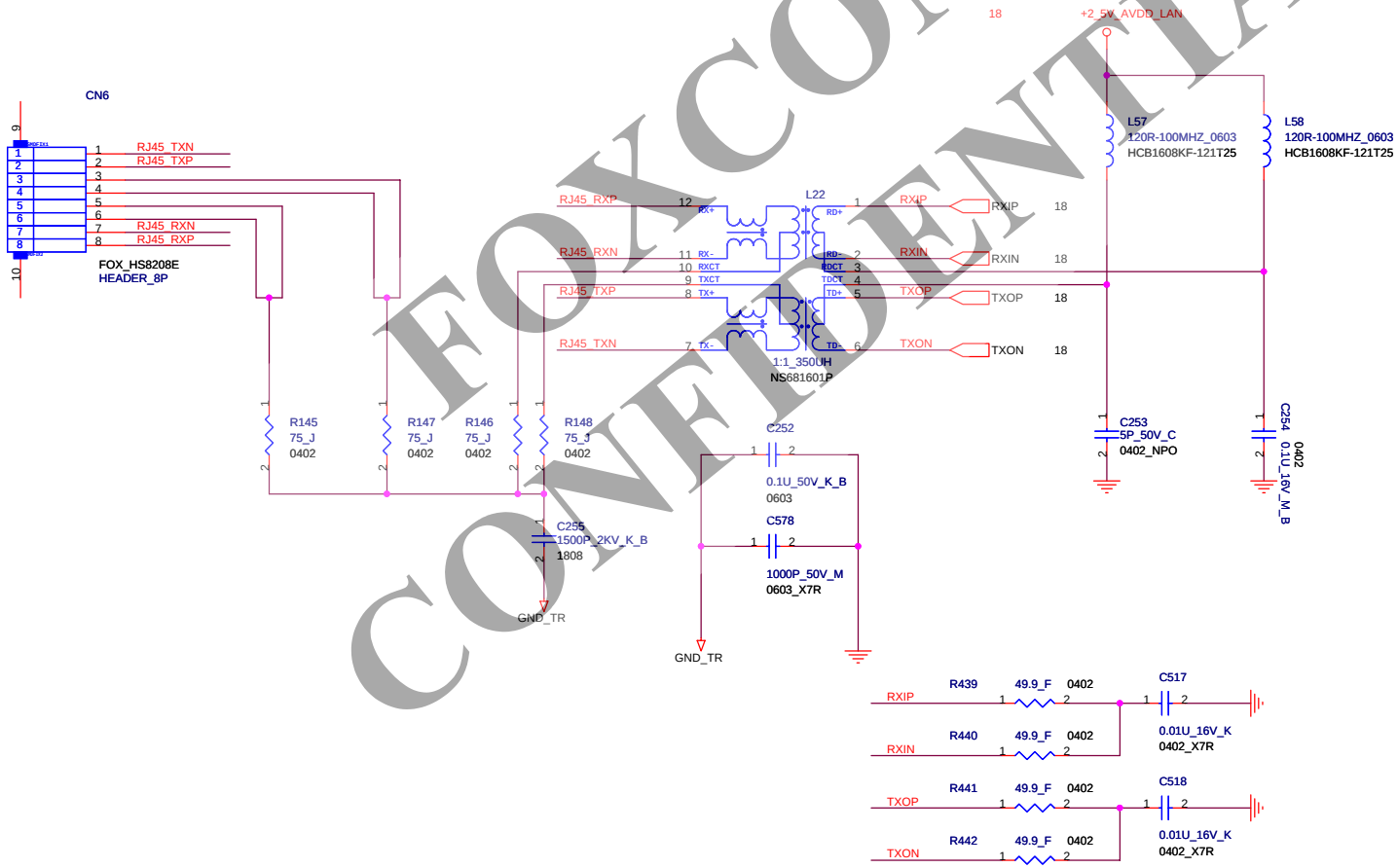

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
Title	LAN Transformer		
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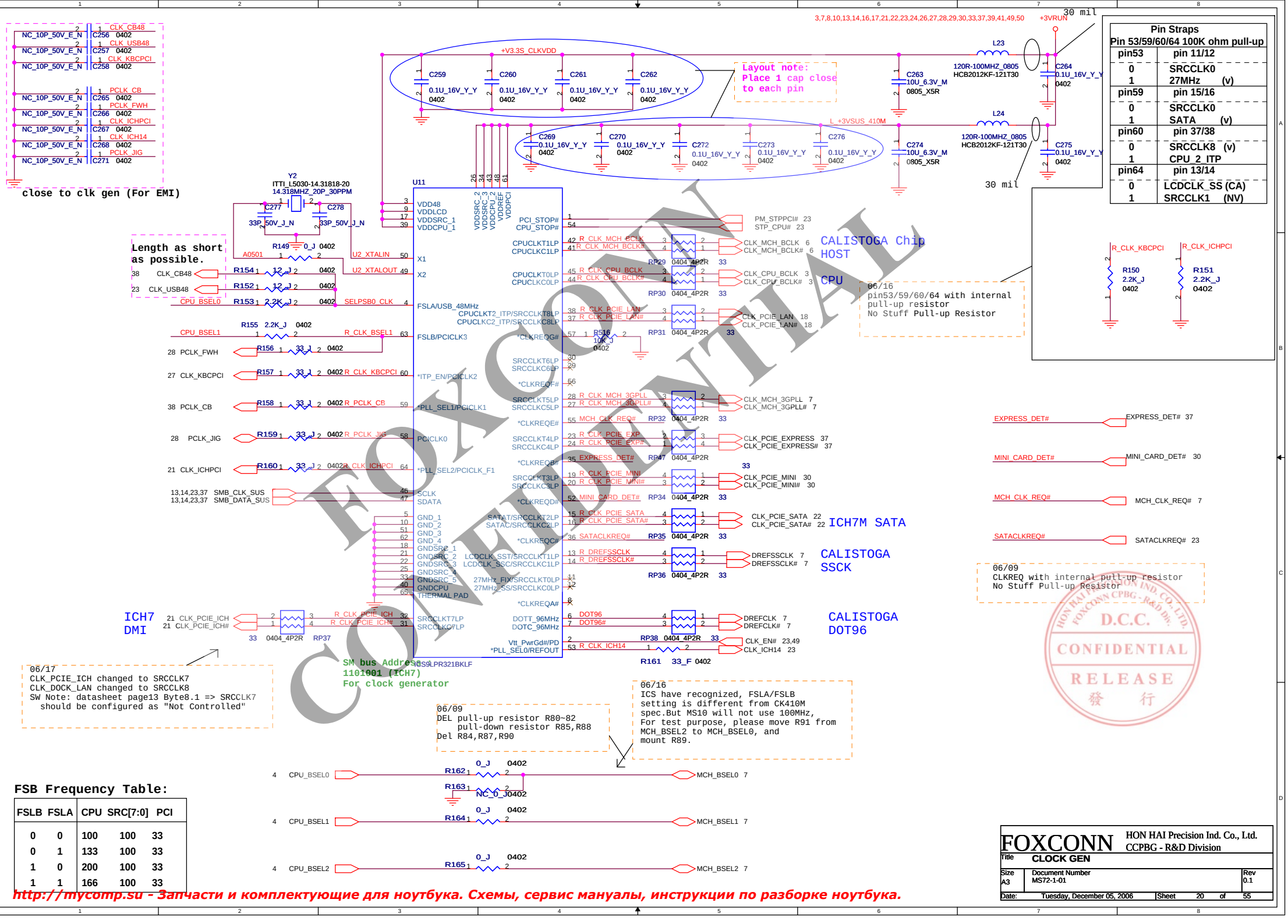
close to clk gen (For EMI)
Length as short as possible.
Layout note: Place 1 cap close to each pin
CALISTOGA Chip HOST CPU
ICH7M SATA
CALISTOGA SSCK
CALISTOGA DOT96
ICH7 DMI
SM bus Address 1101001 (ICH7) For clock generator
Pin Straps
Pin 53/59/60/64 100K ohm pull-up
pin53 pin 11/12
0 SRCCLK0 27MHz (v)
1 SRCCLK0 SATA (v)
pin59 pin 15/16
0 SRCCLK8 (v)
1 CPU_2_ITP
pin60 pin 37/38
0 LCDCLK_SS (CA)
1 SRCCLK1 (NV)
pin64 pin 13/14
06/16 pin53/59/60/64 with internal pull-up resistor No Stuff Pull-up Resistor
06/09 CLKREQ with internal pull-up resistor No Stuff Pull-up Resistor
06/17 CLK_PCIE_ICH changed to SRCCLK7 CLK_DOCK_LAN changed to SRCCLK8 SW Note: datasheet page13 Byte8.1 => SRCCLK7 should be configured as "Not Controlled"
06/09 DEL pull-up resistor R80~82 pull-down resistor R85,R88 Del R84,R87,R90
06/16 ICS have recognized, FSLA/FSLB setting is different from CK410M spec.But MS10 will not use 100MHz, For test purpose, please move R91 from MCH_BSEL2 to MCH_BSEL0, and mount R89.
FSB Frequency Table:
FSLB FSLA CPU SRC[7:0] PCI
0 0 100 100 33
0 1 133 100 33
1 0 200 100 33
1 1 166 100 33
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
Title CLOCK GEN
Size A3 Document Number MS72-1-01 Rev 0.1
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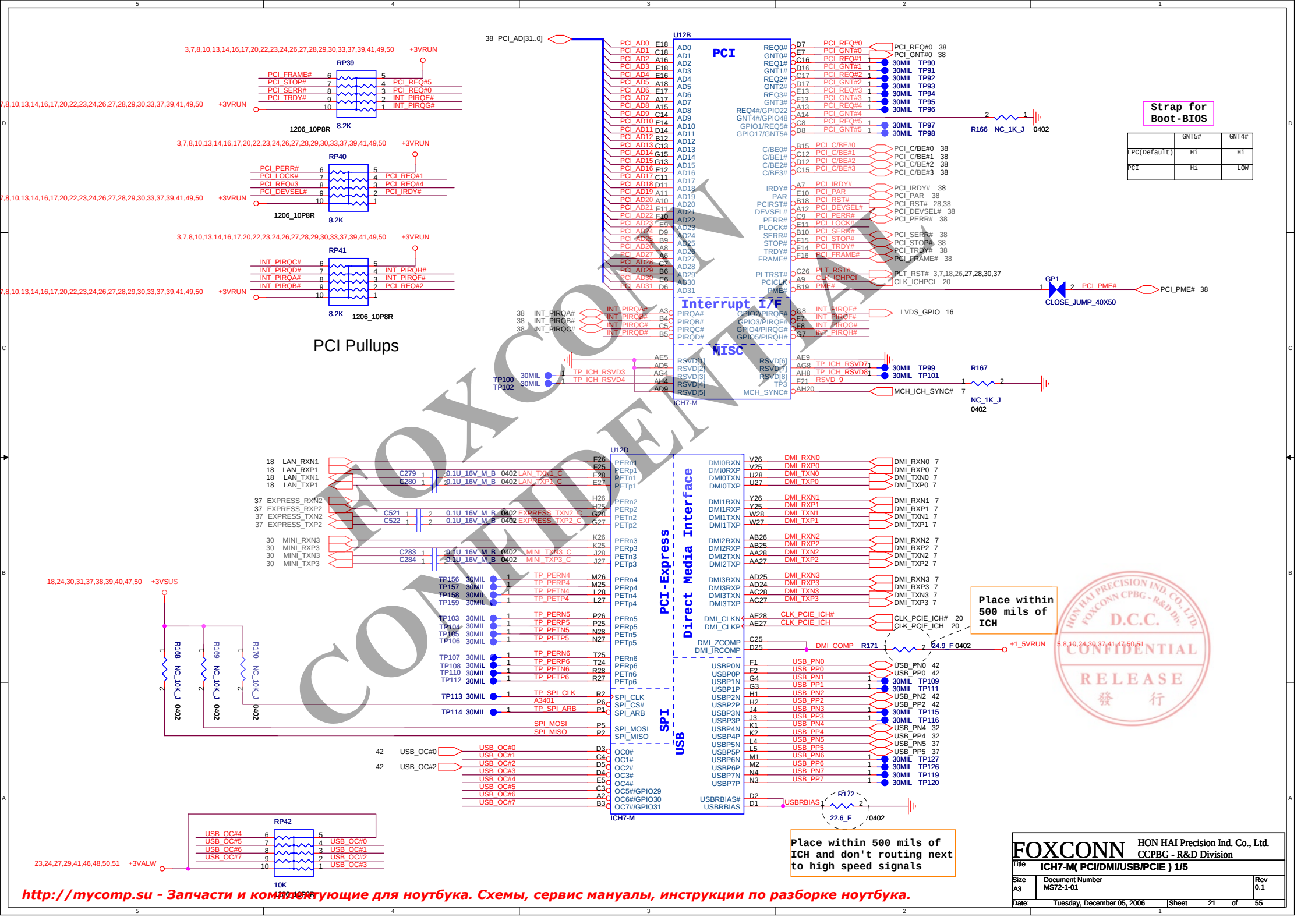
PCI Pullups
Strap for Boot-BIOS
GNT5# GNT4#
LPC(Default) H1 H1
PCI H1 LOW
Place within 500 mils of ICH
Place within 500 mils of ICH and don't routing next to high speed signals
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
Title ICH7-M(PCI/DMI/USB/PCIE) 1/5
Size A3
Document Number MS72-1-01
Rev 0.1
Date: Tuesday, December 05, 2006
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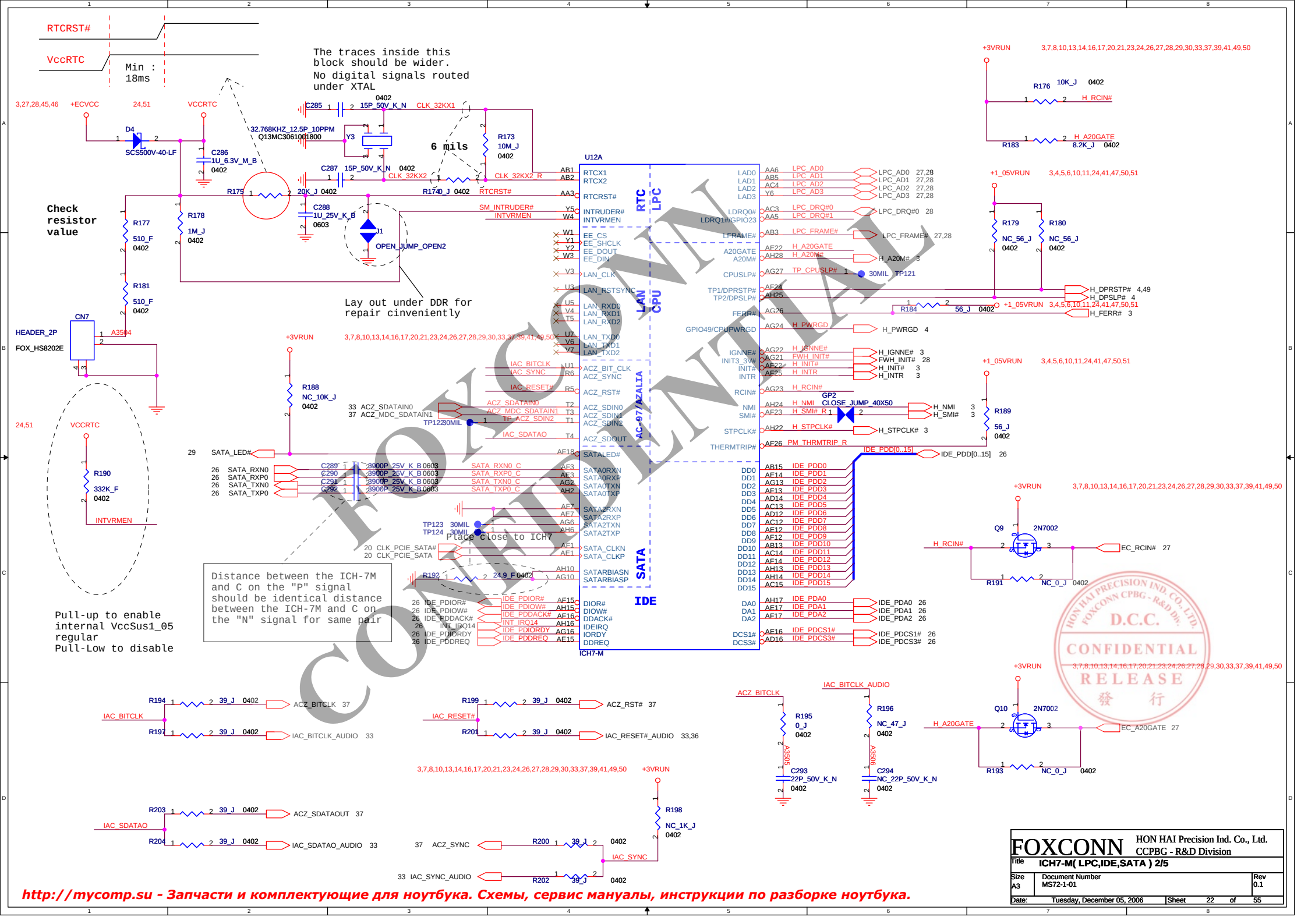
RTCRST#
VccRTC
Min : 18ms
The traces inside this block should be wider.
No digital signals routed under XTAL
6 mils
Check resistor value
Lay out under DDR for repair cinveniently
Place Close to ICH7
Distance between the ICH-7M and C on the "P" signal should be identical distance between the ICH-7M and C on the "N" signal for same pair
Pull-up to enable internal VccSus1_05 regular
Pull-Low to disable
U12A
ICH7-M
RTC
LPC
LAN
CPU
AC-97/AZALIA
SATA
IDE
SCS500V-40-LF
32.768KHZ_12.5P_10PPM Q13MC3061001800
OPEN_JUMP_OPEN2
HEADER_2P FOX_HS8202E
GP2 CLOSE_JUMP_40X50
2N7002
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
ICH7-M(LPC,IDE,SATA) 2/5
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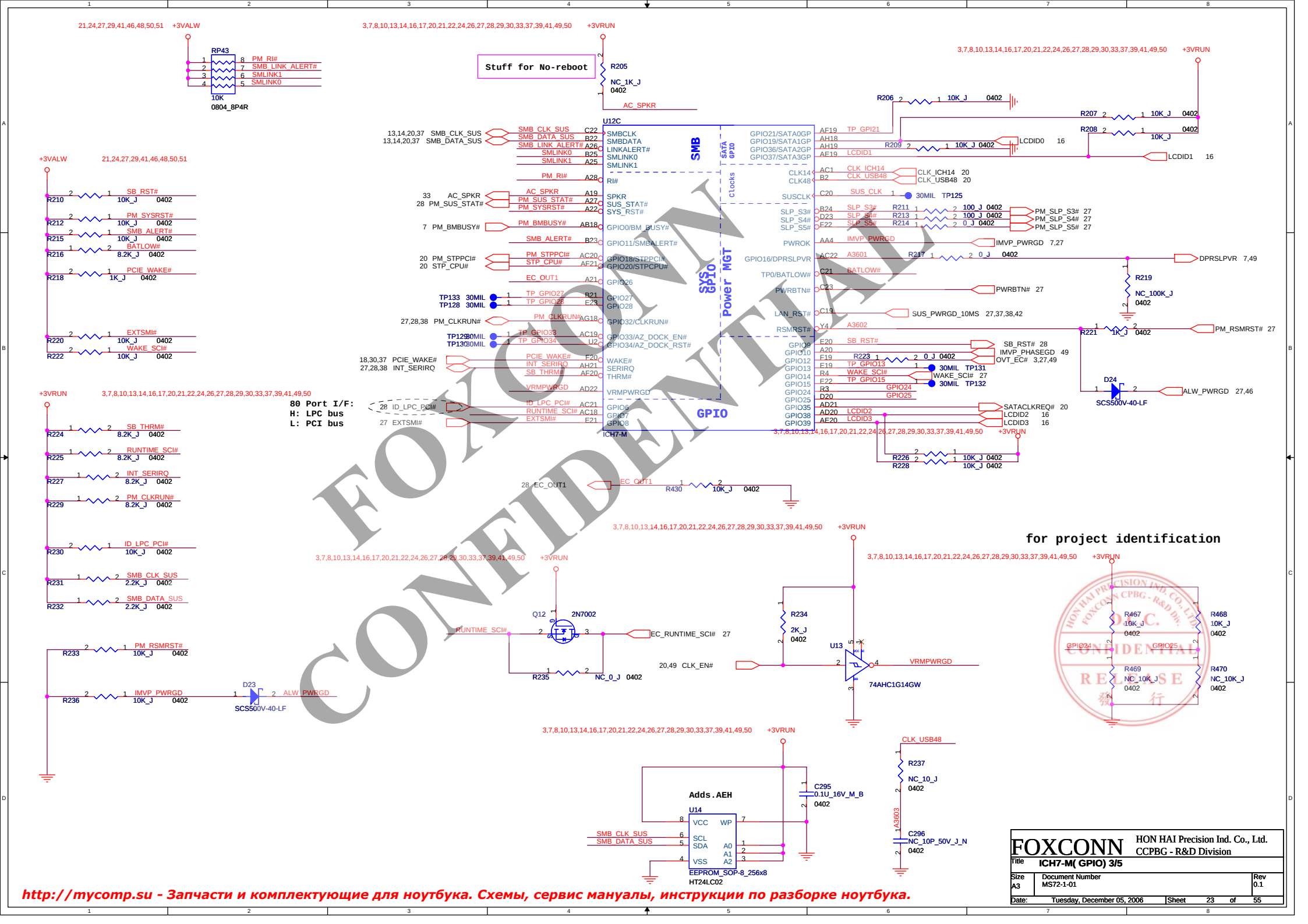
Stuff for No-reboot
80 Port I/F:
H: LPC bus
L: PCI bus
for project identification
U12C
ICH7-M
SMB
SYS GPIO
Power MGT
GPIO
Clocks
SATA GPIO
U13
74AHC1G14GW
Q12
2N7002
U14
EEPROM_SOP-8_256x8
HT24LC02
Adds.AEH
D23
SCS500V-40-LF
D24
SCS500V-40-LF
RP43
10K
0804_8P4R
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HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
ICH7-M(GPIO) 3/5
Document Number
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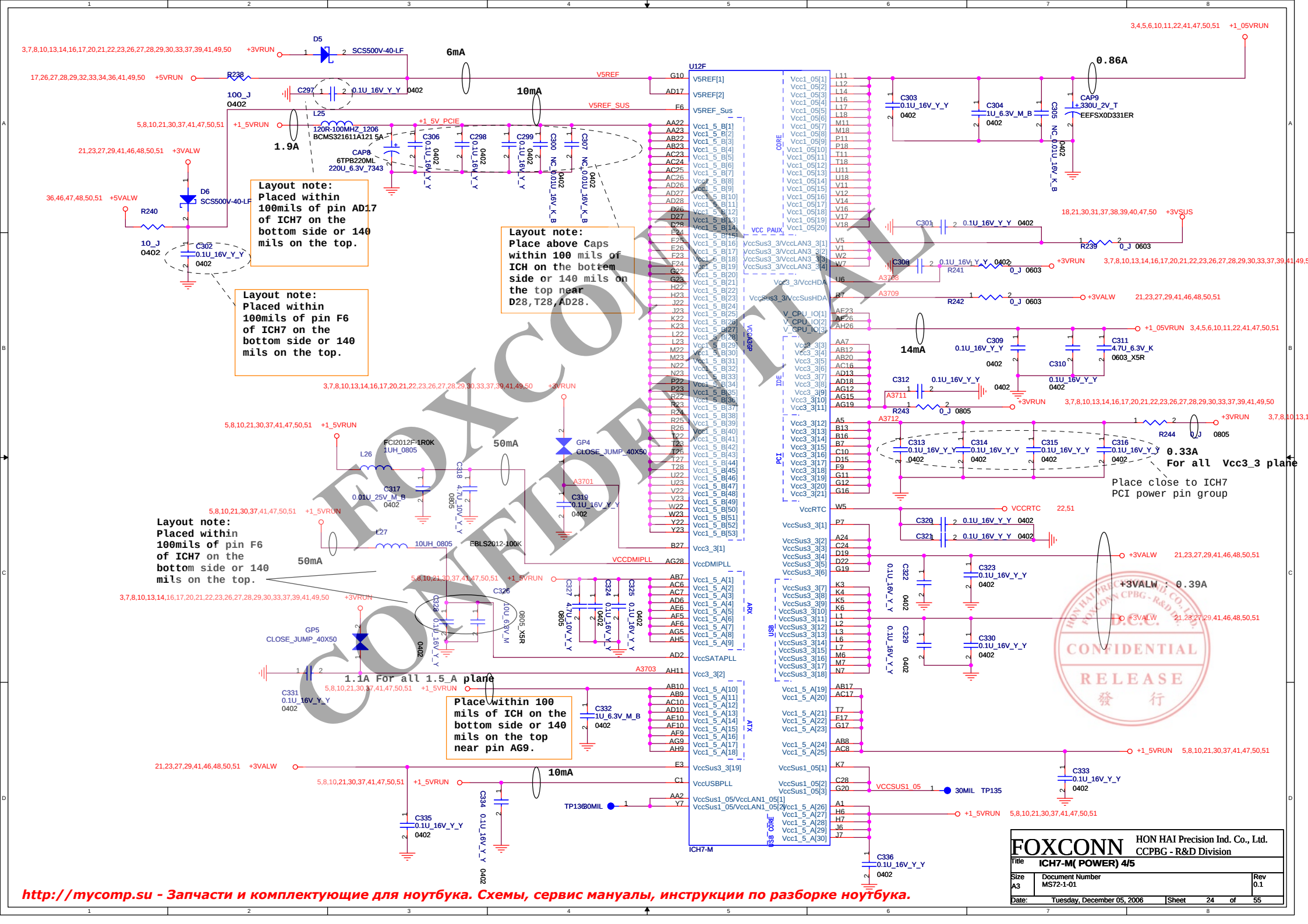
U12F
ICH7-M
Layout note: Placed within 100mils of pin AD17 of ICH7 on the bottom side or 140 mils on the top.
Layout note: Place above Caps within 100 mils of ICH on the bottem side or 140 mils on the top near D28,T28,AD28.
Layout note: Placed within 100mils of pin F6 of ICH7 on the bottom side or 140 mils on the top.
Layout note: Placed within 100mils of pin F6 of ICH7 on the bottom side or 140 mils on the top.
Place within 100 mils of ICH on the bottom side or 140 mils on the top near pin AG9.
0.86A
14mA
0.33A
For all Vcc3_3 plane
Place close to ICH7 PCI power pin group
+3VALW : 0.39A
1.1A For all 1.5_A plane
6mA
10mA
50mA
1.9A
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
ICH7-M(POWER) 4/5
Document Number MS72-1-01
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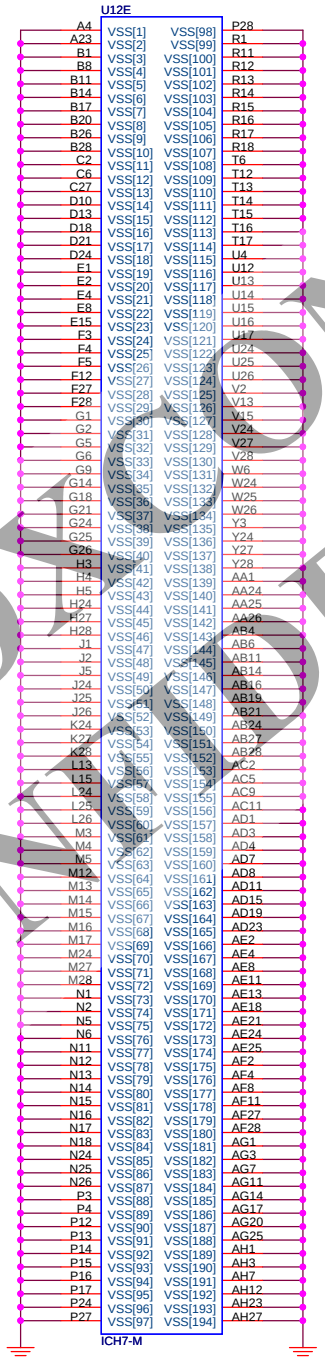

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	ICH7-M(GND) 5/5	
Size A3	Document Number MS72-1-01	Rev 0.1
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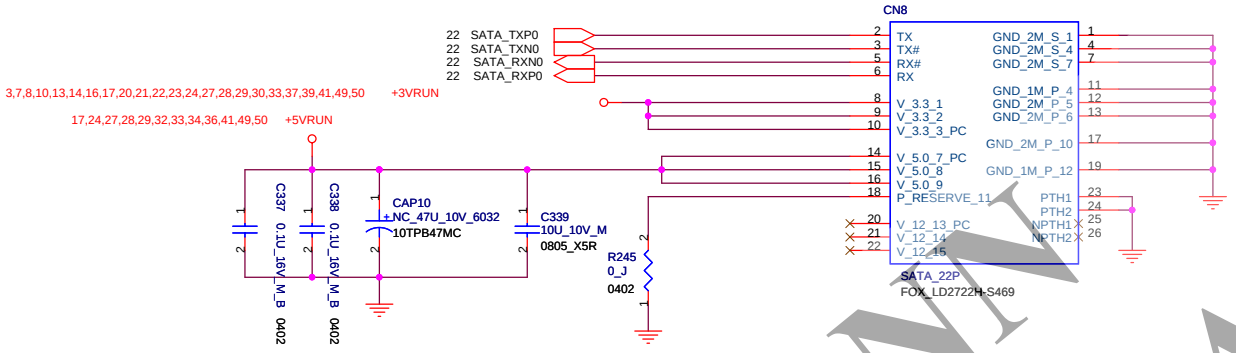
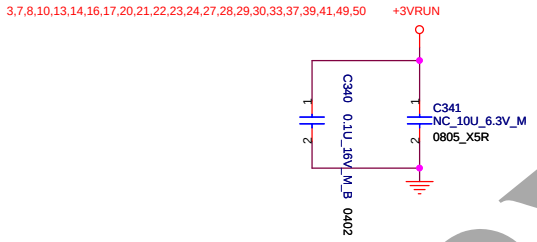
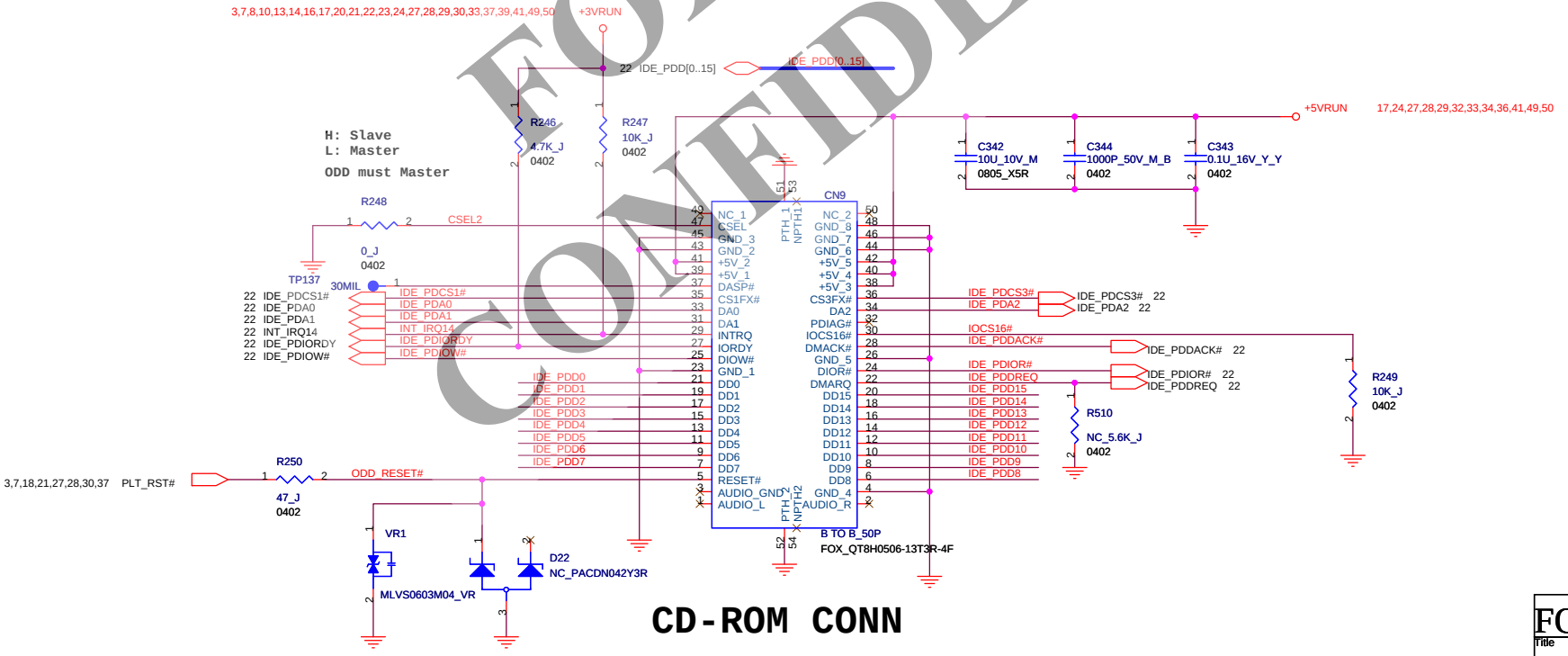
SATA HDD CONN



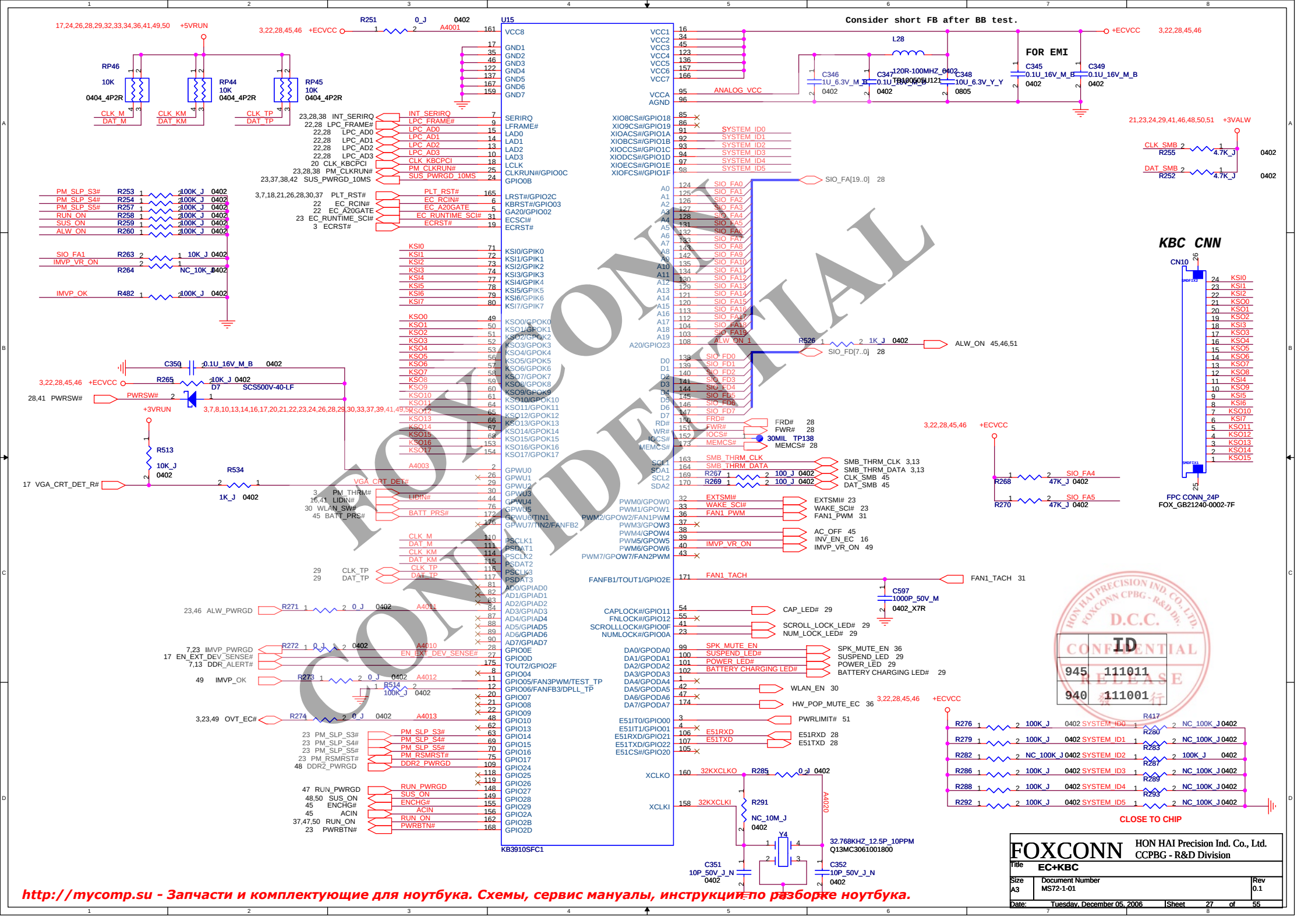




CD-ROM CONN

Follow Adoi san suggest ODD: Master/HDD:Slave

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	SATA HDD/CD-ROM	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 26 of 55	


Consider short FB after BB test.
KBC CNN
FOR EMI
KB3910SFC1
FOXCONN
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division
EC+KBC
MS72-1-01
Tuesday, December 05, 2006
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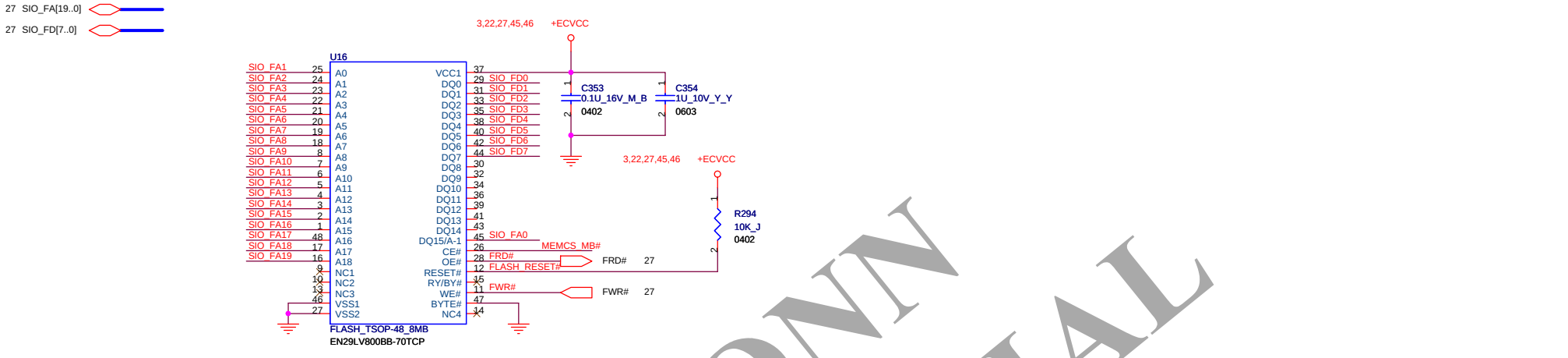
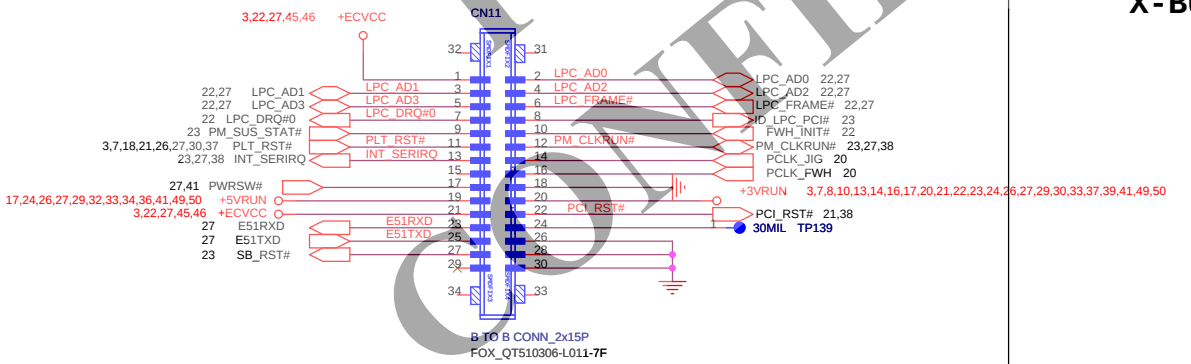
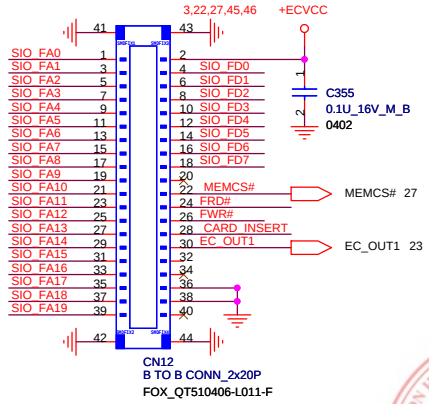
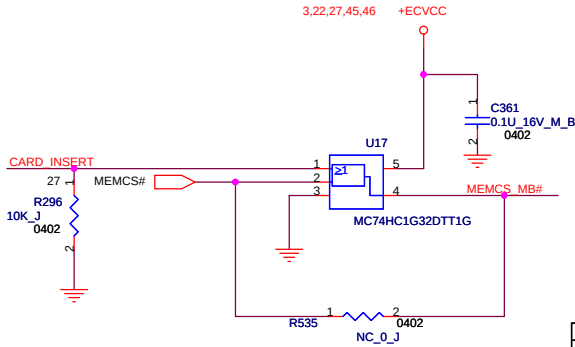


FLASH BIOS







FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	Flash ROM/X-Bus/LID SW#	
Size A3	Document Number MS72-1-01	Rev 0.1
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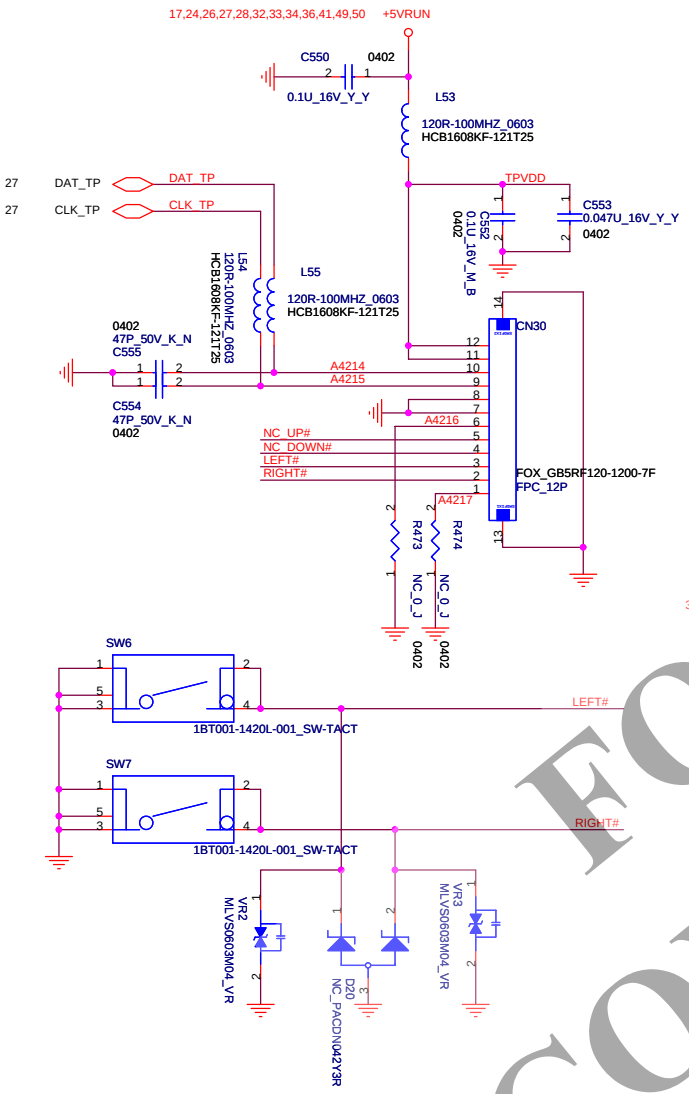
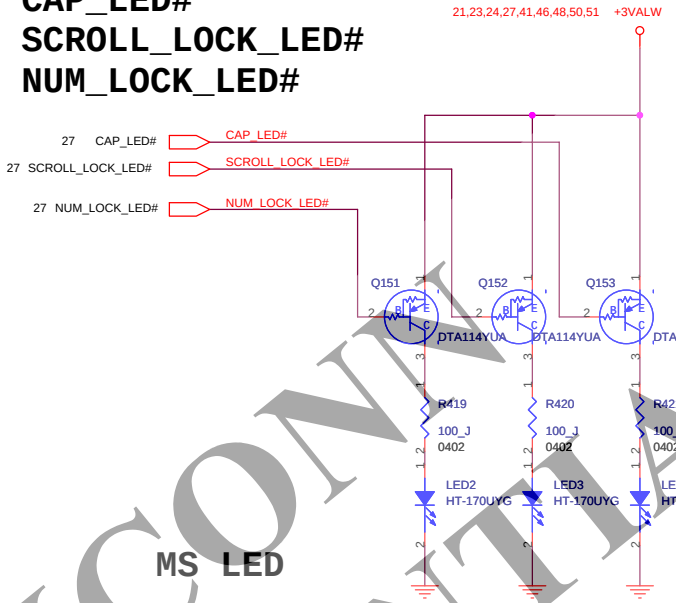
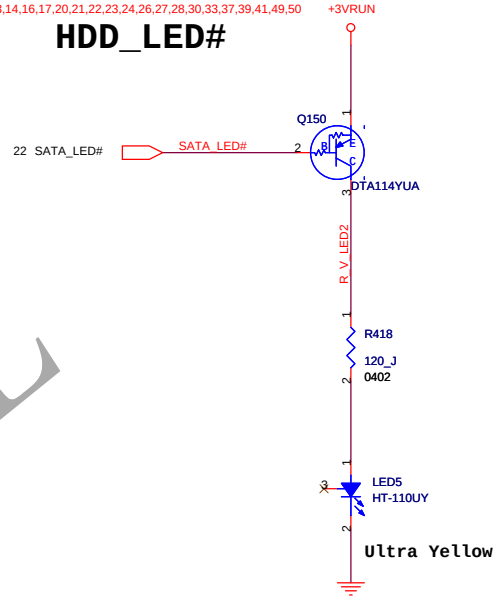
Touch Pad Board



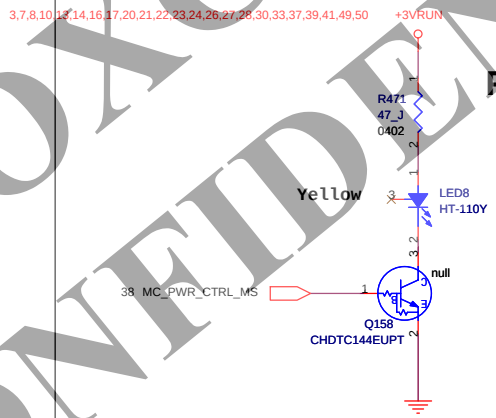
CAP_LED#
SCROLL_LOCK_LED#
NUM_LOCK_LED#



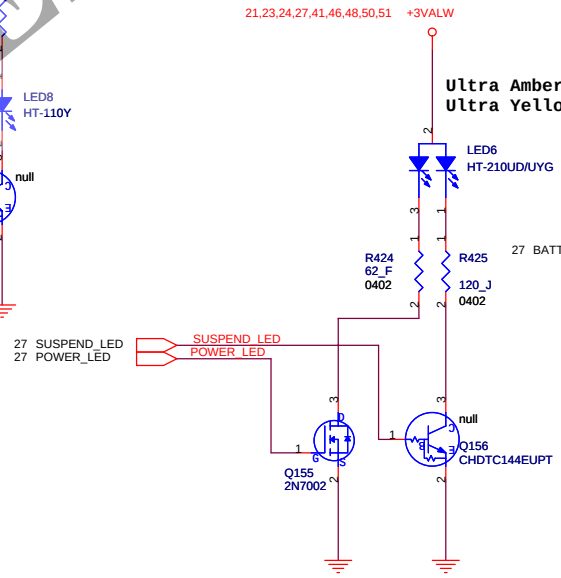
HDD_LED#



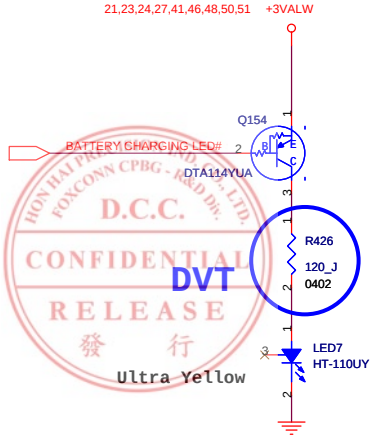
MS LED



POWER/SUSPEND LED



BATTERY CHARGING LED#



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	LED/Touch PAD	
Size A3	Document Number MS72-1-01	Rev 0.1
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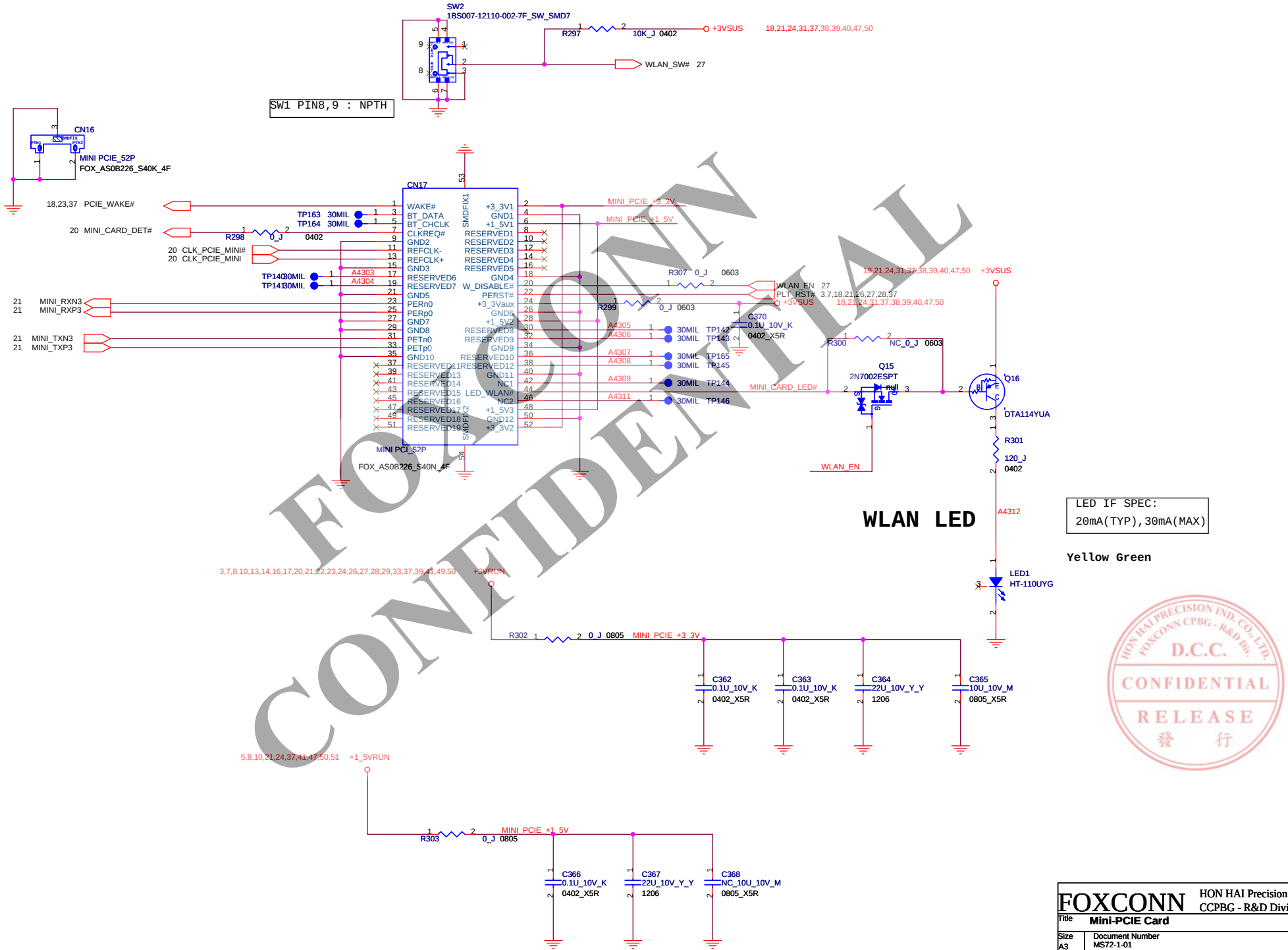
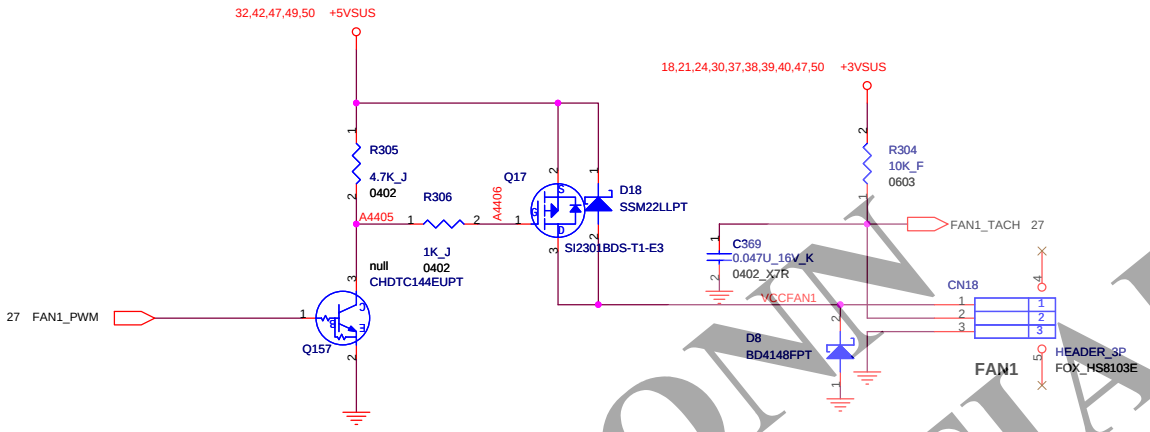
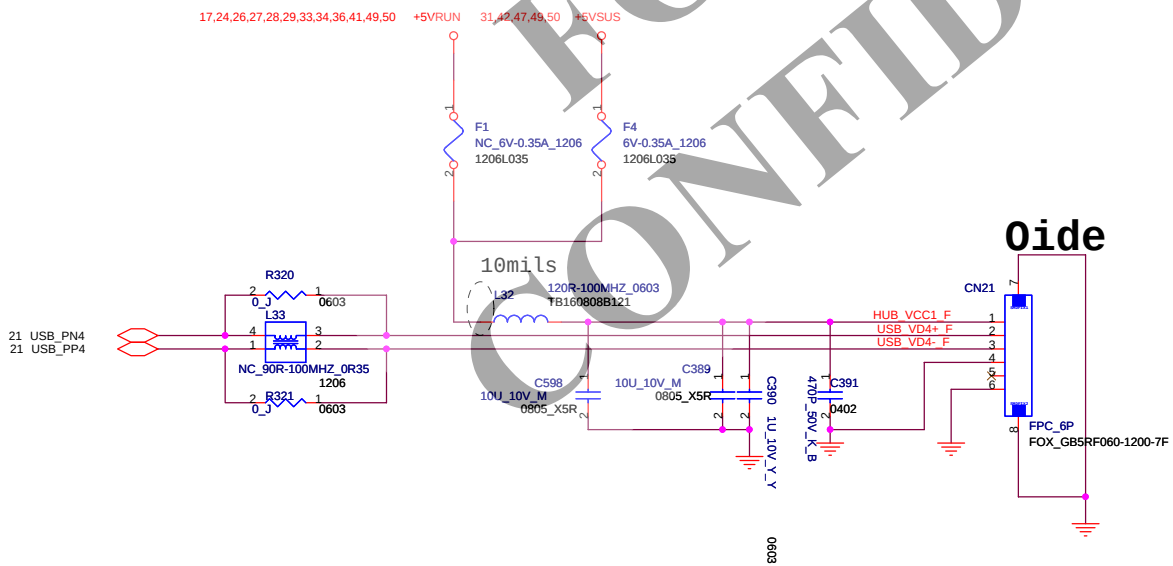


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
Title: Mini-PCIE Card			
Size: A3	Document Number: MS72-1-01		Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 30 of 55		

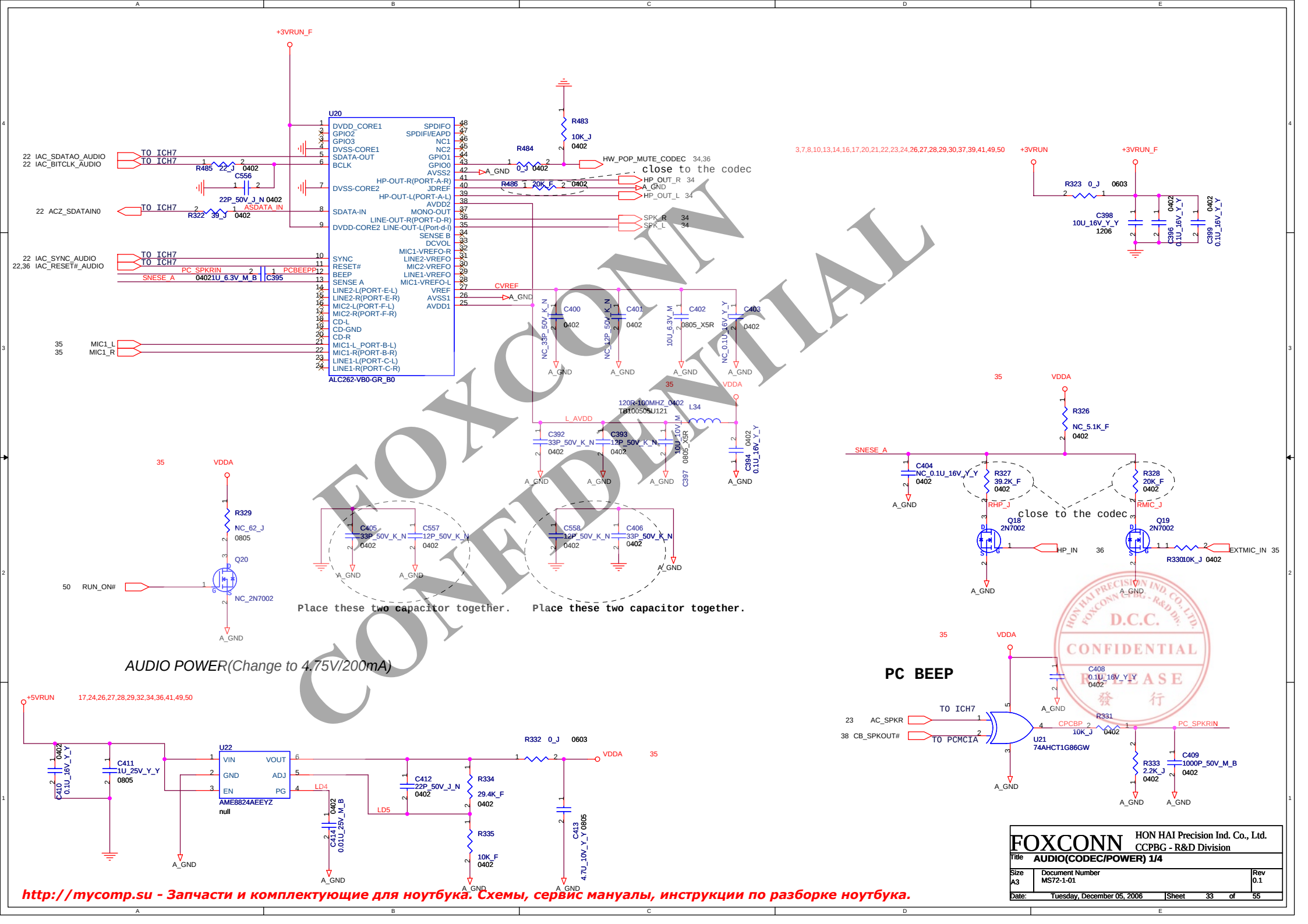
FAN1

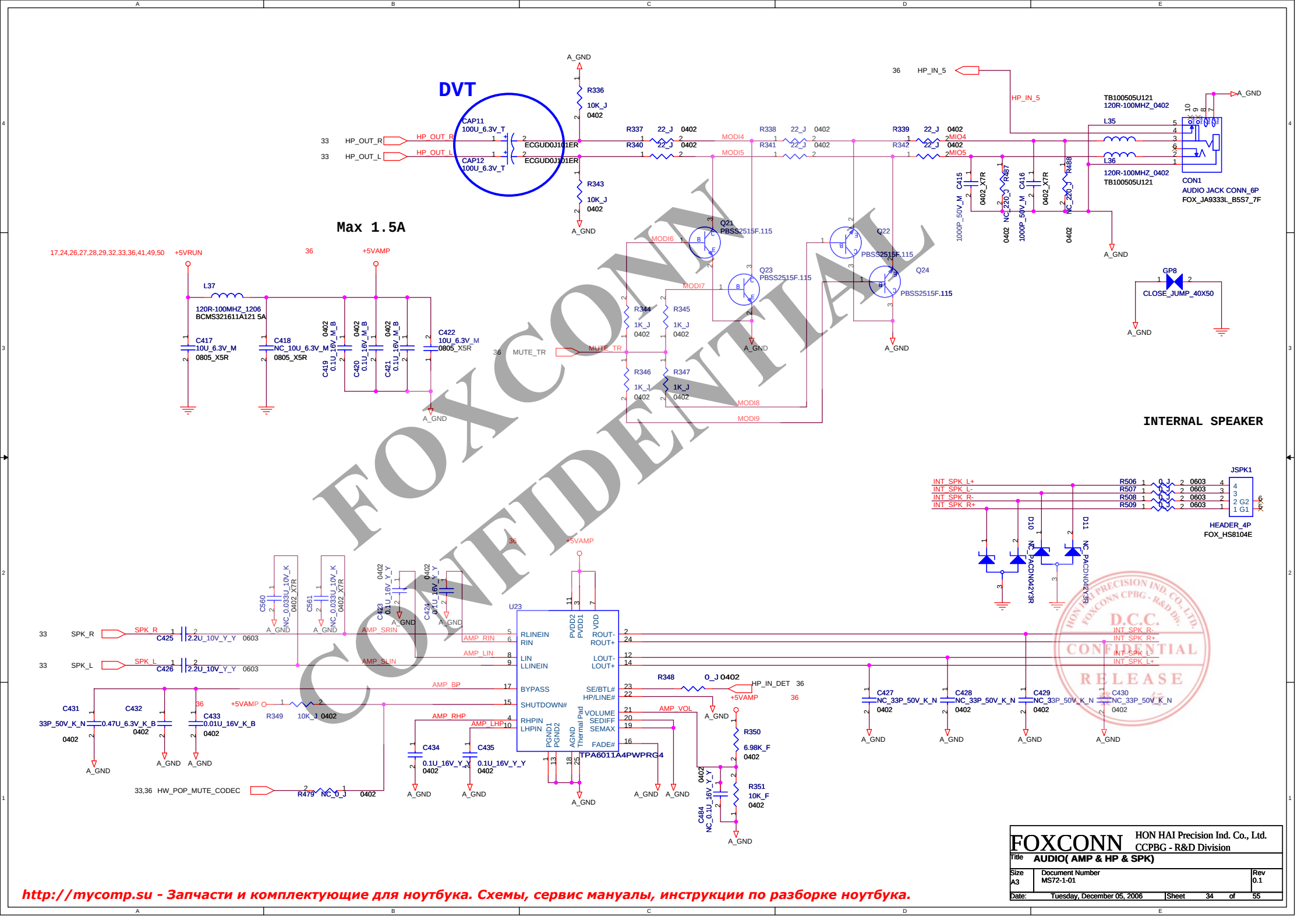


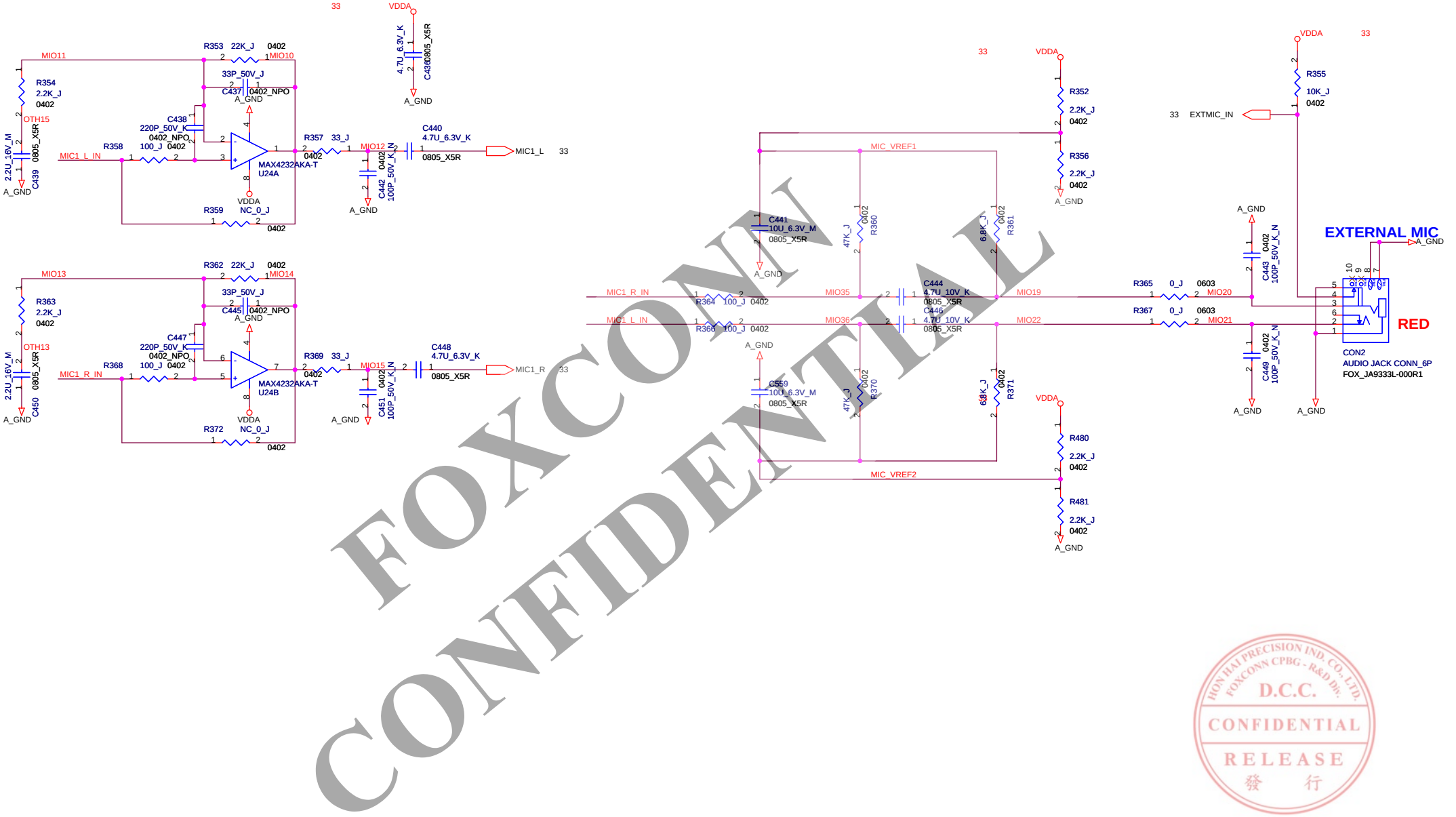
FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: FAN		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 31 of 55	



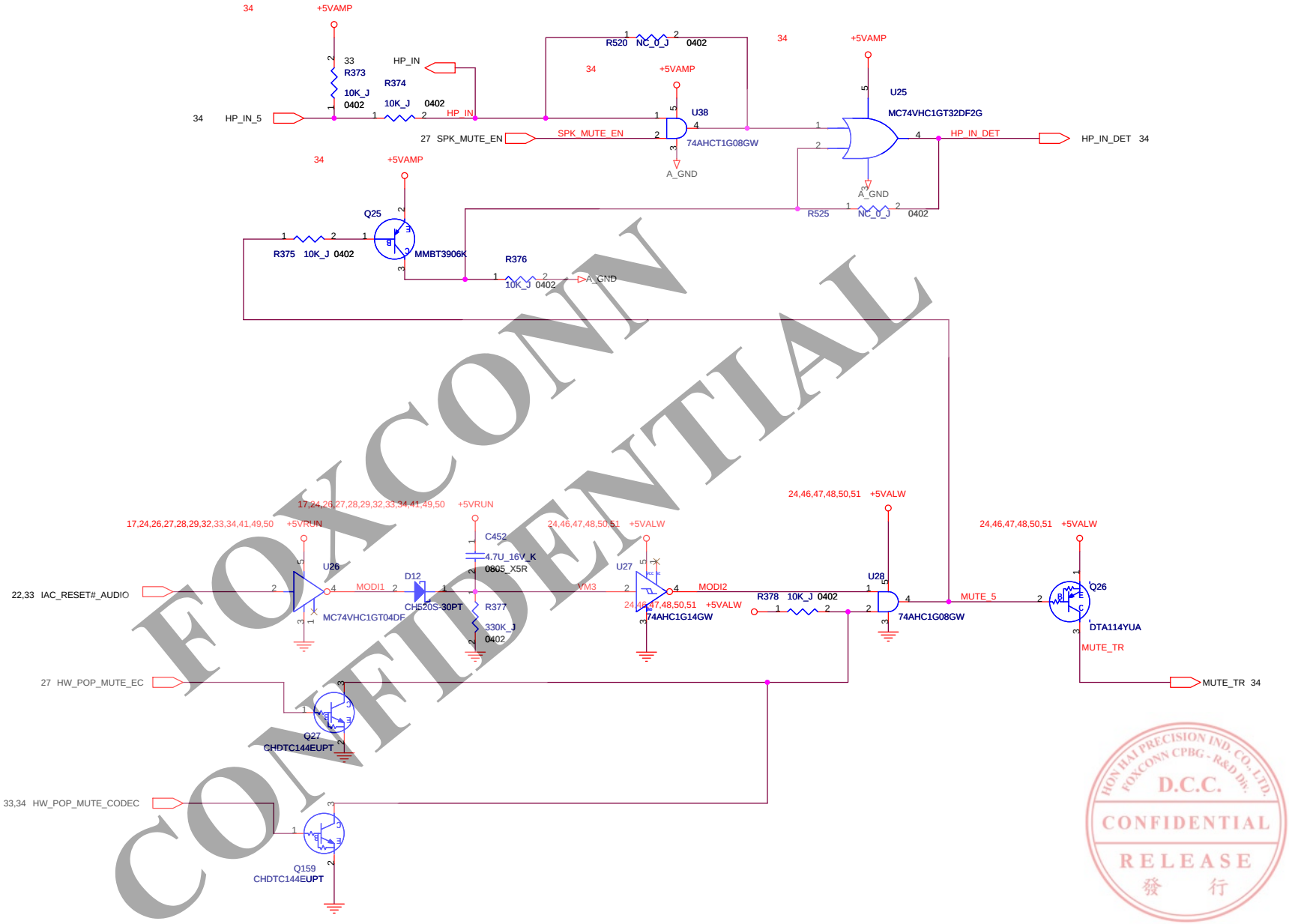
FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: OIDE		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 32 of 55	







FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	AUDIO(EXT MIC) 3/4	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 35 of 55	



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	AUDIO (MUTE) 4/4	
Size A3	Document Number MS72-1-01	Rev 0.1
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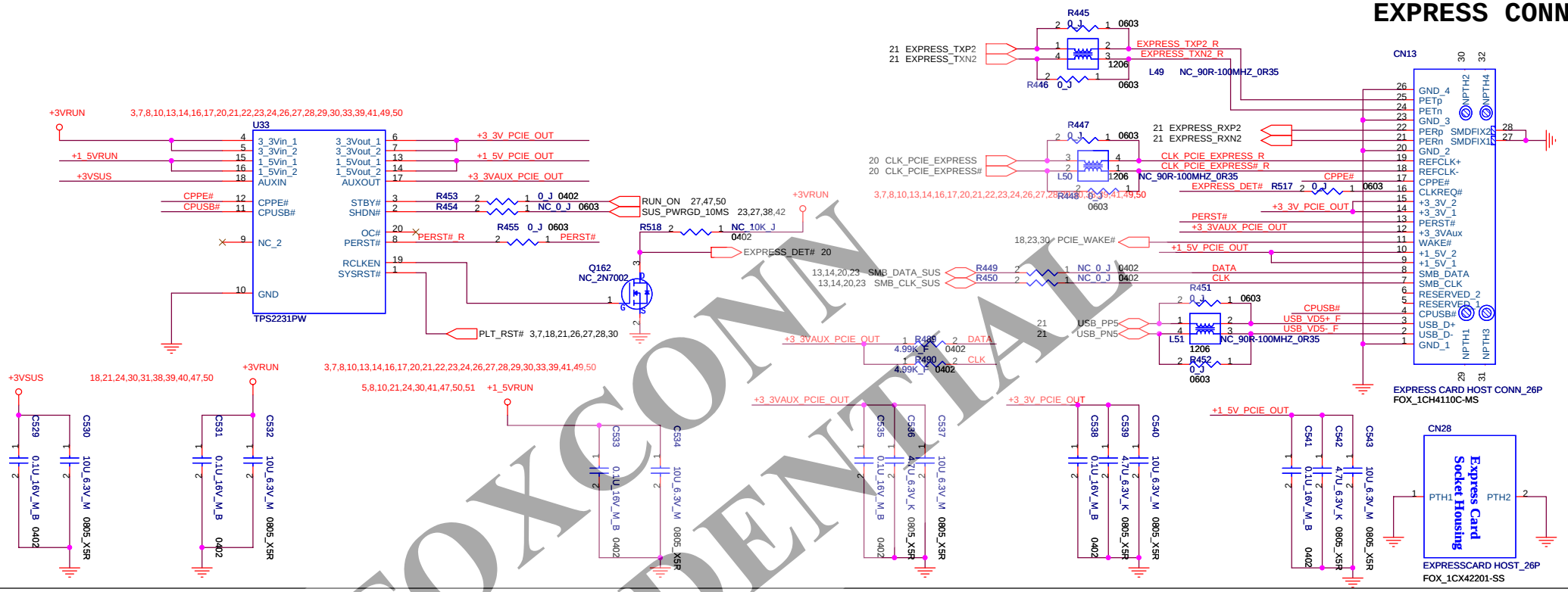
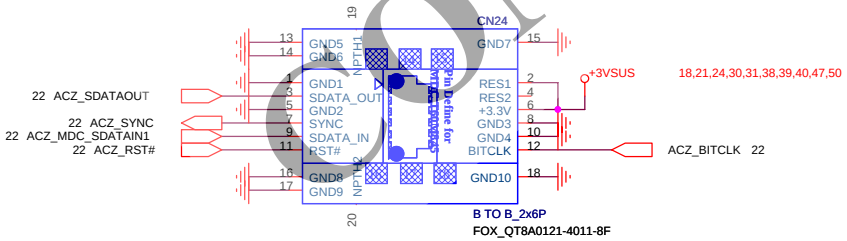
EXPRESS CONN



MDC CONN.



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: EXPRESS CARD		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
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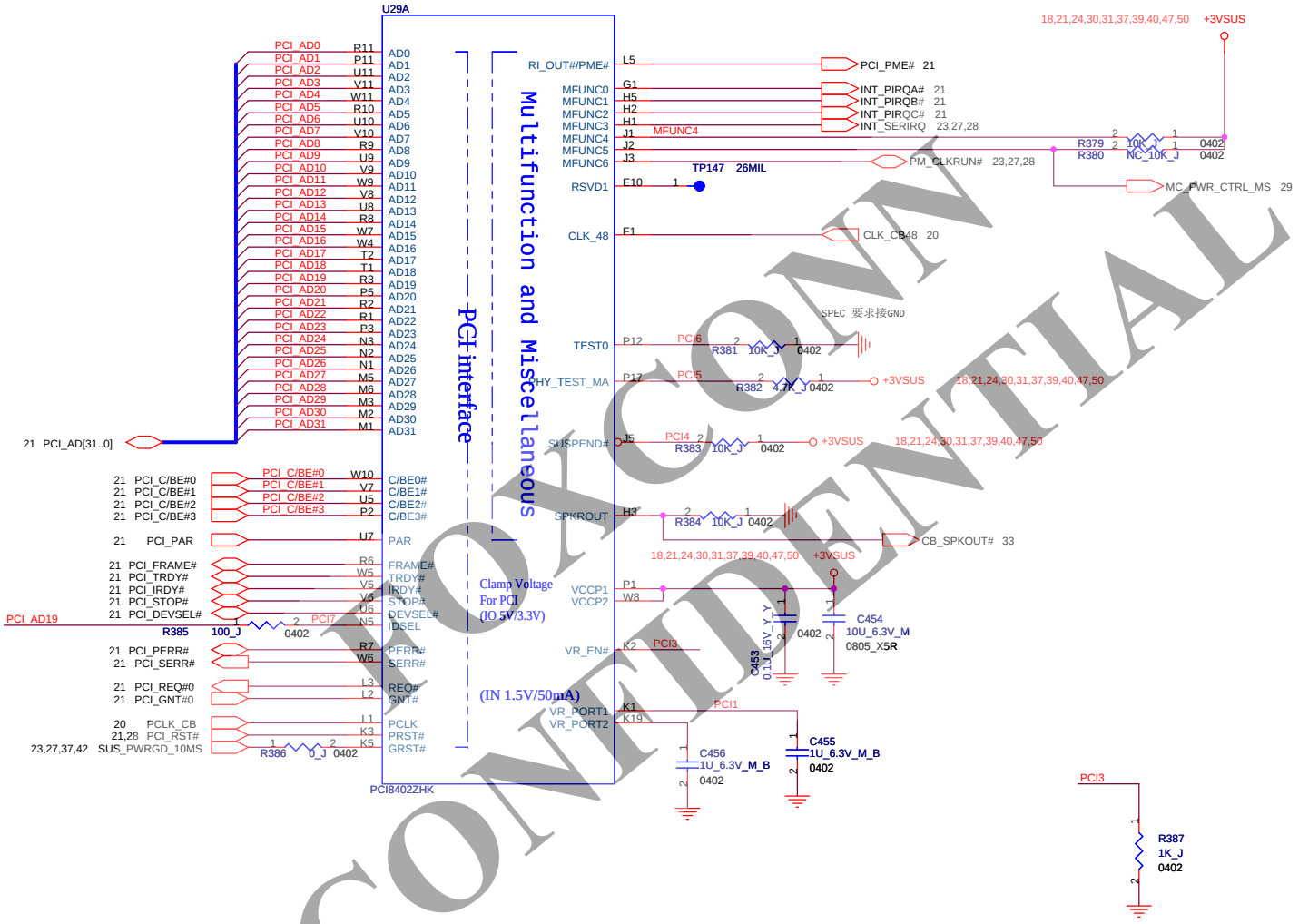

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	PCI (PCI BUS) 1/3	
Size A3	Document Number MS72-1-01	Rev 0.1
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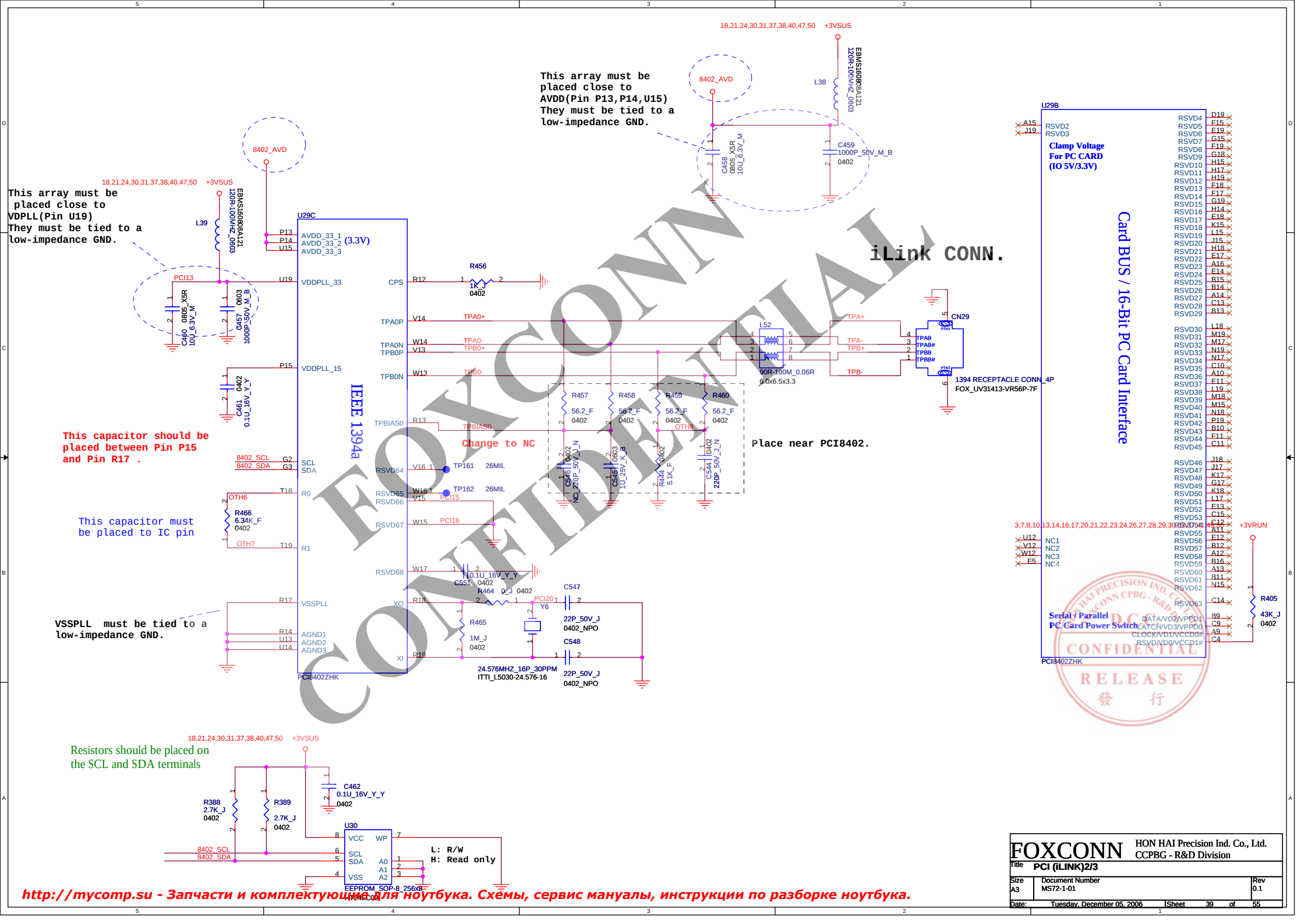
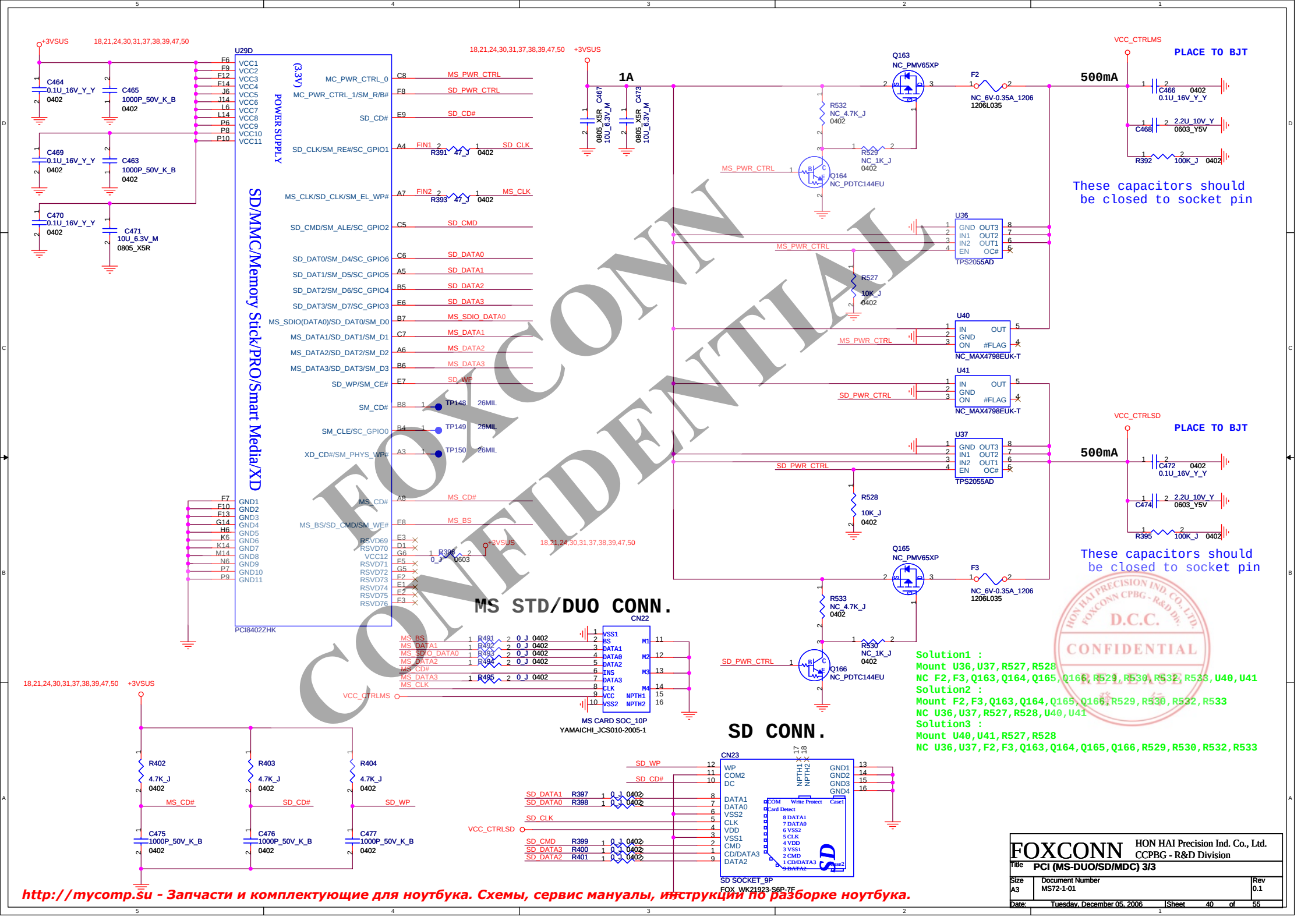
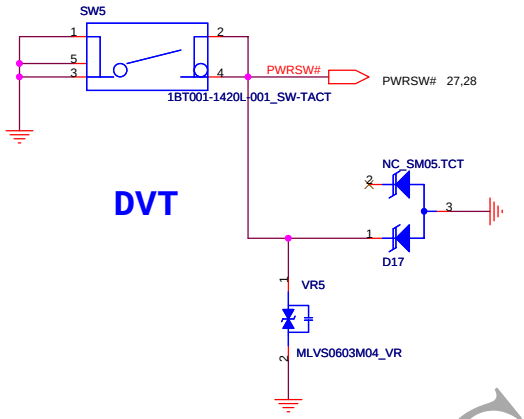
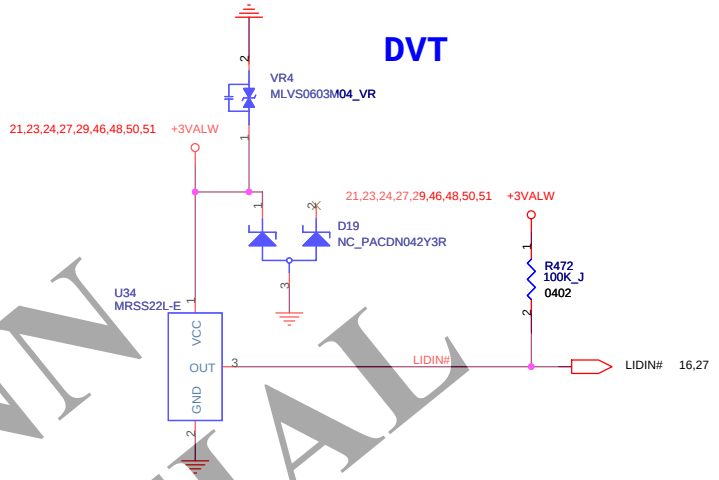




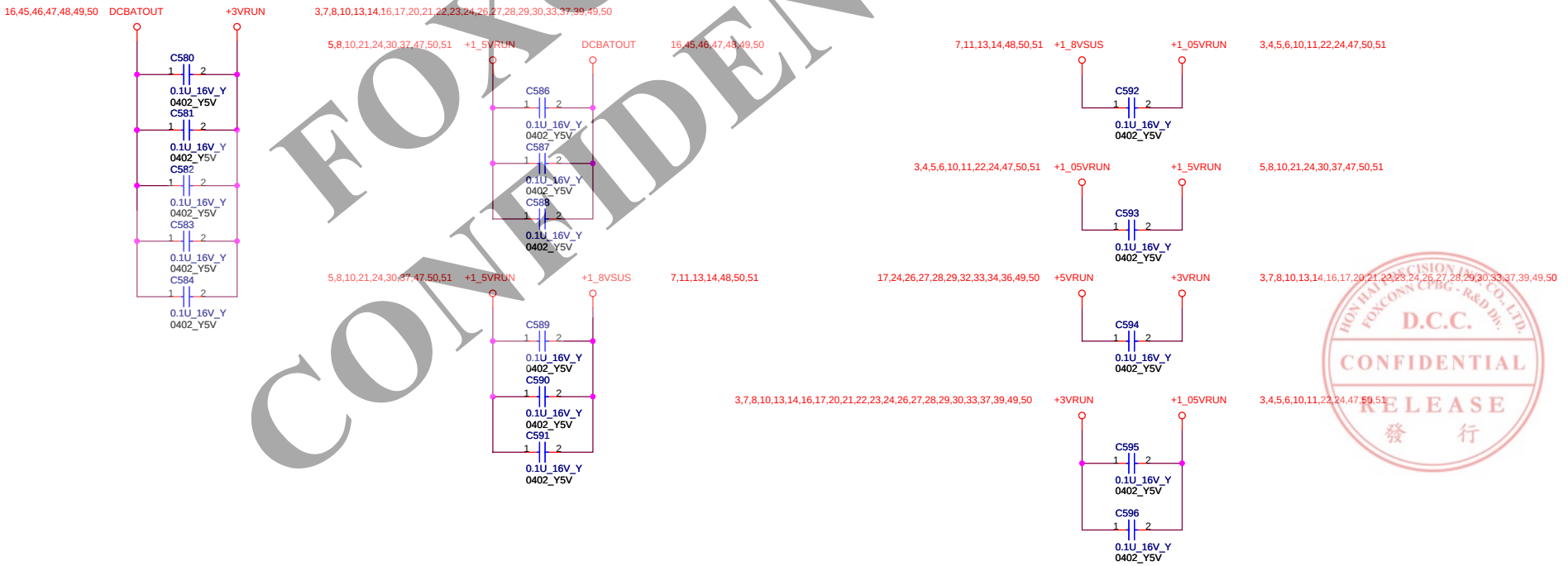
POWER BUTTON



LID Switch



EMI CAP



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
Title	Button/LID Switch/EMI CAP		
Size A3	Document Number MS72-1-01		Rev 0.1
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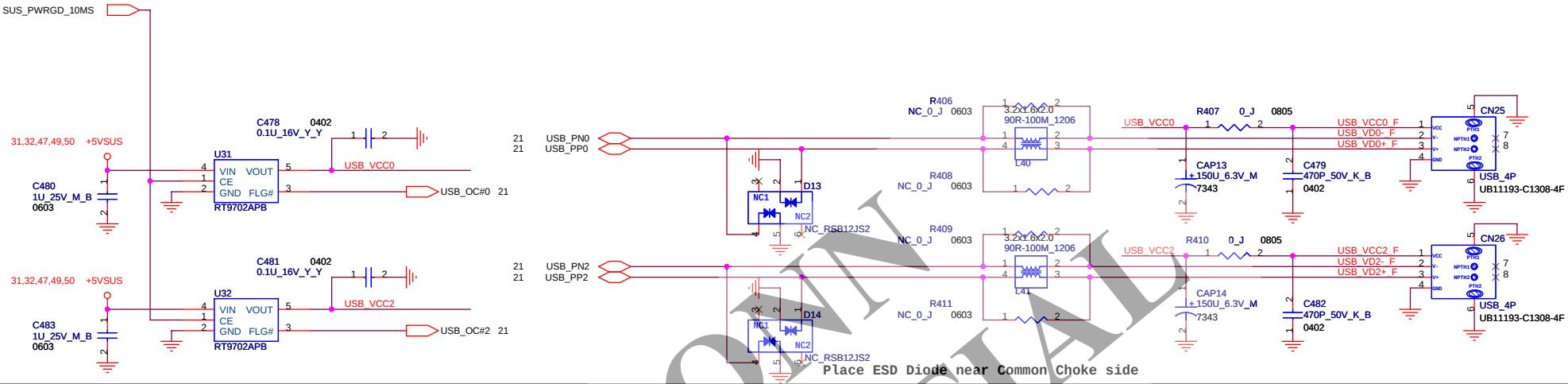
USB CONN X 2



FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: USB2.0		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
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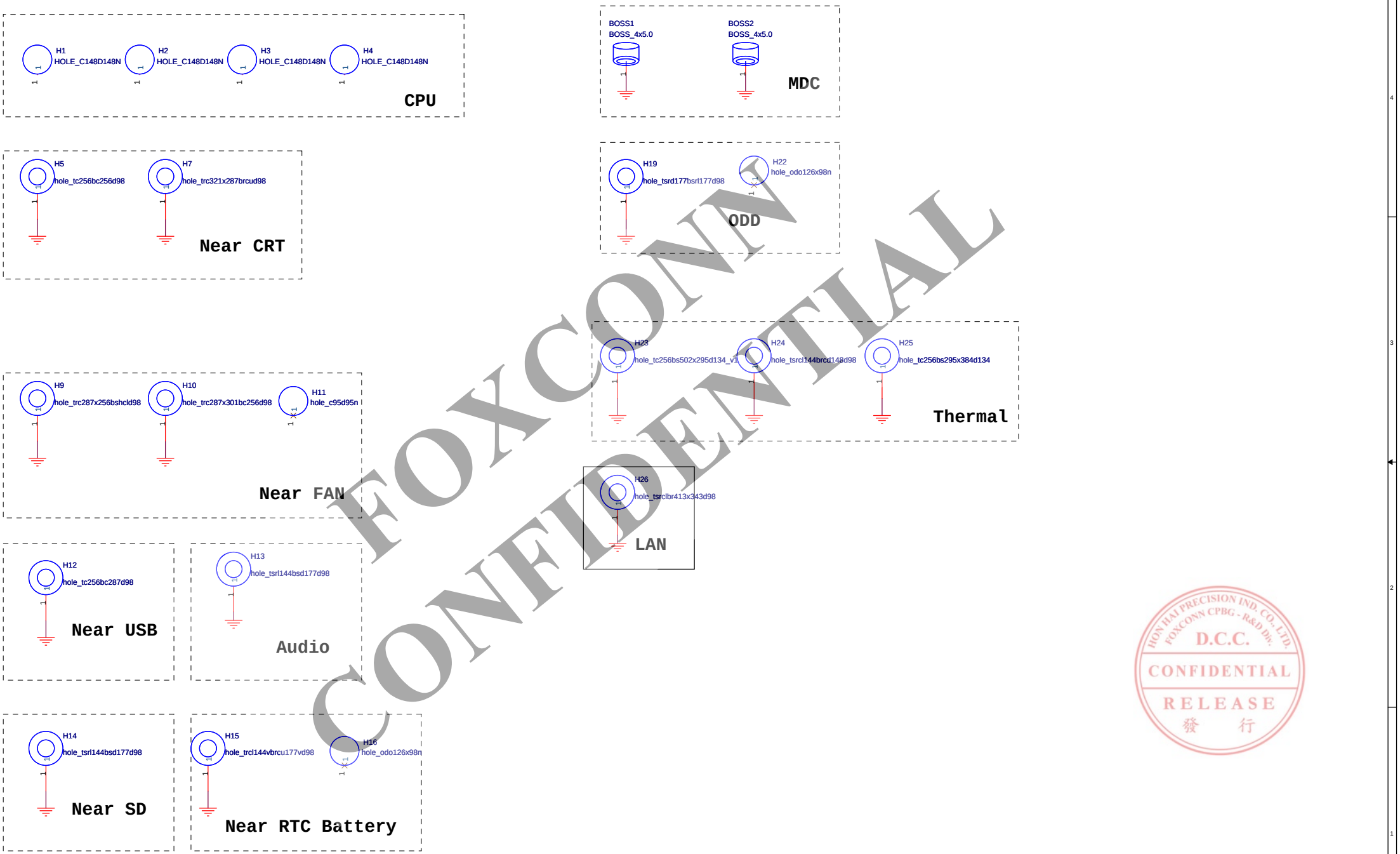
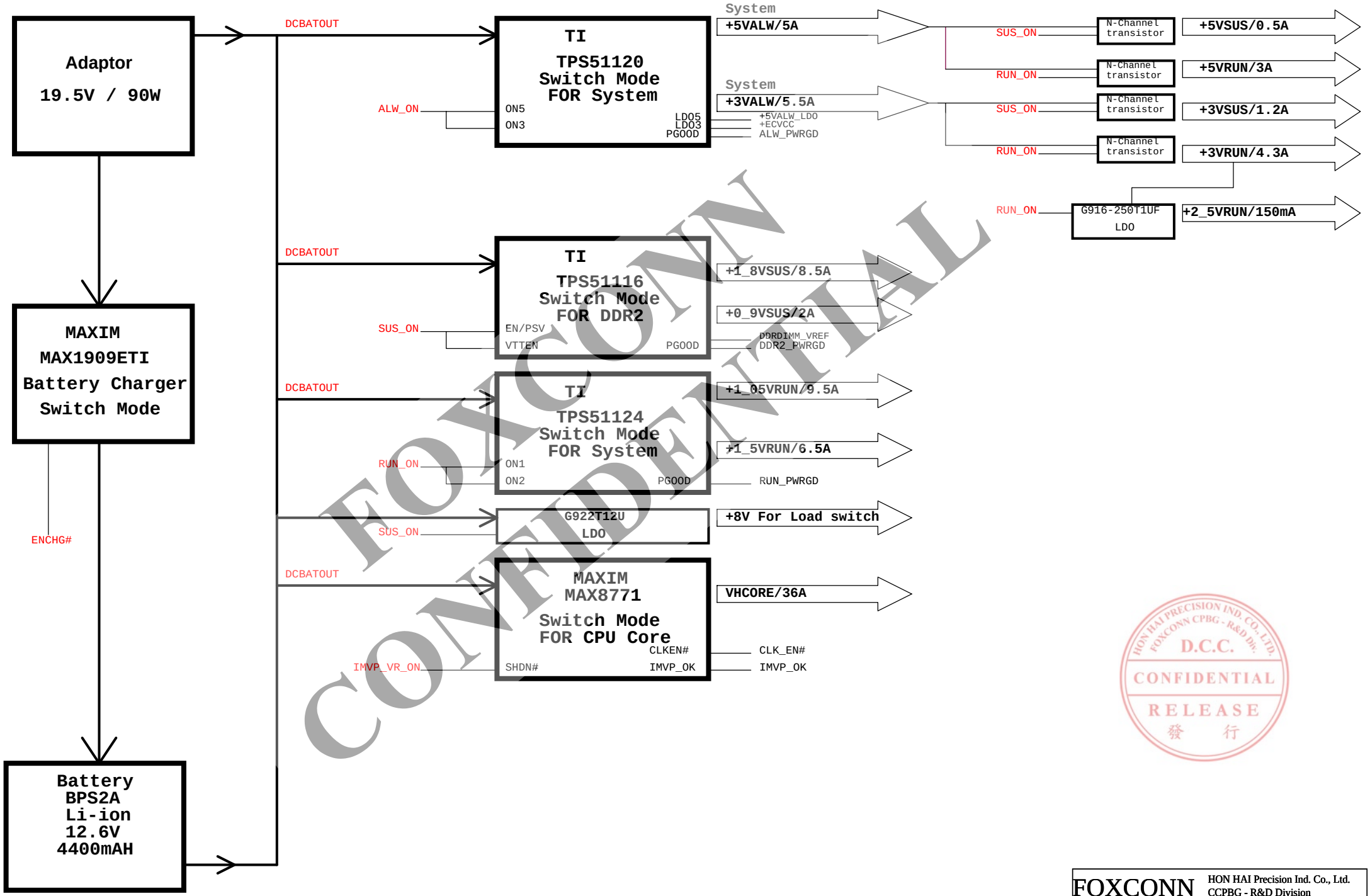
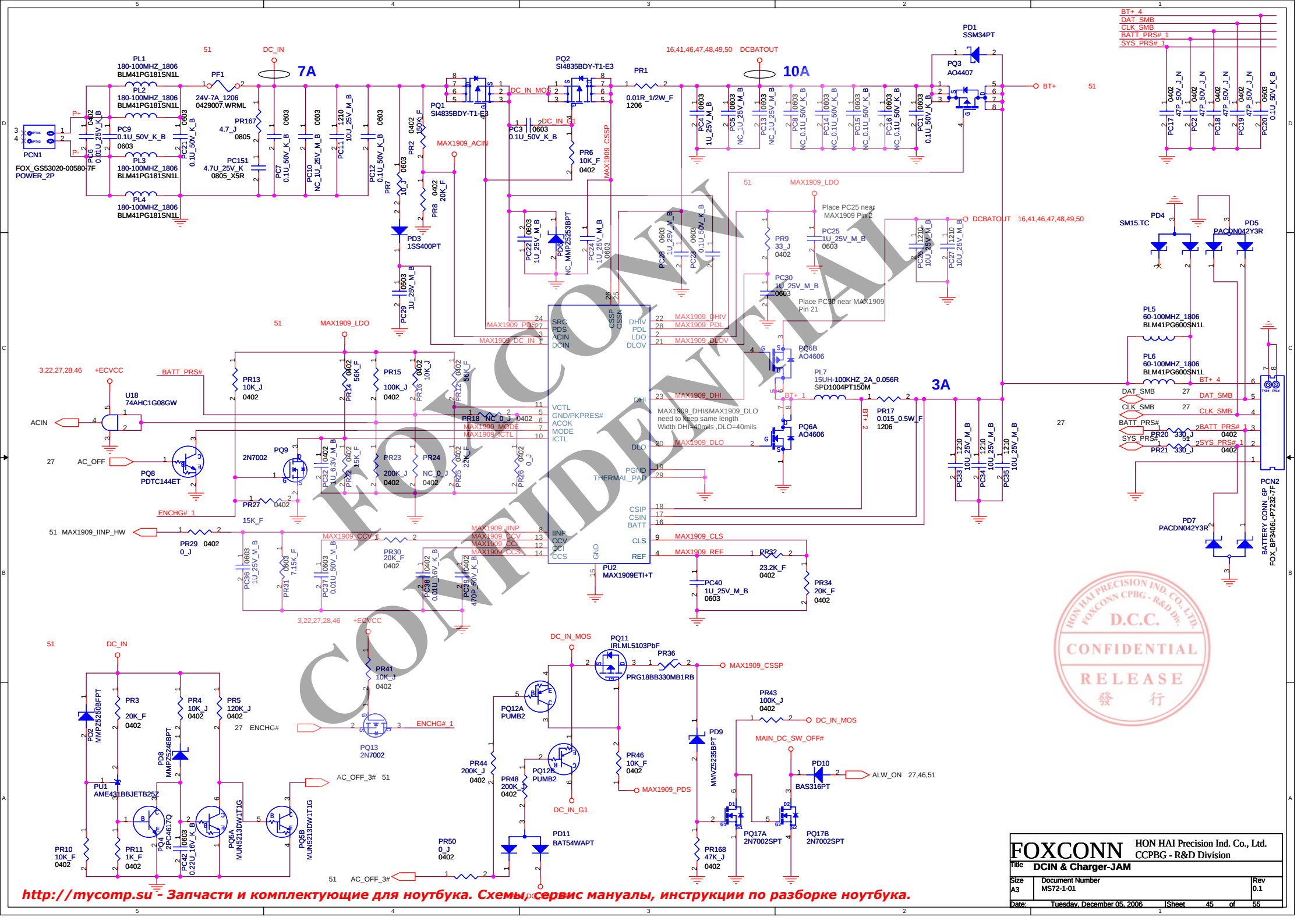
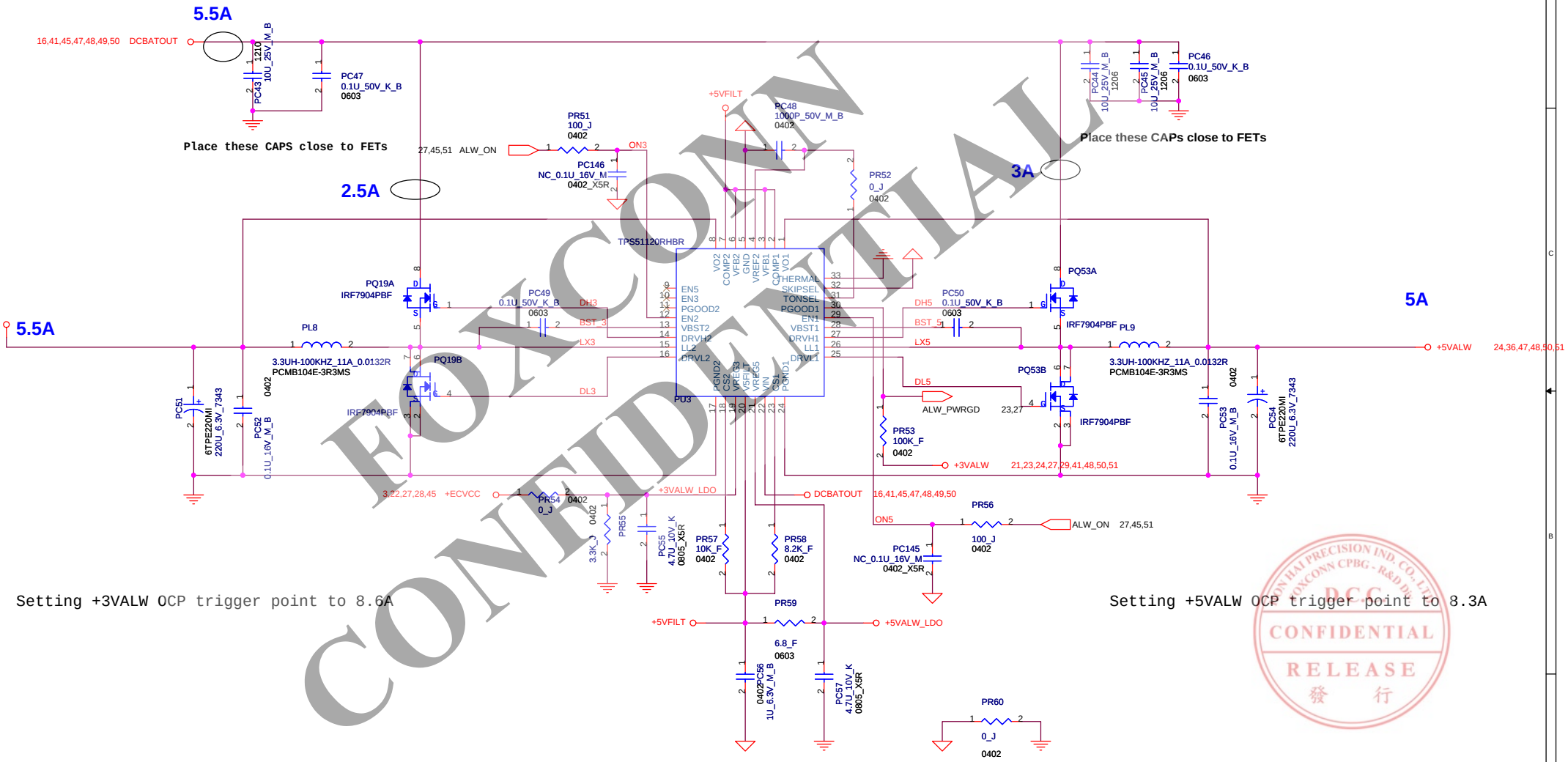


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	HOLE	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 43 of 55	

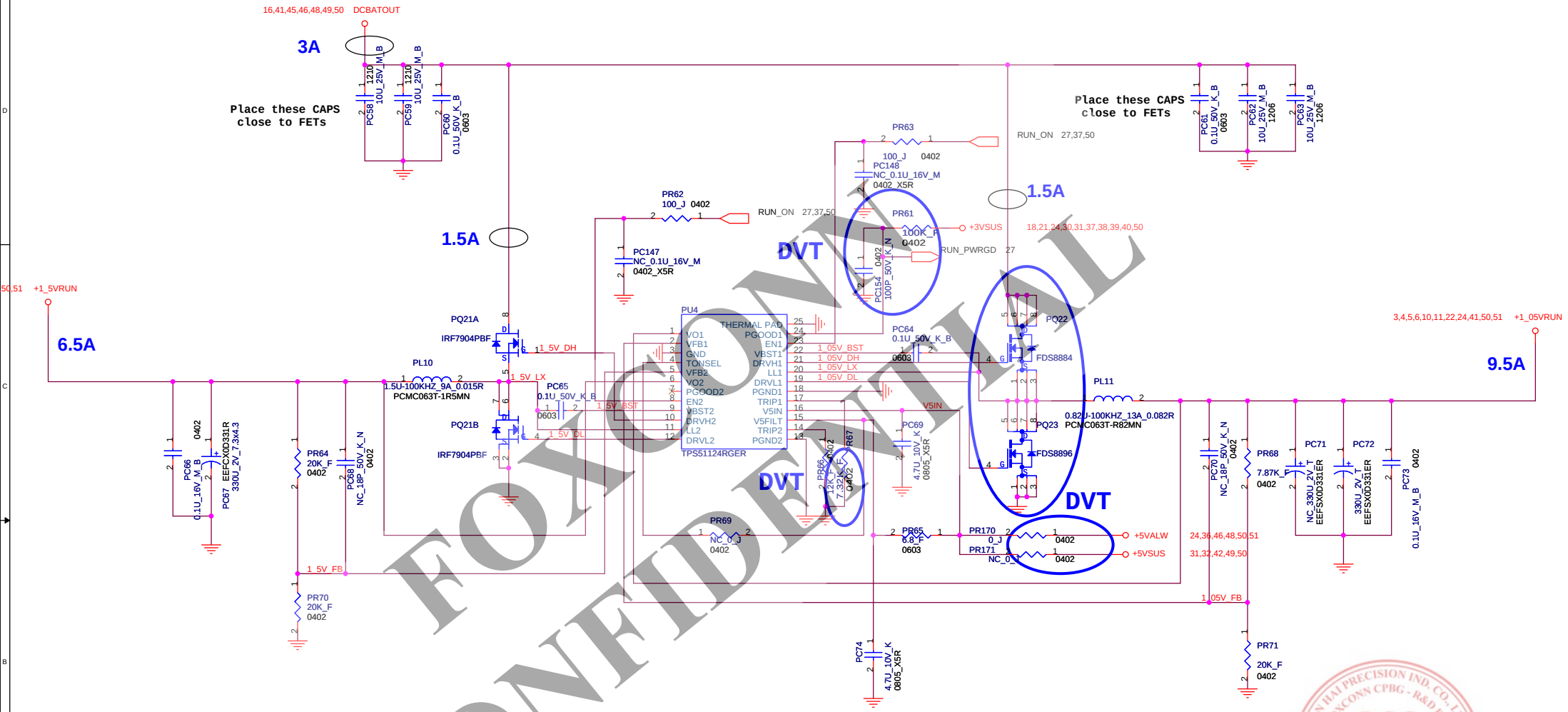


FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: Power Design Diagram-ZG		
Size: A3	Document Number: MS72-1-01	Rev: 0.1
Date: Tuesday, December 05, 2006	Sheet 44 of 55	





FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	D/D Power-JAM	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 46 of 55	



Setting +1_5VRUN OCP trigger point to 10.5A

Setting +1_05VRUN OCP trigger point to 12.8A

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	1.5V/1.05V-JAM	
Size A3	Document Number MS72-1-01	Rev 0.1
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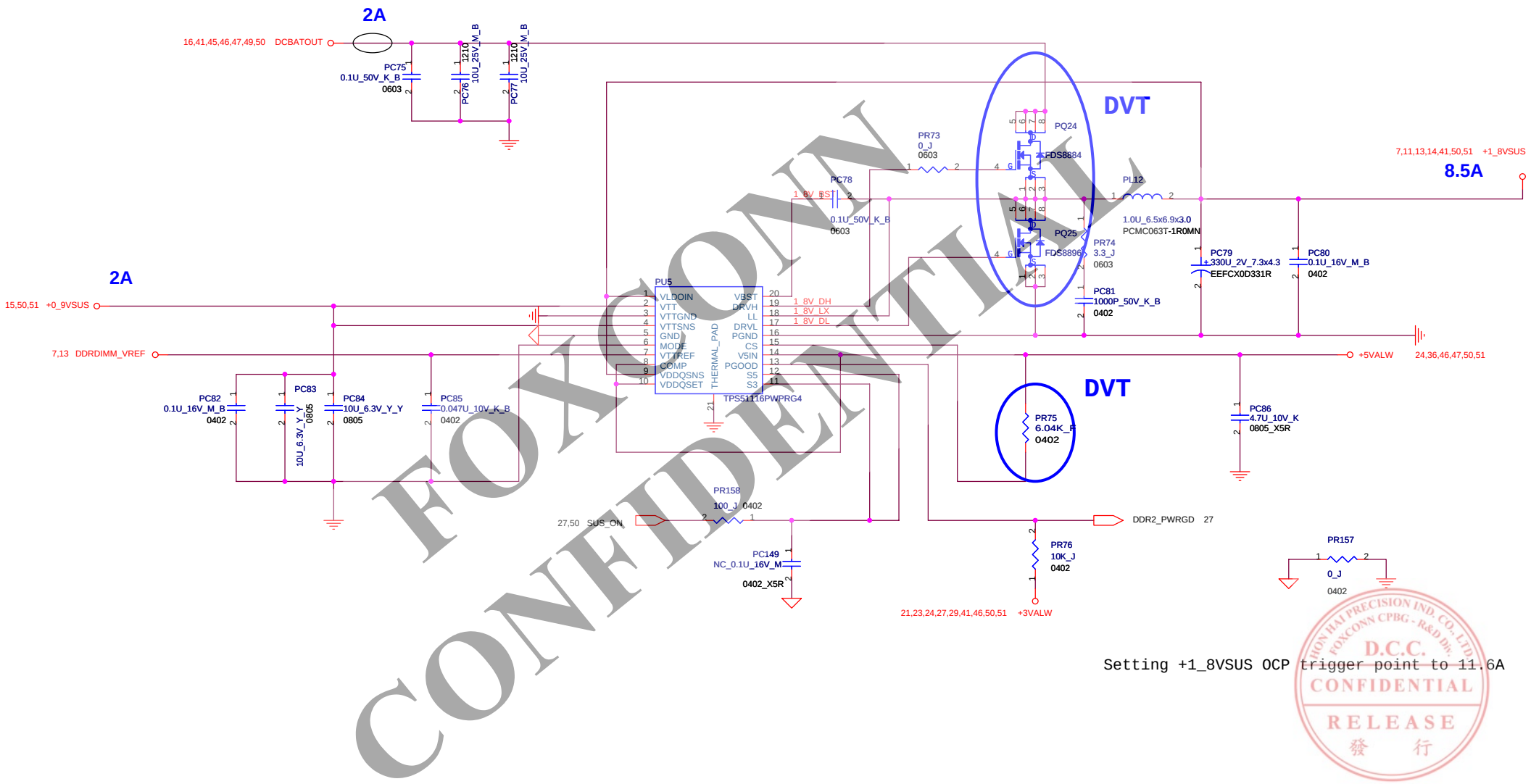
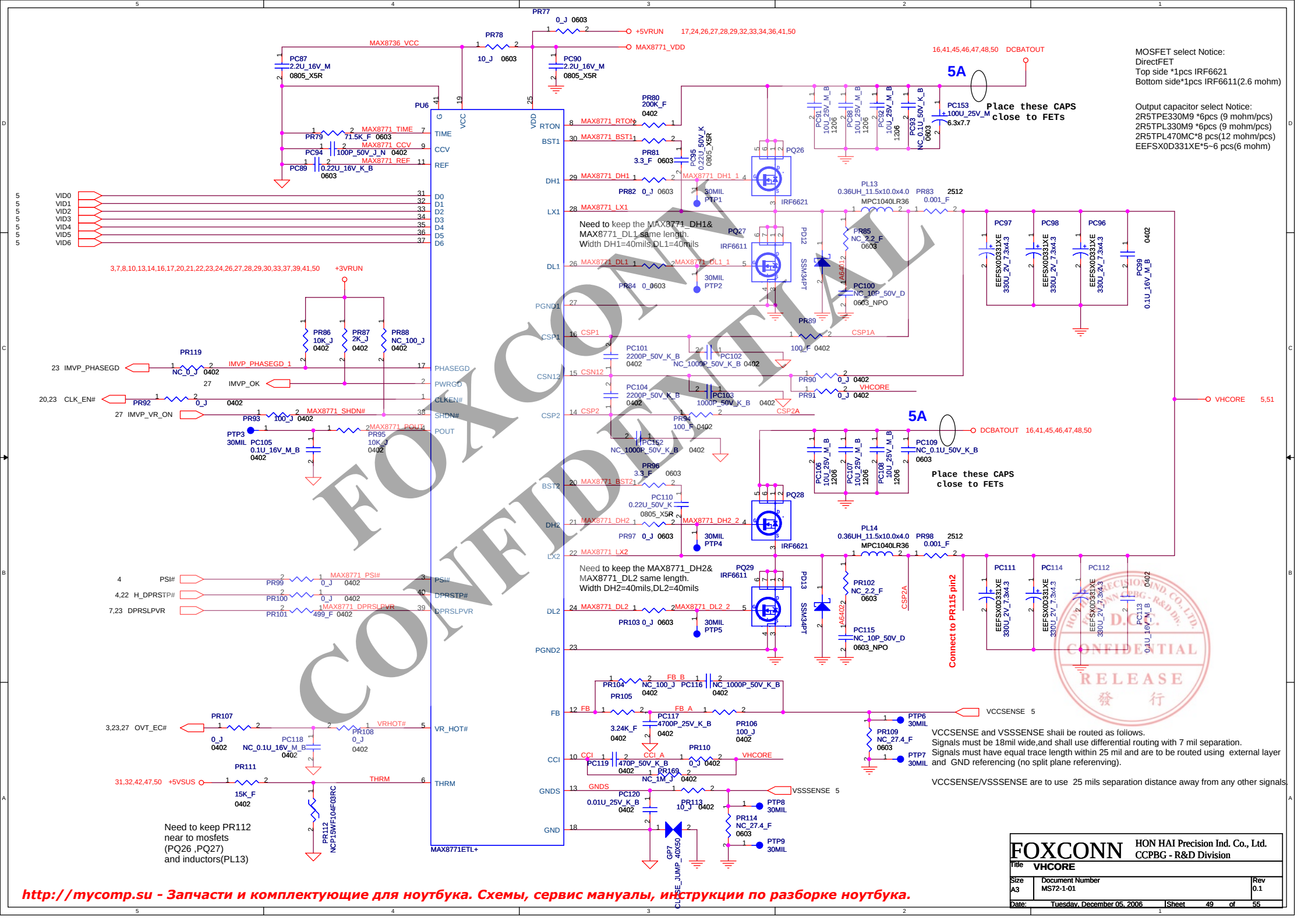
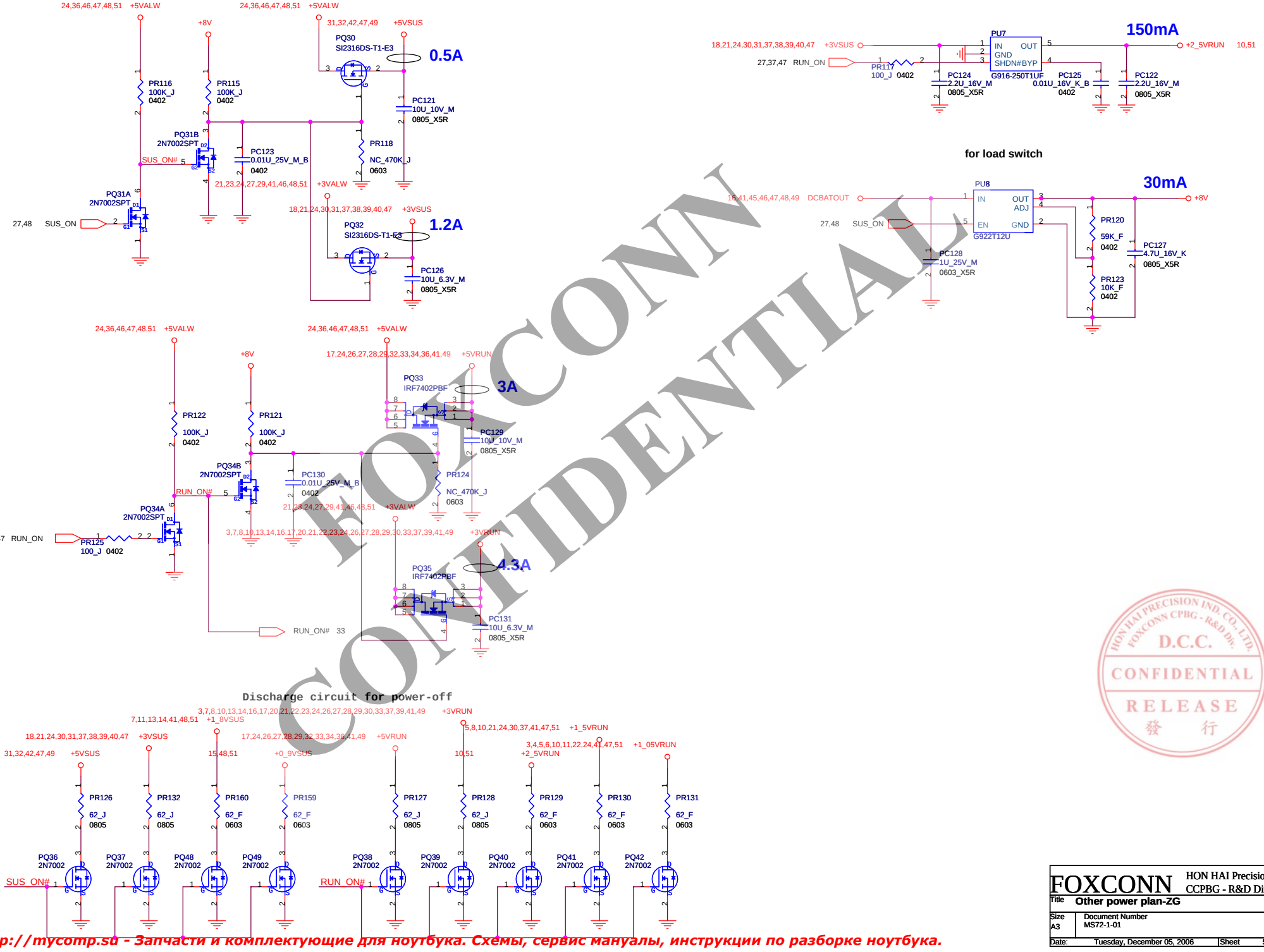


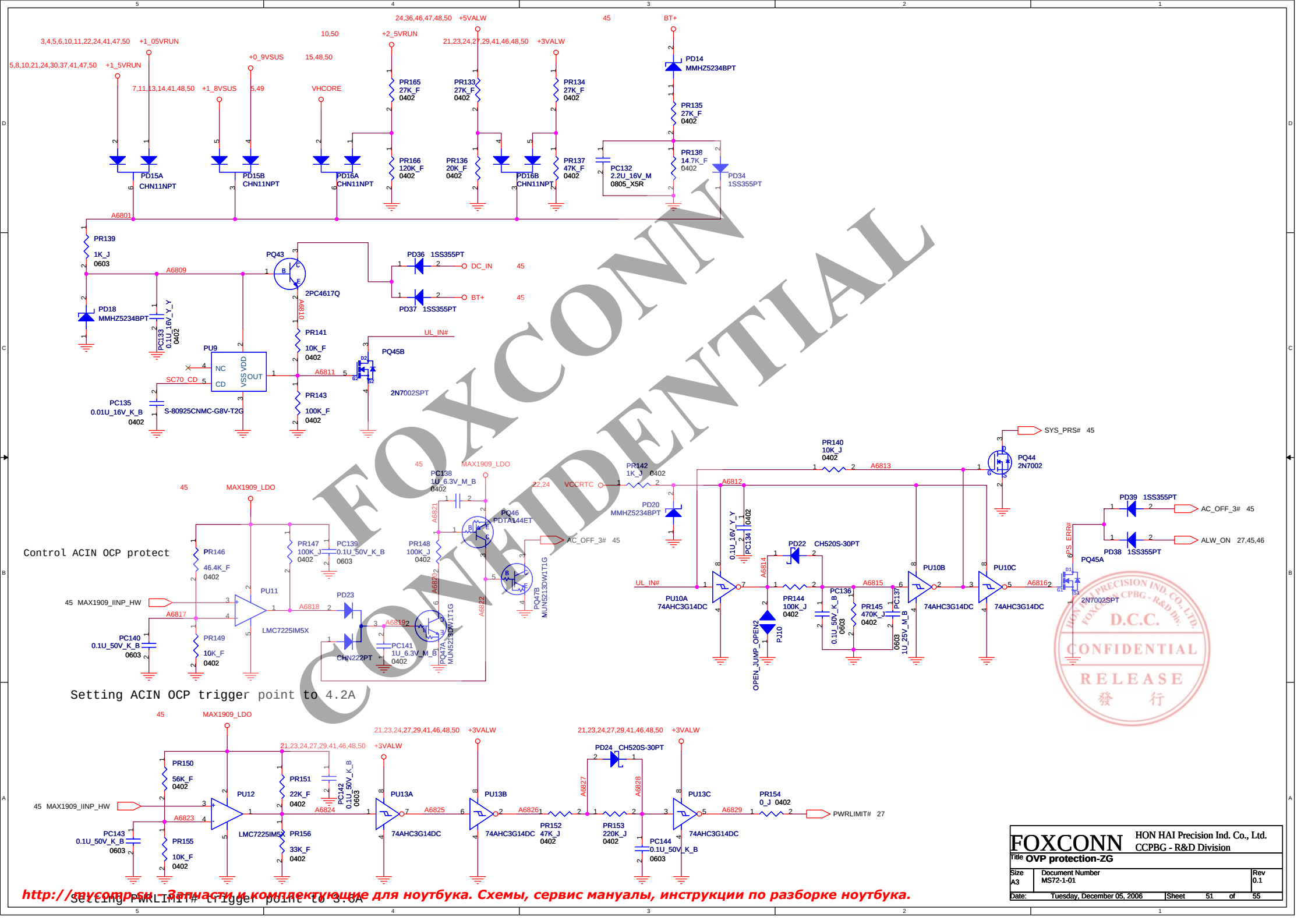
Setting +1_8VSUS OCP trigger point to 11.6A

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title	1.8V/0.9V-CWK	
Size A3	Document Number MS72-1-01	Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 48 of 55	





FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
Title	Other power plan-ZG		
Size A3	Document Number MS72-1-01		Rev 0.1
Date: Tuesday, December 05, 2006	Sheet 50 of 55		



HISTORY (1)

DVT

(2006/11/14)

P.11 Add C608 and NC CAP7 for purchase convenient.
P.17 Add R544 and NC R121for CRB requirement(refer to Capell_Valley_CRB_Schematics_Rev1_502 page18)
P.33 NC R483 for Page36 had pull low 10k resistor to GND
P.34 Change CAP11,CAP12 from 1C-31T0337-M101 to 1C-42T0107-M100 for application modification
P.36 Add Q167, Q168, R536 and NC U26, U27,U28, D12, C452, R377,R378 for application modification
Change Q27, Q159, from 17-CHDTC14-4E01 to 17-PMBT390-4200 for application modification
Change Q26 from 17-DTA114Y-UA00 to 17-MMBT390-6K00 for application modification
Add R540,R541,R542,R543,R537,R538,R539,C609 for application modification

(2006/11/15)

P.47 Change PQ22 from 17-1RF7807-2000 to 17-FDS8884-0000 for application modification
Change PQ23 from 17-1RF8113-0000 to 17-FDS8896-0000 for application modification
P.48 Change PQ24 from 17-1RF7807-2000 to 17-FDS8884-0000 for application modification
Change PQ25 from 17-1RF8113-0000 to 17-FDS8896-0000 for application modification

(2006/11/20)

P.11 Del C608 and mount CAP7 for MOR requirement
P.17 Del R544 and mount R121 for MOR requirement
P.47 Change PR67 from 1R-0000472-F200 to 1R-0007321-F200 for power PWM OCP application modification
P.48 Change PR75 from 1R-0000562-F200 to 1R-0006041-F200 for power PWM OCP application modification

(2006/11/21)

P.33 Add R483 for MOR requirement
P.36 Del Q167, Q168, R536 and add U26, U27,U28,D12, C452, R377,R378 for MOR requirement
Change Q27, Q159, from 17-PMBT390-42001 to 17-CHDTC14-4E01for MOR requirement
Change Q26 from 17-MMBT390-6K00 to 17-DTA114Y-UA00 for MOR requirement
Del R540,R541,R542,R543,R537,R538,R539,C609 for MOR requirement

(2006/11/22)

P.28 Add R535 0om for X-BUS CONN del on MP
P.47 Add PC154 and change PR61 from 10k to 100k for RUN_PWRGD glitch issue
Change the power supply of PU4 pin16 from +5VSUS to +5VALW for application modification

(2006/11/23)

P.47 Add PR170,PR171 for MOR requirement

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: History (1)		
Size: C	Document Number: MS72-1-01	Rev: 0.1
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HISTORY (2)

FOXCONN	HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division	
Title: History (2)		
Size: C	Document Number: MS72-1-01	Rev: 0.1
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