

10 System Schematic Diagrams

10-1 System Main Board Schematic Diagrams

Devices	IDSEL#	REQ/GNT#	Interrupts

Devices	IDSEL#	REQ/GNT#	Interrupts

Name	Name	Max current

Name	Name	Max current

[illegible]

Devices	Address	Hex	Bus

PORT NUMBER	ASSIGNED TO
1	1
2	2
3	3
4	4
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98	98
99	99
100	100

PORT NUMBER	ASSIGNED TO
1	1
2	2
3	3
4	4
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99	99
100	100

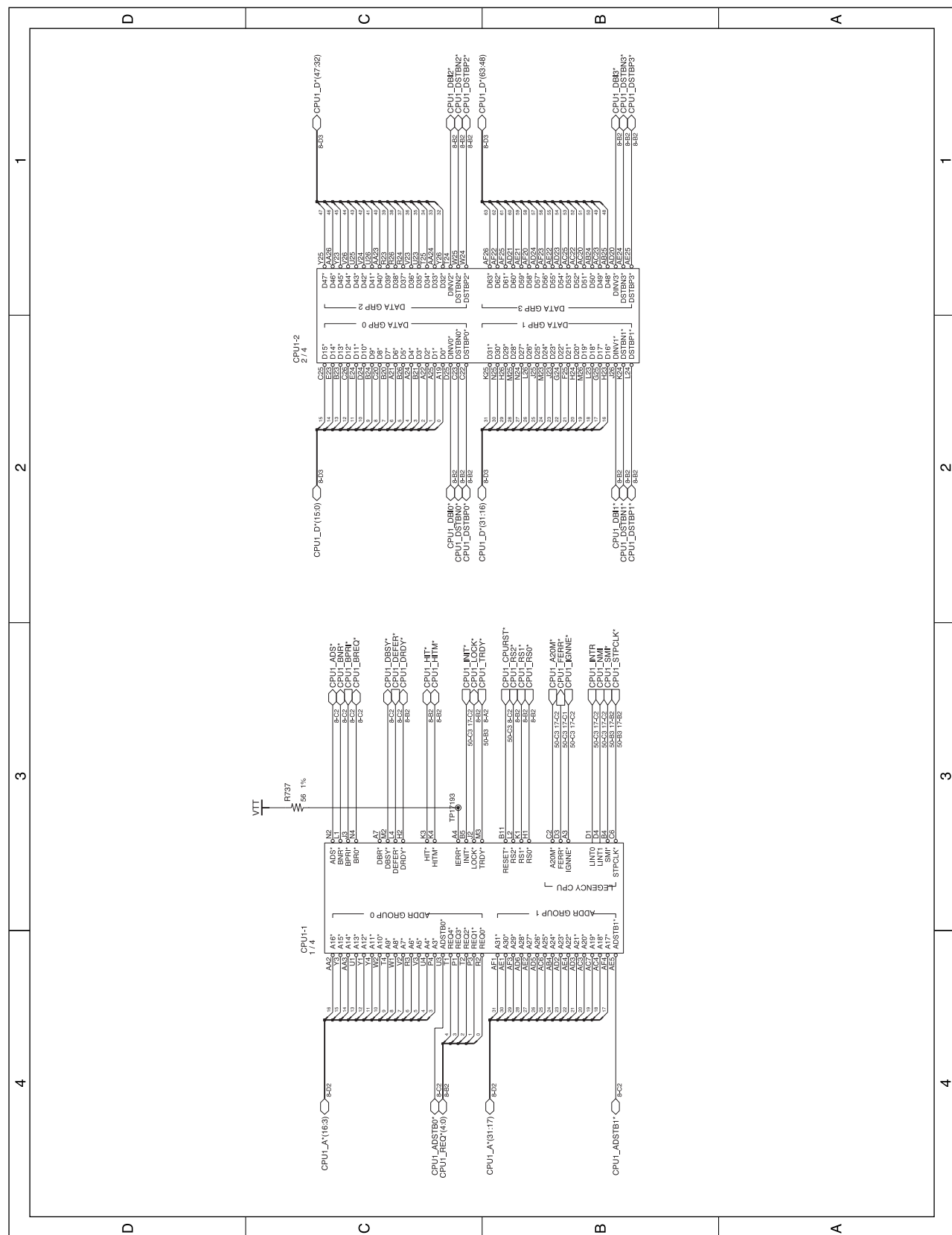
[illegible][illegible]

CHP3_SLP3³ S3, Suspend-To-Disk(STD) : The system context is maintained in system DRAM, but power is shut off to non-critical circuits. Memory is refreshed, and refreshes continue. All clocks stop except RTC clock.

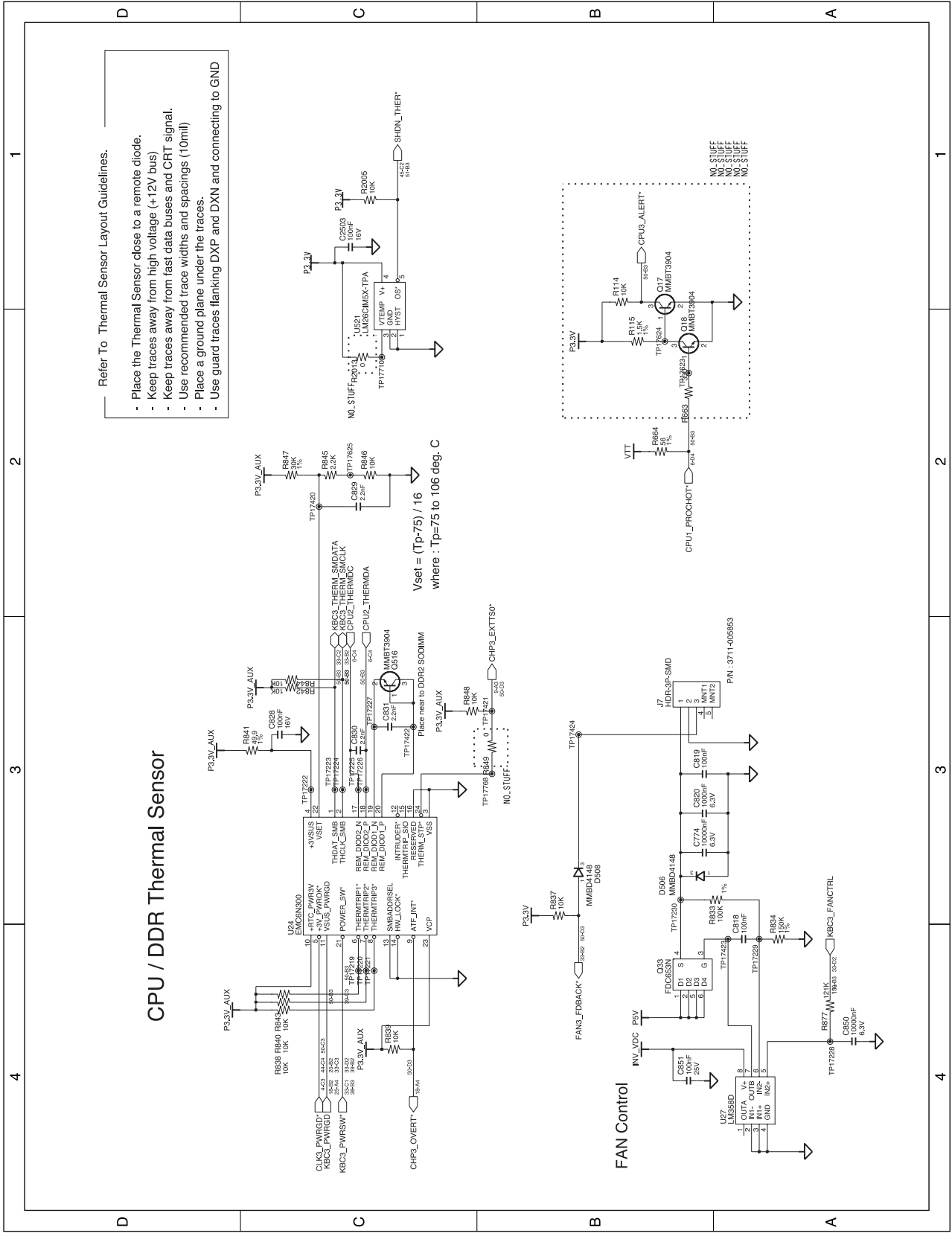
CHP3_SLP4⁴ S4, Suspend-To-Disk(STD) : The context of the system is maintained on the disk. All power is then shut off to the system except for the logic required to resume.

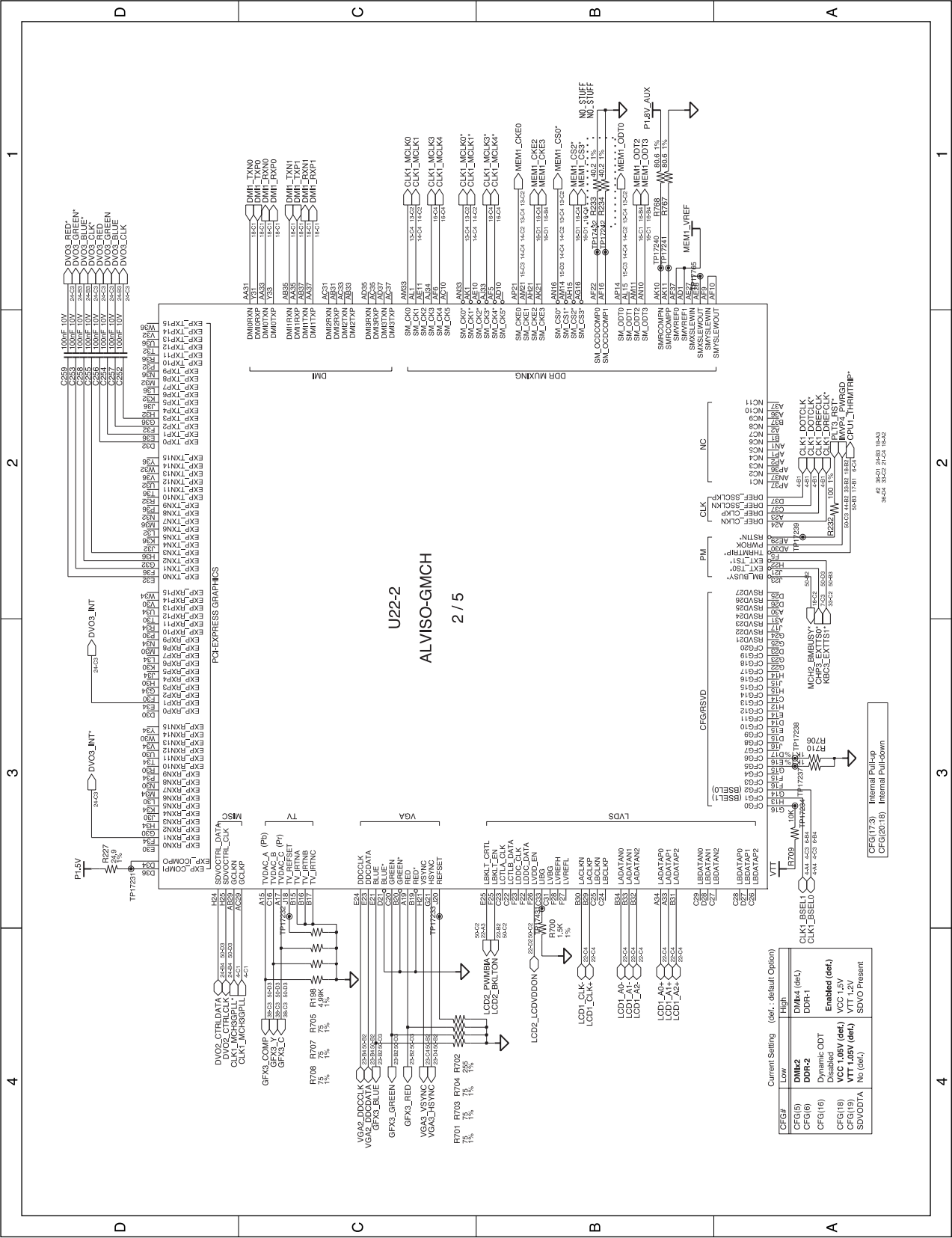
CHP3_SLP5⁵ S5, Soft Off(SOF) : System context is not maintained. All power is shut off except for the logic required to restart. A full boot is required when waking.

* In case of Deeper Sleep 0.726 ~ 0.748V (B1, QS)

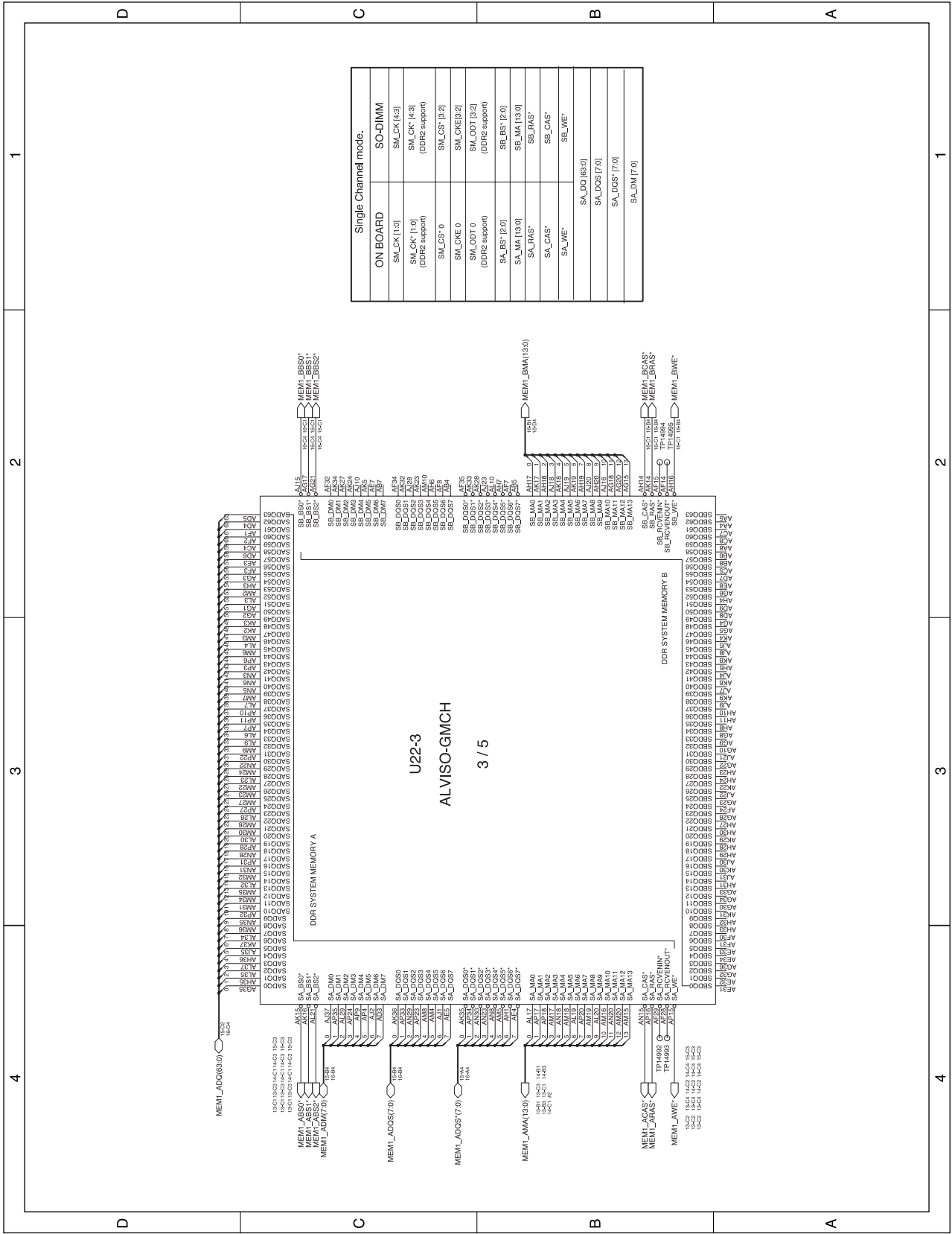


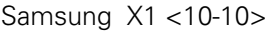
10-1-1(e) Main Board Schematic Sheet 6 of 50



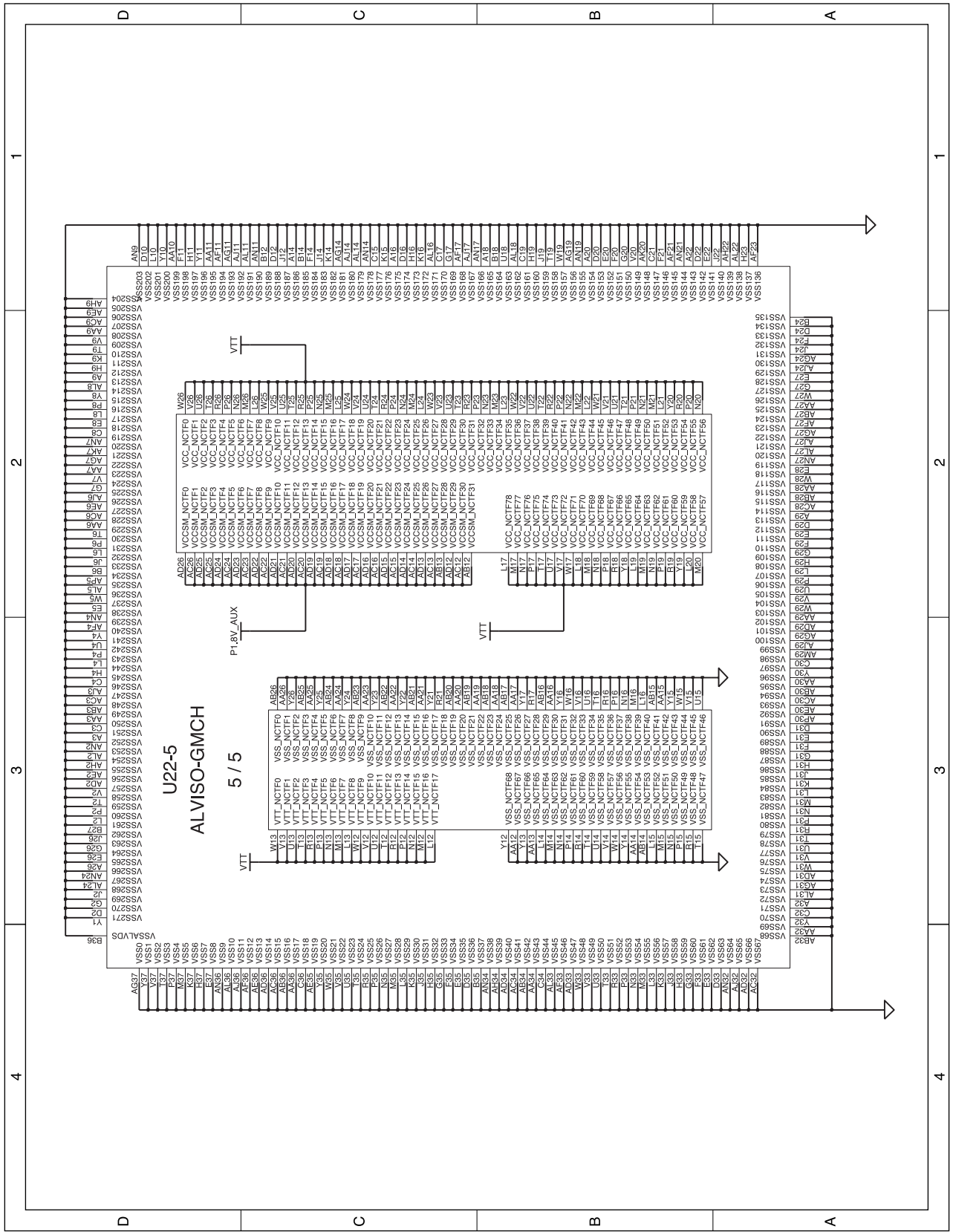


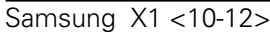
10-1-1(h) Main Board Schematic Sheet 9 of 50

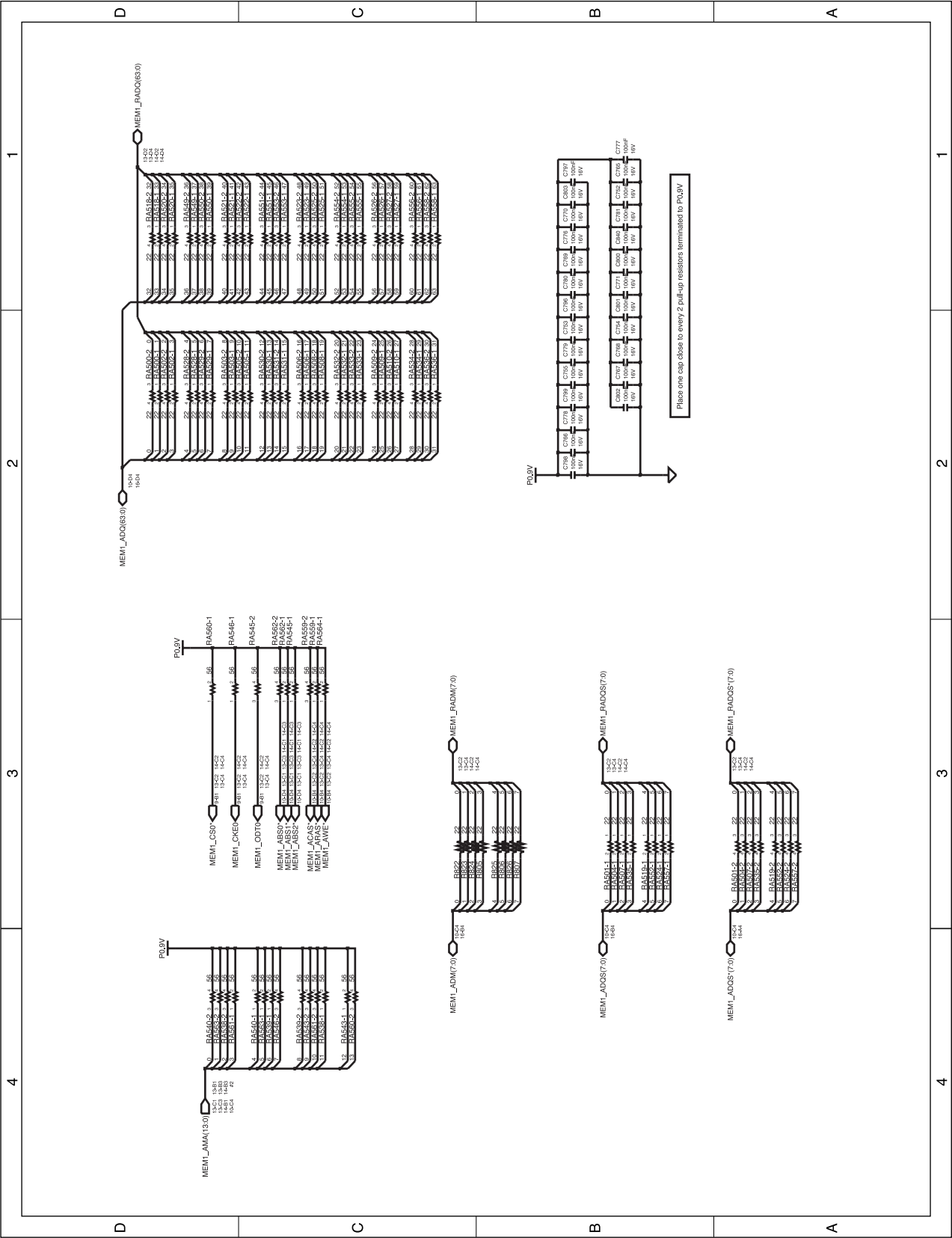




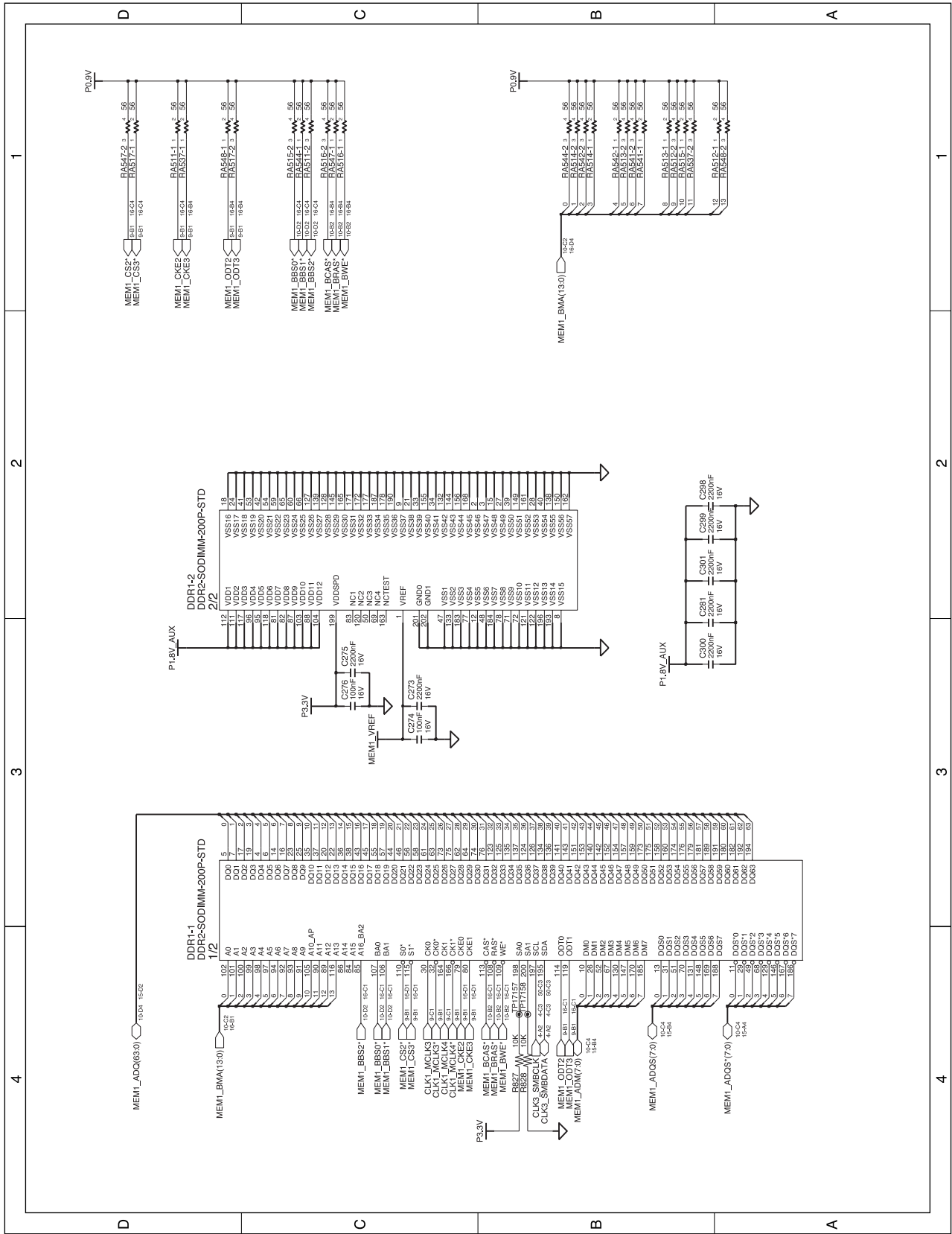
10-1-1(j) Main Board Schematic Sheet 11 of 50

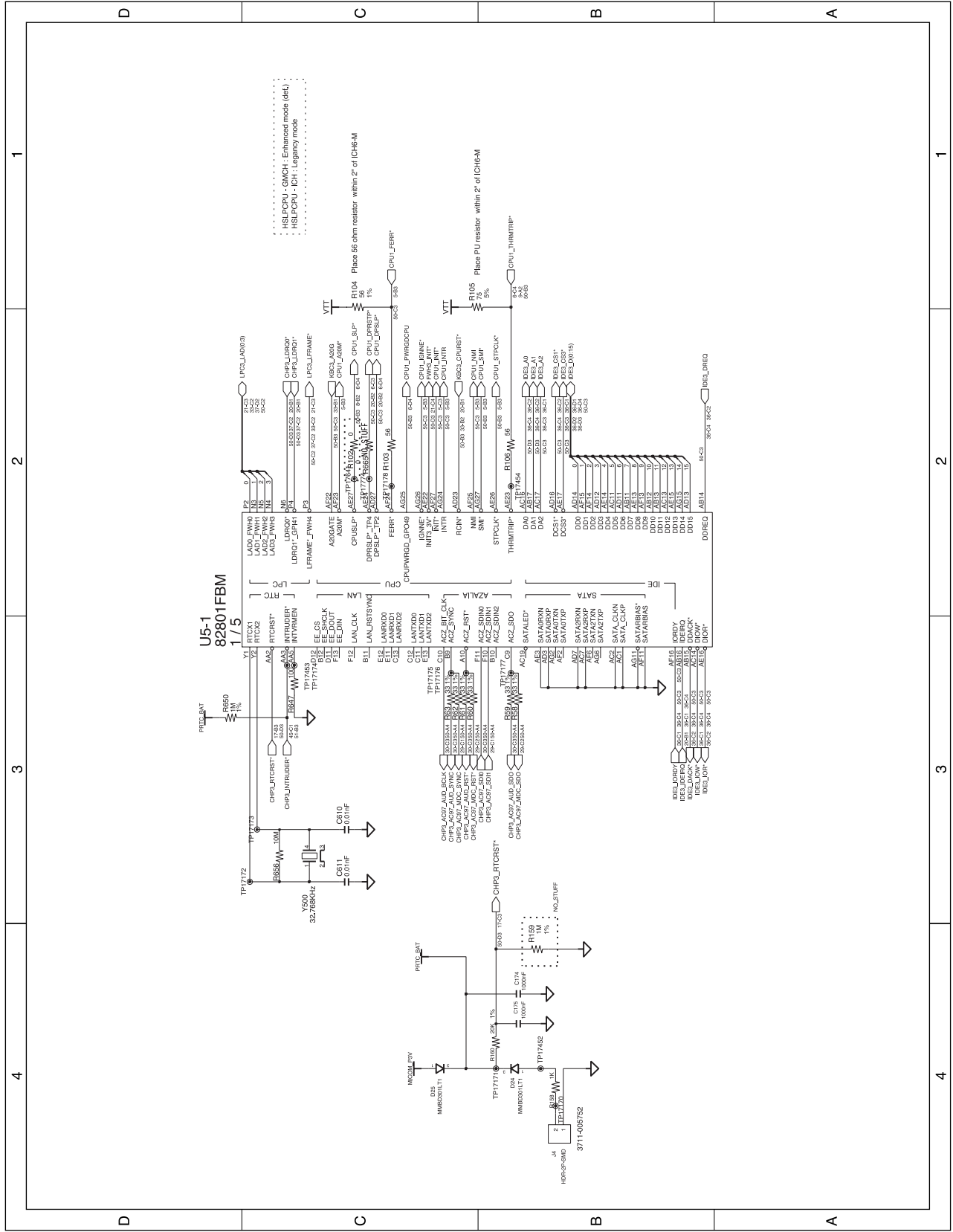


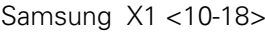




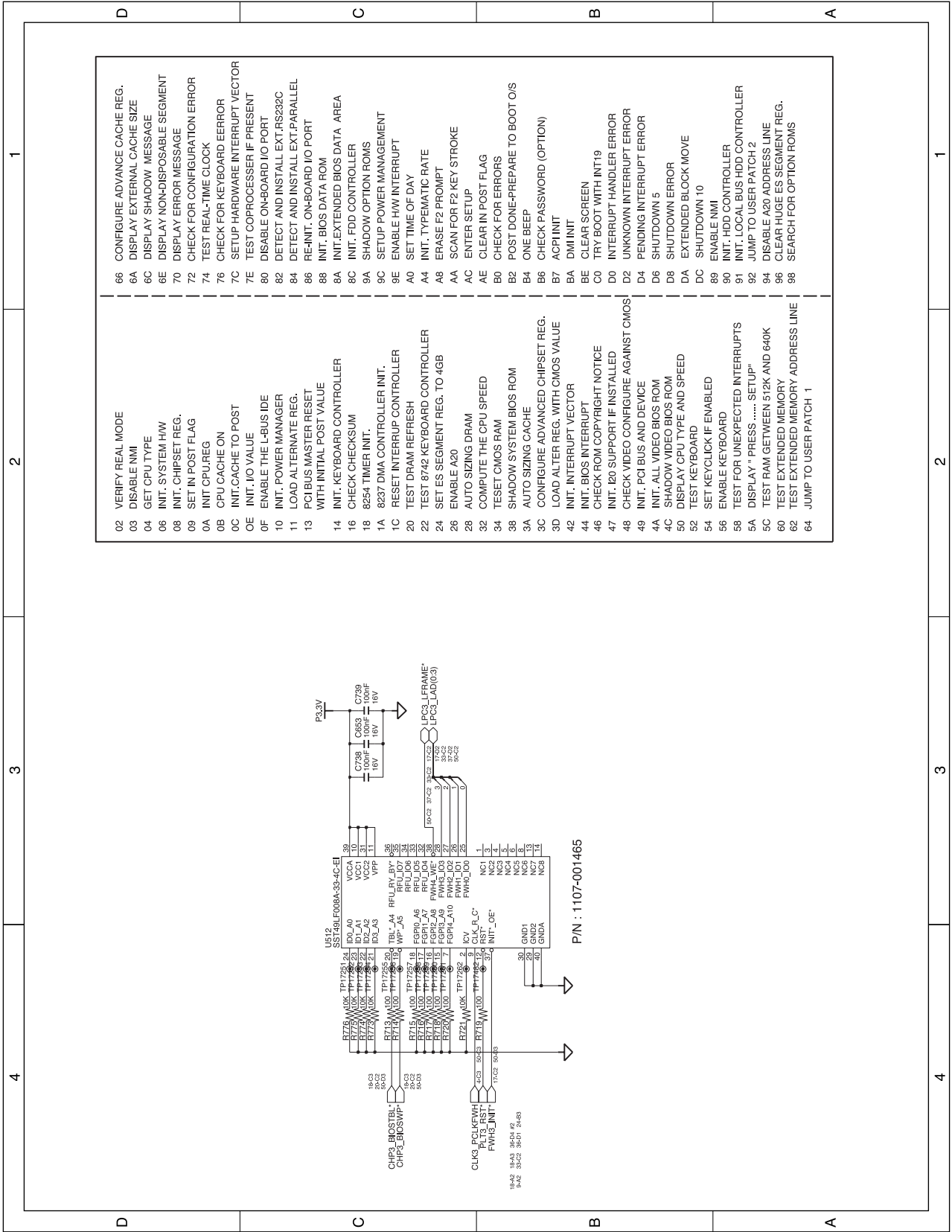
10-1-1(n) Main Board Schematic Sheet 15 of 50



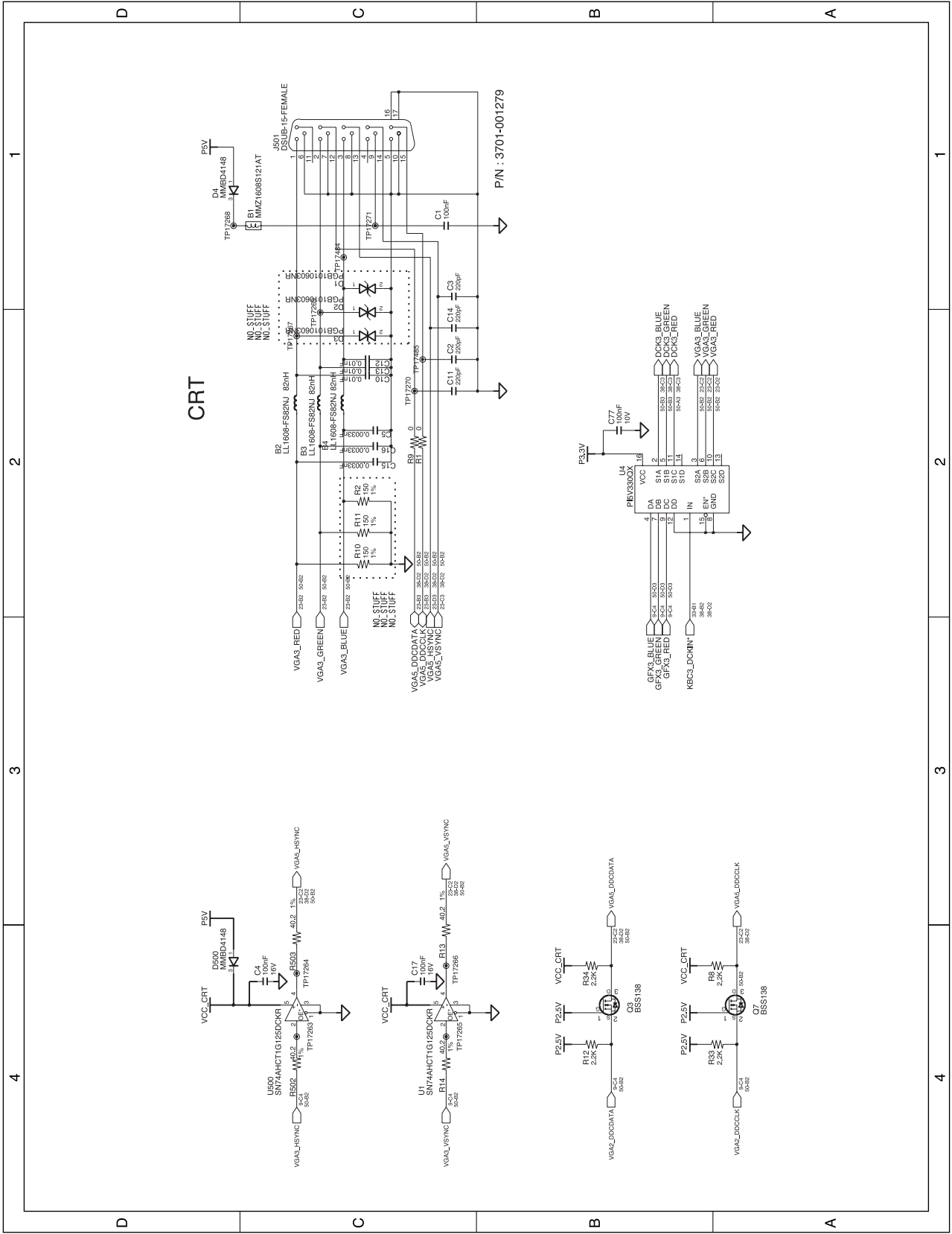


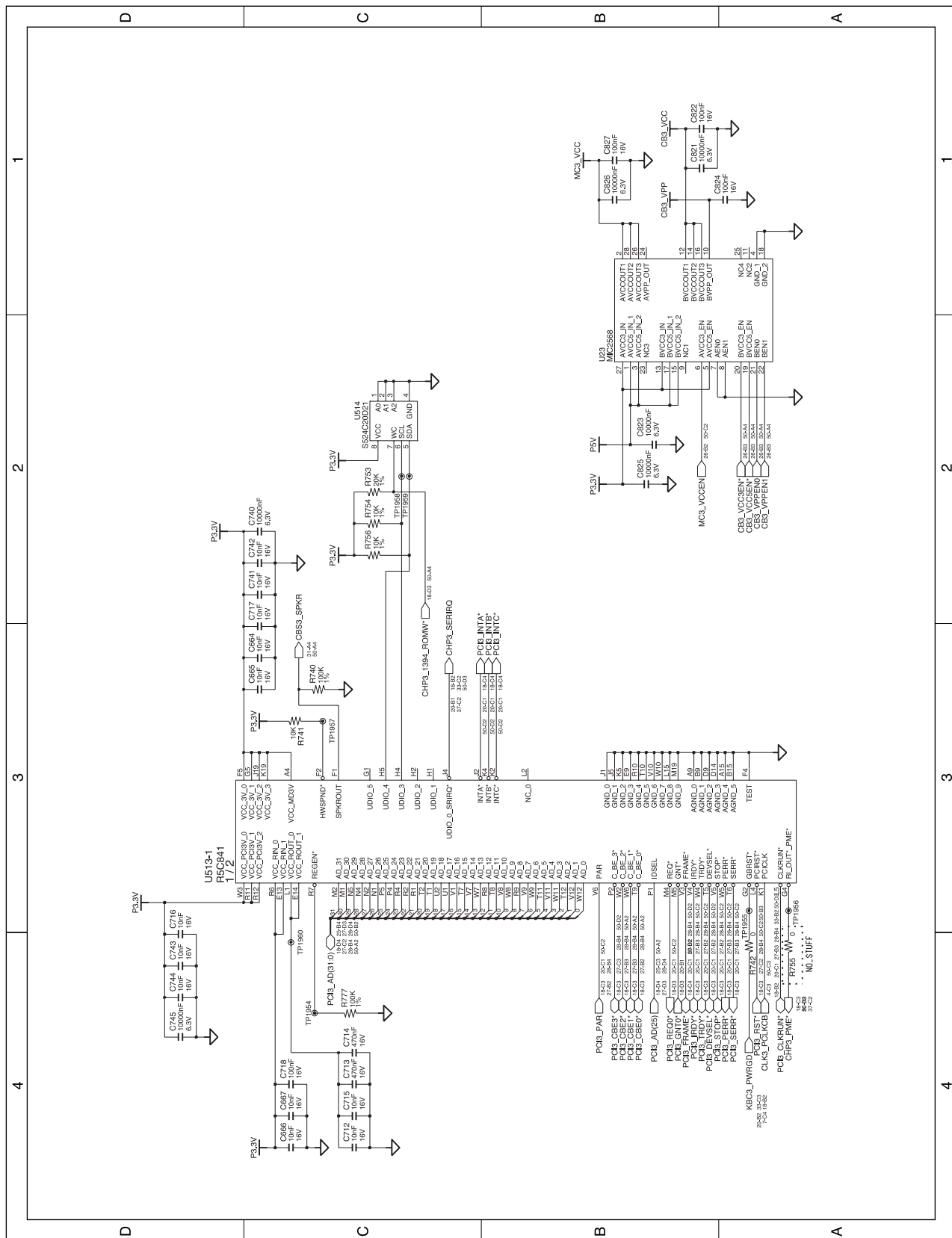


10-1-1(s) Main Board Schematic Sheet 20 of 50

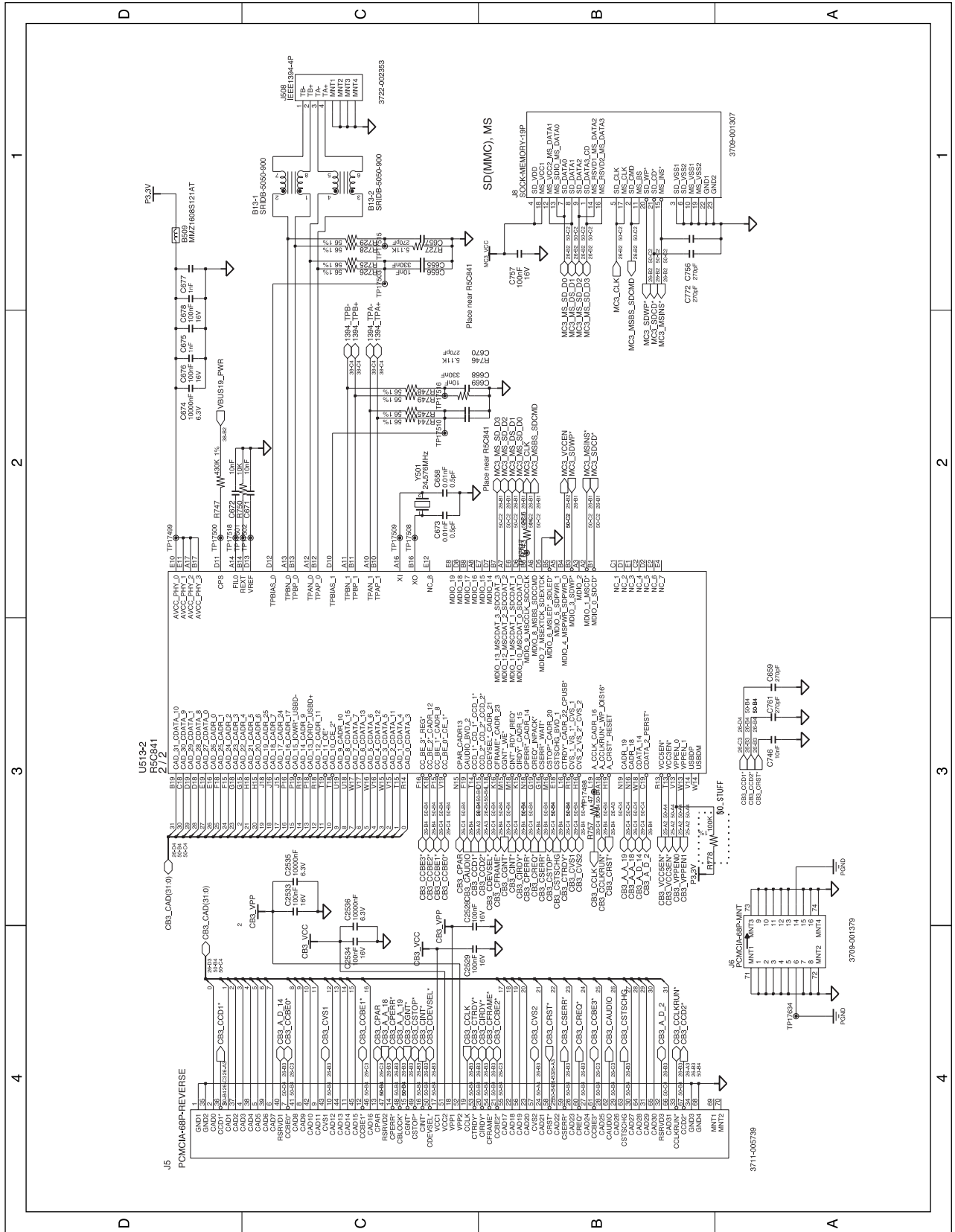


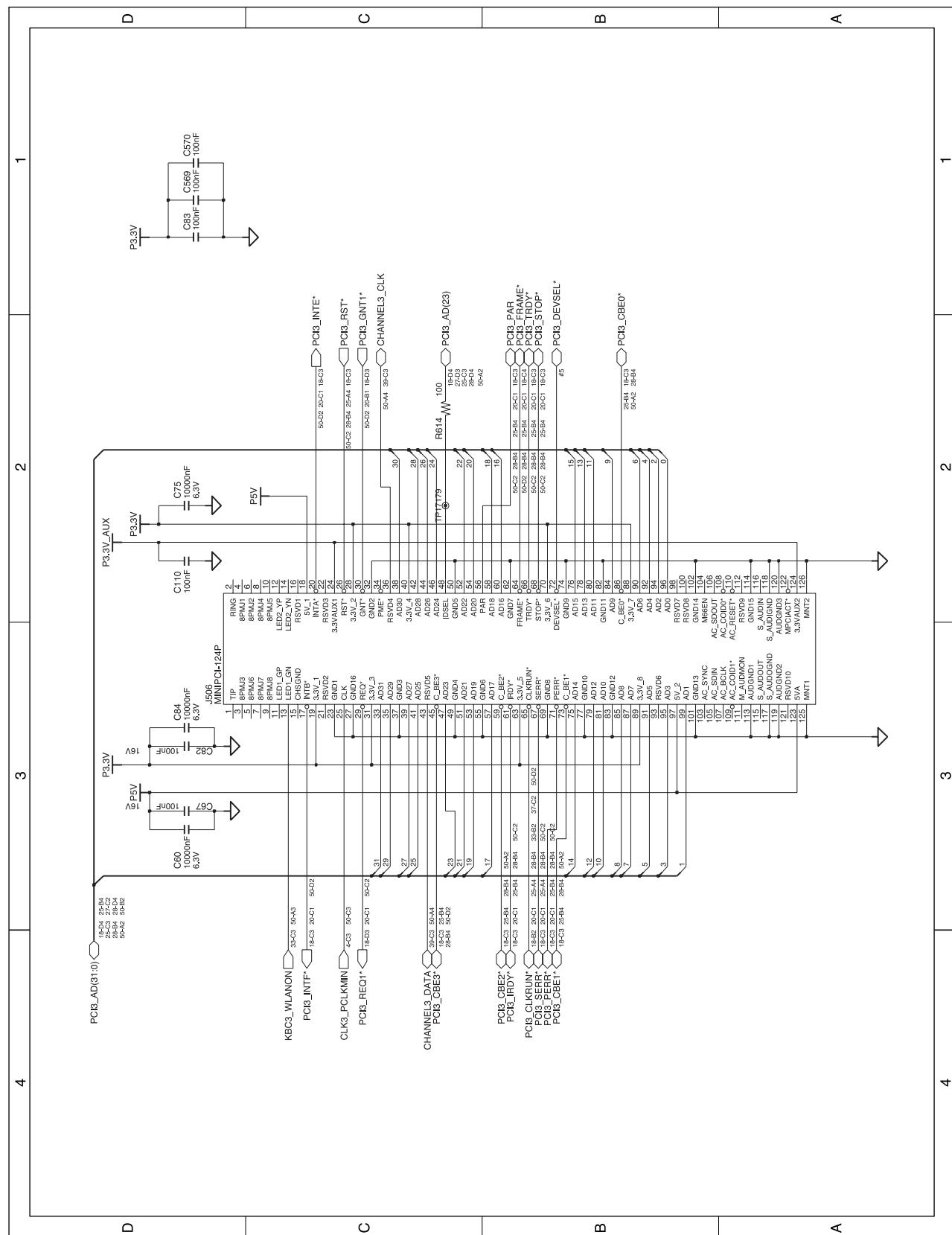
10-1-1(u) Main Board Schematic Sheet 22 of 50



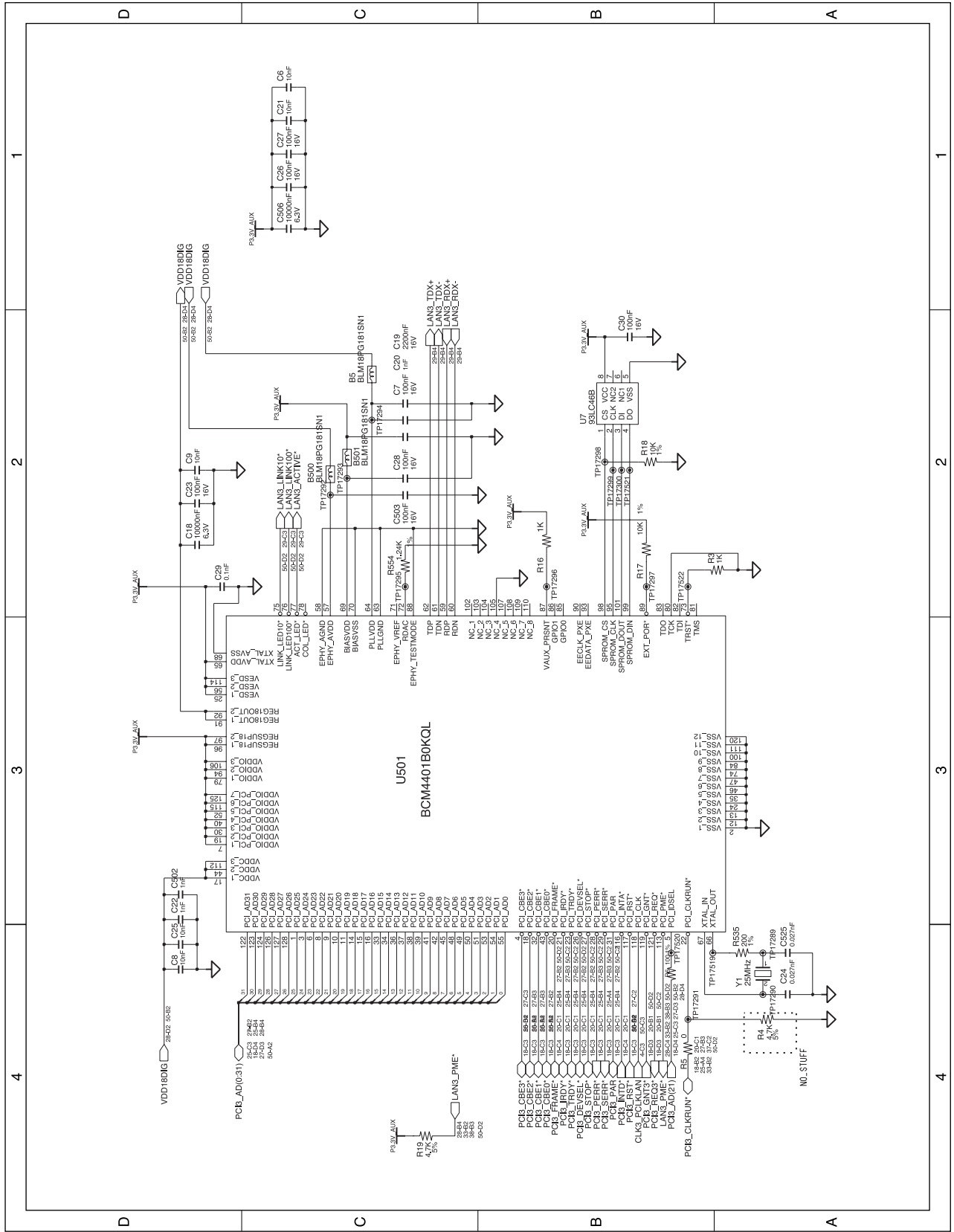


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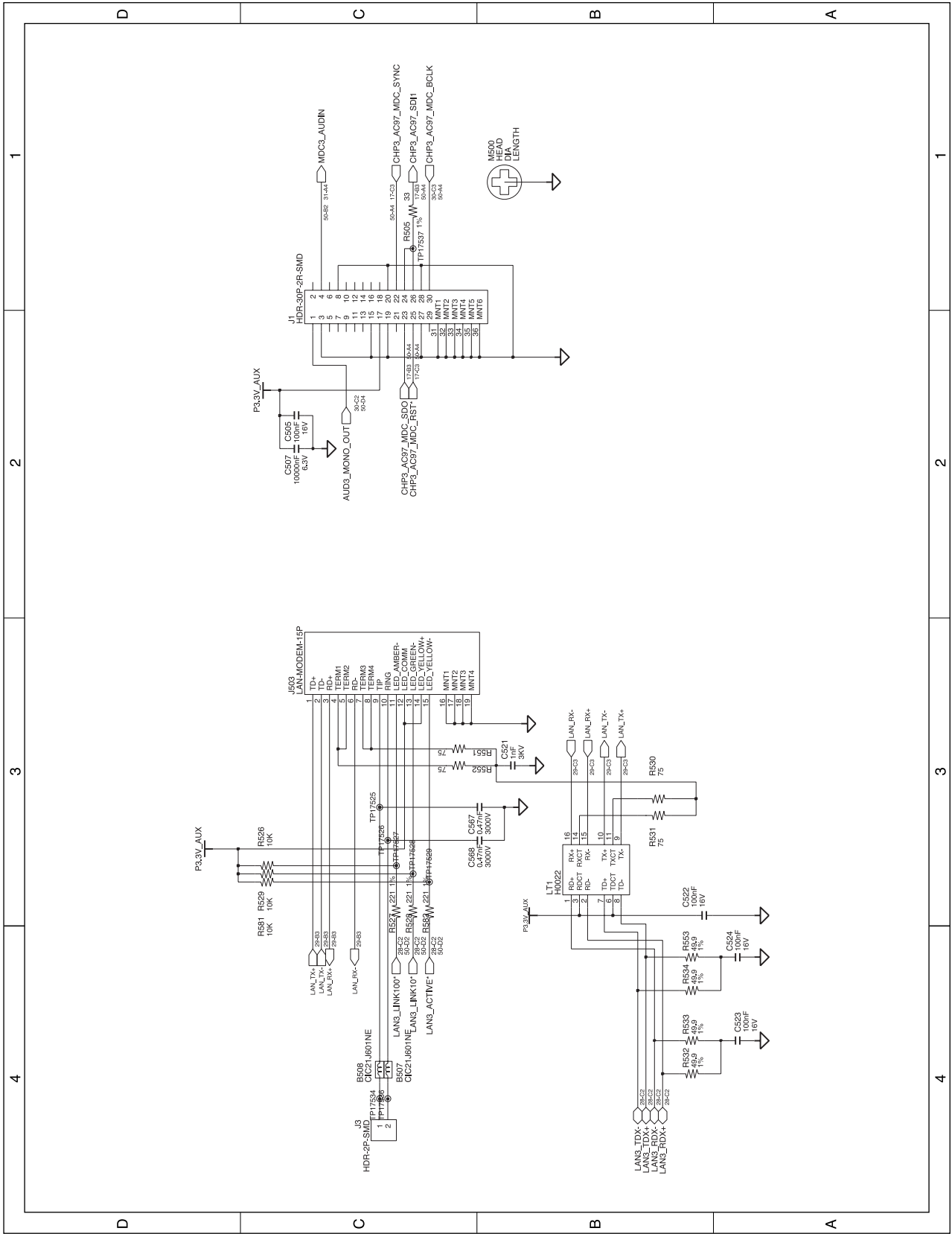




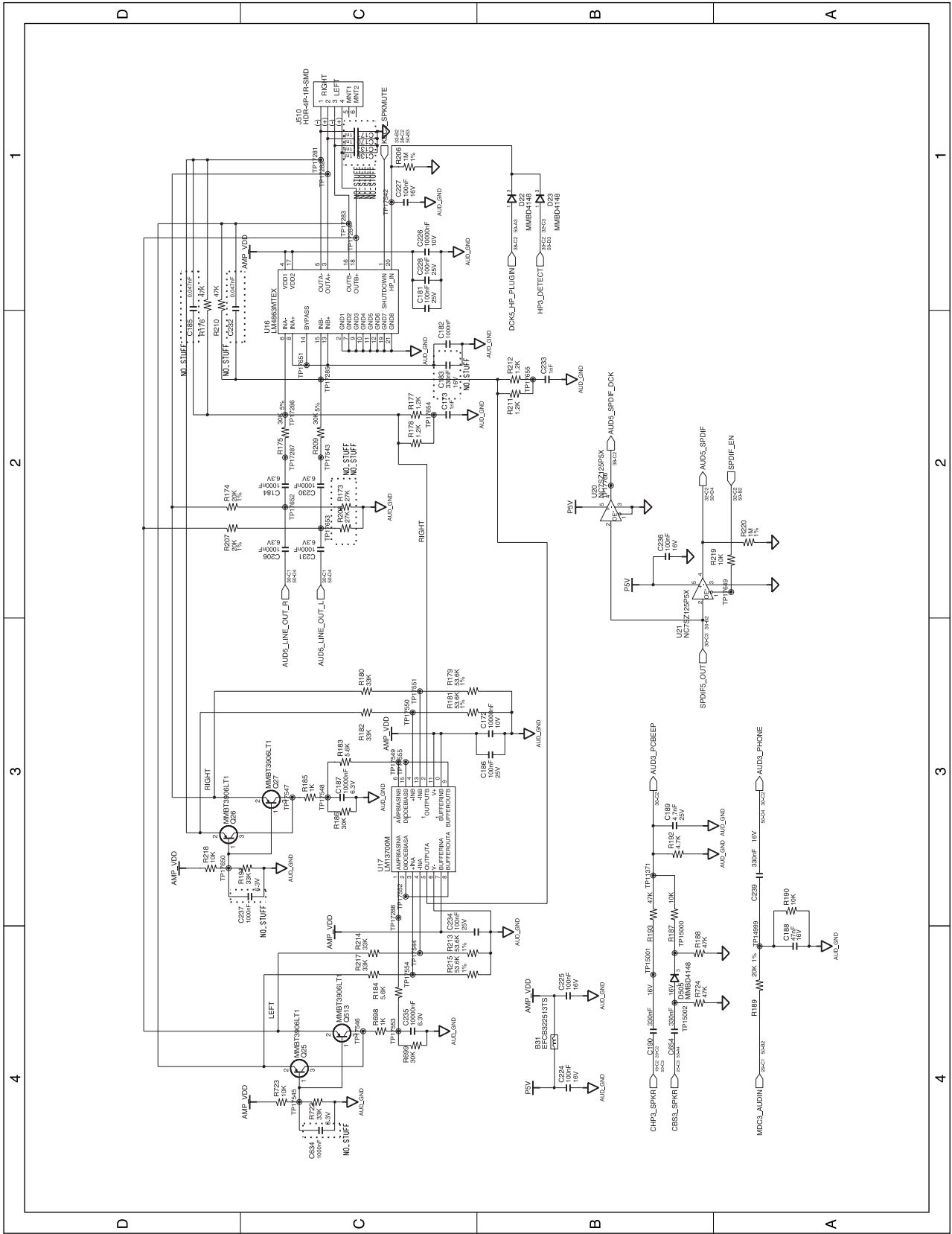
10-1-1(z) Main Board Schematic Sheet 27 of 50



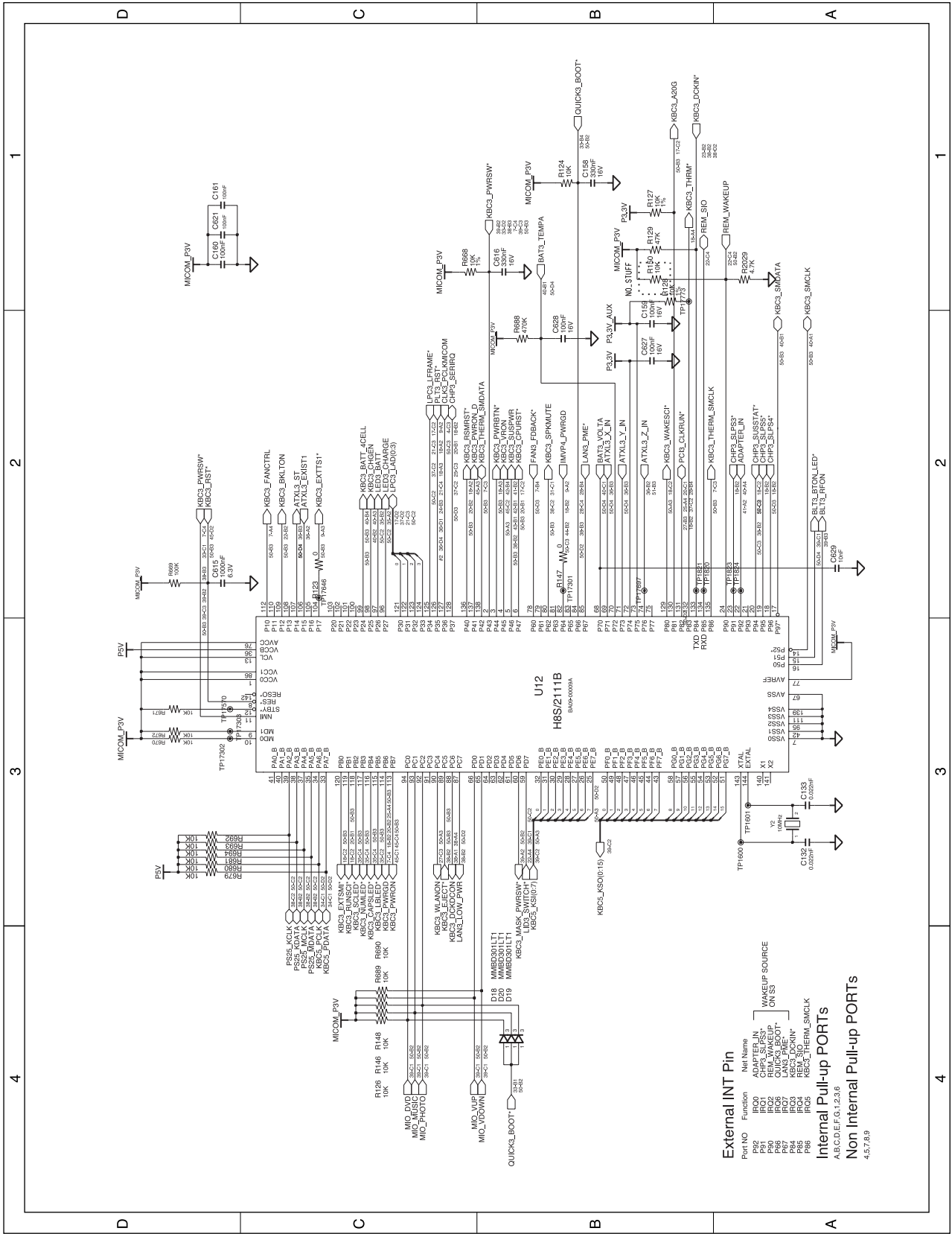
10-1-1(aa)Main Board Schematic Sheet 28 of 50



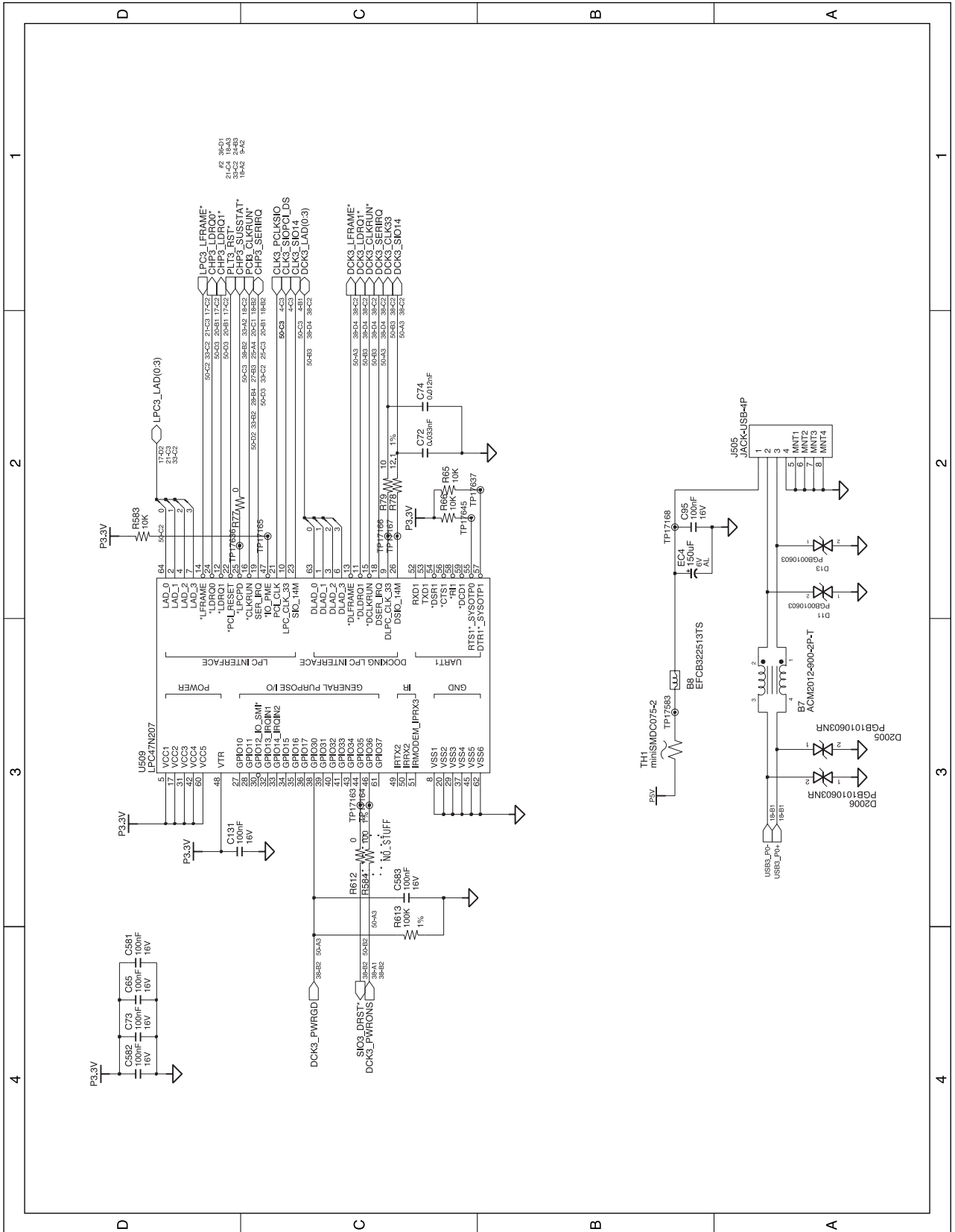
10-1-1(cc) Main Board Schematic Sheet 30 of 50

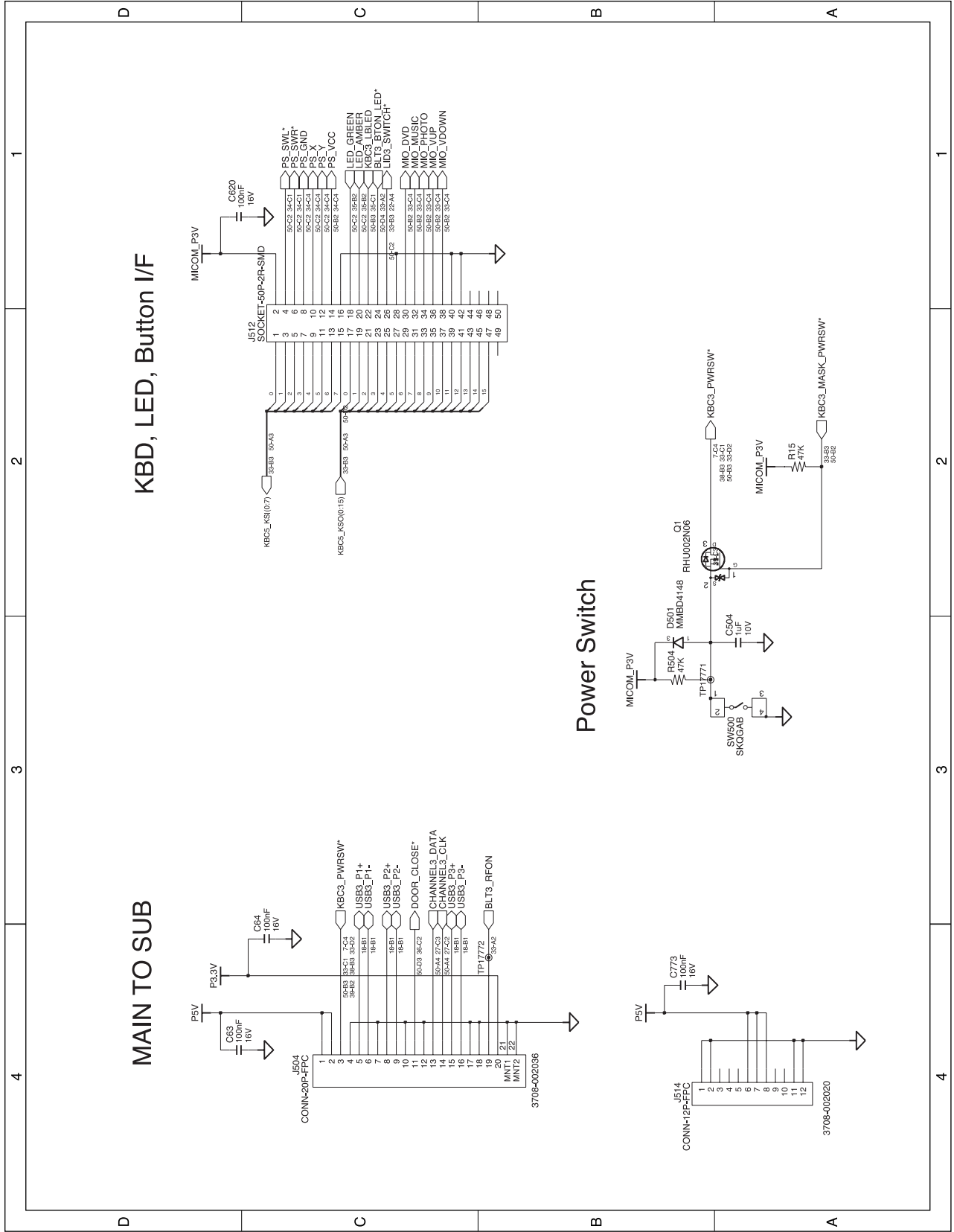


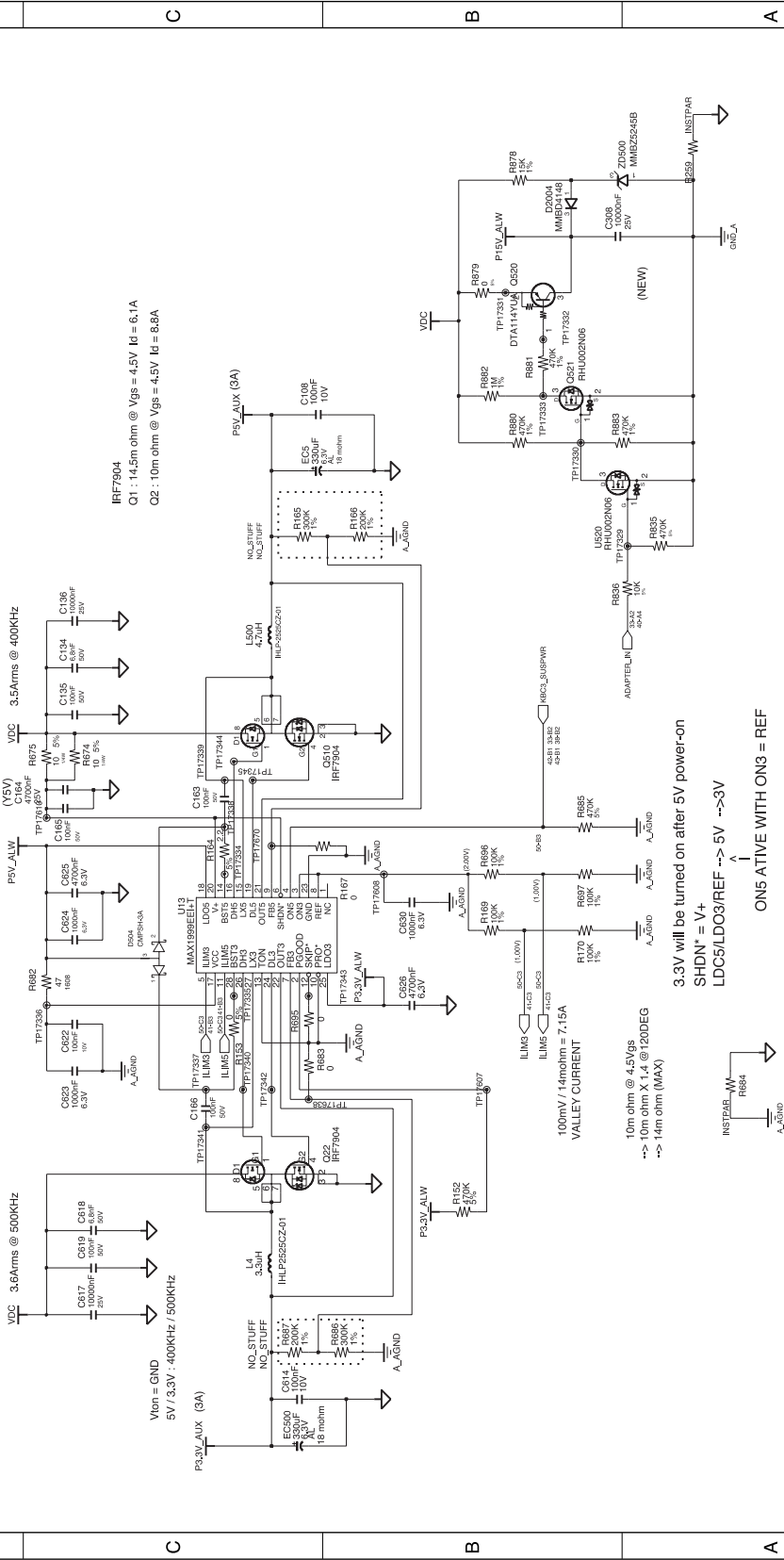
10-1-1(ee) Main Board Schematic Sheet 32 of 50



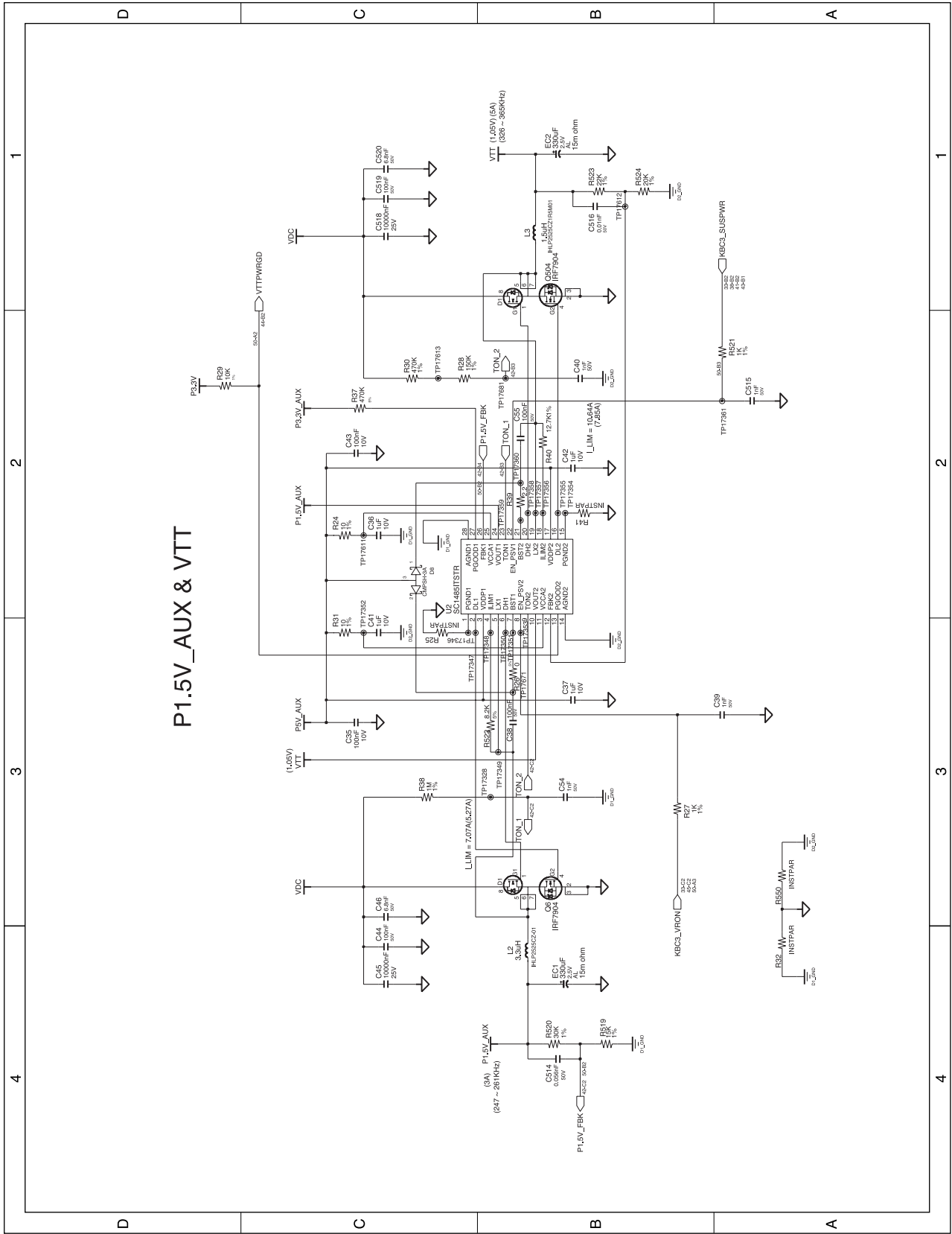
10-1-1(ii) Main Board Schematic Sheet 36 of 50

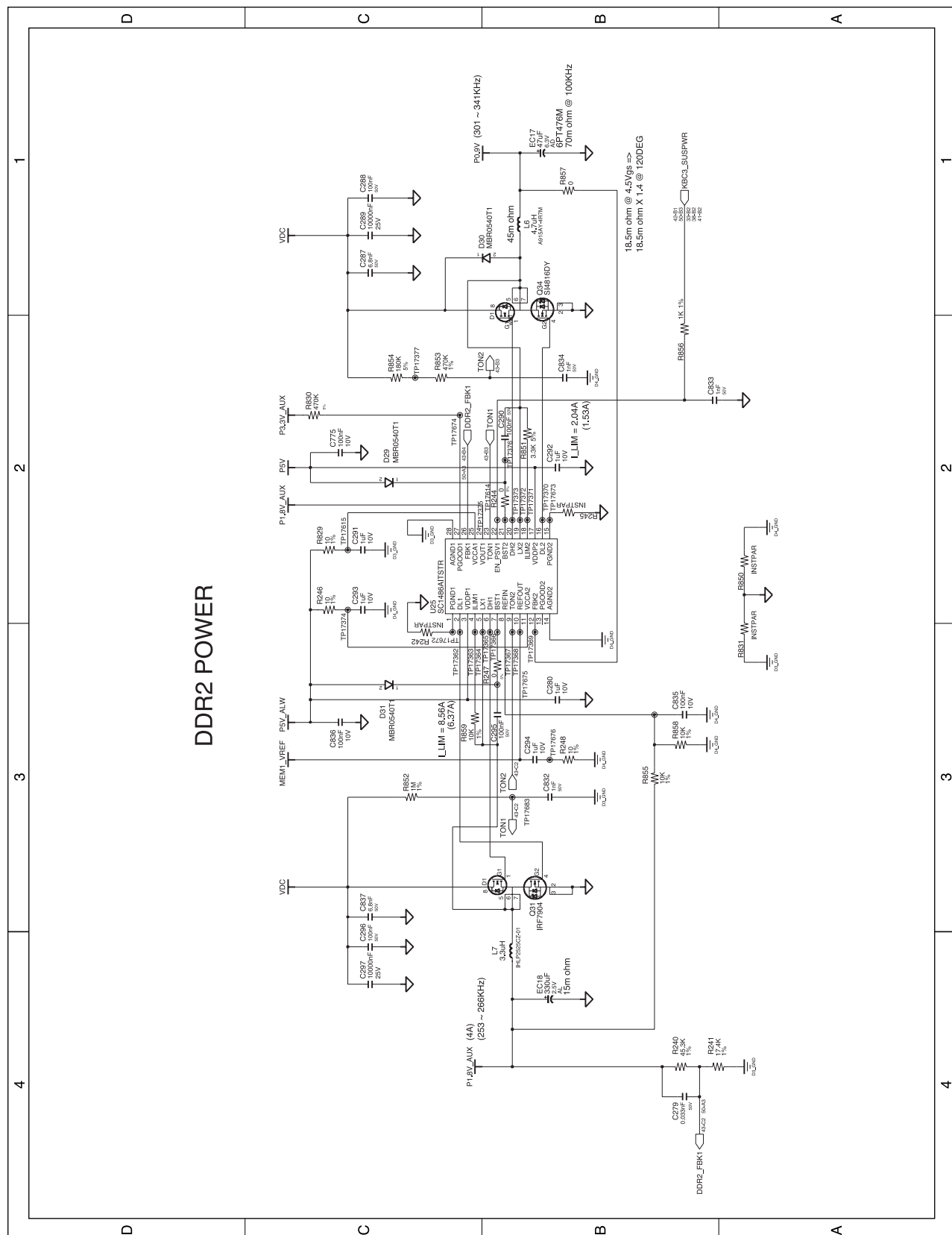


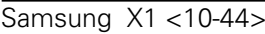




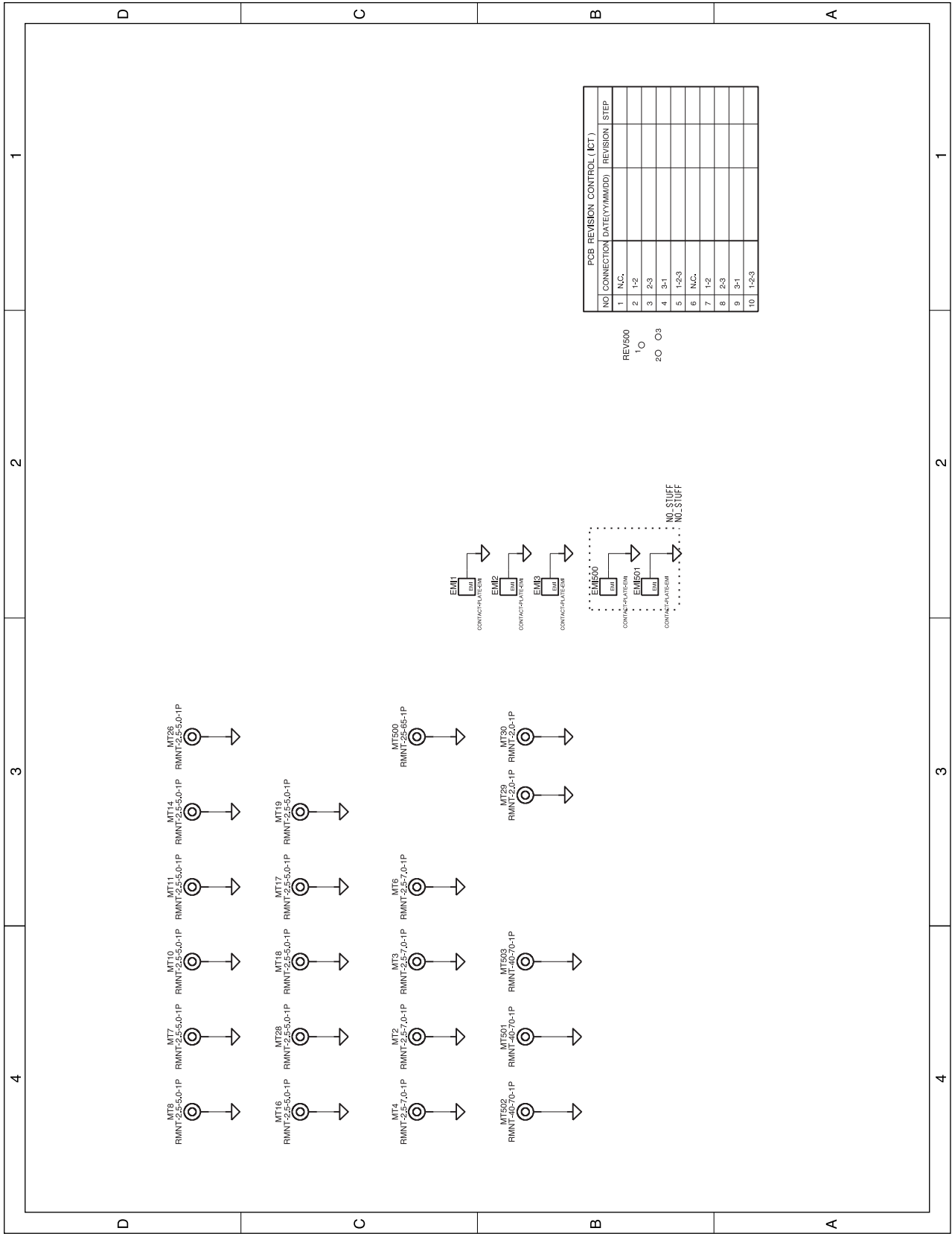
10-1-1(nn) Main Board Schematic Sheet 41 of 50

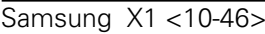


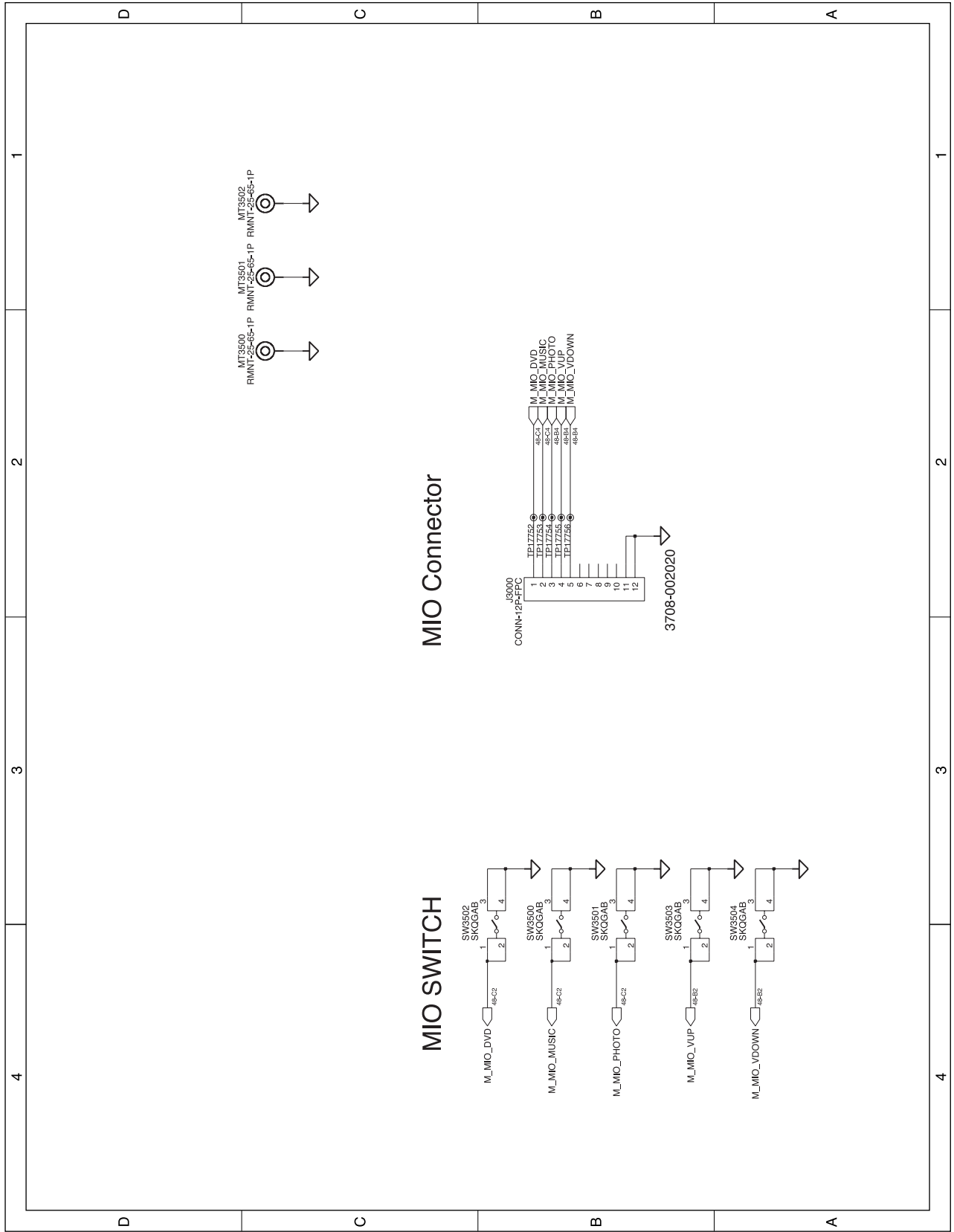




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10-1-1(uu) Main Board Schematic Sheet 48 of 50

