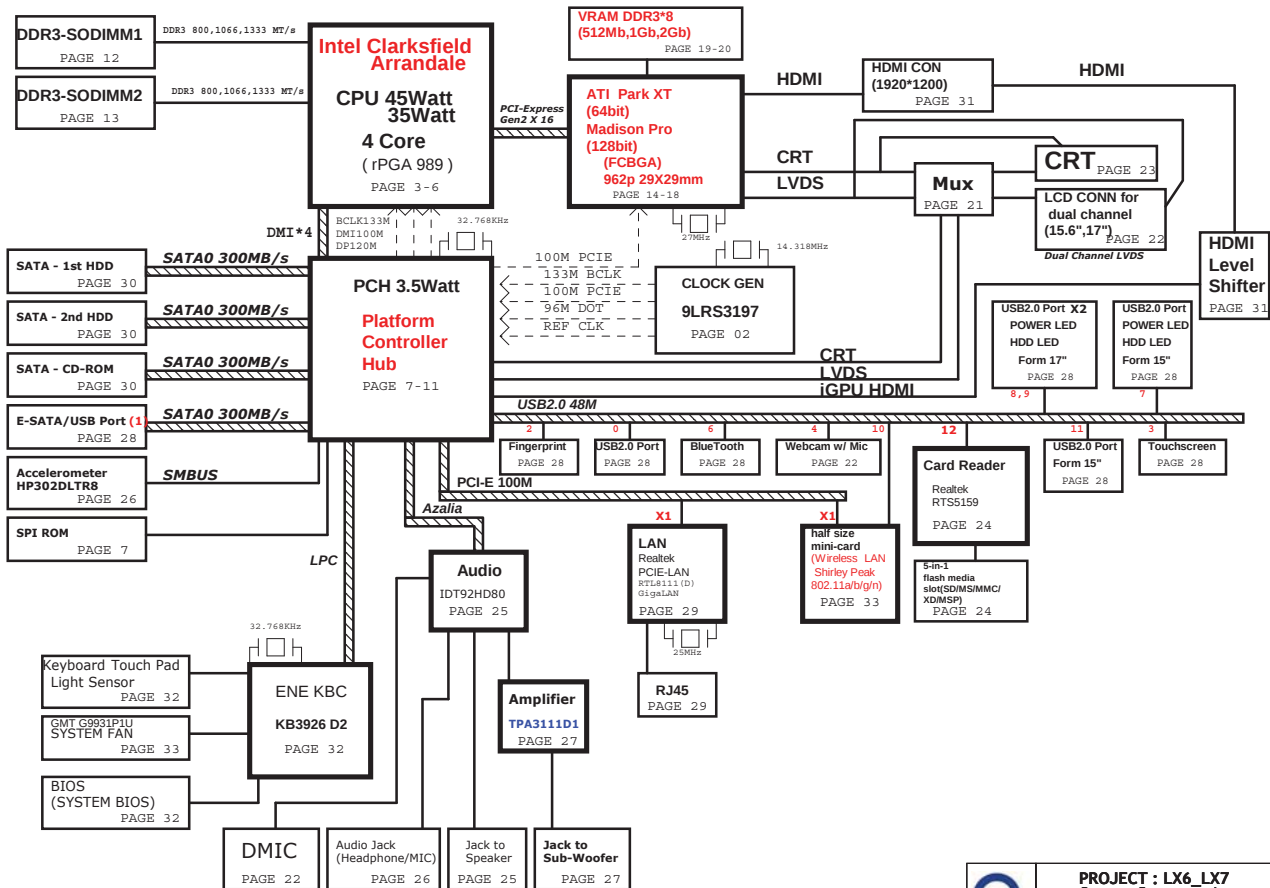


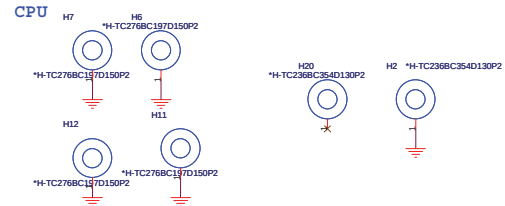
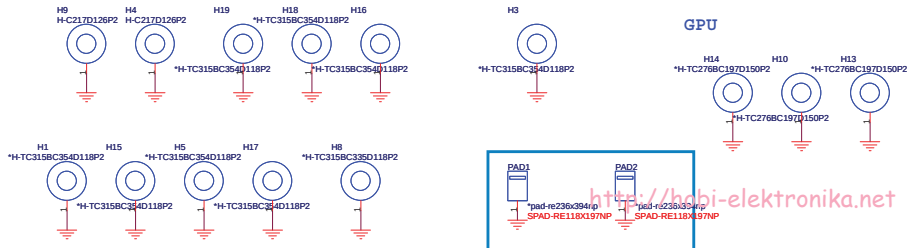
PCB STACK UP

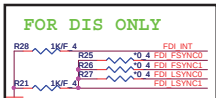
6L Dis.

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1(High)
LAYER 4 : IN2(Low)
LAYER 5 : SVCC
LAYER 6 : BOT

LX6/7 (Liverpool) BLOCK DIAGRAM

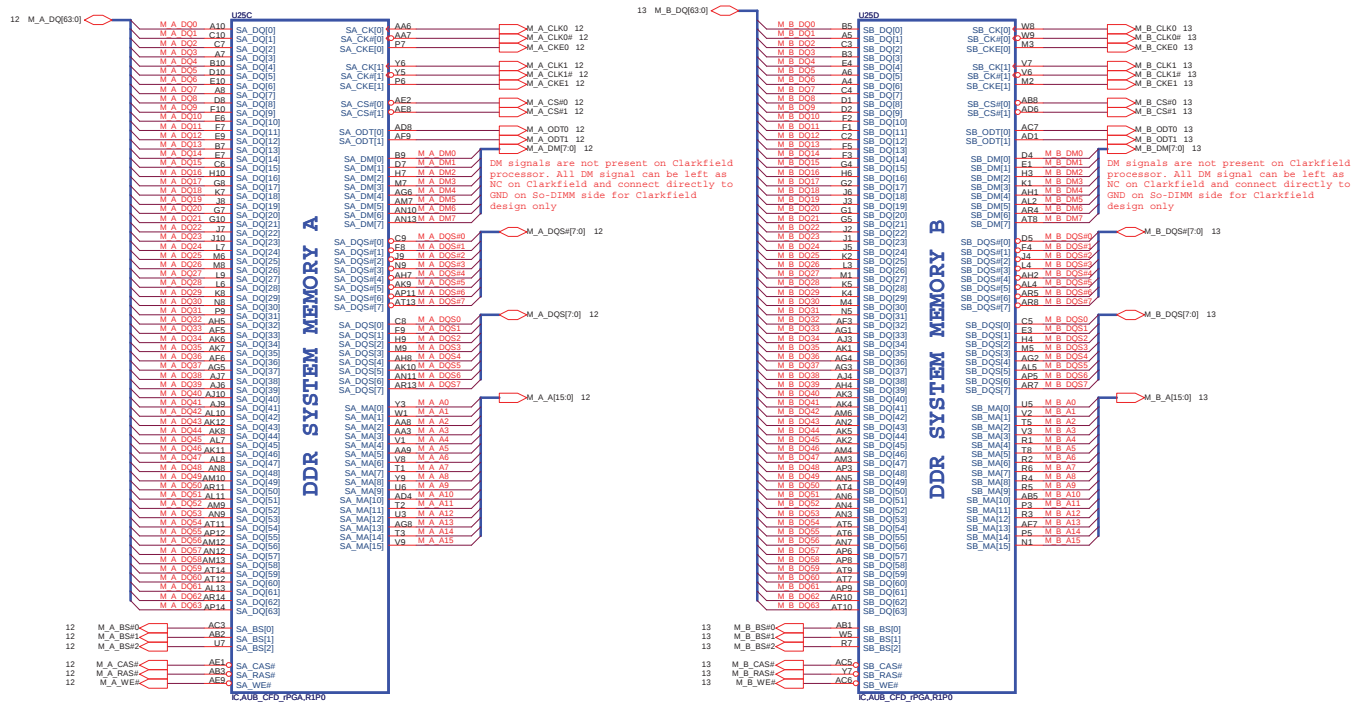






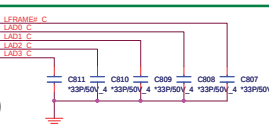
 NB5	PROJECT : LX6_LX7 Quanta Computer Inc.			
	Size Custom	Document Number PROCESSOR 1/4(HOST&PEX)		
Date: Tuesday, February 02, 2010		1 Sheet 3 of 44		

AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)

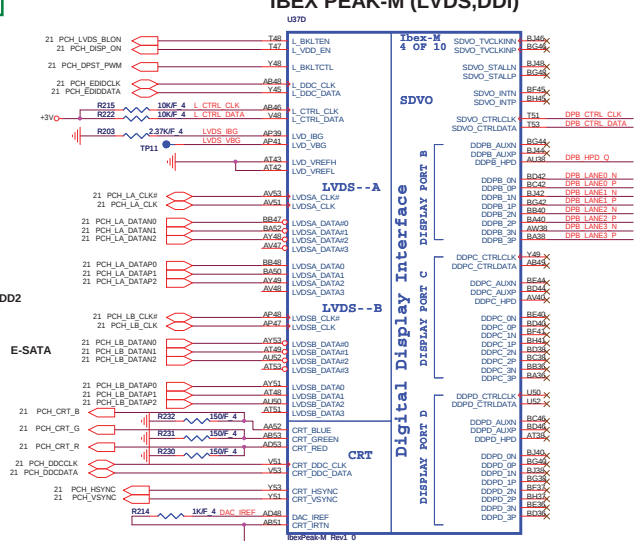




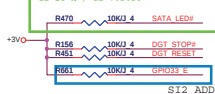
IBEX PEAK-M (HDA,JTAG,SATA)



IBEX PEAK-M (LVDS,DDI)



1205 The SATALED# signal is an active-low, open-collector and requires a 10 k Ω weak external pull-up to +V3.3.

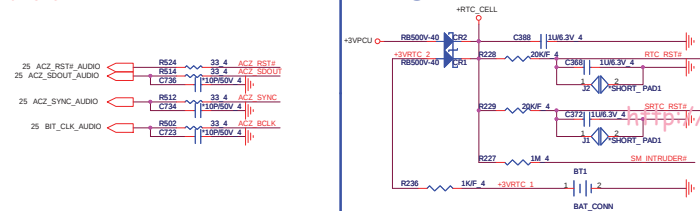


DPB_CTRL_C	R545	*10 4	SDVO_CLK	31
DPB_CTRL_DATA	R546	*10 4	SDVO_DATA	31
DPB_LANE0_N	C369	*1010V 4	IN_D0?	31
DPB_LANE0_P	C374	*1010V 4	IN_D2?	31
DPB_LANE1_N	C362	*1010V 4	IN_D3?	31
DPB_LANE1_P	C357	*1010V 4	IN_D1?	31
DPB_LANE2_N	C370	*1010V 4	IN_D0?	31
DPB_LANE2_P	C373	*1010V 4	IN_D0	31
DPB_LANE3_N	C355	*1010V 4	IN_CLK#	31
DPB_LANE3_P	C346	*1010V 4	IN_CLK	31



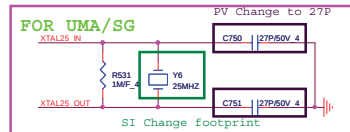
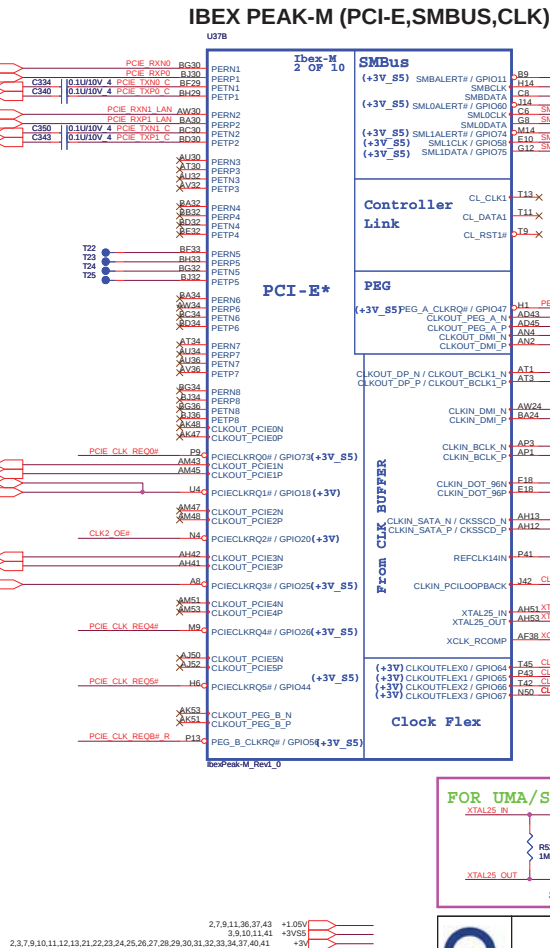
2,3,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,34,37,40,41
22,28,32,33,34,35,36,39,41,42,43
11 +RTC CELL

RTG

[illegible]

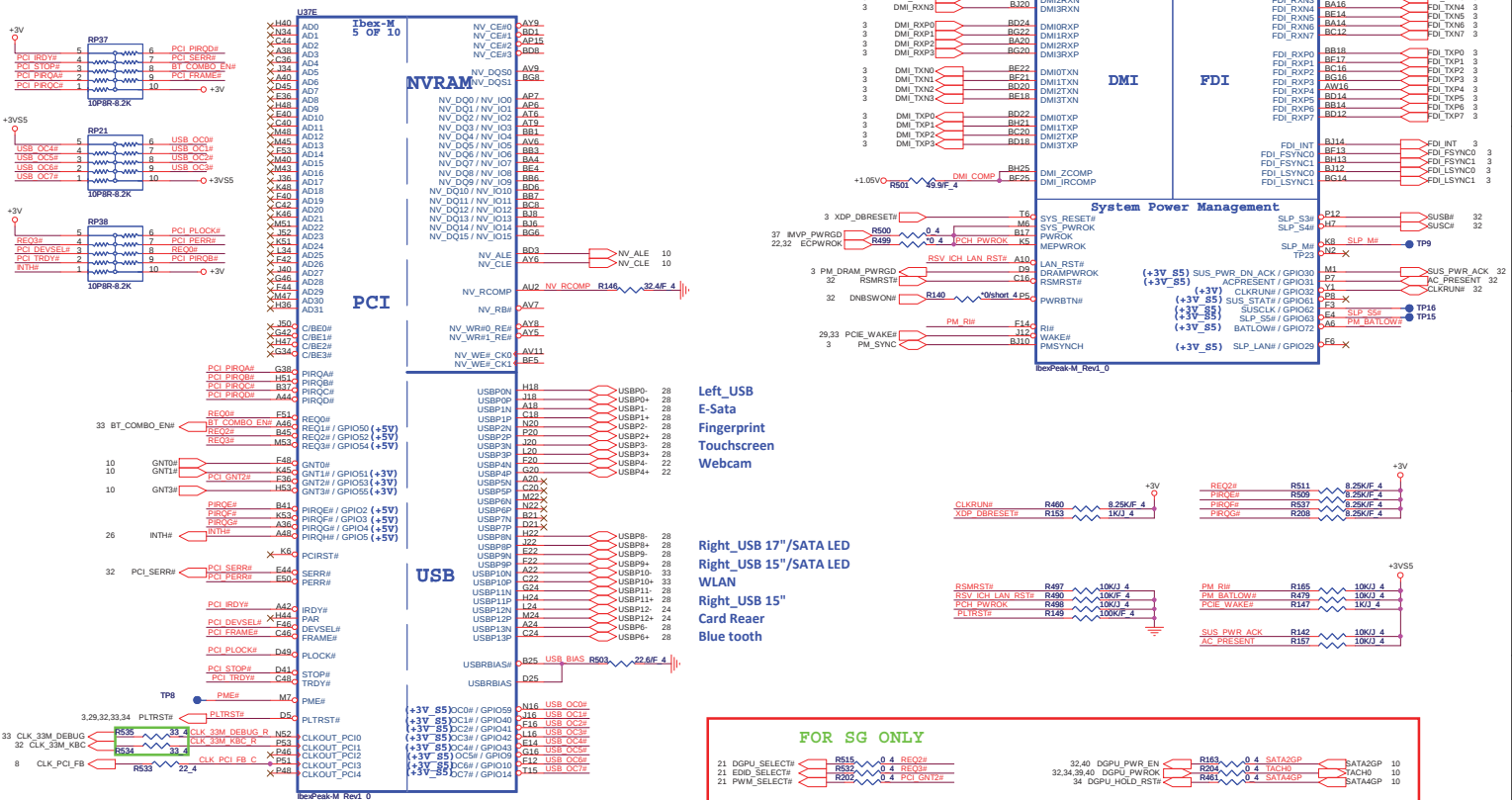
PROJECT : LX6_LX7
Quanta Computer Inc.

Size Custom	Document Number PCH 1/5 (SATA,HDA,LPC)	Rev 1A
Date: Tuesday, February 02, 2010 1 Sheet 7 of 11		

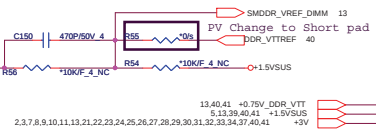
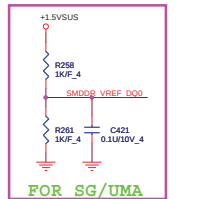
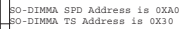


IBEX PEAK-M (DMI,FDI,GPIO)

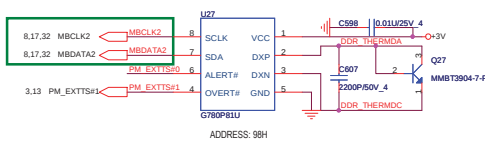
IBEX PEAK-M (PCI,USB,NVRAM)



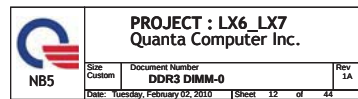


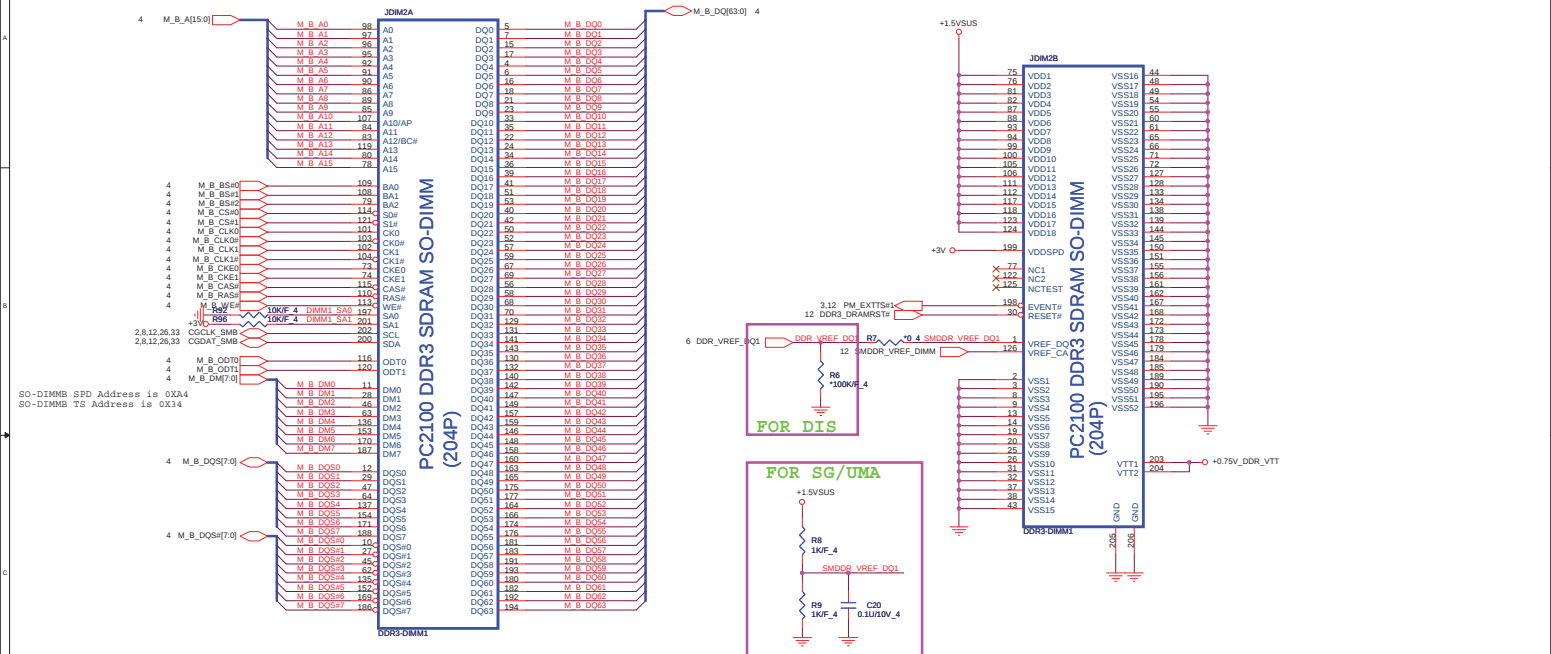


Some Projects replace 10UF 0805 by 4.7UF 0603
It can cost down 30%



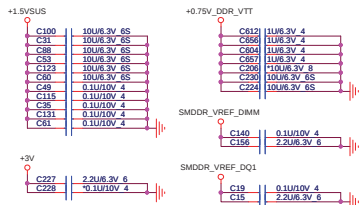
<http://hobi-elektronika.net>





Place these Caps near So-Dimm1.

Some Projects replace 10UF 0805 by 4.7UF 0603
It can cost down 30%



12.40.1 +0.75V_VTT
5.12.39.40.41 +1.5V_VSUS
2.3.7.8.9.10.11.12.21.22.23.24.25.26.27.28.29.30.31.32.33.34.37.40.41 +3V

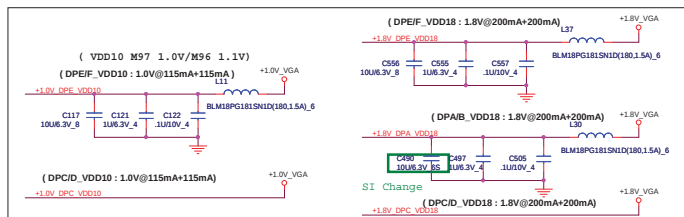
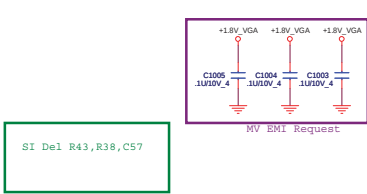


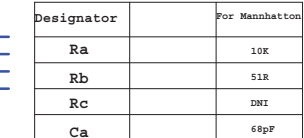
Size
Custom

Document Number
DDR3 DIMM-1

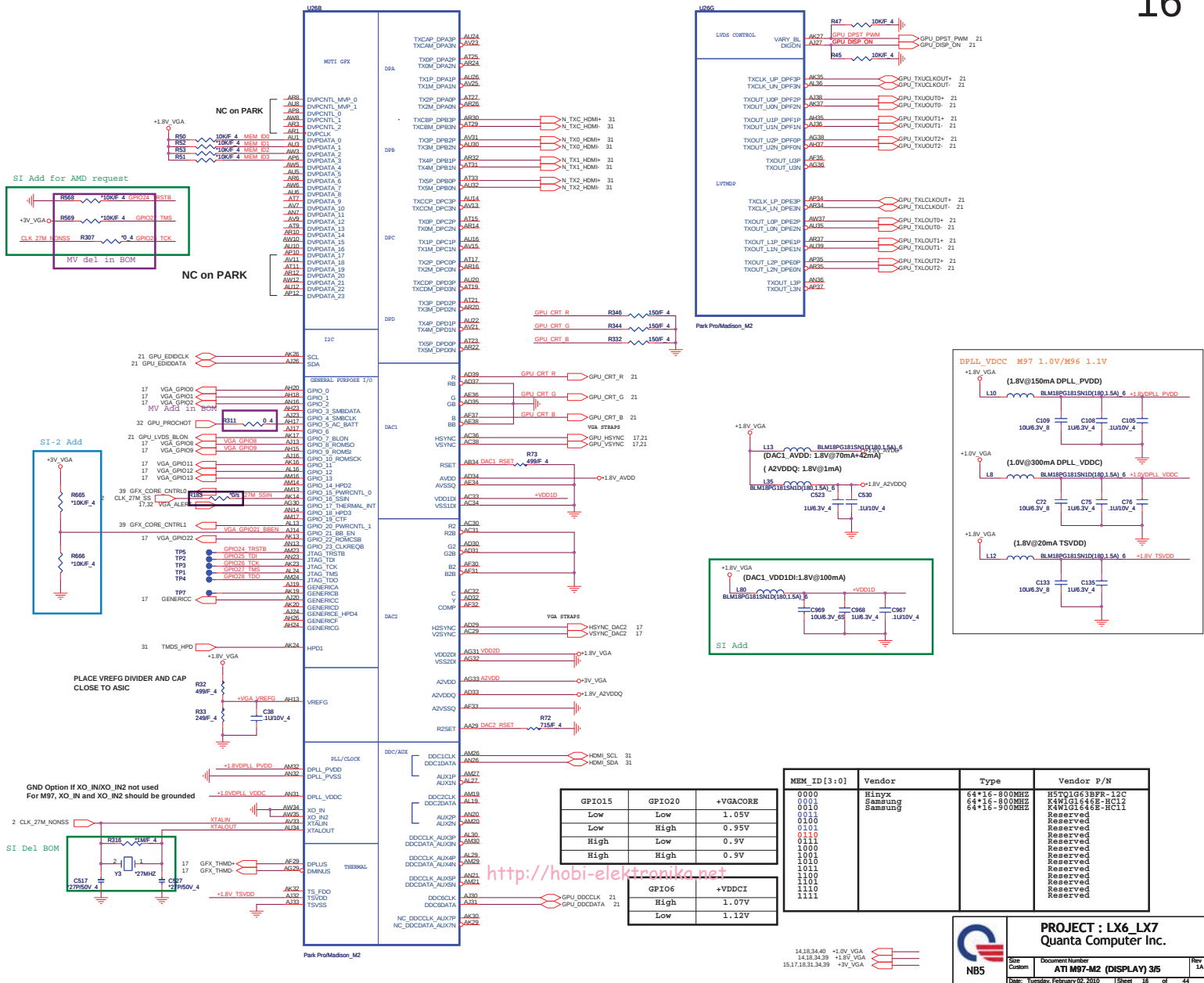
Project : LX6_LX7
Quanta Computer Inc.

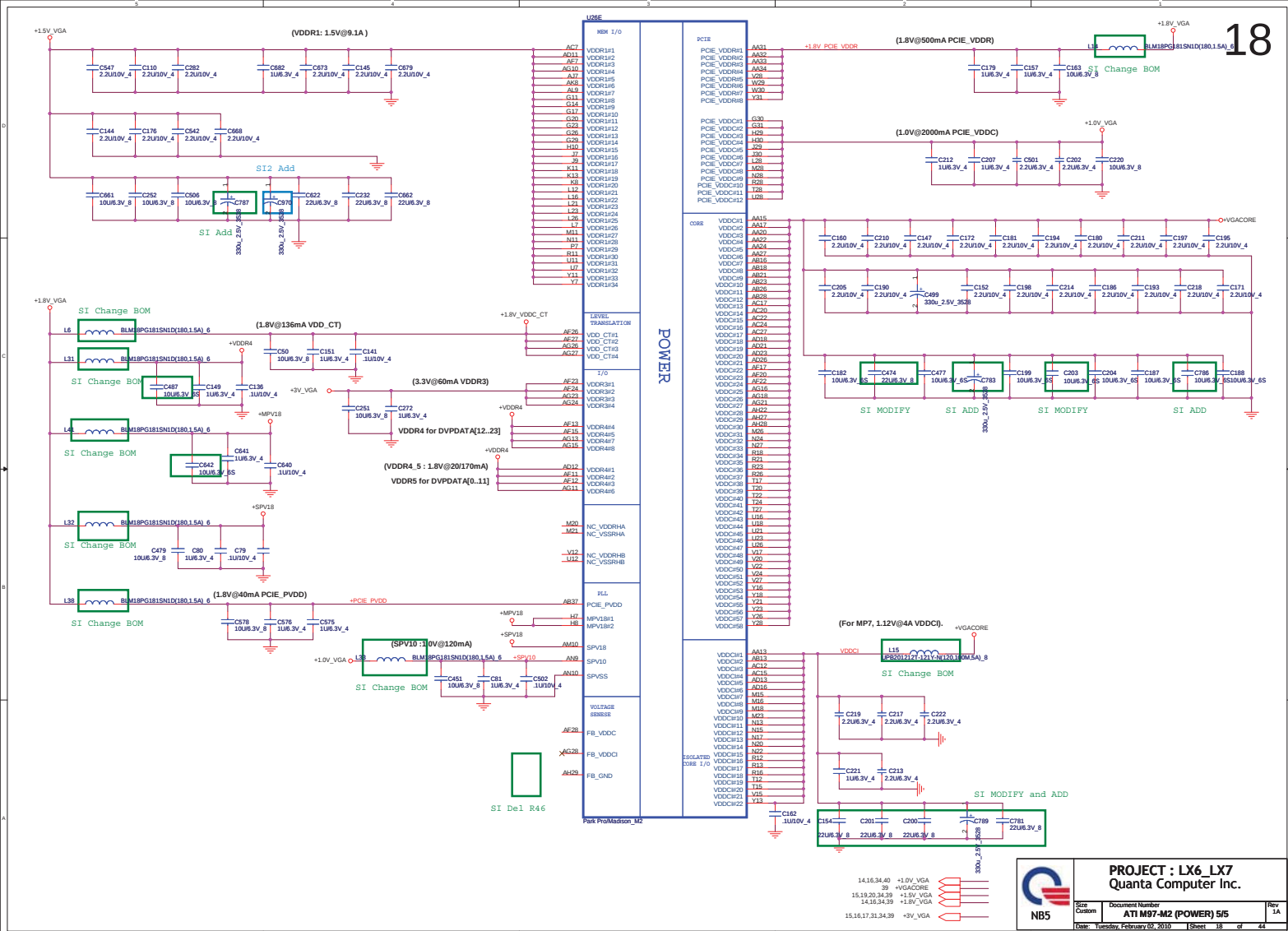
Date: Tuesday, February 02, 2010 Sheet 13 of 44





	GDDR5	GDDR3	DDR3
+1.5V_VGA	1.5V	1.8V/1.5V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R

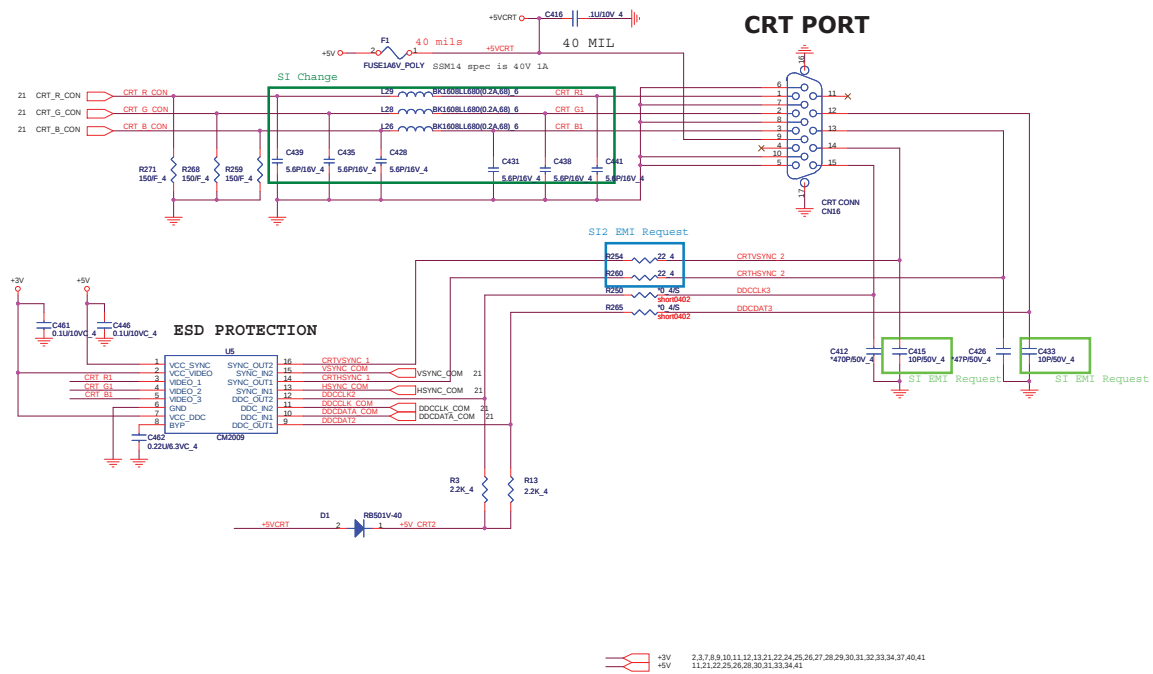




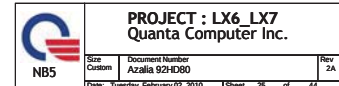








			PROJECT : LX6_LX7 Quanta Computer Inc.	
Set Consist	Document Number CRT/HDMI Conn	Rev 1A		
Date: Tuesday, February 03, 2010		Sheet 23	of 44	

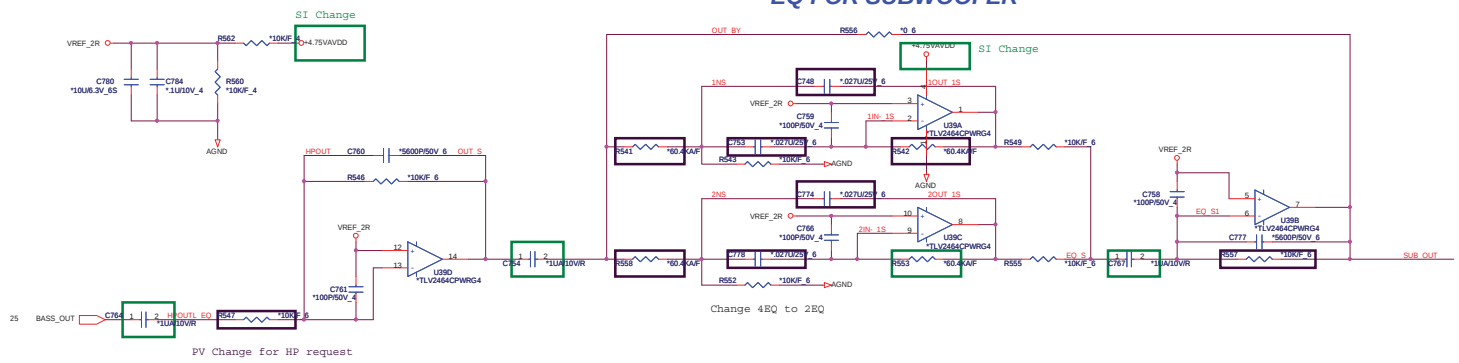




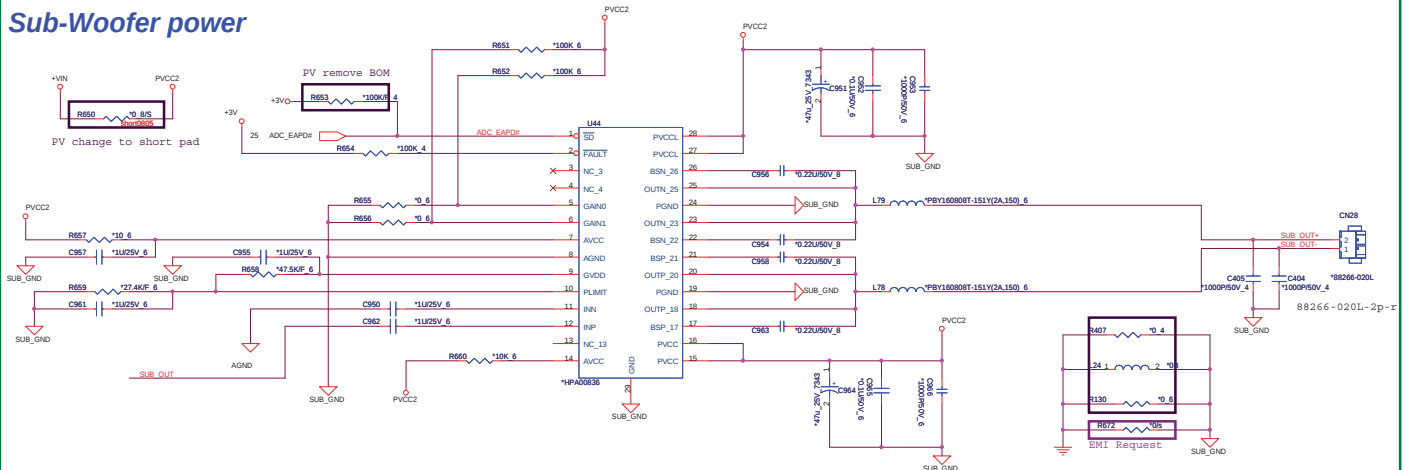
SGT-LIS302DLTR interrupt pin default is low / active Hi , BIOS need to programming 22h to change status from active Hi to low



<http://hobi-elektronika.net>



Sub-Woofers power



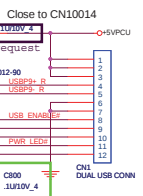
GAIN1	GAIN0	dB
0	0	12
0	1	18
1	0	23.6
1	1	36

PV Del R574, R241, R240

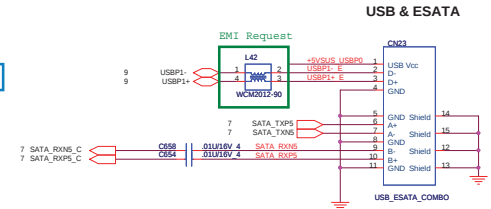
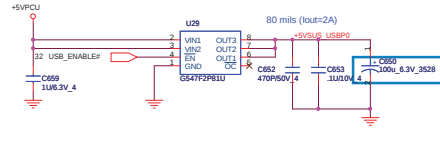
3V 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,28,29,30,31,32,33,34,37,40,41
 4.75VAVDD 25 22,35,36,37,39,40,41,42,43
 VIN

PROJECT : LX6_LX7
Quanta Computer Inc.

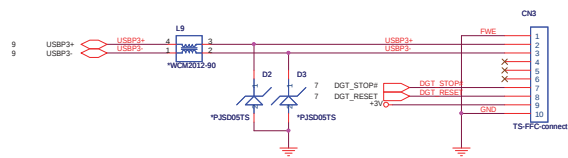
Rev	1A
Doc	Subwoofer (EQ & AMP)
Date	Tuesday, February 03, 2010
Sheet	27 of 44



E-SATA



Touch screen for 15"



PROJECT : LX6_LX7
Quanta Computer Inc.

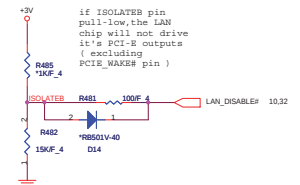
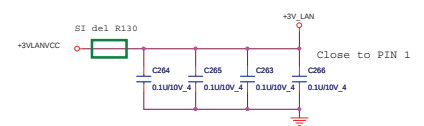
Size Custom	Document Number BTWC/FT/Touchscreen	Rev 1A
Date: Tuesday, February 02, 2010		Sheet 28 of 44

T : Stuffed for RTL8111DL(10/100/1000)

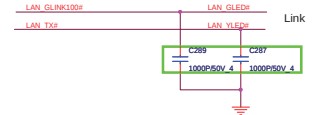
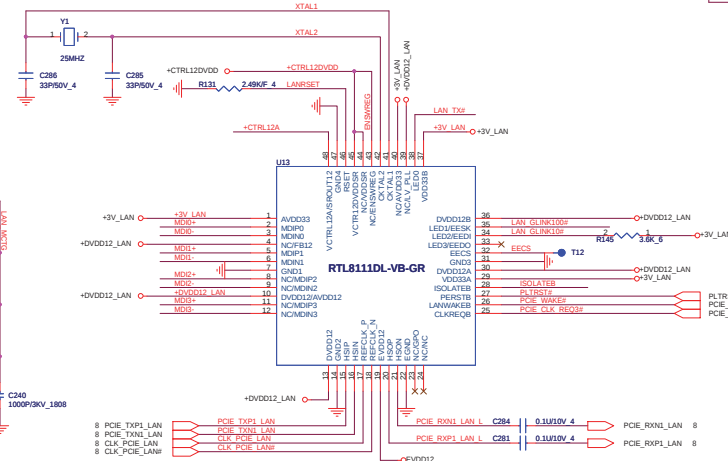
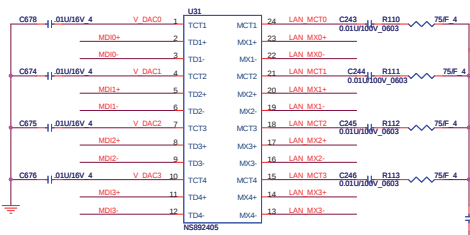
for RTL8111DL use: close Pin 44, 45

Remove R3571, R3573

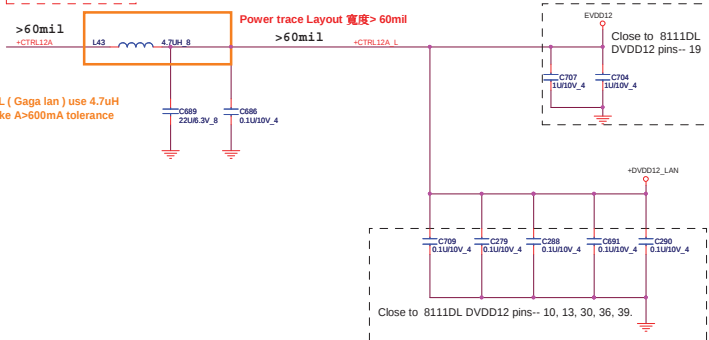
R3571 and R3573 are used in RTL8111DL, remove R3573 if switching regulator is enable, Remove R3571 external power is used.



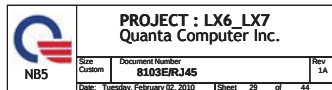
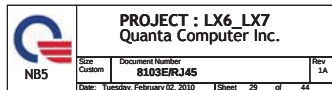
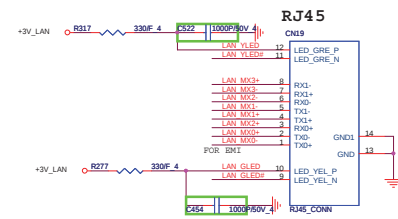
AL08111DB00 RTL8111DL-GR

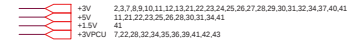


NS892402:GIGABIT

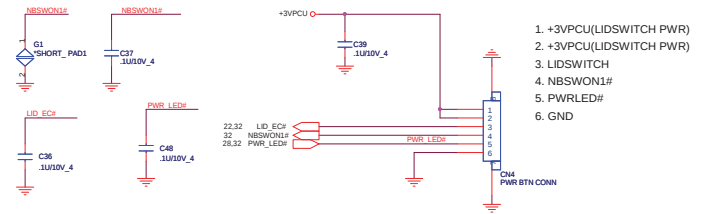


L23
RTL8111DL (Giga lan) use 4.7uH
power choke A>600mA tolerance
±15%



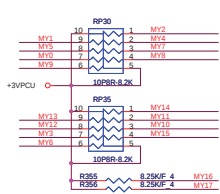


POWER BOTTON CONNECT

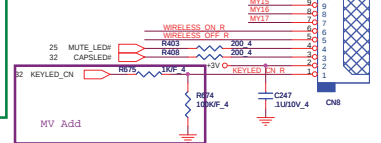
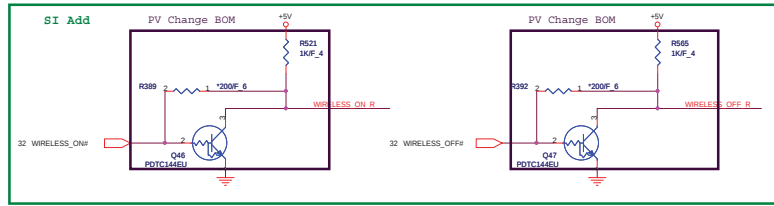


- | | | | |
|--|--|--|------------------|
| 
NB5 | PROJECT : LX6_LX7
Quanta Computer Inc. | | |
| | Size
Custom | Document Number
MINI PCIE CONN/FAN | Rev
1A |
| | Date: Tuesday, February 02, 2010 Sheet 33 of 44 | | |

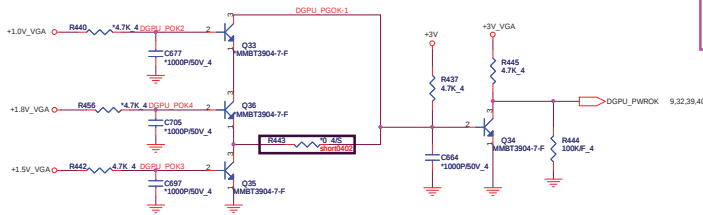
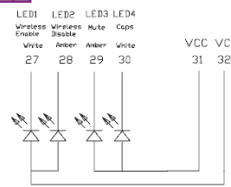
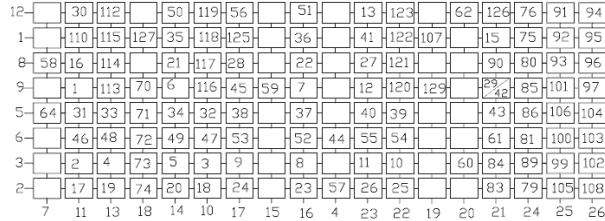
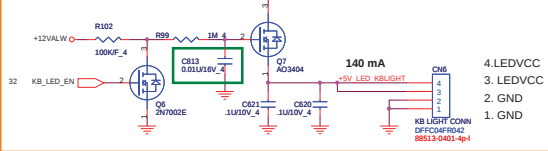
KEYBOARD PULL-UP



clear ABS 758 resin for key cap.
7 LEDs for 15.4" (total LED current 140mA)
11 LEDs for 17" (Total LED current 220mA)



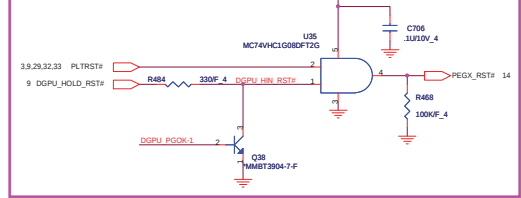
KB backlight for 15"



FOR DIS ONLY

PLTRST# R551 10.4 PEXG_RST#

FOR SG ONLY



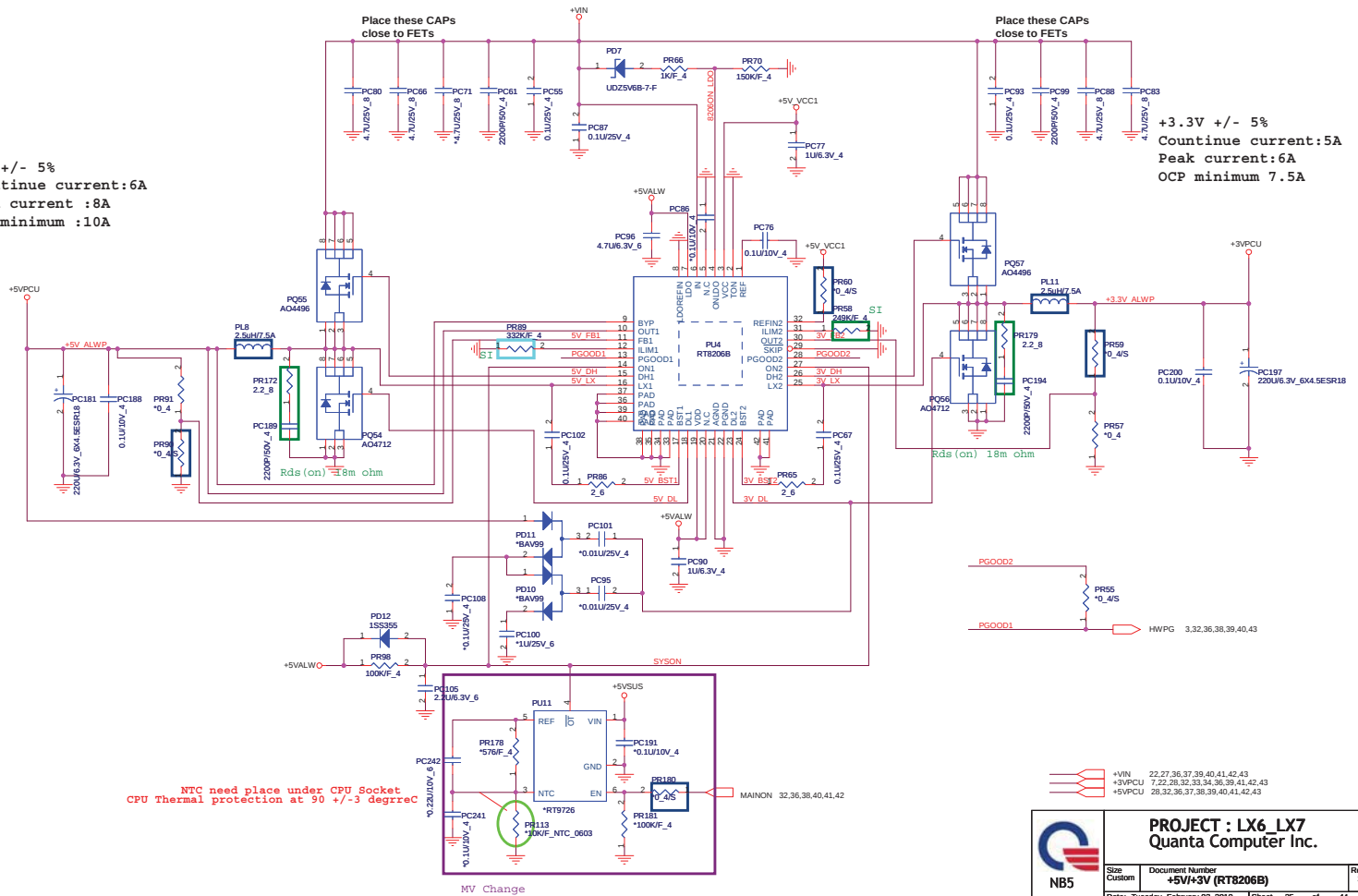
+3V 2.3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,37,40,41
+5V 11,21,22,23,25,26,28,30,31,33,41
+3VPCU 7,22,28,32,33,35,36,38,41,42,43
+5V_VGA 15,16,17,18,31,39
+12VVALW 22,30,39,41
+1.0V_VGA 14,16,19,40
+1.5V_VGA 15,18,19,20,39
+1.8V_VGA 14,16,18,39

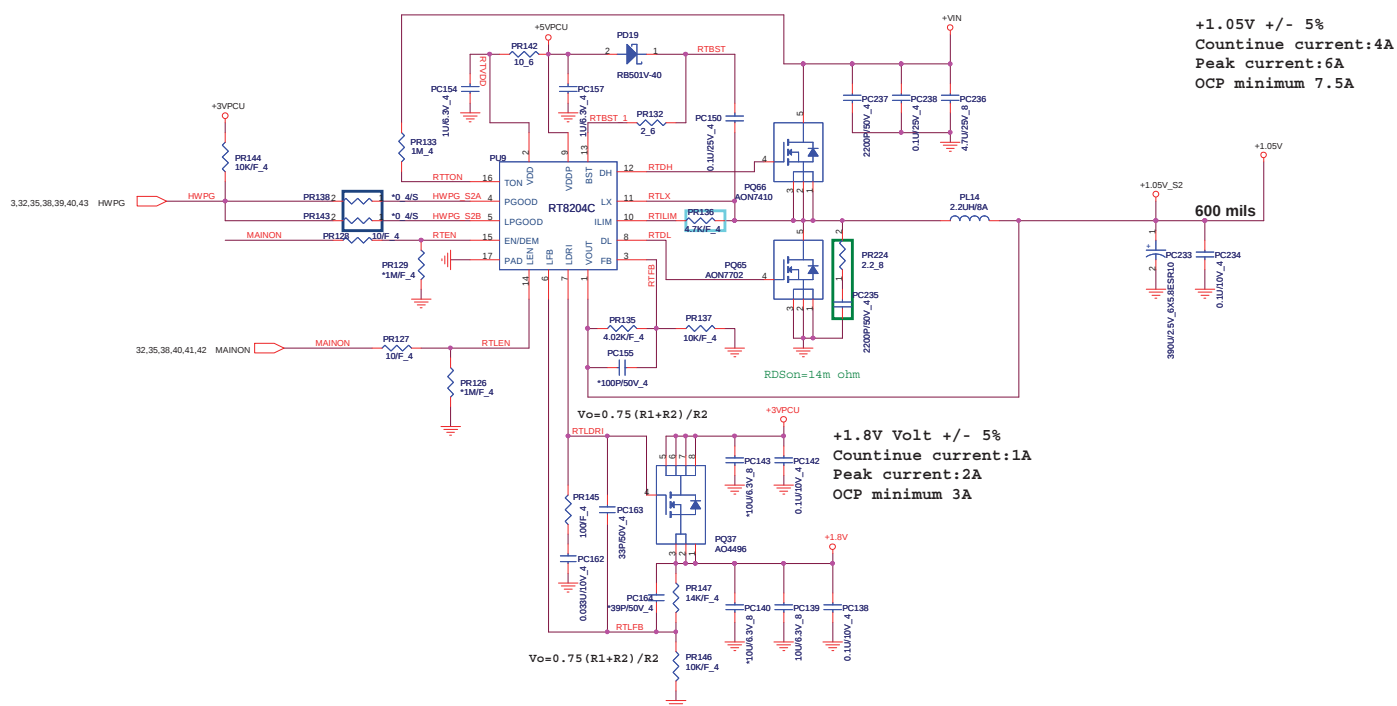
PROJECT : LX6_LX7
Quanta Computer Inc.

Size	Document Number	Rev
Cust	KB	1A
Draw/Format	February 02, 2015	Sheet 34 of 44

+5V +/- 5%
 Countinue current:6A
 Peak current :8A
 OCP minimum :10A

+3.3V +/- 5%
 Countinue current:5A
 Peak current:6A
 OCP minimum 7.5A





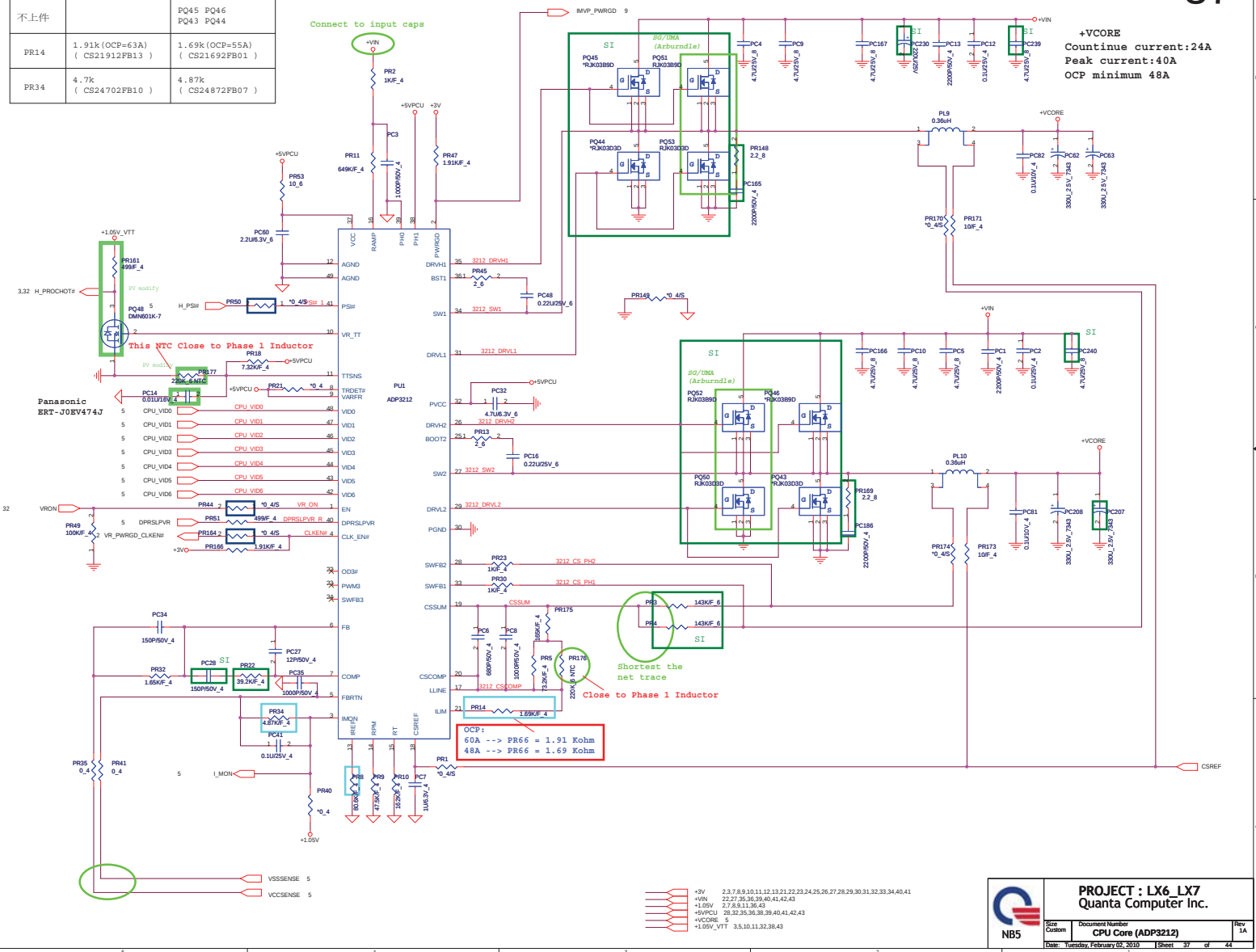
+1.8V	5,10,11,21,41
+VIN	22,27,35,37,39,40,41,42,43
+1.05V	2,7,8,9,11,37,43
+3VPCU	7,22,28,32,33,34,35,39,41,42,43
+5VPCU	28,32,35,37,38,39,40,41,42,43



PROJECT : LX6_LX7
Quanta Computer Inc.

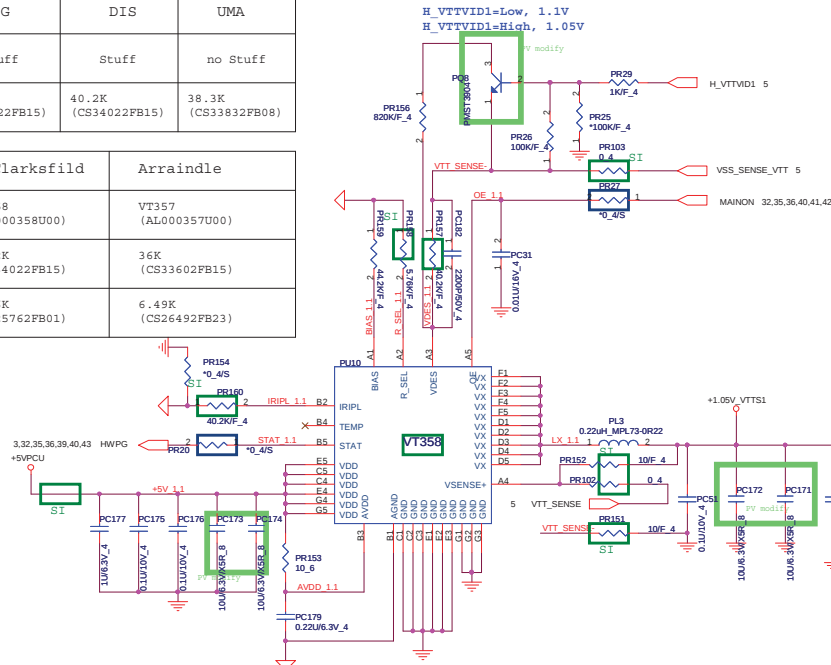
Size	Document Number	Rev
Custom	+1.05V/+1.8V (RT8204C)	1A
Date: Tuesday, February 02, 2010	Sheet 36 of 44	

	DIS (Clarksfield)	SG/UMA (Arbuckle)
不上件		PQ45 PQ46 PQ43 PQ44
PR14	1.91k (OCP=63A) (CS21912FB13)	1.69k (OCP=55A) (CS21692FB01)
PR34	4.7k (CS24702FB10)	4.87k (CS24872FB07)



	SG	DIS	UMA
PR156 PR26 PR29 PQ8	Stuff	Stuff	no Stuff
PR157	40.2K (CS34022FB15)	40.2K (CS34022FB15)	38.3K (CS33832FB08)

	Clarksfield	Arrairdle
PU10	VT358 (AL000358U00)	VT357 (AL000357U00)
PR160	40.2K (CS34022FB15)	36K (CS33602FB15)
PR158	5.76K (CS25762FB01)	6.49K (CS26492FB23)

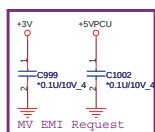
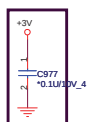
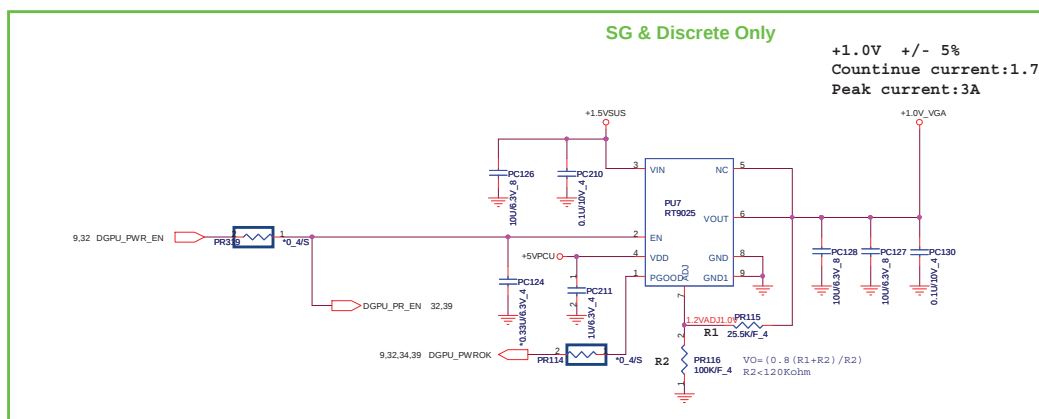
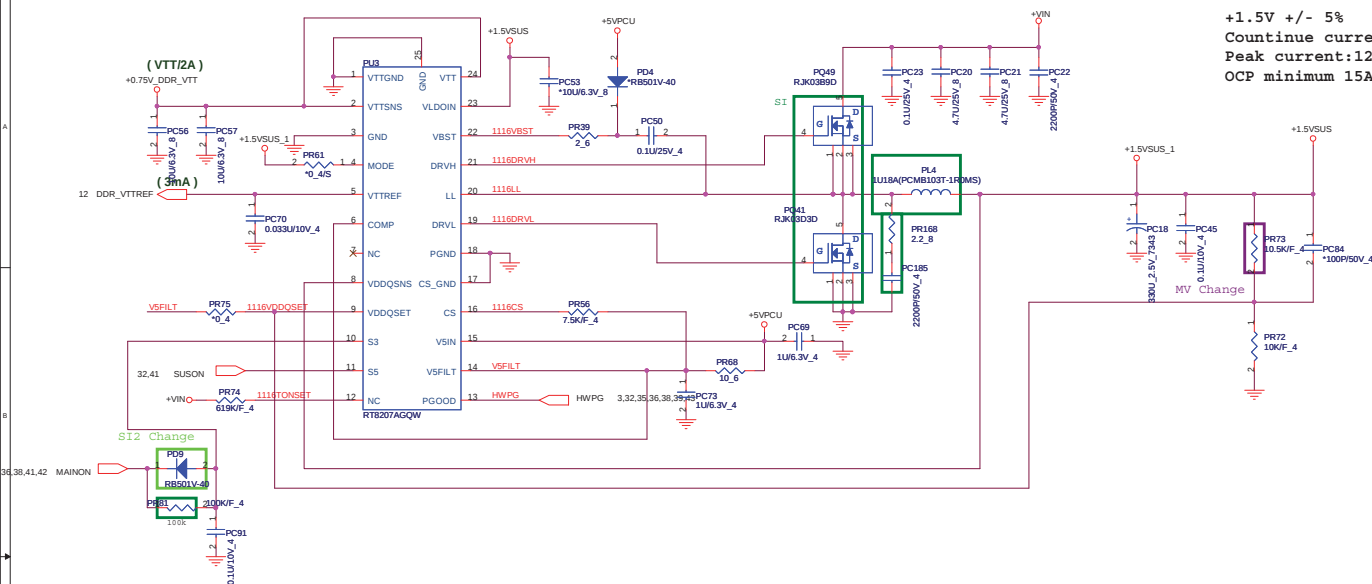


+1.05V_VTT +/- 5%
 Countinue current:12A
 Peak current:15A
 OCP minimum 18A



PROJECT : LX6_LX7
 Quanta Computer Inc.

Size	Document Number	Rev
Custom	+1.05V_VTT (VT358)	1A
Date: Tuesday, February 02, 2010	Sheet 38 of 44	



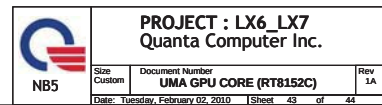
- | | |
|----------------|---|
| +3V | 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,34,37,41 |
| +VIN | 22,27,35,36,37,39,41,42,43 |
| +5VPCU | 28,32,35,36,37,38,39,41,42,43 |
| +1.5VSUS | 5,12,13,39,41 |
| +1.0V_VGA | 14,16,18,34 |
| +0.75V_DDR_VTT | 12,13,41 |

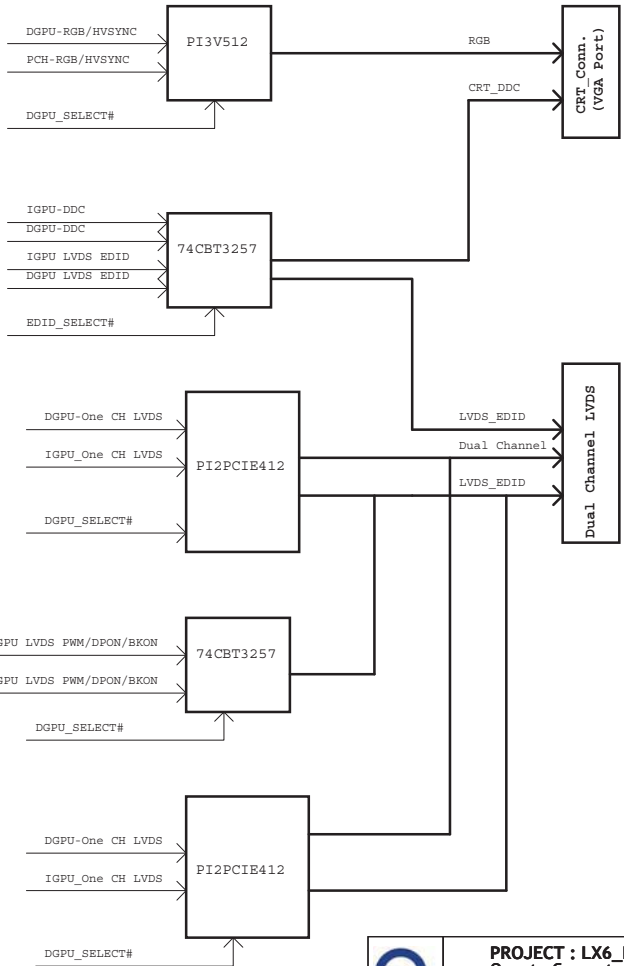
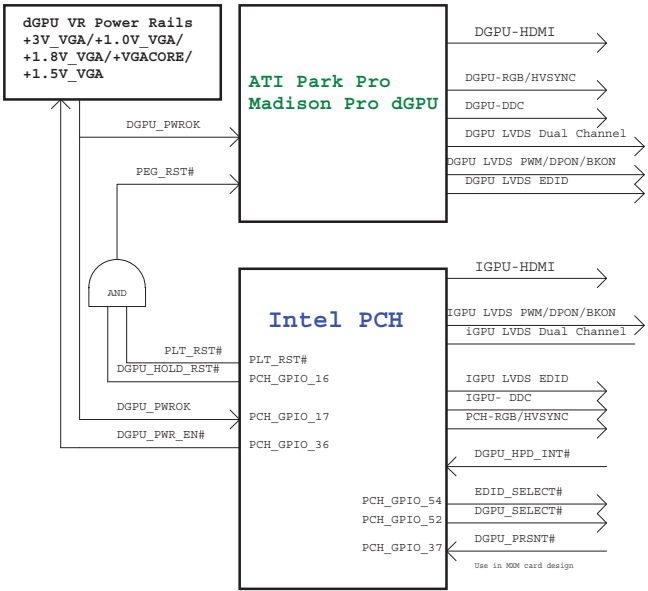


PROJECT : LX6_LX7
Quanta Computer Inc.

Size Custom	Document Number DDR3 (RT8207)	Rev 1A
Date: Tuesday, February 02, 2010		Sheet 40 of 44







Switchable GPIOs	Descriptions
PCH_GPIO52	DGPU_SELECT#
PCH_GPIO16	DGPU_HOLD_RST#
PCH_GPIO36	DGPU_PWR_EN#
PCH_GPIO17	DGPU_PWR_OK
PCH_GPIO54	EDID_ELECT#
PCH_GPIO37	DGPU_PRSENT#



PROJECT : LX6_LX7

Quanta Computer Inc.

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