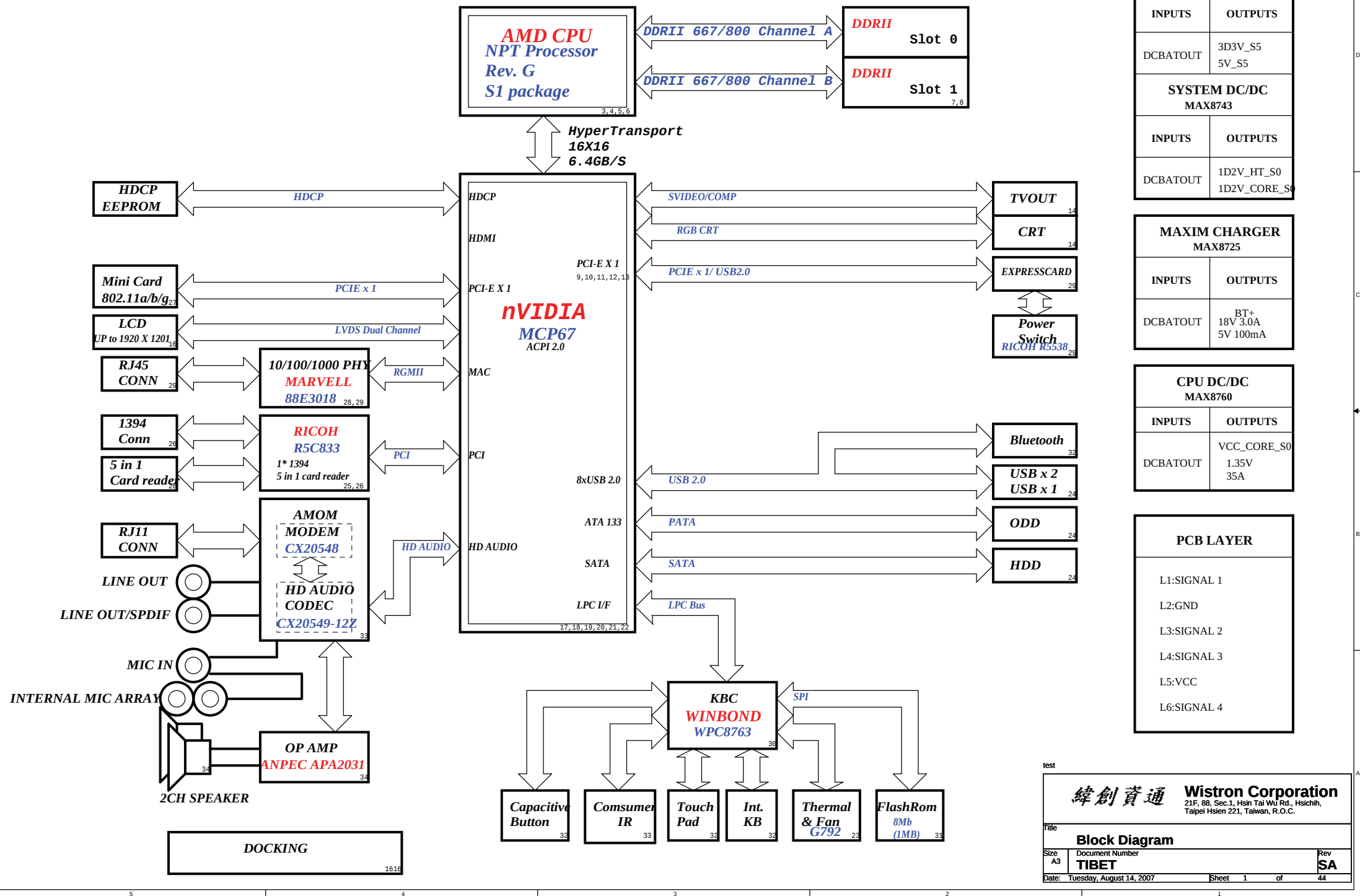
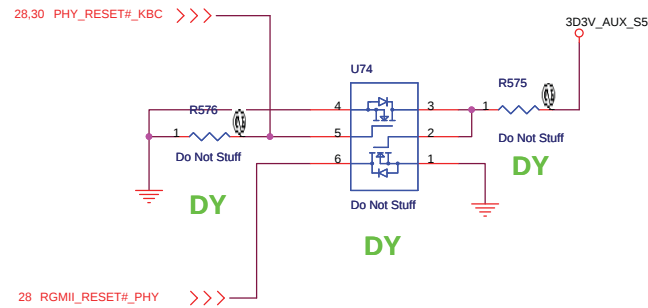
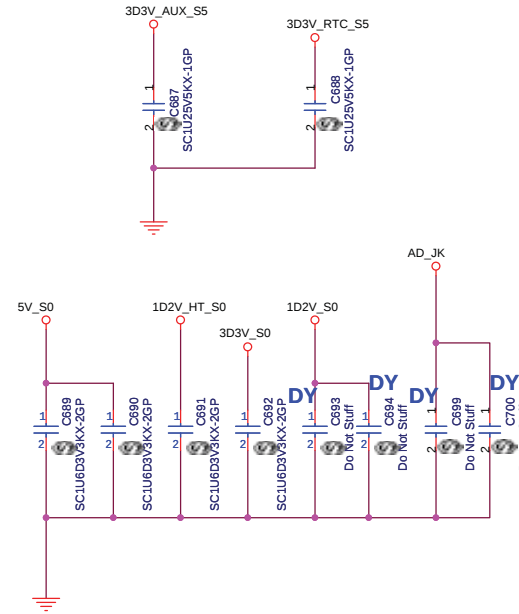
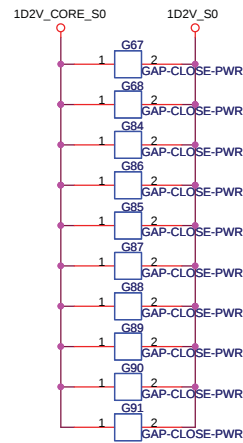
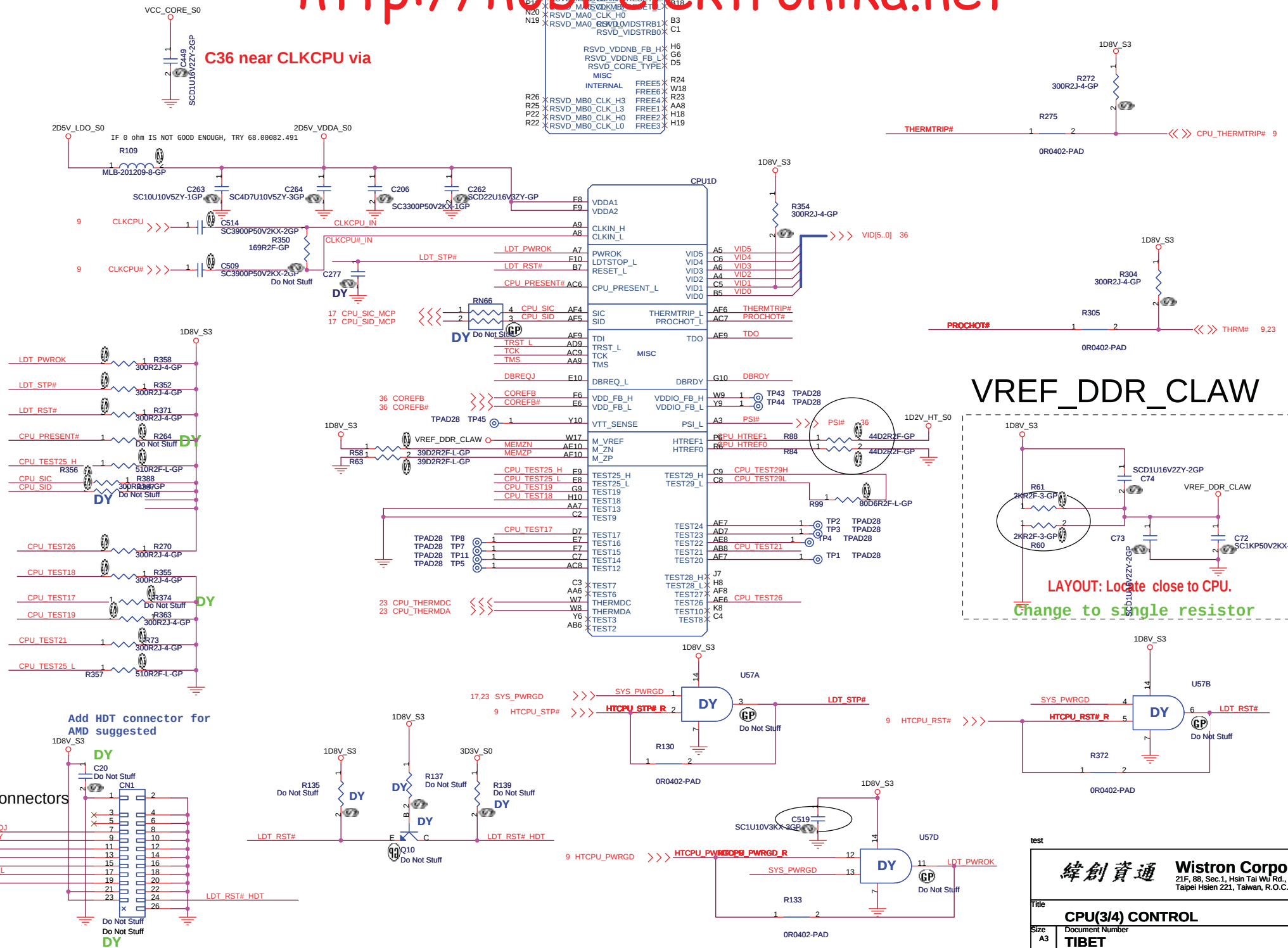
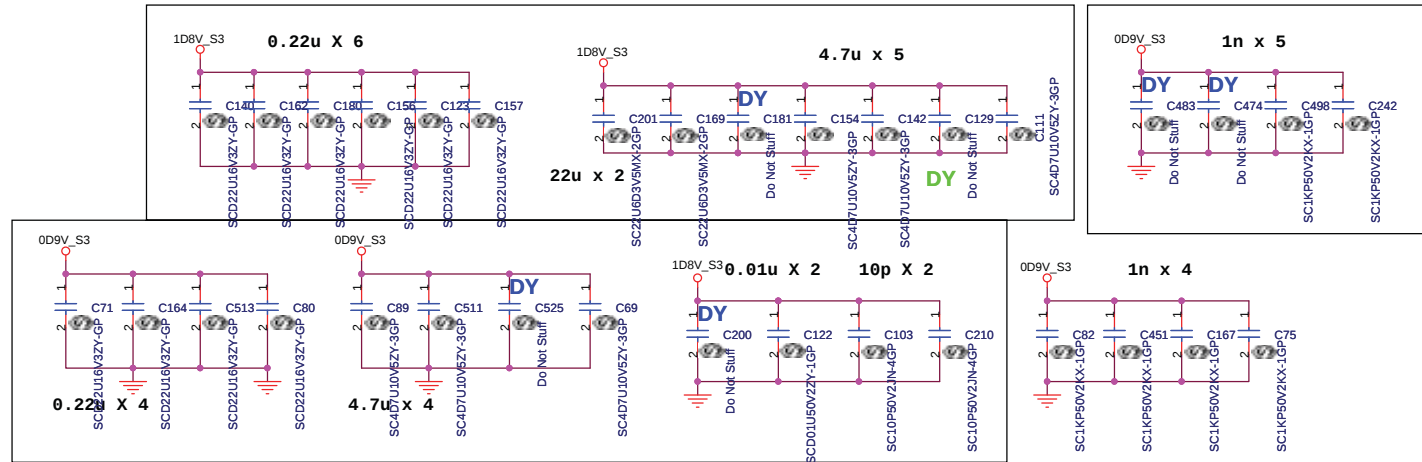
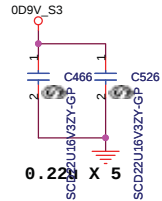
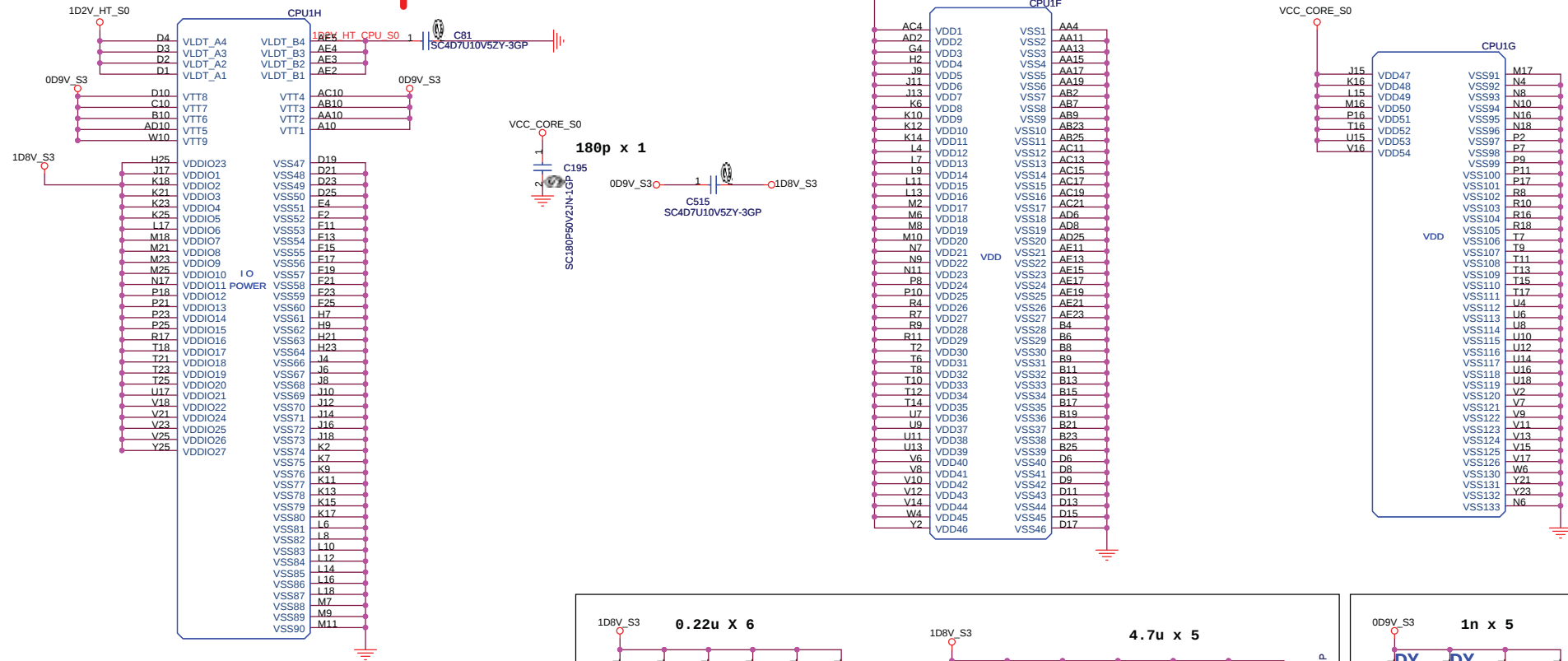




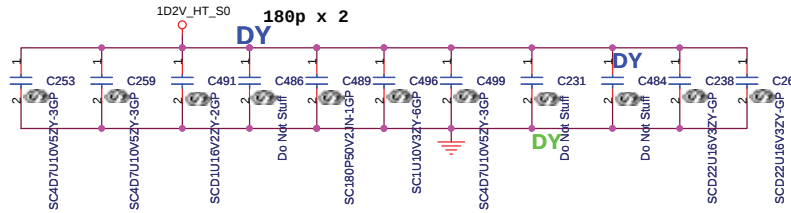
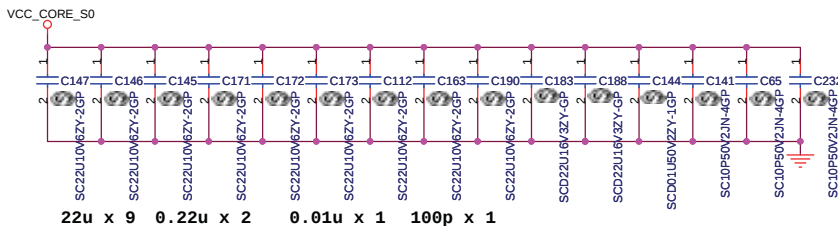


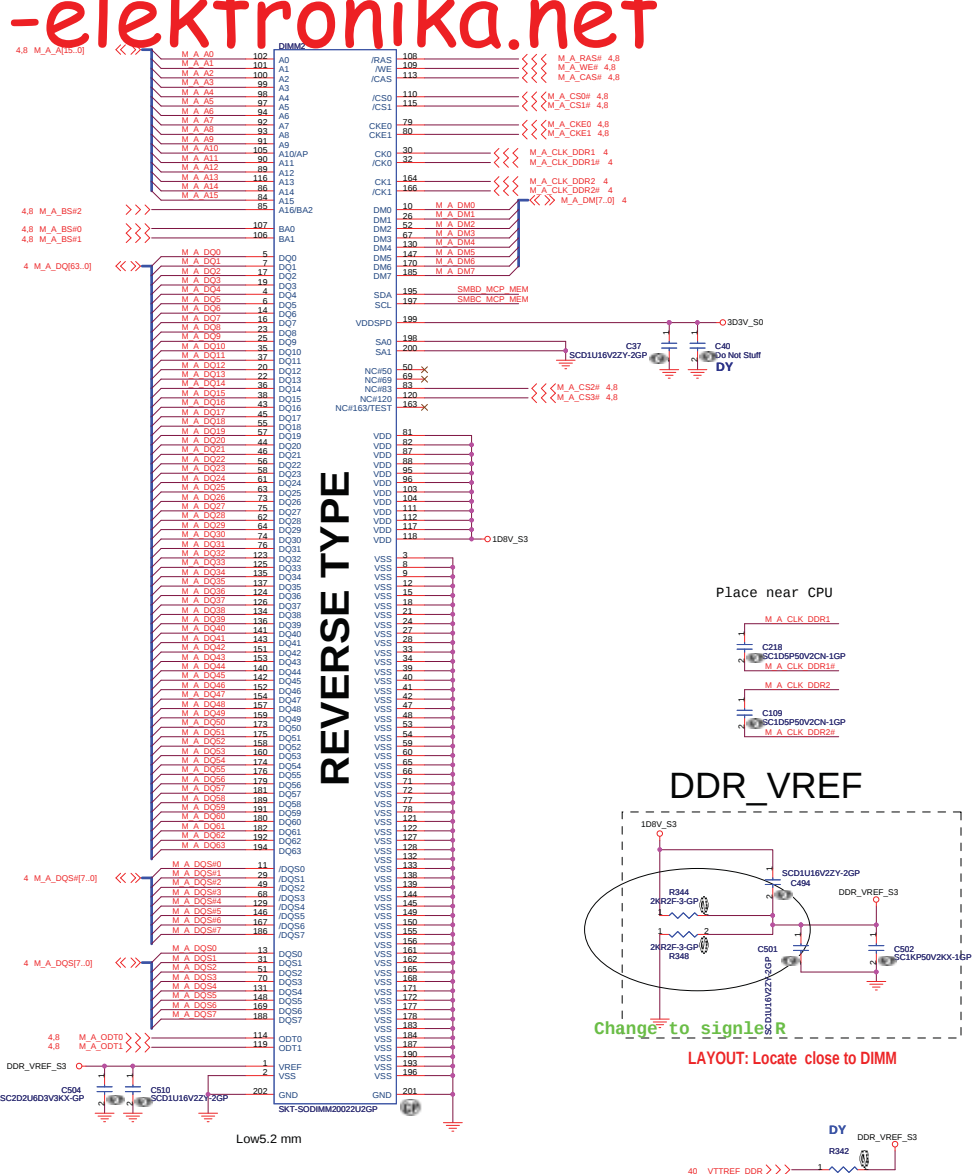
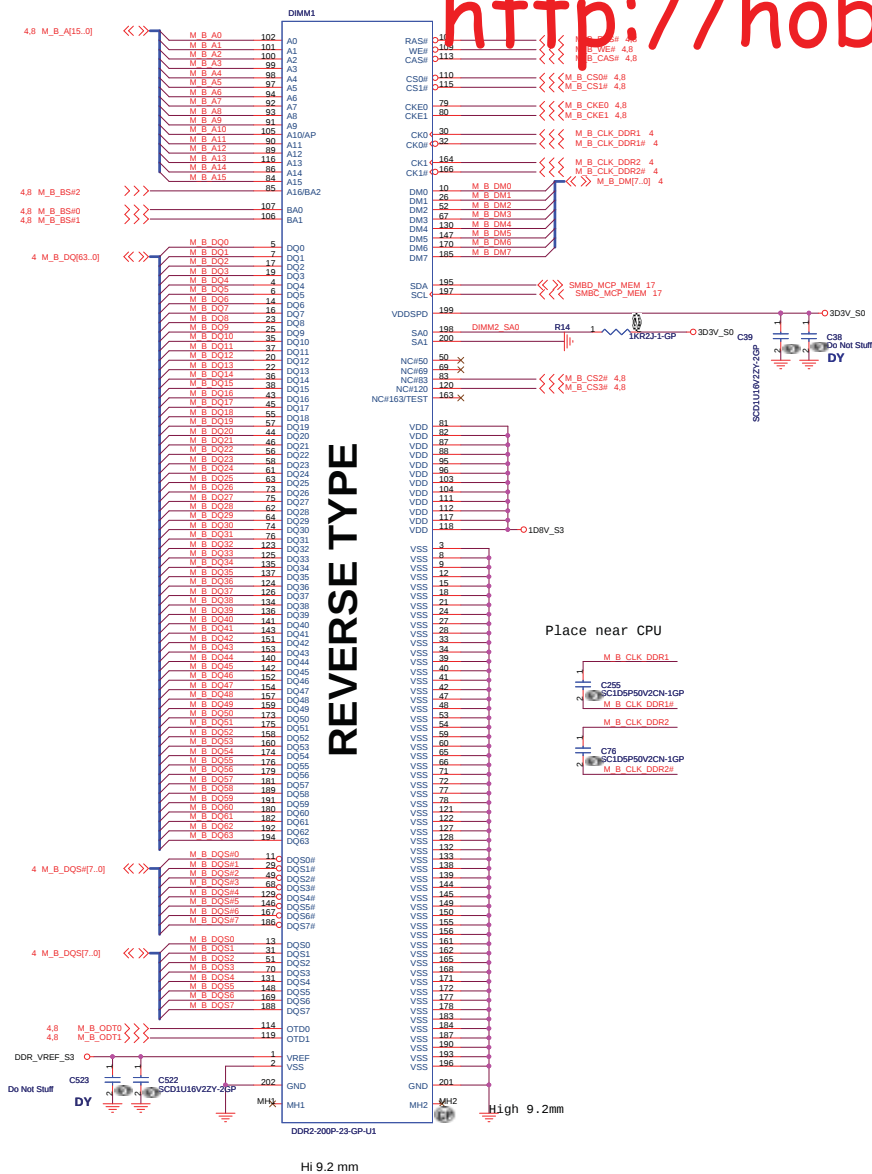
test





LAYOUT: Place on backside of processor.





<http://hobi-elektronika.net>

0.9V_S3

1 8 M A A11
2 7 M A A4
3 6 M A A6
4 5 M A A7

SRN47J-7-GP

1 8 M A A5
2 7 M A A3
3 6 M A A1
4 5

SRN47J-7-GP

1 8 M A A10
2 7 M A BS#0
3 6 M A WE#
4 5

SRN47J-7-GP

1 4 M A CAS#
2 3 M A CS1#

RN46
SRN47J-7-GP

1 4 M_B BS#2
2 3 M_B CEK0

SRN47J-7-GP

1 8 M_B CS1#
2 7 M_B CAS#
3 6 M_B WE#
4 5 M_B BS#0

SRN47J-7-GP

1 8 M_B A9
2 7 M_B A12
3 6 M_B A8
4 5

SRN47J-7-GP

1 8 M_B A10
2 7 M_B A1
3 6 M_B A3
4 5 M_B A5

SRN47J-7-GP

1 8 M_A CS0#
2 7 M_A ODT0
3 6 M_A A13
4 5 M_A CS3#

SRN47J-7-GP

1 8 M_A A2
2 7
3 6
4 5

SRN47J-7-GP

1 8 M_A A12
2 7 M_A A9
3 6 M_A A8
4 5

SRN47J-7-GP

1 8 M_B A6
2 7 M_B A4
3 6 M_B A0
4 5 M_B A2

SRN47J-7-GP

1 8 M_B BS#1
2 7 M_B RAS#
3 6 M_B CS0#
4 5 M_B A13

SRN47J-7-GP

1 8 M_B CEK1
2 7 M_B A15
3 6 M_B A7
4 5 M_B A11

SRN47J-7-GP

1 4 M_A CS2#
2 3 M_A BS#2

SRN47J-7-GP

1 4 M_A CEK0

R328 47R2J-2-GP

1 4 M_A A15
2 3 M_A CEK1

SRN47J-7-GP

1 4 M_B A14
2 3

R87 47R2J-2-GP

1 4 M_B ODT1
2 3

R68 47R2J-2-GP

1 4 M_B CS3#
2 3 M_B ODT0

SRN47J-7-GP

1 4 M_A ODT1
2 3 M_A A14

R274 47R2J-2-GP

1 4

R91 47R2J-2-GP

0.9V_S3

[illegible]



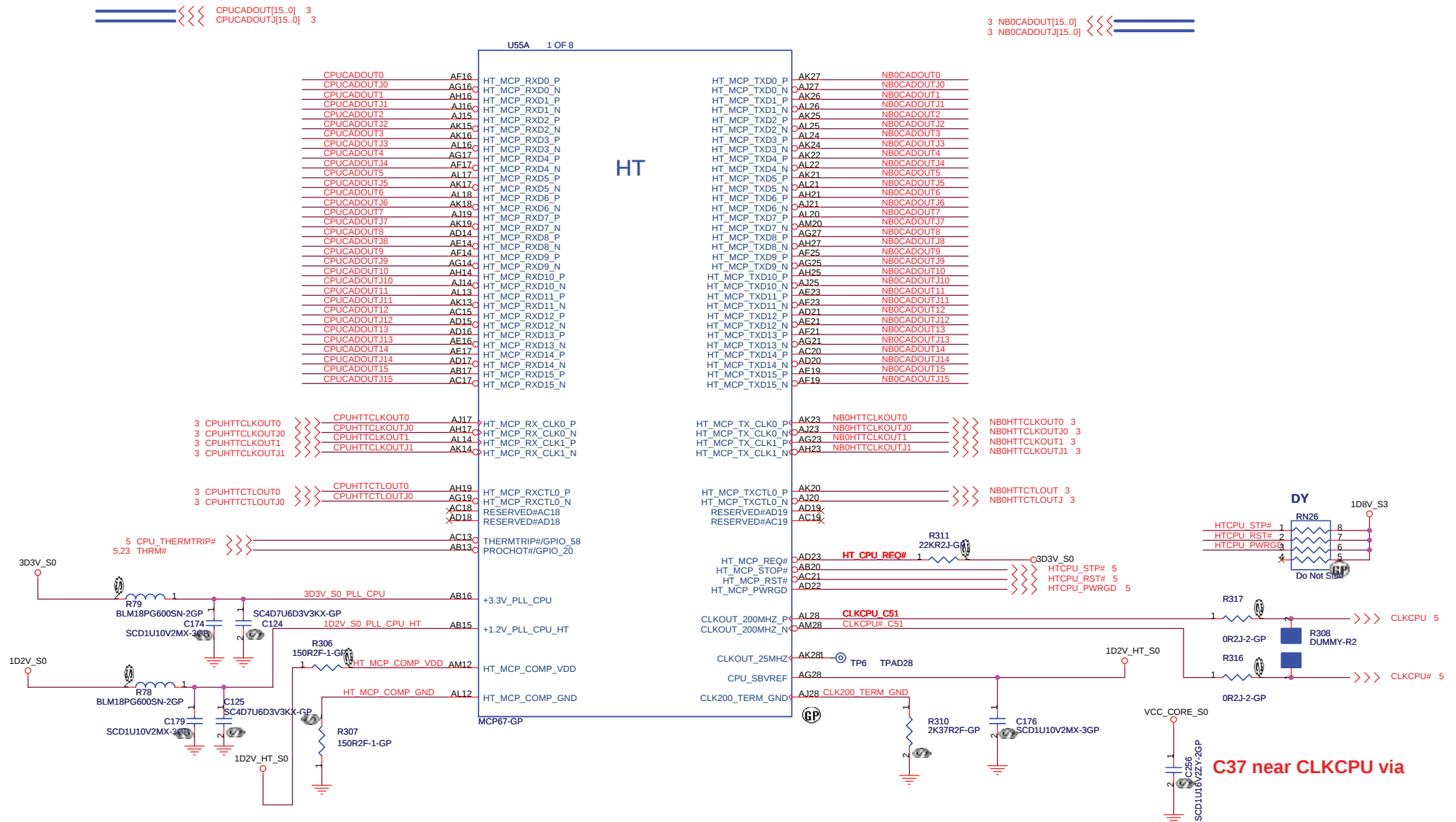
緯創資通

Wistron Corporation

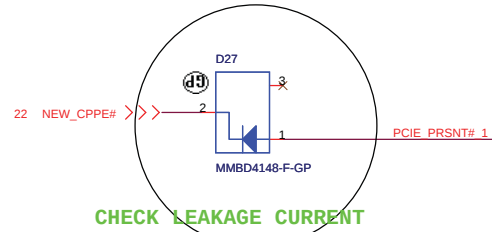
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

DDR2-RESISTOR

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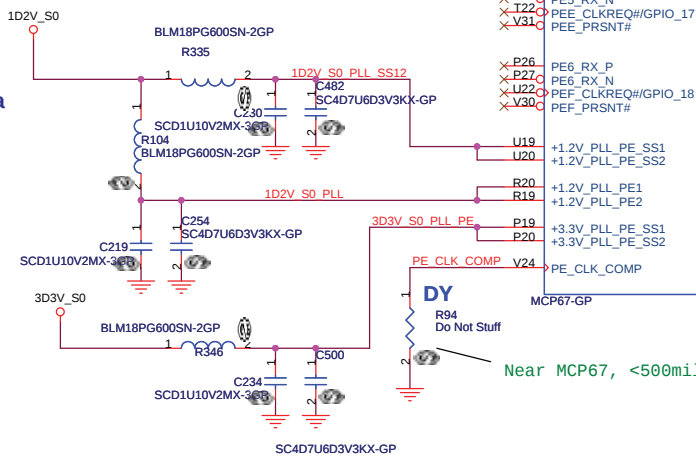
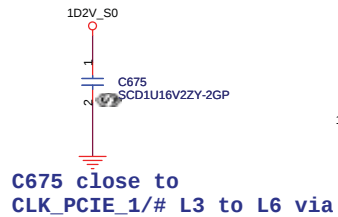
test



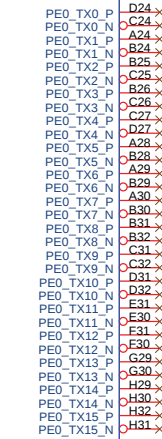
NEW CARD

MINI CARD1

MINI CARD2



PCIE



Near Connector, <500mil

For New_Card

test

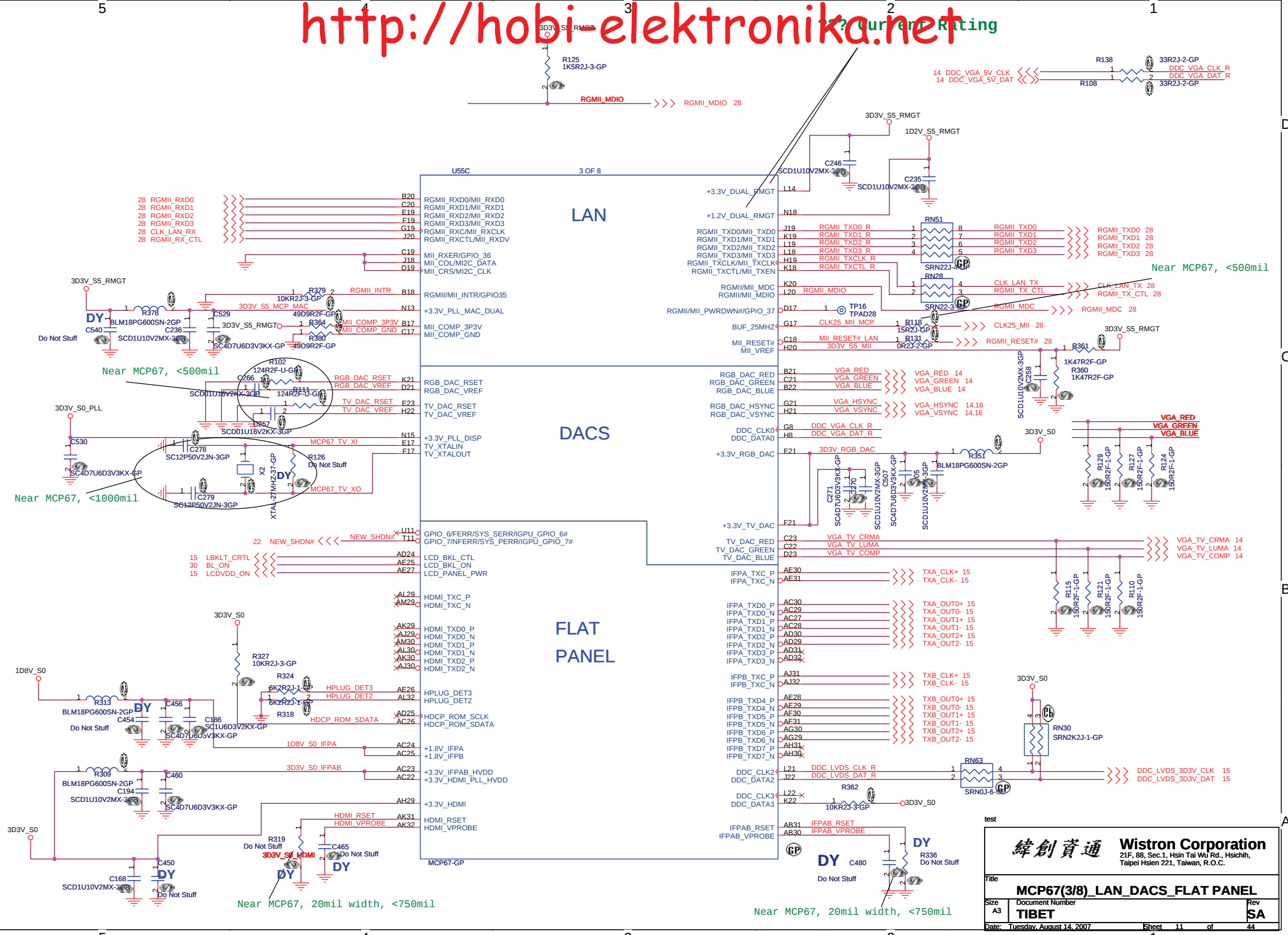
緯創資通 Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title MCP67(2/8)_PCIE	
Size A3	Document Number TIBET
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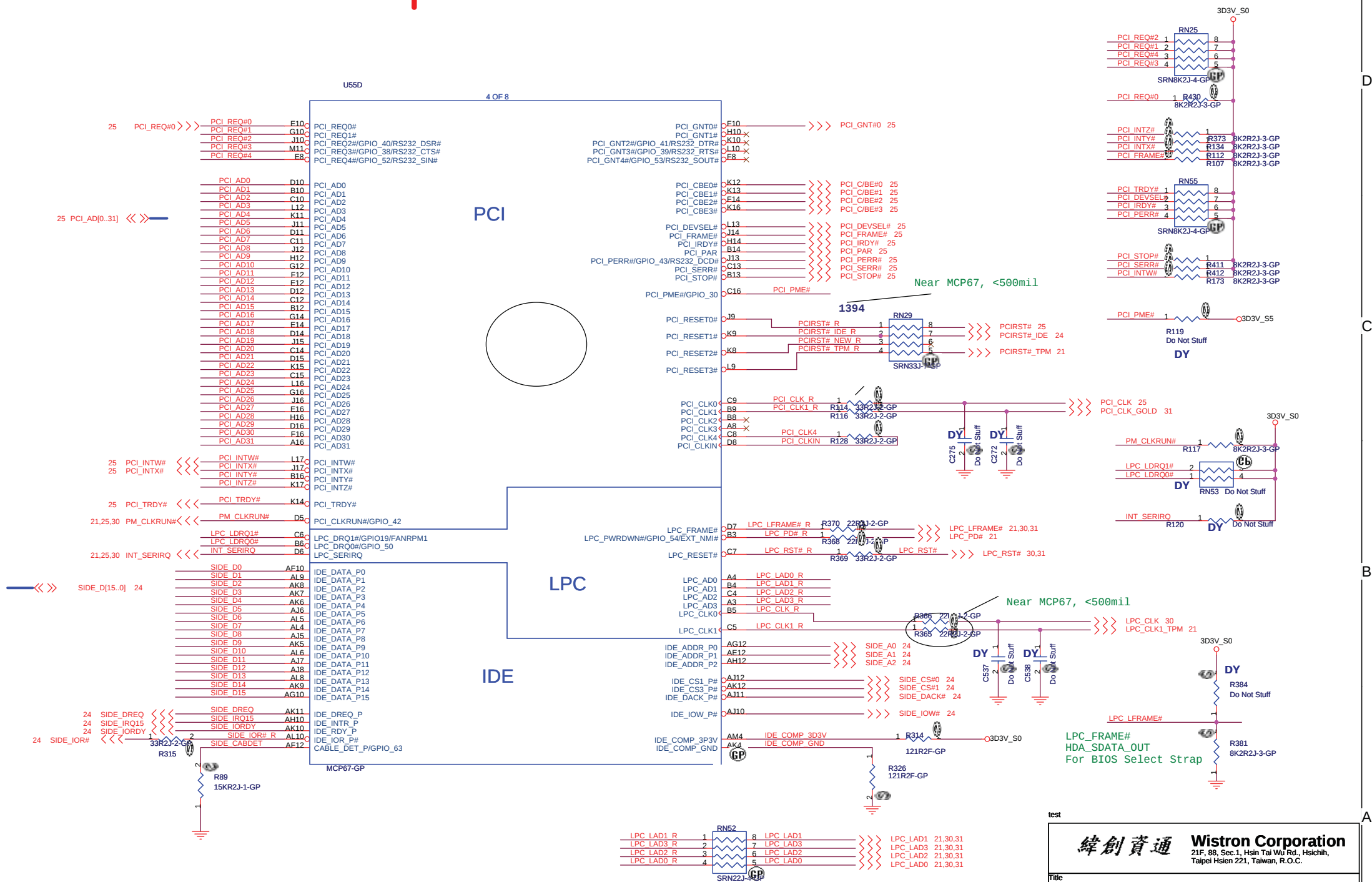
D

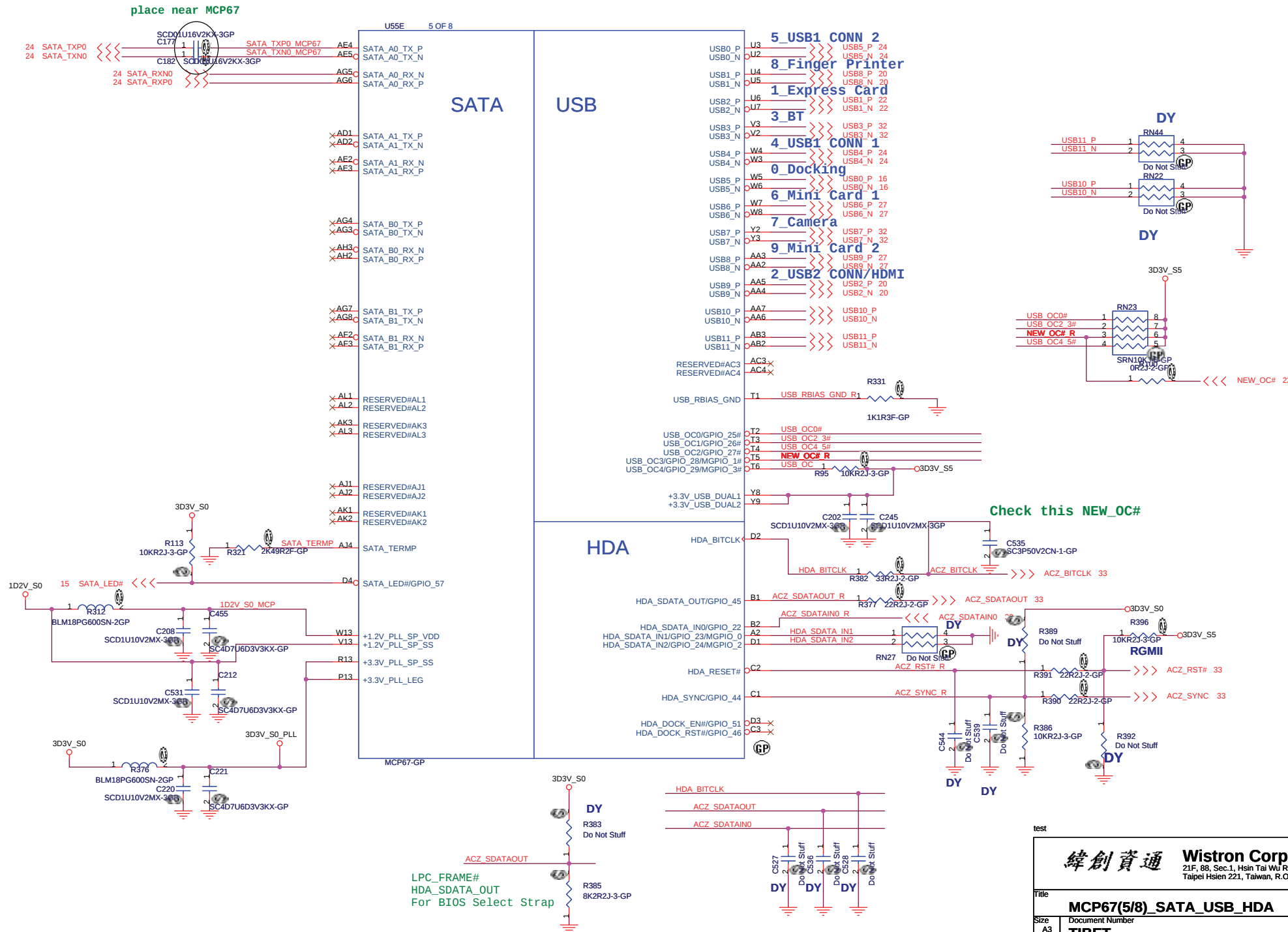
C

B

A



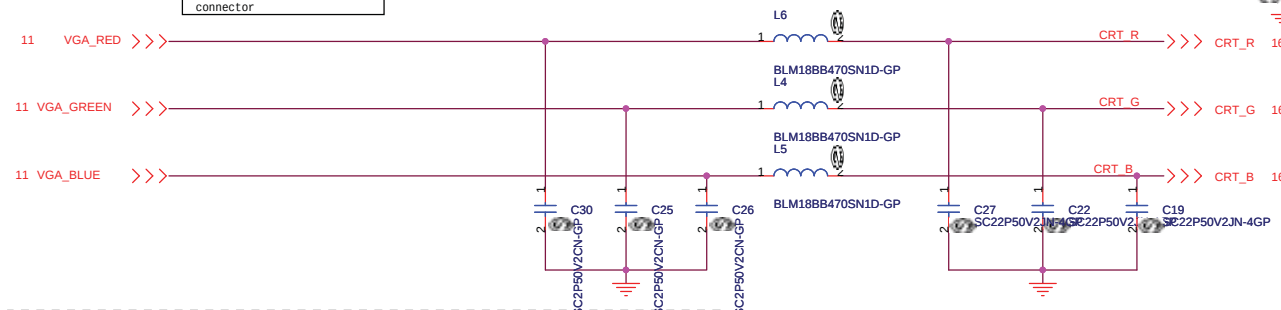




CRT I/F & CONNECTOR

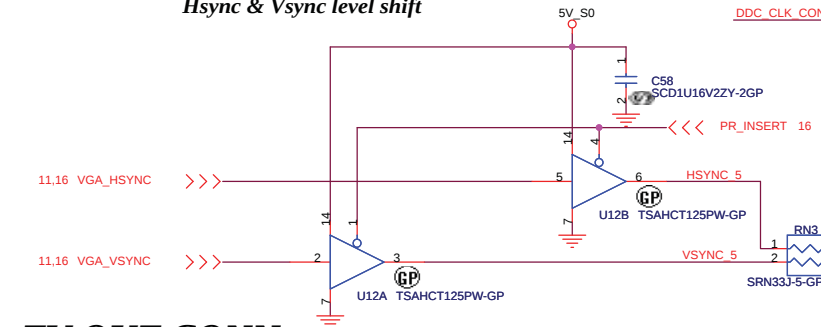
<http://hobi-elektronika.net>

Layout Note:
Place these resistors
close to the CRT-out
connector



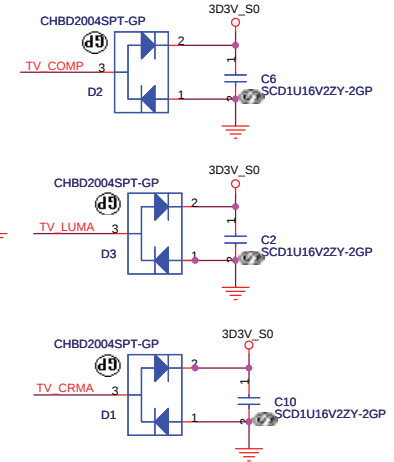
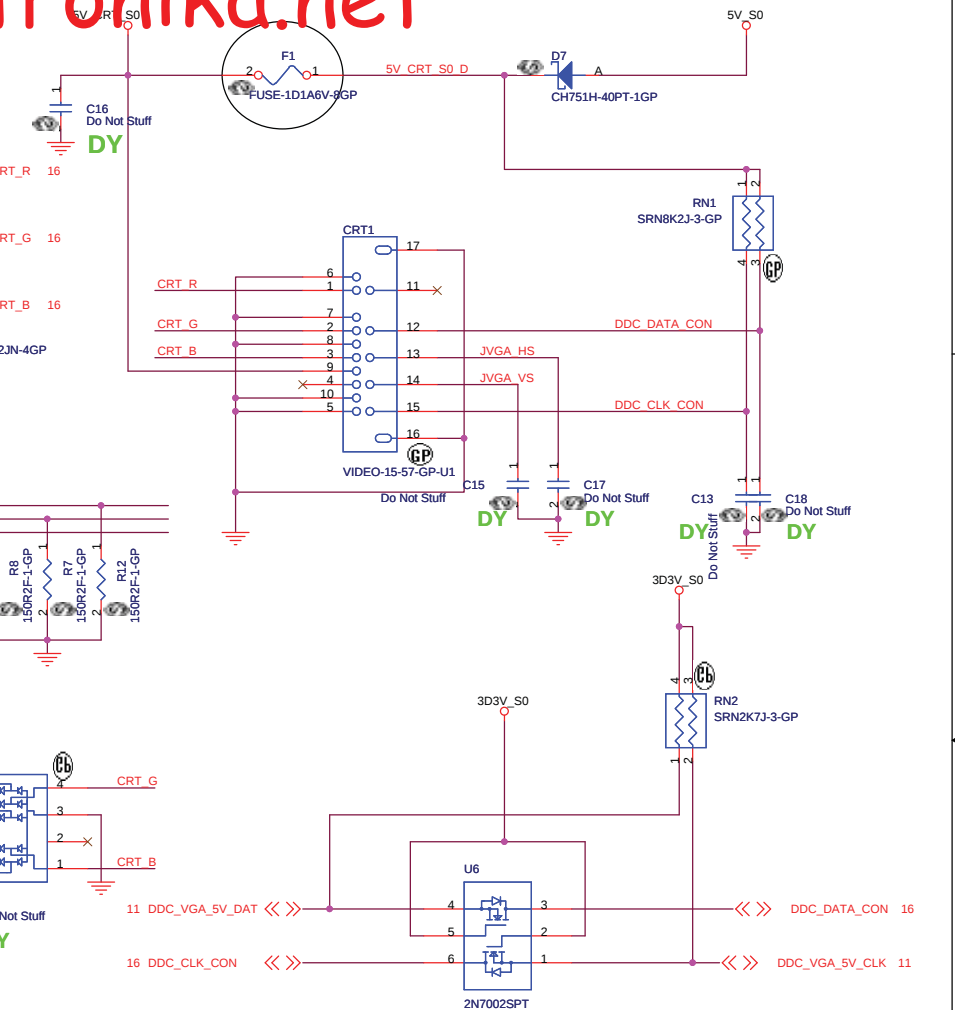
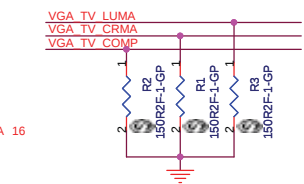
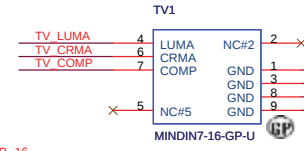
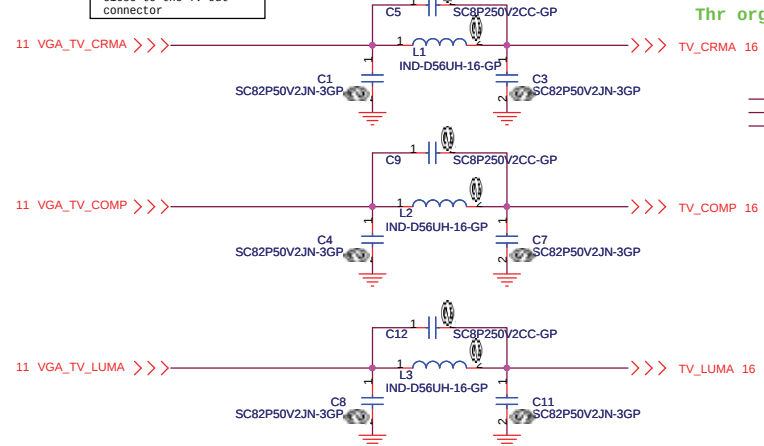
Layout Note:
* Must be a ground return path between this ground and the ground on the VGA connector.
Pi-filter & 150 Ohm pull-down resistors should be as close as to CRT CONN. RGB will hit 75 Ohm first, pi-filter, then CRT CONN.

Hsync & Vsync level shift



TV OUT CONN

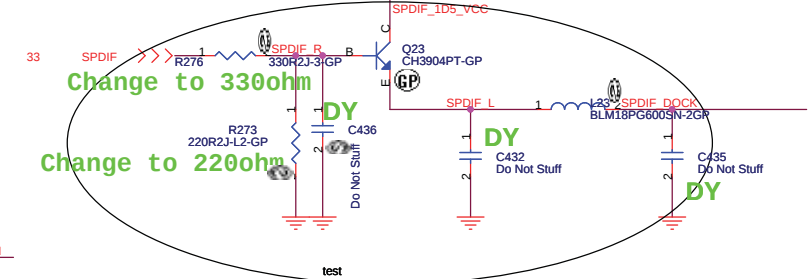
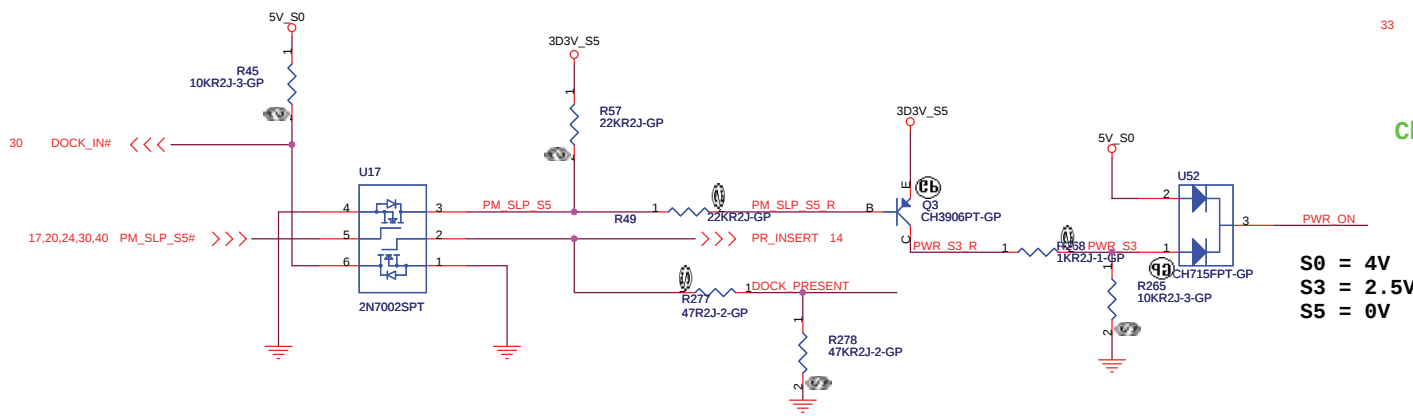
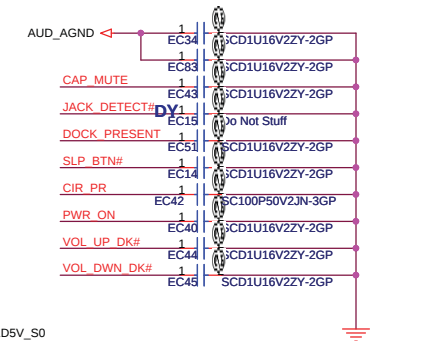
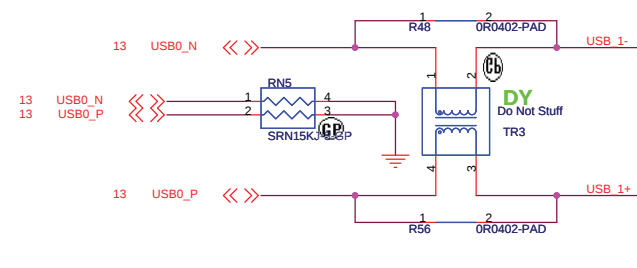
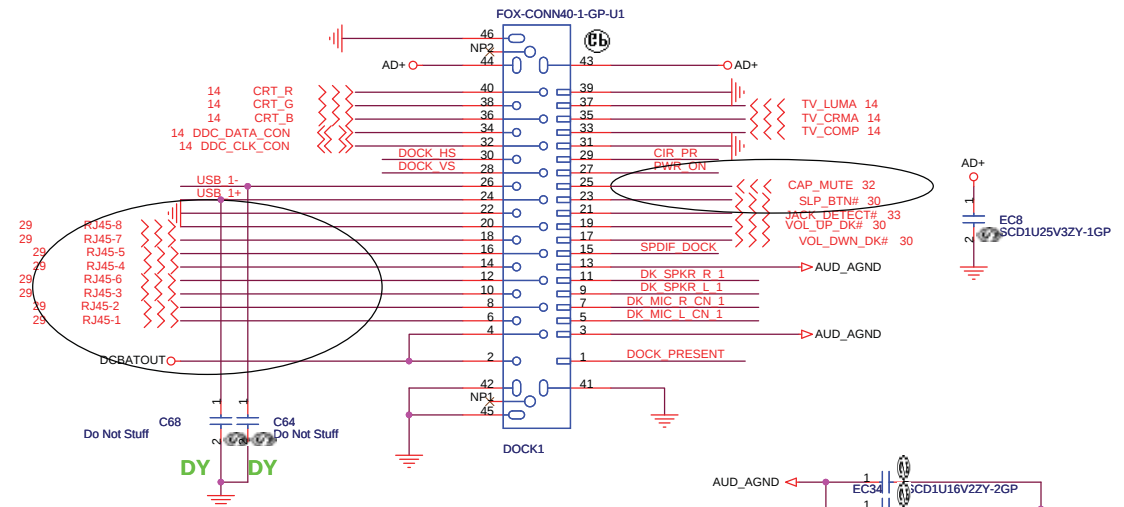
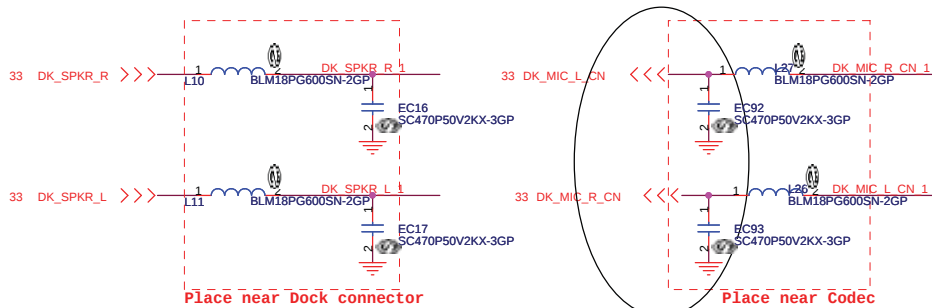
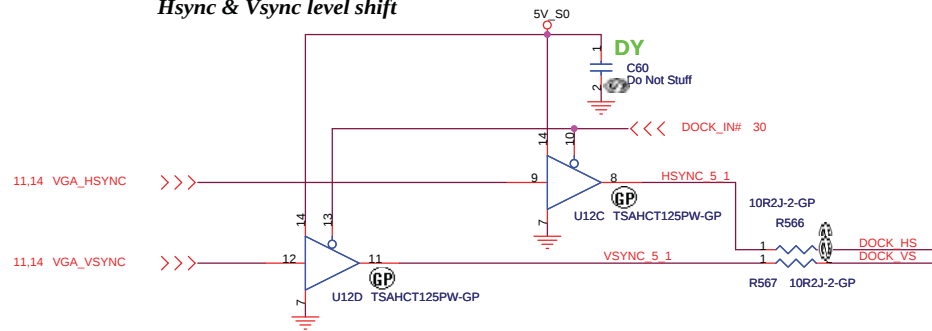
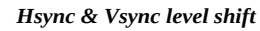
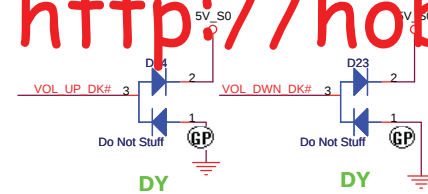
Layout Note:
Place this 2 resistors
close to the TV-out
connector

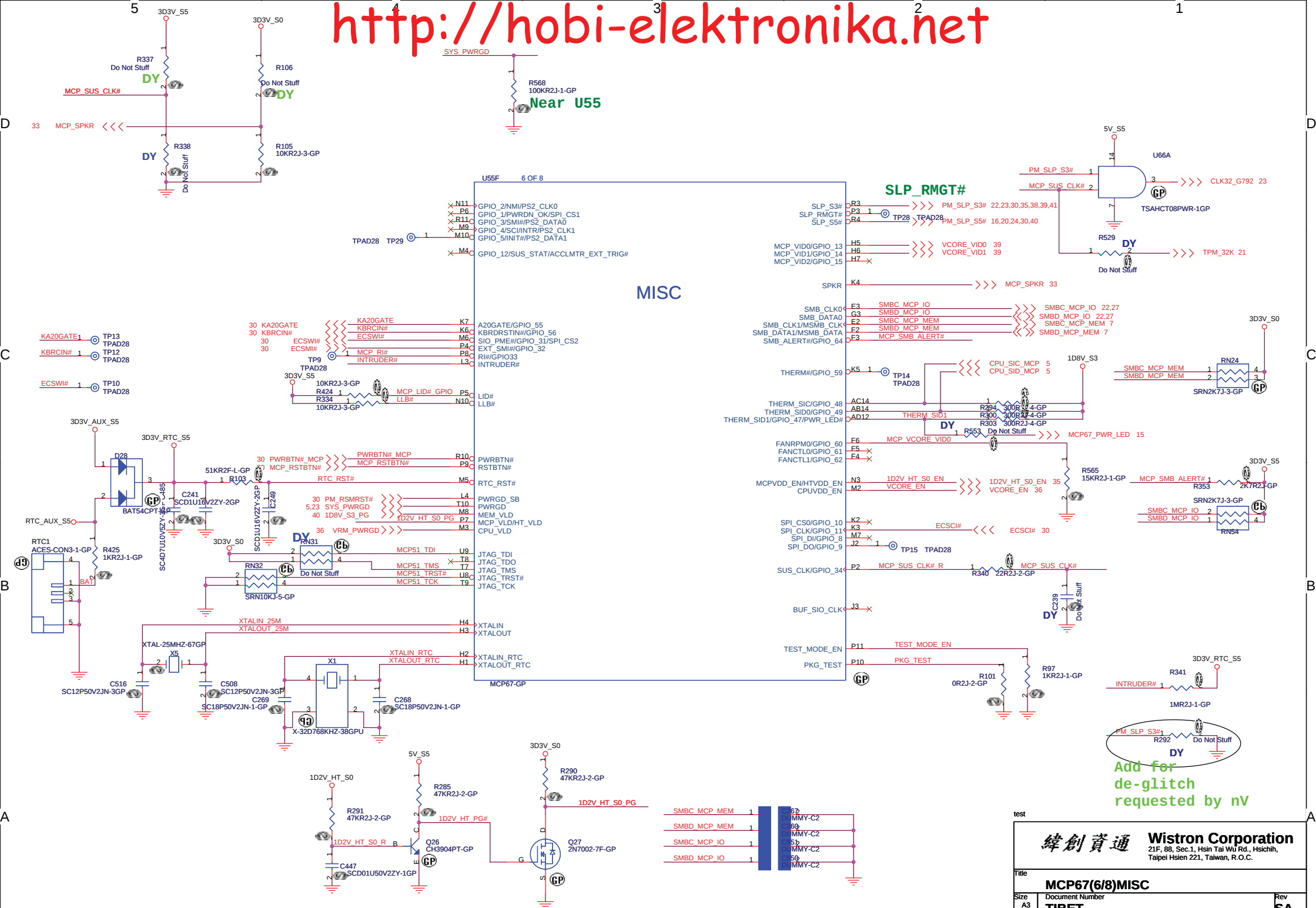


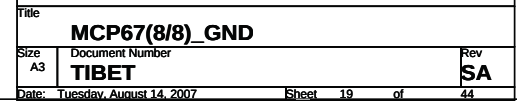
5V @ ext. CRT side

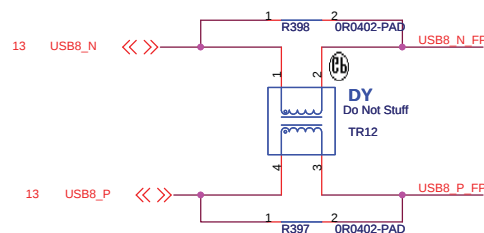
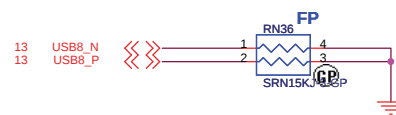
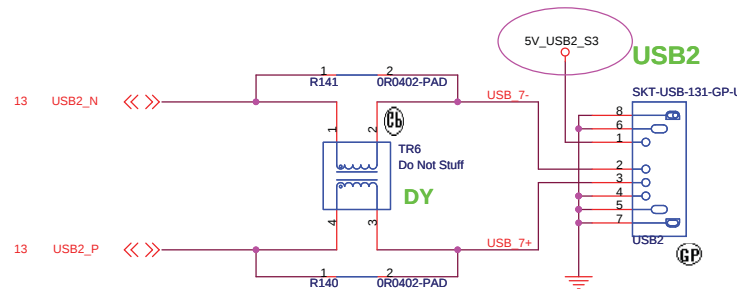
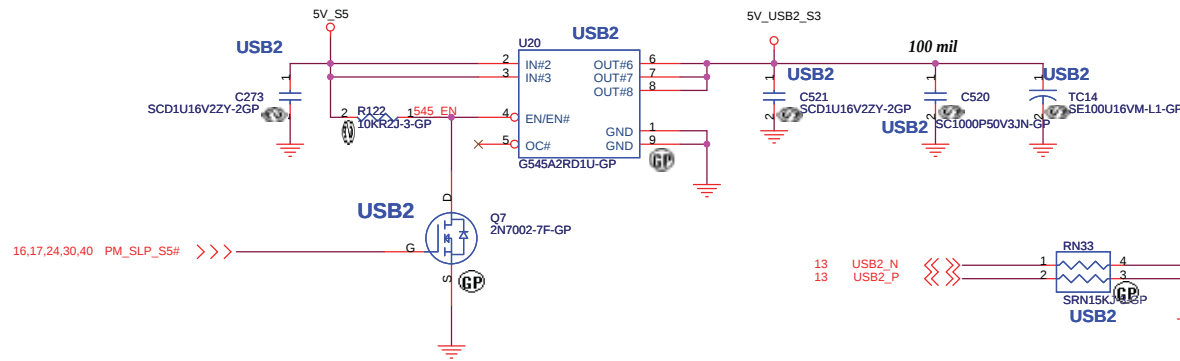
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
CRT/TV CONNECTOR	
Size A3	Document Number
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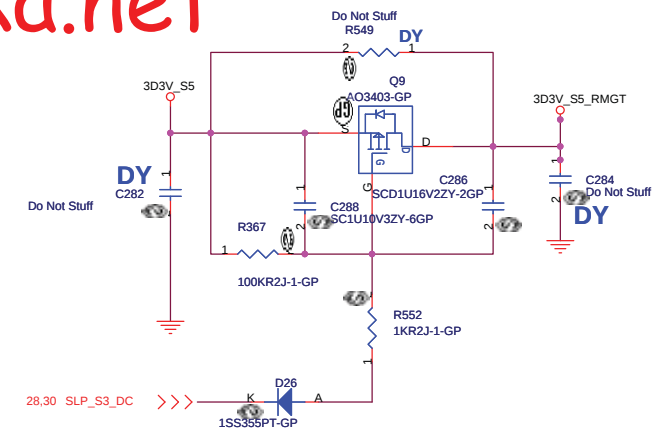
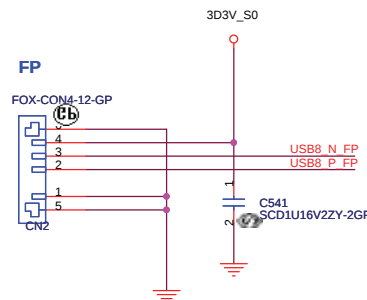




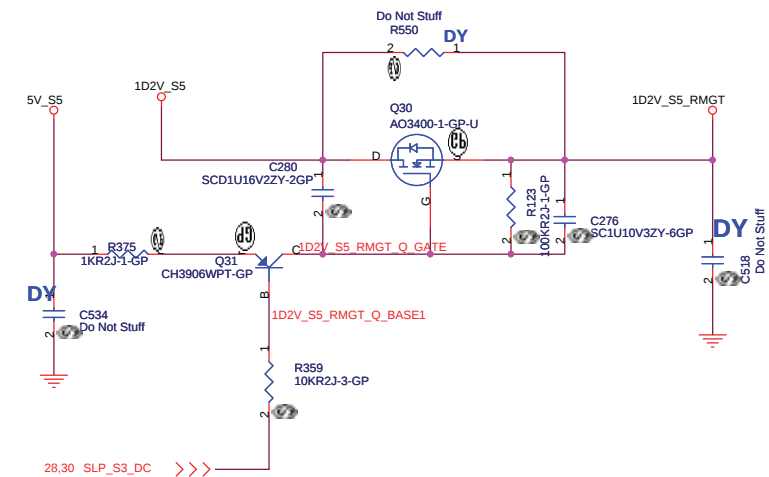




Finger Printer



??? Current Rating

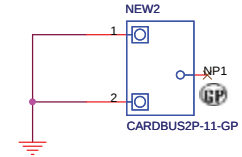
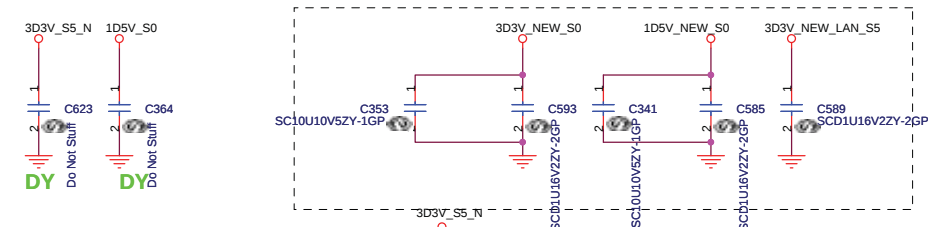


test

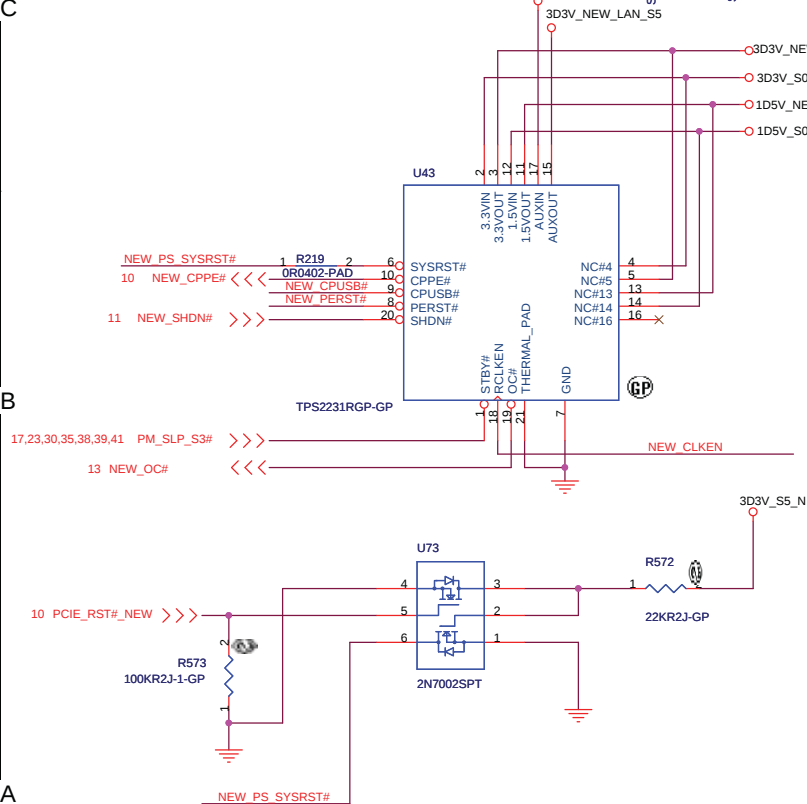
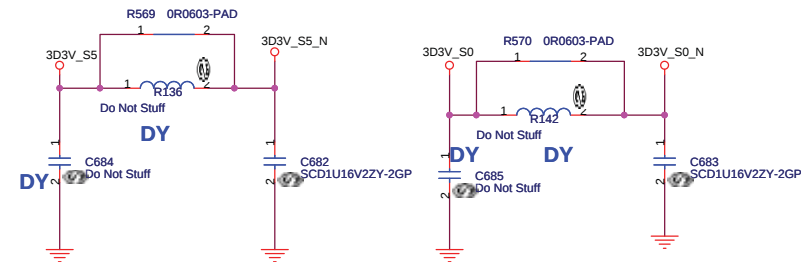
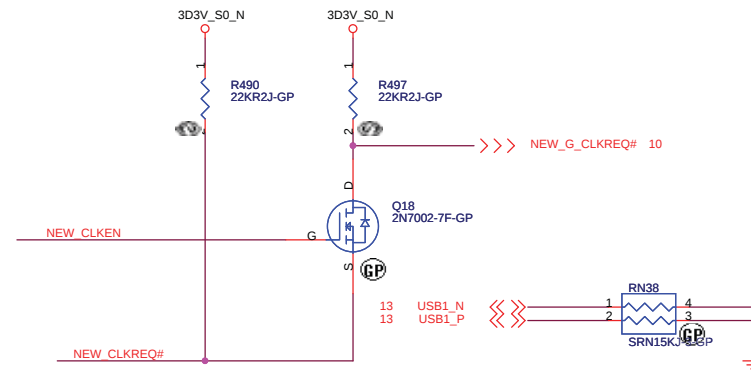
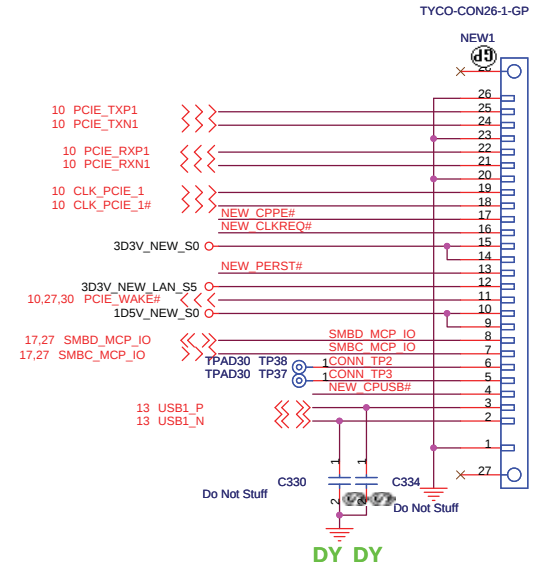
NEWCARD Connector

Place them Near to Chip

Place them Near to Connector



For Newcard socket



test

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

NEW CARD

Size

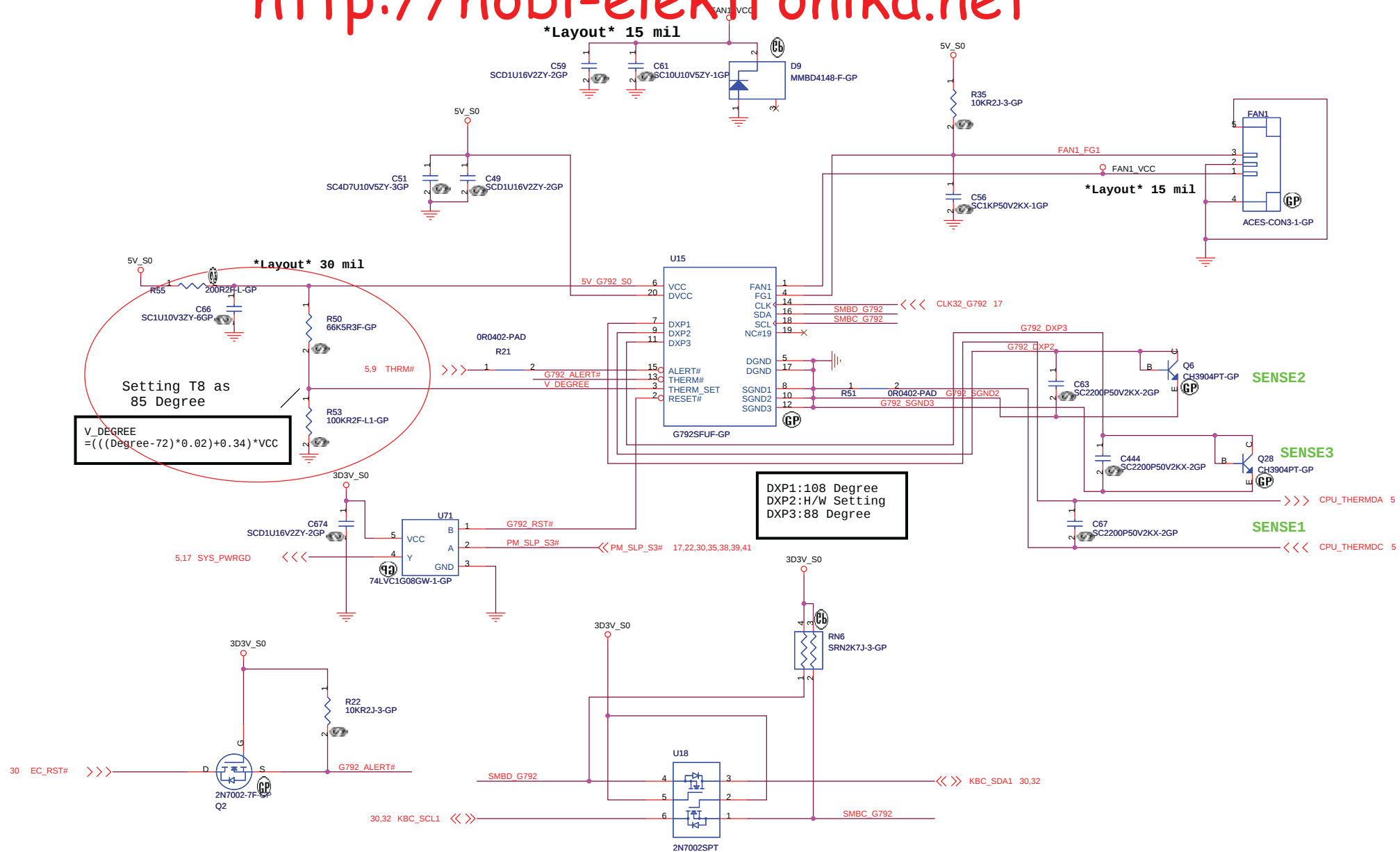
e	Document Number
---	-----------------

TIBET

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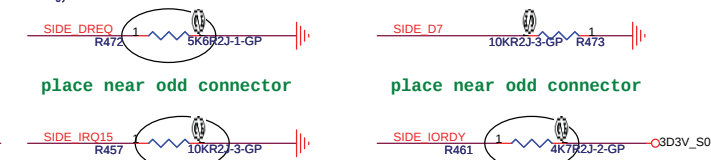
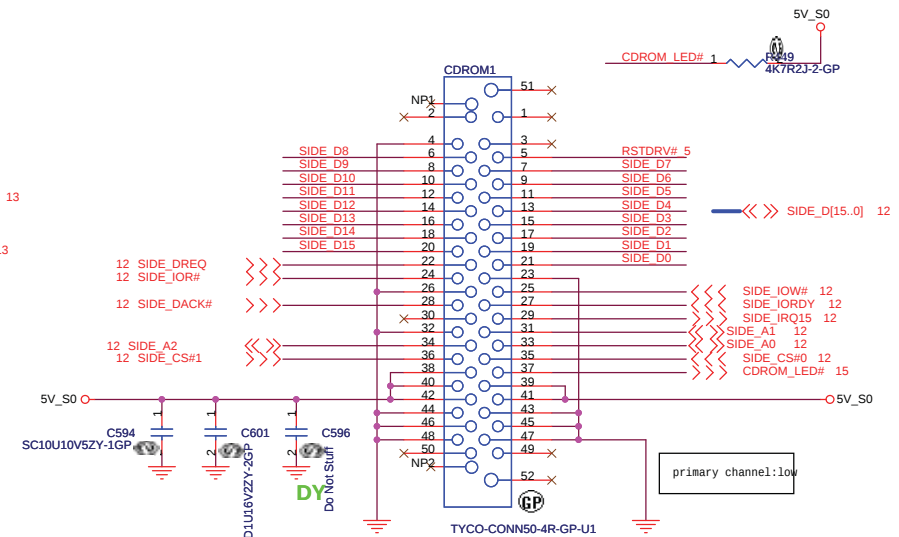
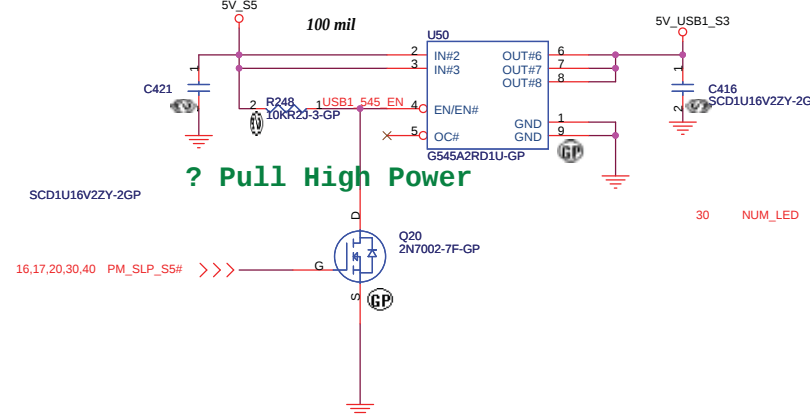
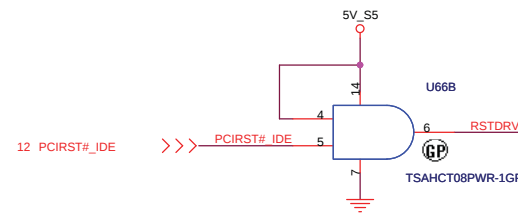
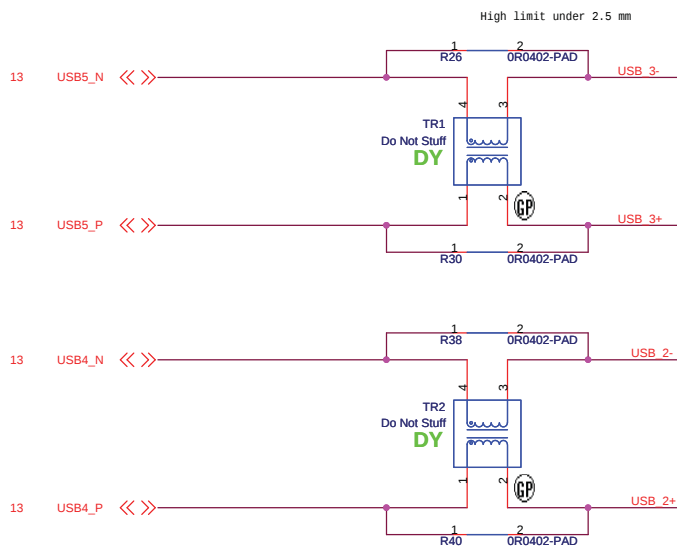
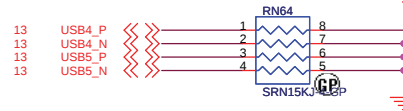
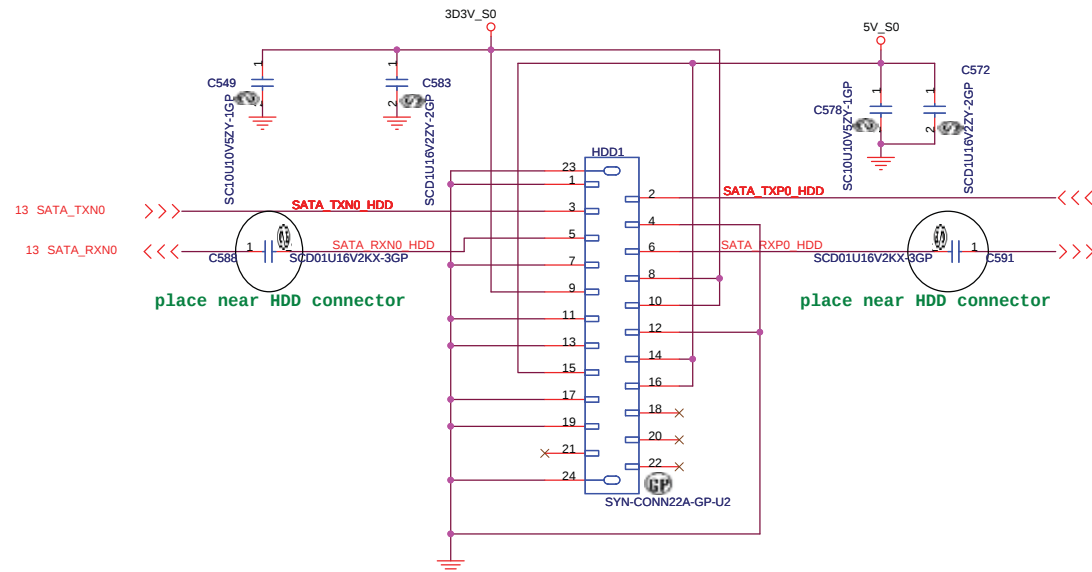


test

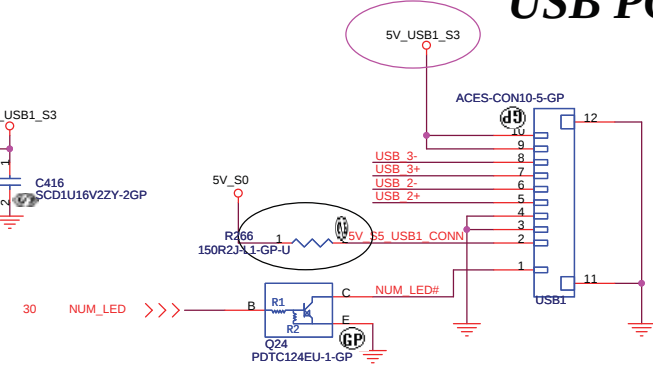
緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Thermal/Fan Controller			
Size	Document Number	Rev	
Custom		TIBET	
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SATA HD Connector

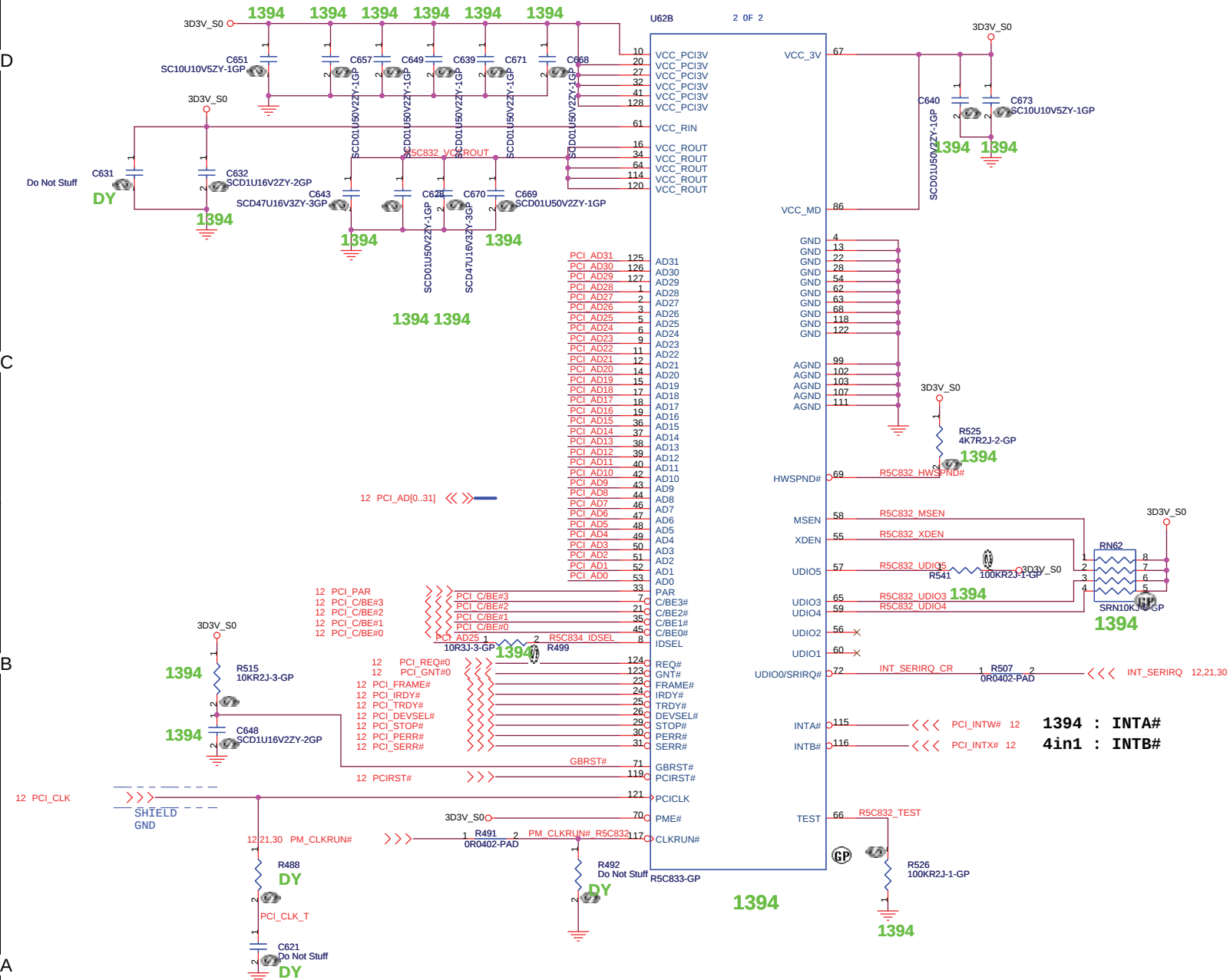
CD-ROM CONNECTOR

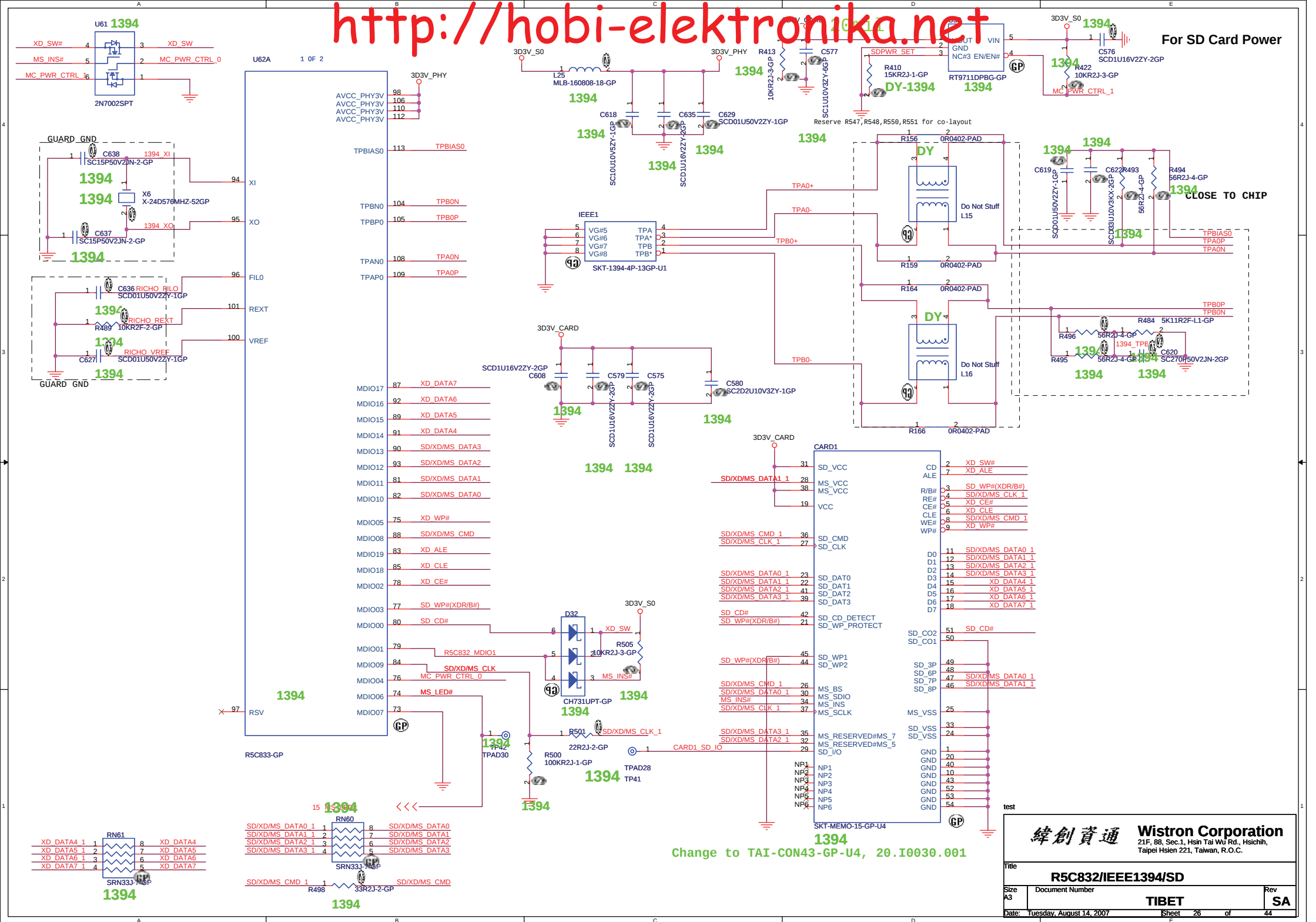


USB PORT



test



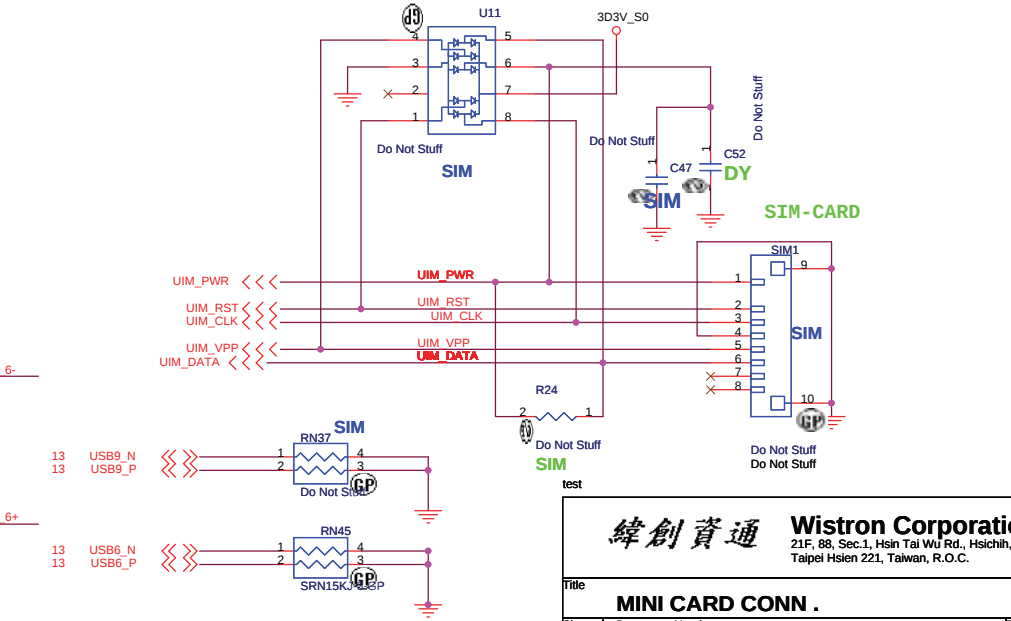
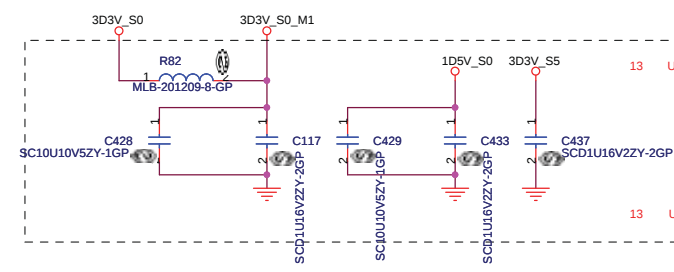
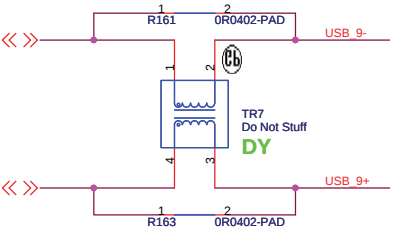
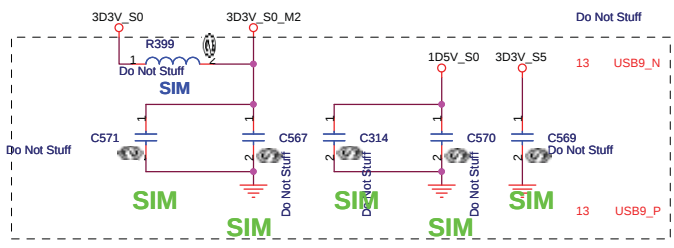
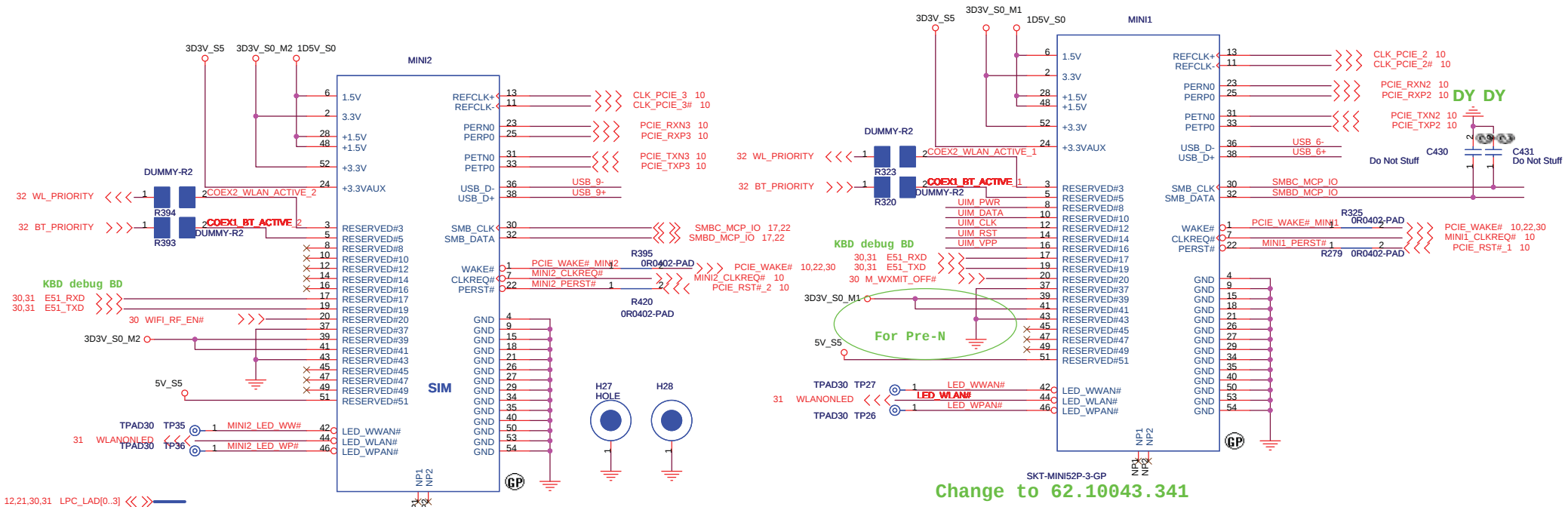


Mini Card Connector 2

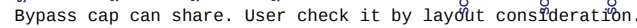
Wireless Card

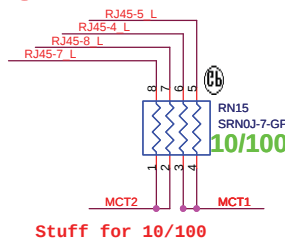
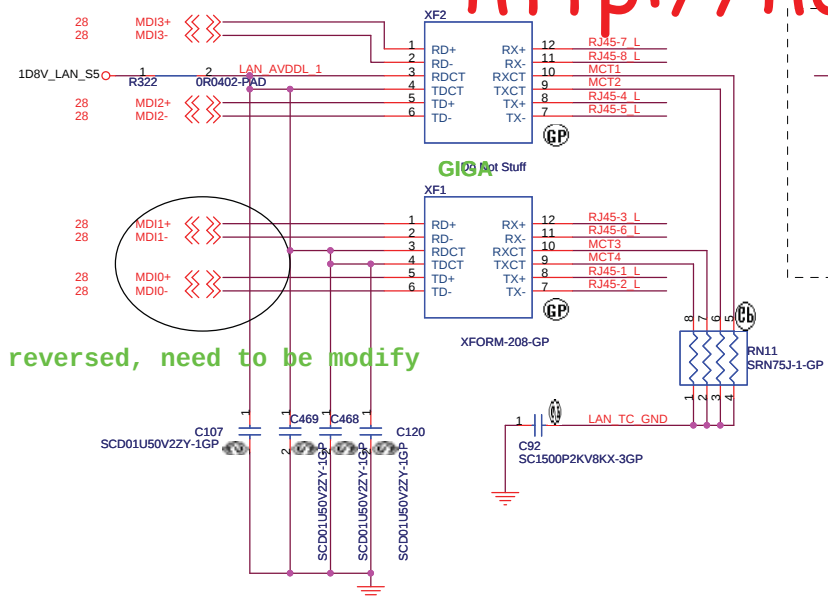
Mini Card Connector 1

WWAN



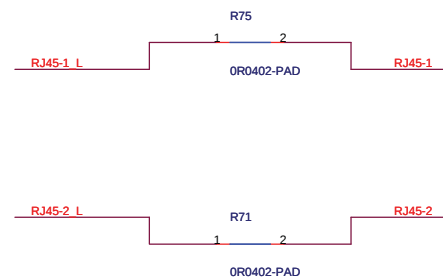
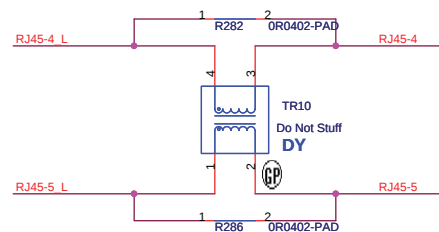
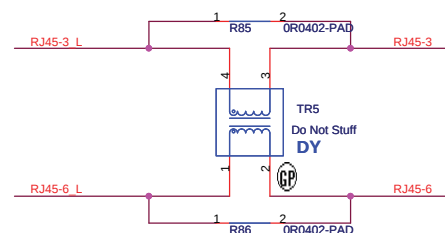
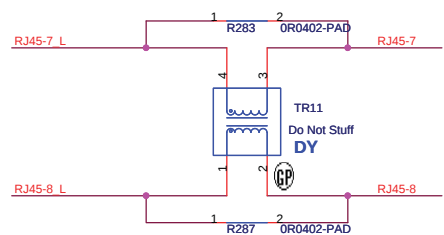
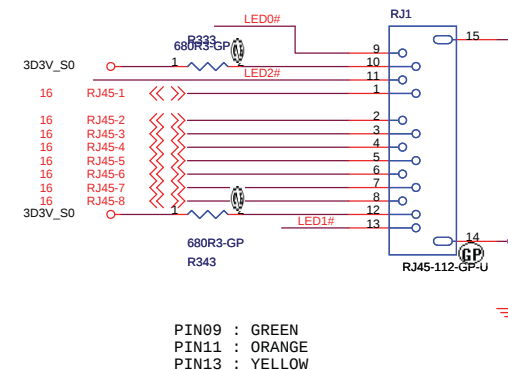
- E1116 use external 2.5V single power supply.
1.8V create by PNP and 1.2V use internal reg.

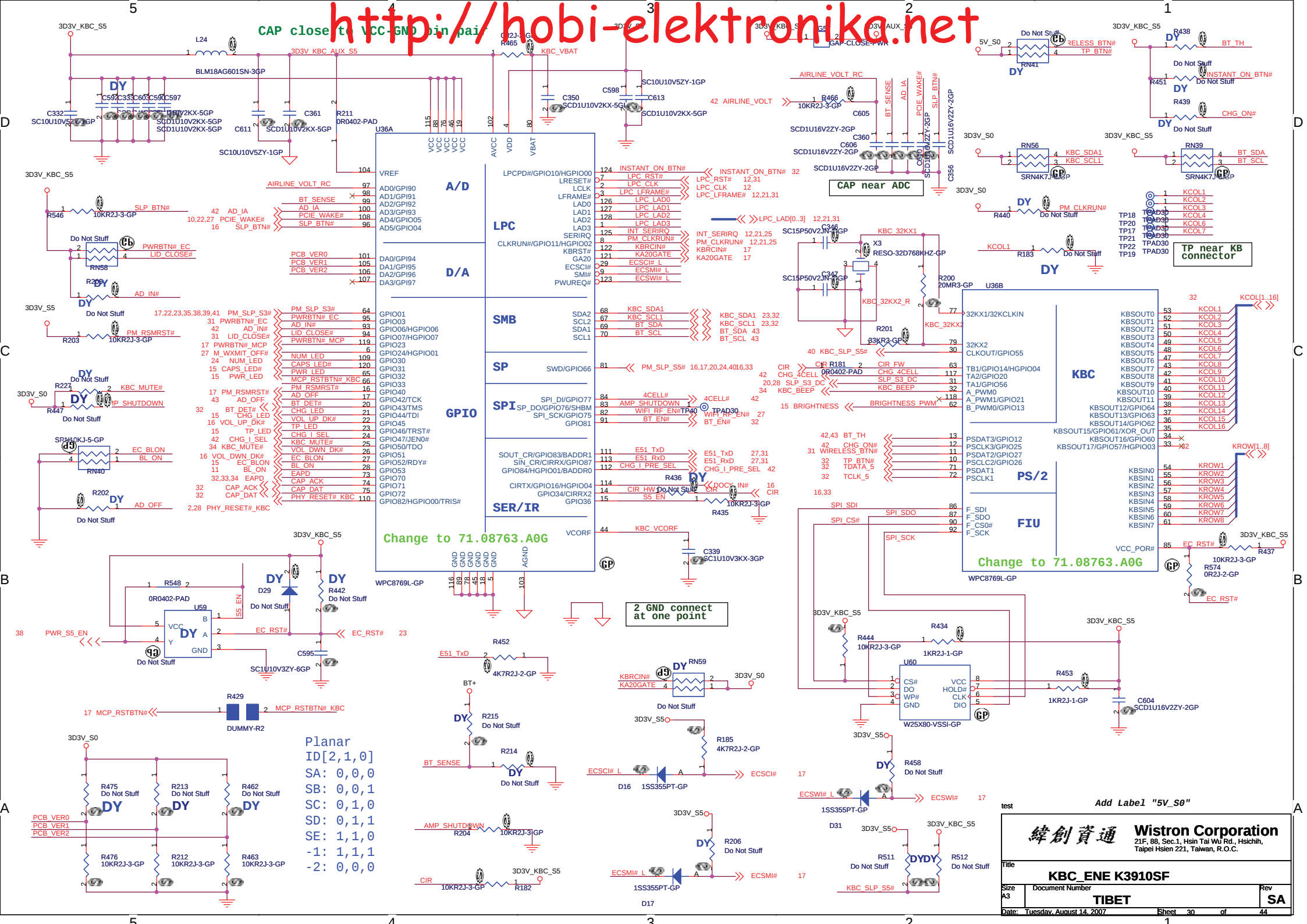


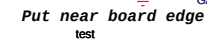
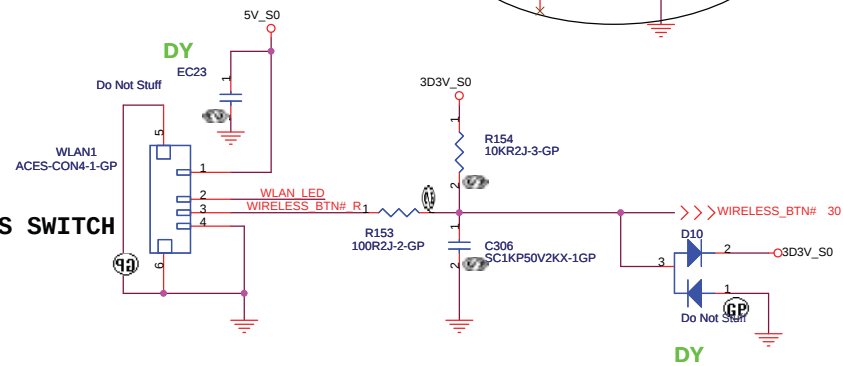


- 1.route on bottom as differential pairs.
- 2.Tx+/Tx- are pairs. Rx+/Rx- are pairs.
- 3.No vias, No 90 degree bends.
- 4.pairs must be equal lengths.
- 5.6mil trace width,12mil separation.
- 6.36mil between pairs and any other trace.
- 7.Must not cross ground moat,except RJ-45 moat.

GREEN: LINK 10/100 Mbps
YELLOW: TX/RX ACTIVITY
ORANGE: LINK 1000Mbps





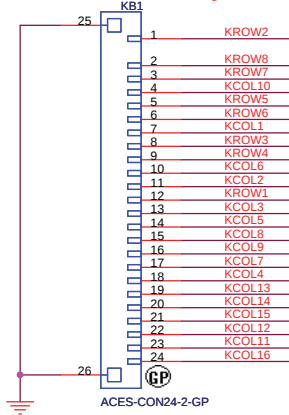


Internal KeyBoard Connector

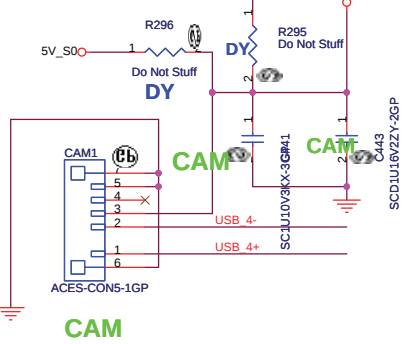
30 KROW[1..8] <<< 
30 KCOL[1..16] <<< 

Keyboard matrix (from vendor)

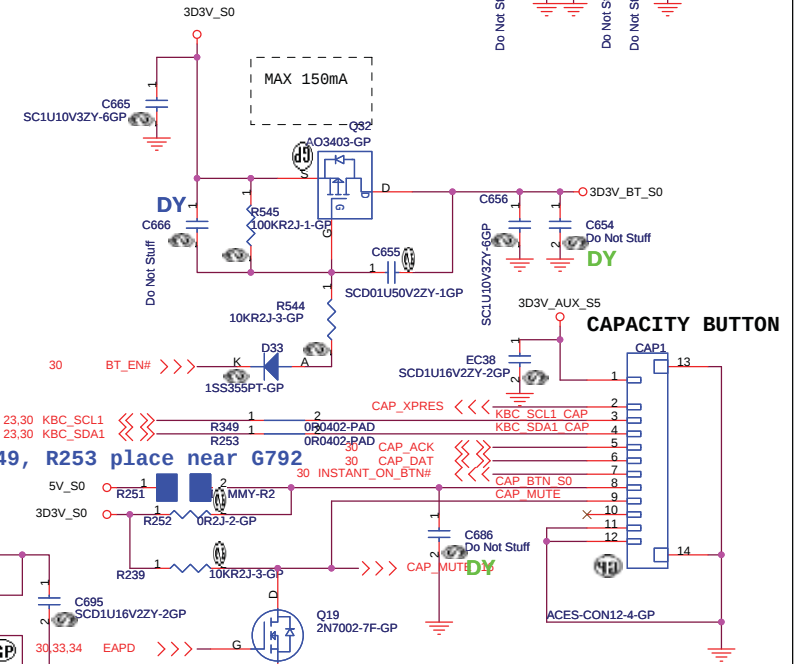
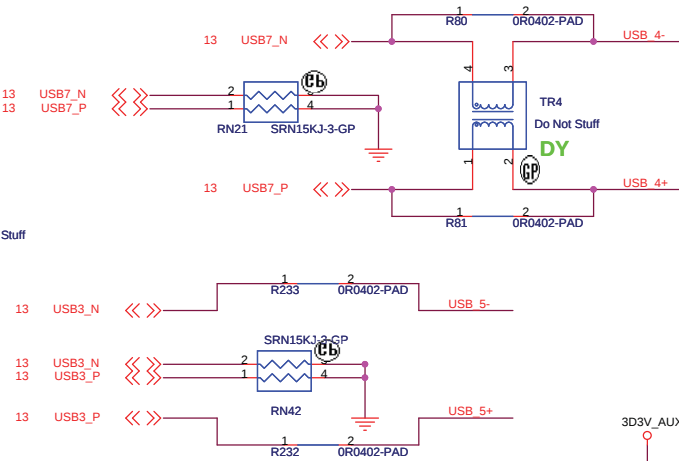
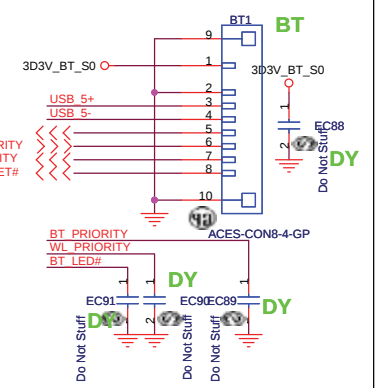
	US	Eur	Jap
MATRIXID1#	0	1	0
MATRIXID2#	0	0	1



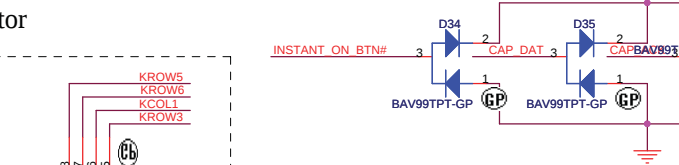
CAMERA



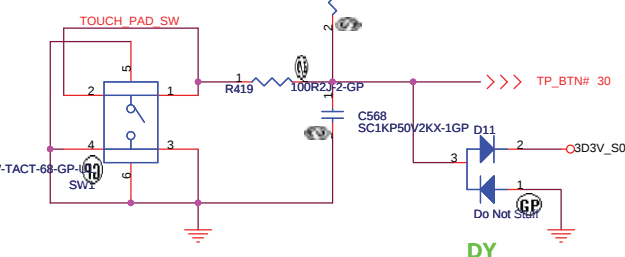
BT



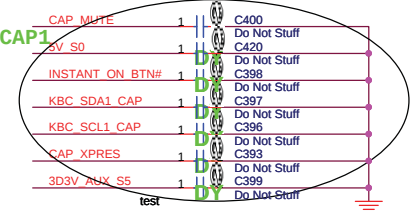
TouchPad Connector

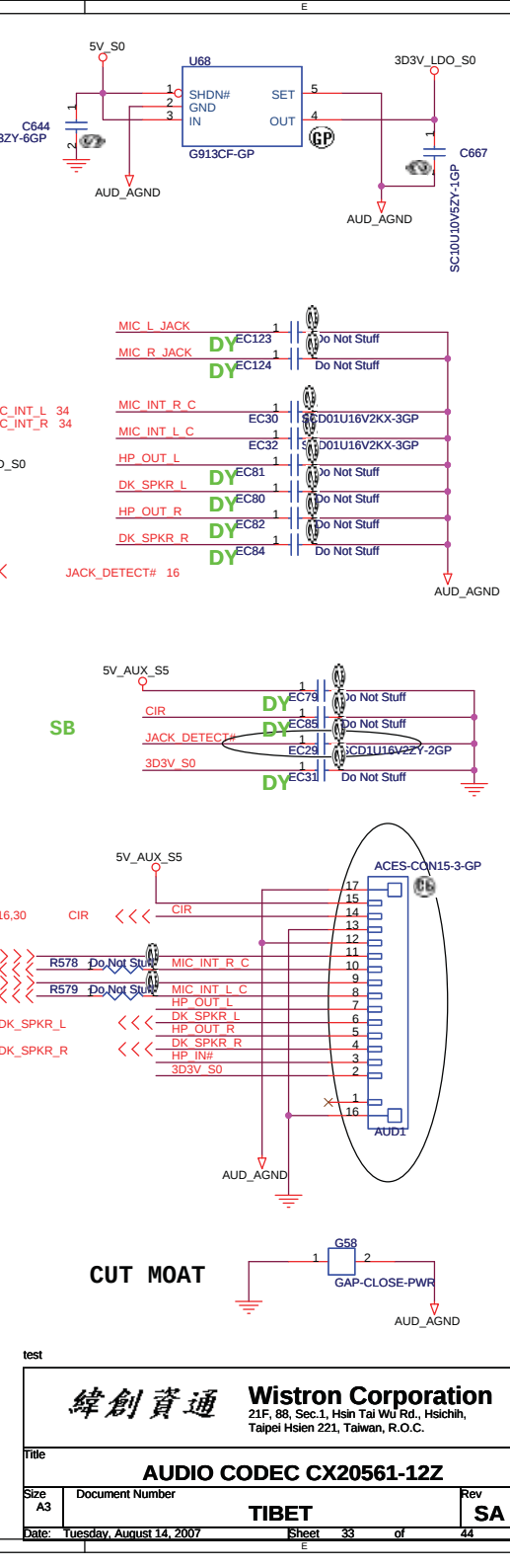


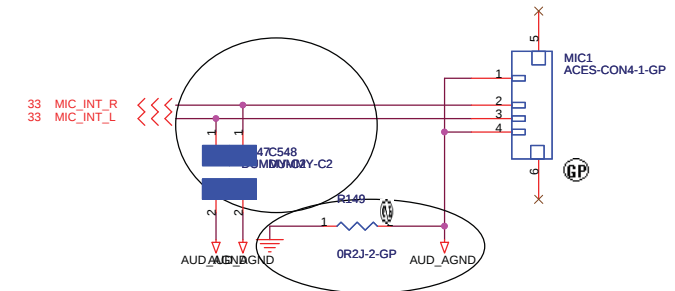
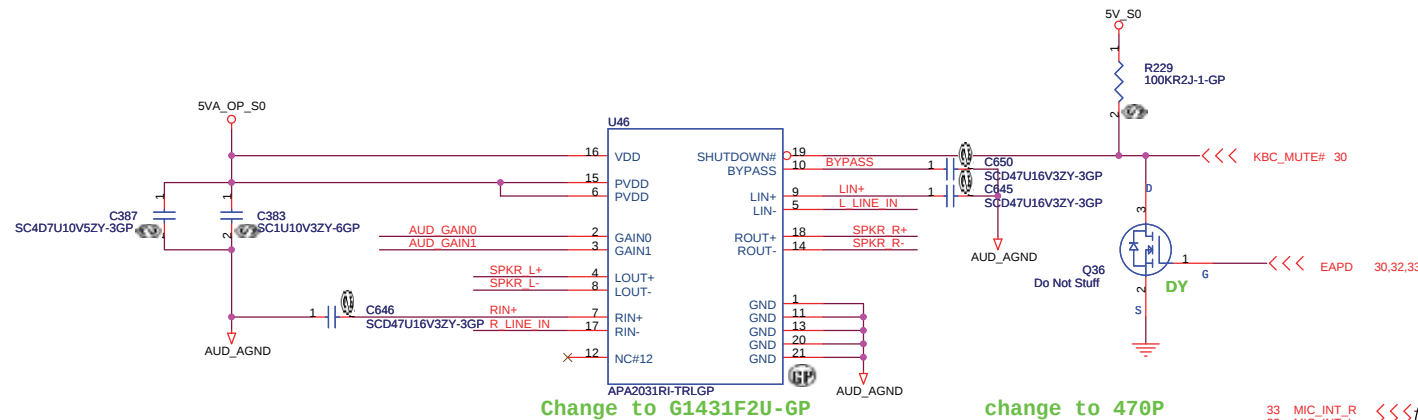
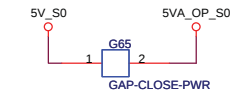
TOUCH-PAD SWITCH



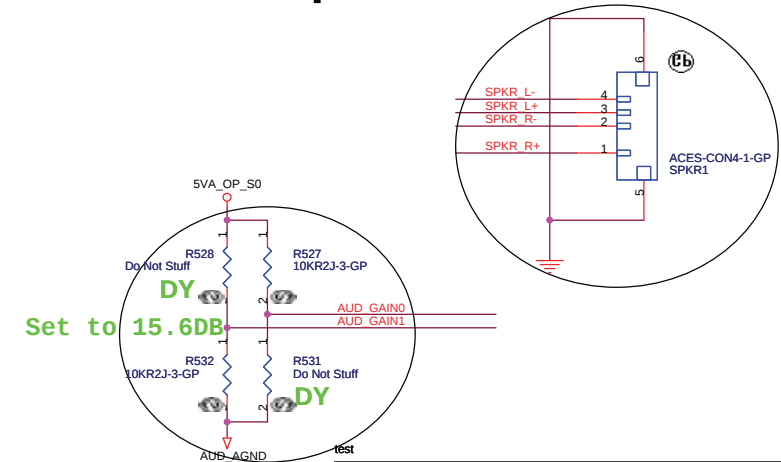
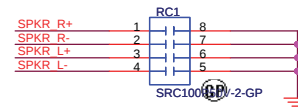
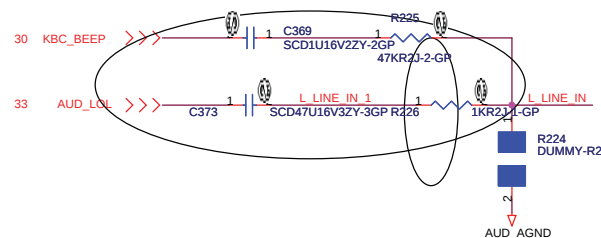
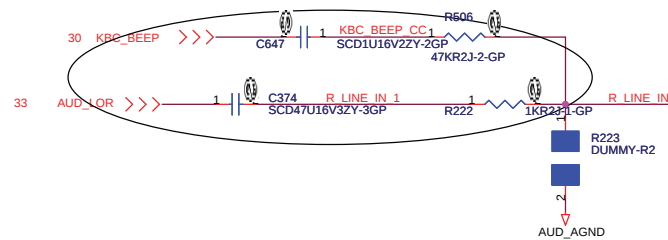
PLACE NEAR CAP1







Speaker

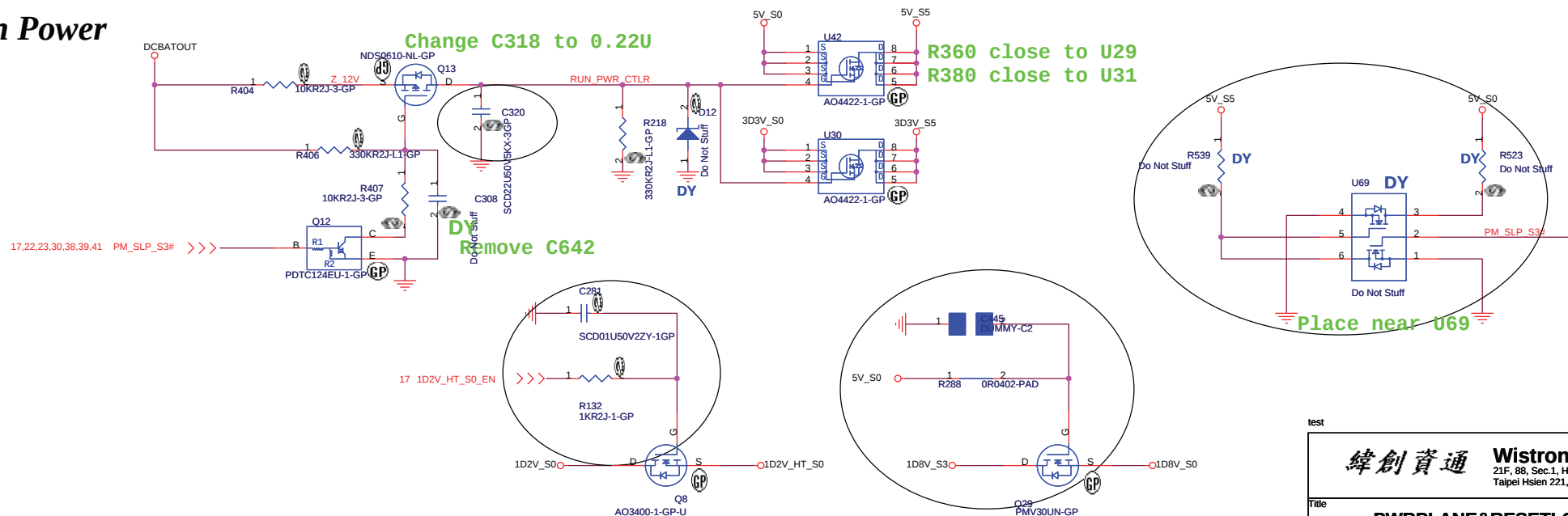


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Title			AUDIO AMP APA3031	
Size	Document Number	Rev		
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Date:	Tuesday, August 14, 2007	Sheet	34	of 44

Remove Dummy PAD

Run Power



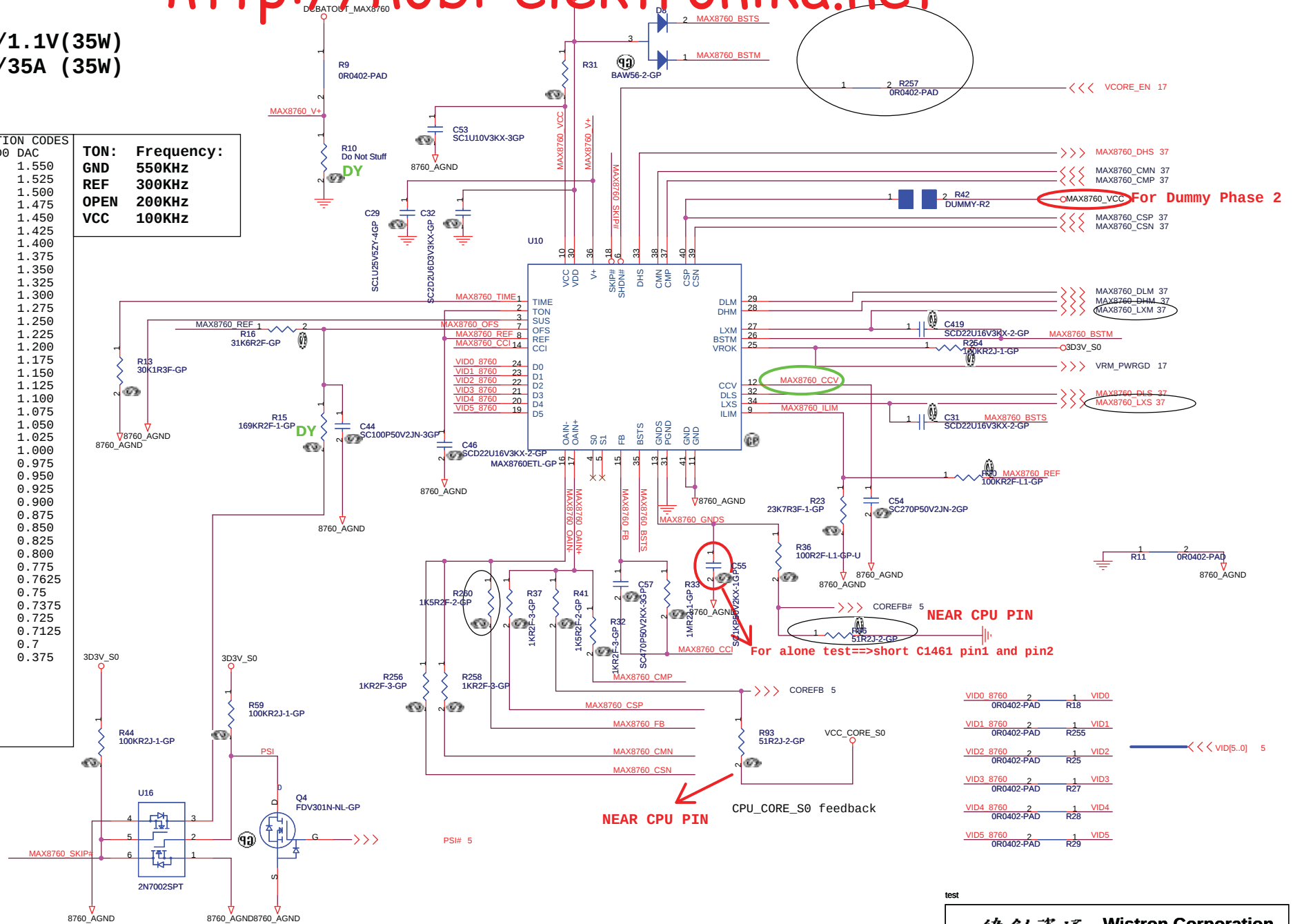
test

CPU_VCORE
VID=1.15V(25W)/1.1V(35W)
Iomax=25A(25W)/35A (35W)
OCP=40A~45A

TABLE 1. VOLTAGE IDENTIFICATION CODES

VID5	VID4	VID3	VID2	VID1	VID0	DAC
0	0	0	0	0	0	1.550
0	0	0	0	0	1	1.525
0	0	0	0	1	0	1.500
0	0	0	0	1	1	1.475
0	0	0	1	0	0	1.450
0	0	0	1	0	1	1.425
0	0	0	1	1	0	1.400
0	0	0	1	1	1	1.375
0	0	1	0	0	0	1.350
0	0	1	0	0	1	1.325
0	0	1	0	1	0	1.300
0	0	1	0	1	1	1.275
0	0	1	1	0	0	1.250
0	0	1	1	0	1	1.225
0	0	1	1	1	0	1.200
0	0	1	1	1	1	1.175
0	1	0	0	0	0	1.150
0	1	0	0	0	1	1.125
0	1	0	0	1	0	1.100
0	1	0	0	1	1	1.075
0	1	0	1	0	0	1.050
0	1	0	1	0	1	1.025
0	1	0	1	1	0	1.000
0	1	0	1	1	1	0.975
0	1	1	0	0	0	0.950
0	1	1	0	0	1	0.925
0	1	1	0	1	0	0.900
0	1	1	0	1	1	0.875
0	1	1	1	0	0	0.850
0	1	1	1	0	1	0.825
0	1	1	1	1	0	0.800
0	1	1	1	1	1	0.775
1	0	0	0	0	0	0.7625
1	0	0	0	0	1	0.75
1	0	0	0	1	0	0.7375
1	0	0	0	1	1	0.725
1	0	0	1	0	0	0.7125
1	0	0	1	0	1	0.7
1	1	1	1	1	1	0.375

TON: Frequency:
GND 550KHz
REF 300KHz
OPEN 200KHz
VCC 100KHz



test

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Title

CPU CORE MAX8760(1/2)

Size

Document Number

A3

TIBET

Rev

SA

Date: Tuesday, August 14, 2007

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44



test

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Taipei Hsien 221, Taiwan, R.O.C.

Title

Size
A

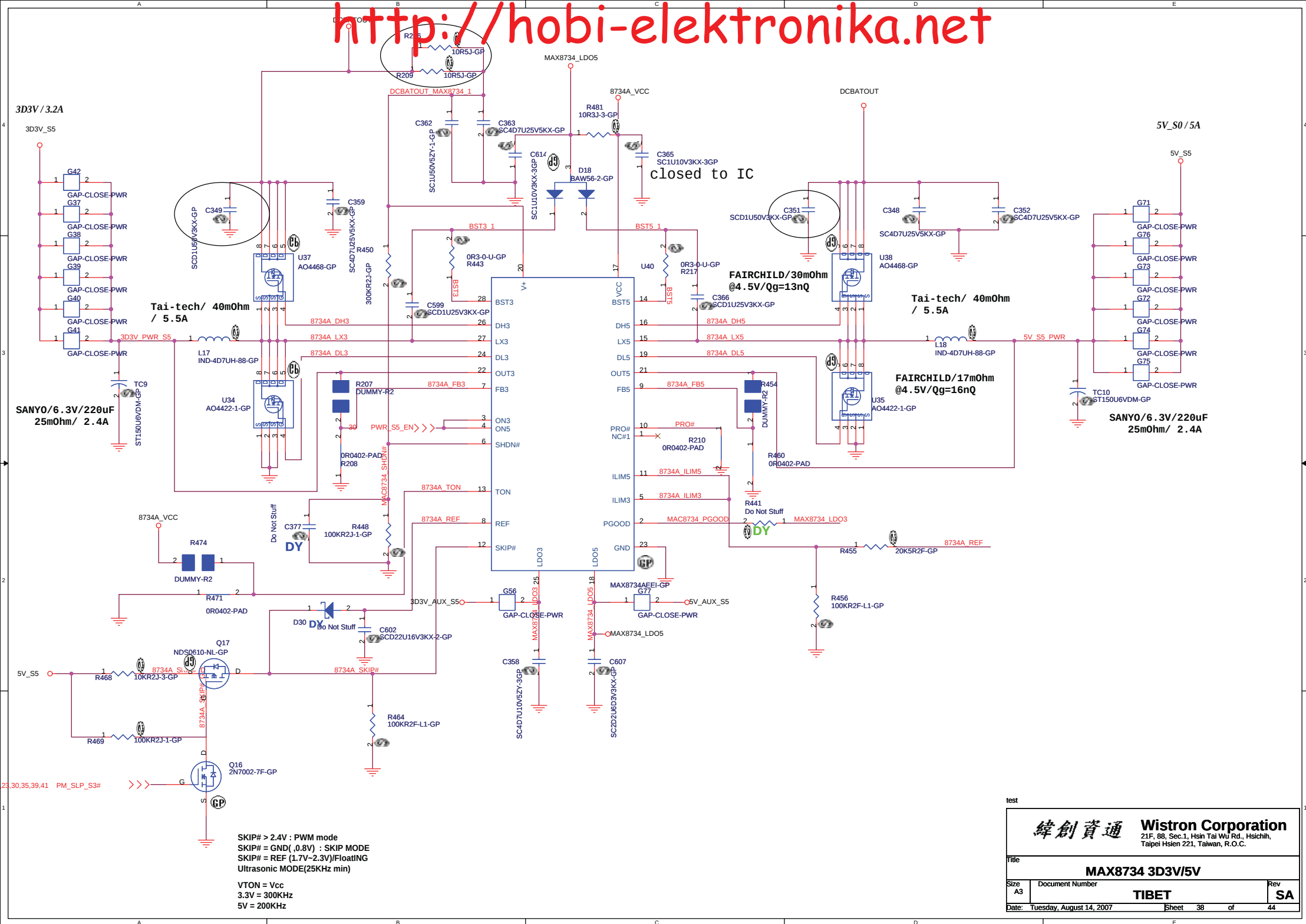
Document Number	
-----------------	--

TIBET

SA

Date: Tuesday, August 14, 2007

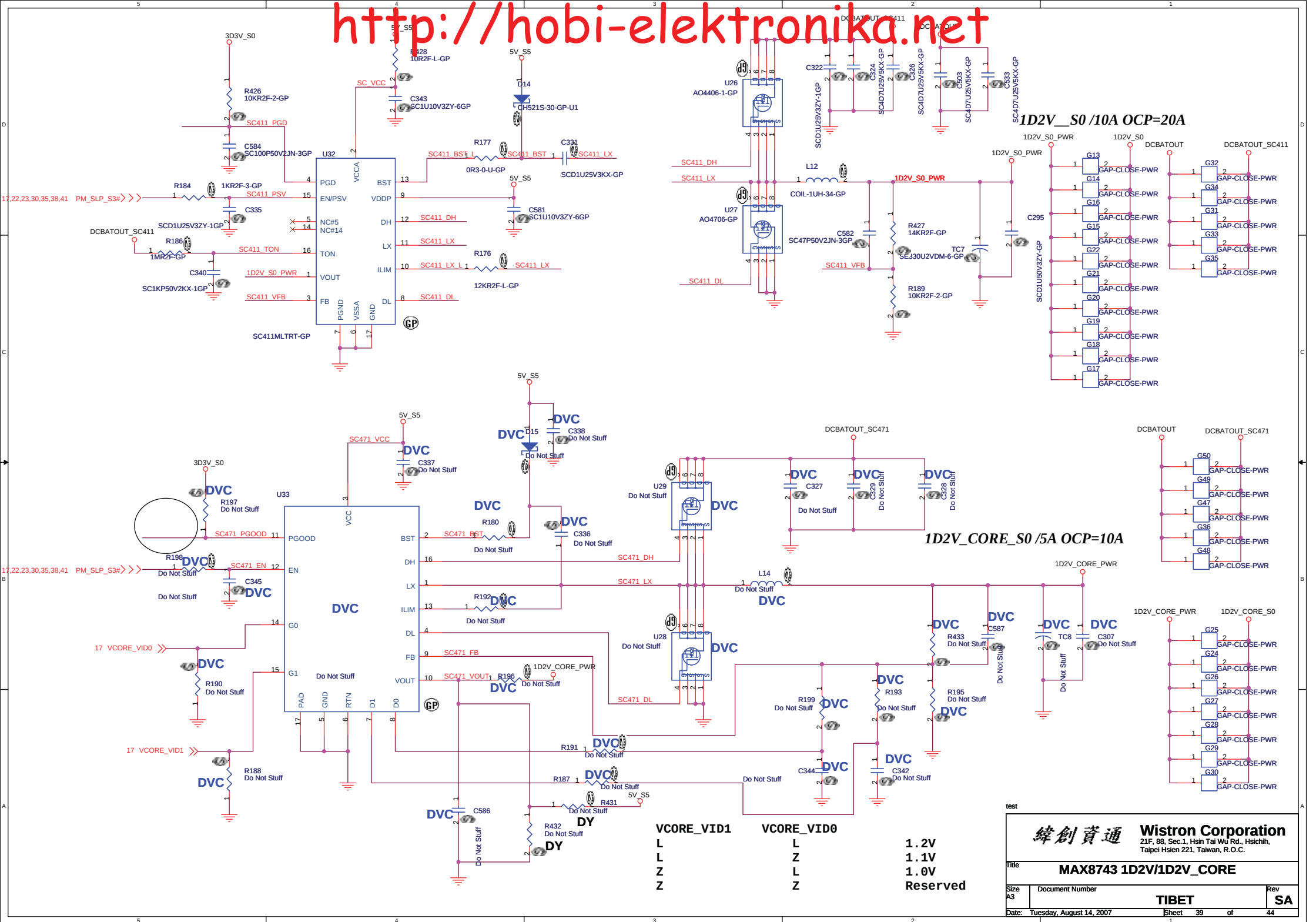
Sheet 37 of 44

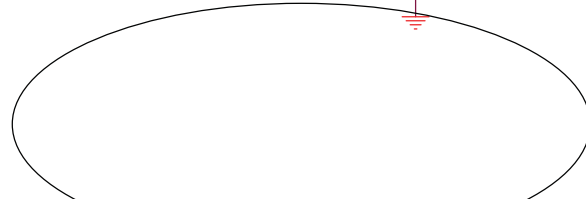


tes

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Title			
MAX8734 3D3V/5V			
Size A3	Document Number	TIBET	Rev SA
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DCBATOUT_TPS51116

U25
AO4468-GP

**AOS/30mOhm
@4.5V/Qg=13nQ**

TPS51116 UGT

TPS51116 VBST 1

C318
SCD1U25V3KX-C

TPS51116 PHS 1

IND-2D2UH-46-GP-U

1D8V_PWR_S3

U24
AO4422-1-GP

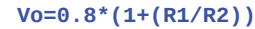
CYNTEC /18mohm/8A

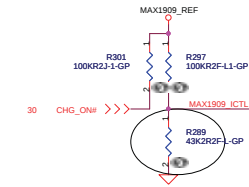
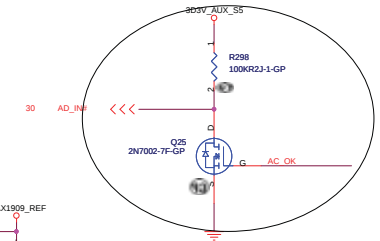
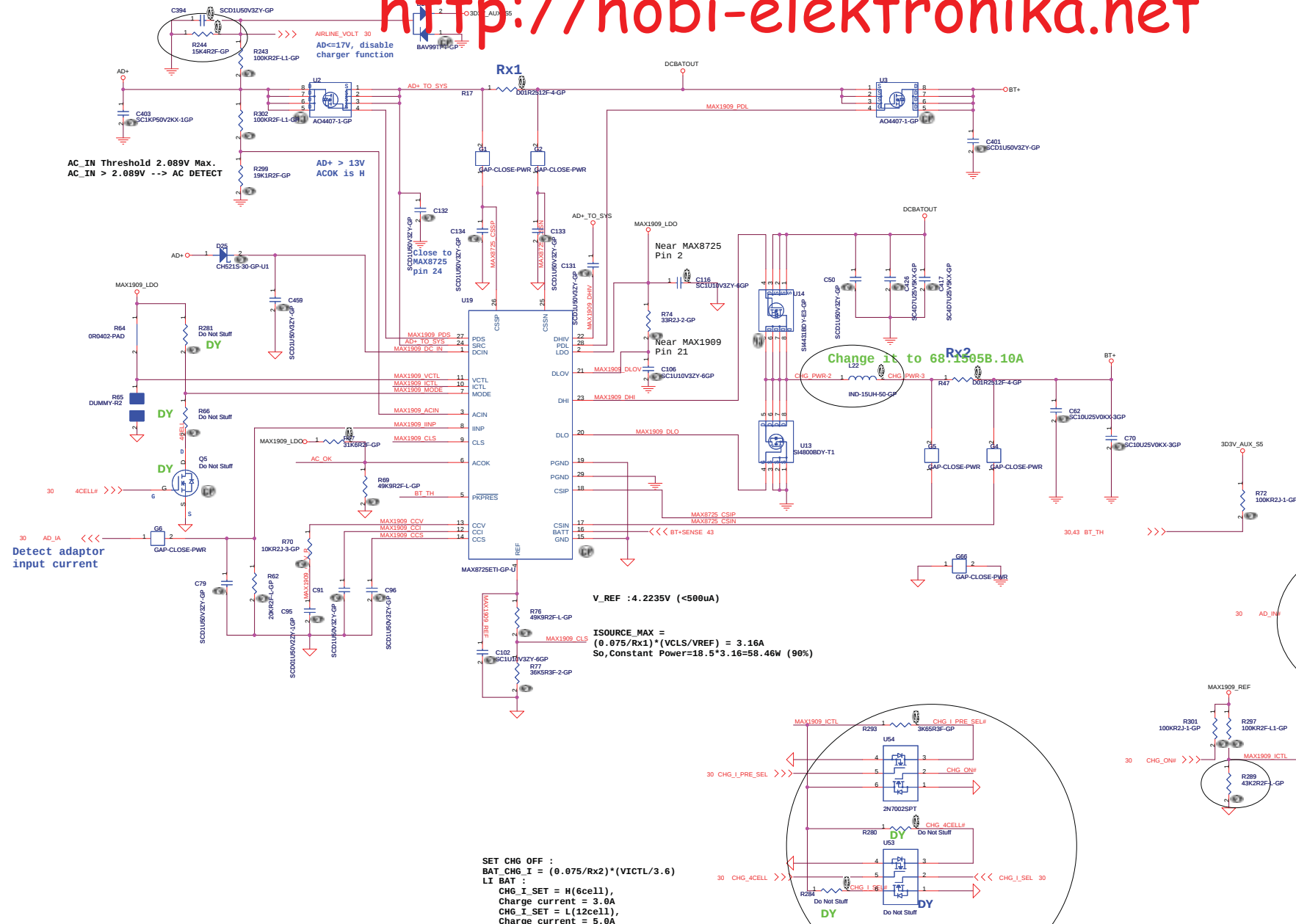
**PANASONIC/2V/150uF
18mOhm/2.5A**

**AOS/17mOhm
@4.5V/Qg=16nQ**

TPS51116 LGT







```
SET CHG OFF :
BAT_CHG_I = (0.075/Rx2)*(VICTL/3.6)
LI BAT :
    CHG_I_SET = H(6cell),
    Charge current = 3.0A
    CHG_I_SET = L(12cell),
    Charge current = 5.0A
```

Pre-Charge :
MAX8725 : CHG_I_PRE_SEL = H,
Pre-Charge current = 300mA

http://hobi-elektronika.net



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AD/BATT CONN

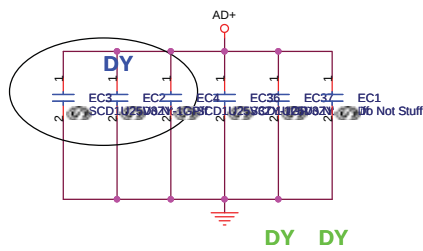
Document Number

TIBET

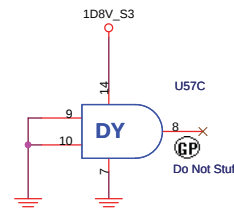
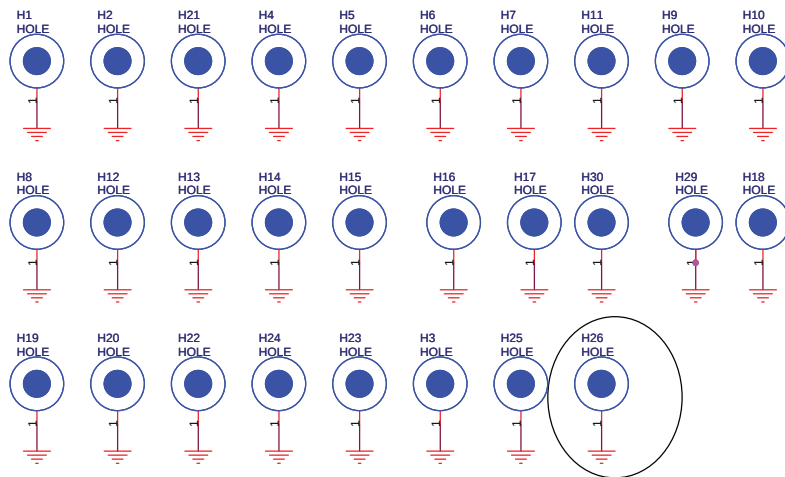
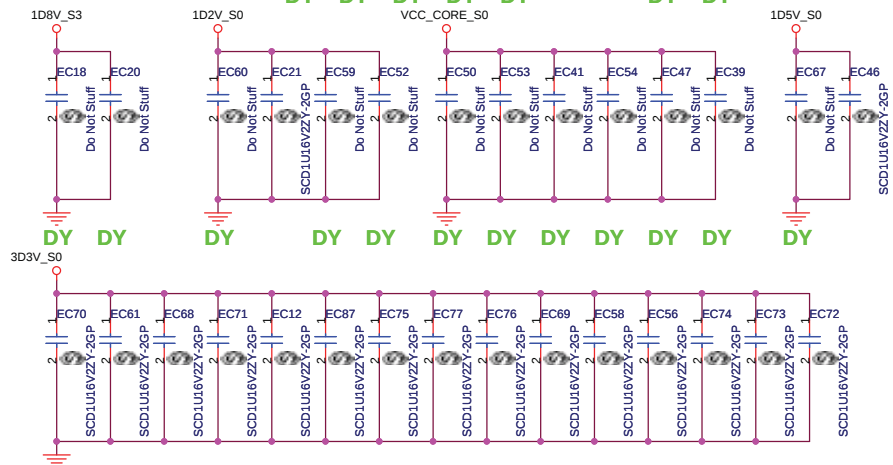
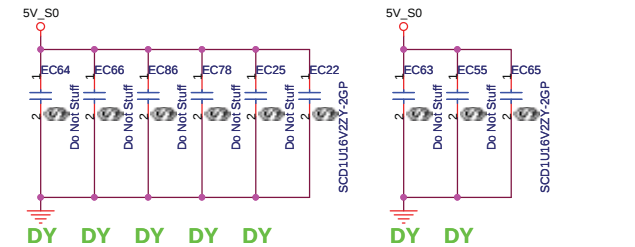
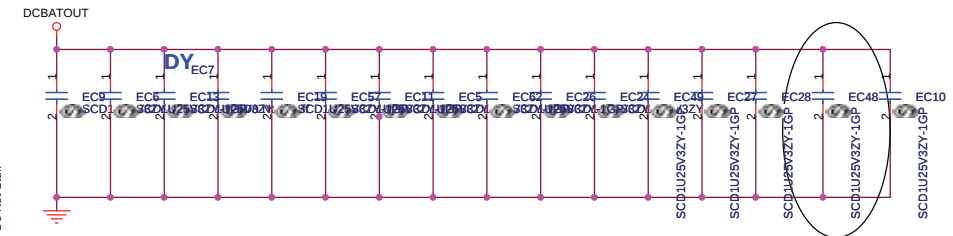
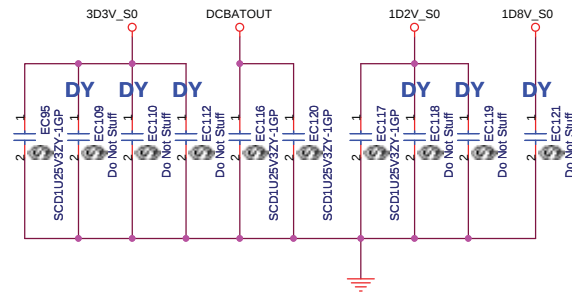
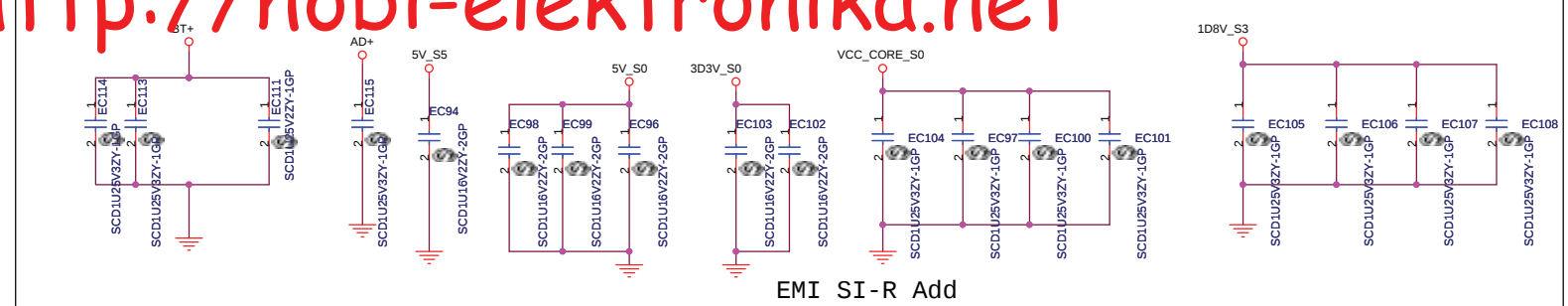
Date: Tuesday, August 14, 2007

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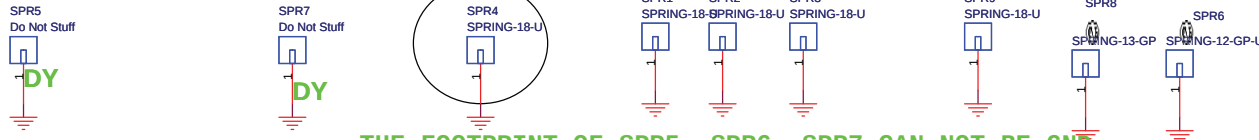
SA



	FF	DF
CAMERA	○	×
LAN	10/100	10/100
1394/5 IN 1	○	×
EXTRA USB	○	×
MIC	○	×
CR LED	○	×



Modify SPR7 P/N manually



THE FOOTPRINT OF SPR5, SPR6, SPR7 CAN NOT BE GND