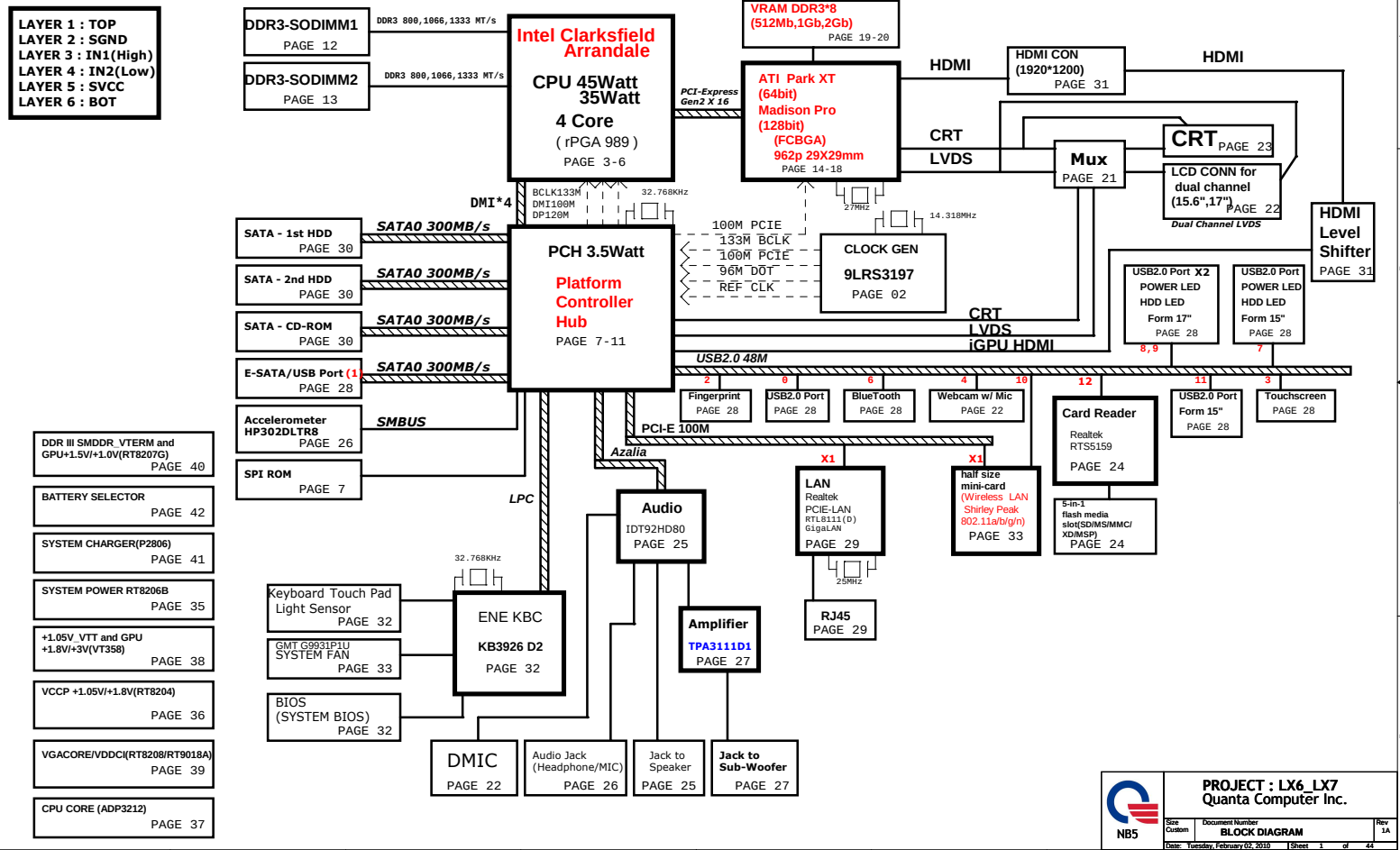
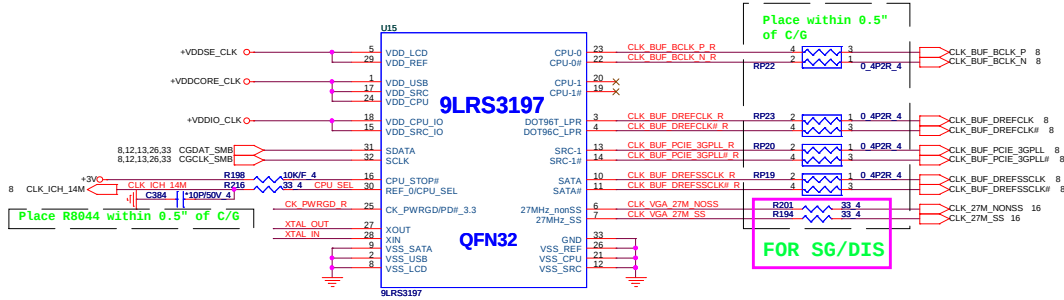
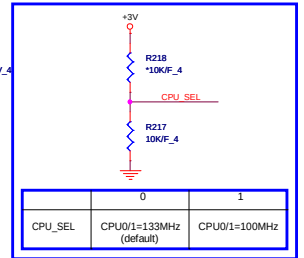
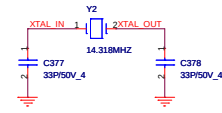
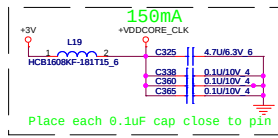
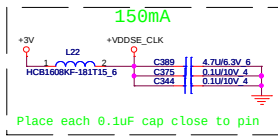
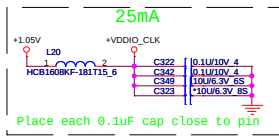


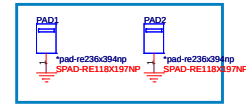
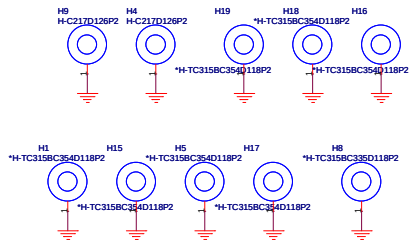
LX6/7 (Liverpool) BLOCK DIAGRAM



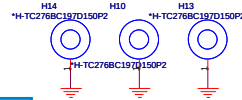


1.05V 7,8,9,11,36,37,43
1.5V 33,41
3V 3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,34,37,40,41

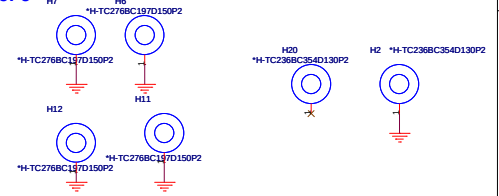
HOLE



GPU



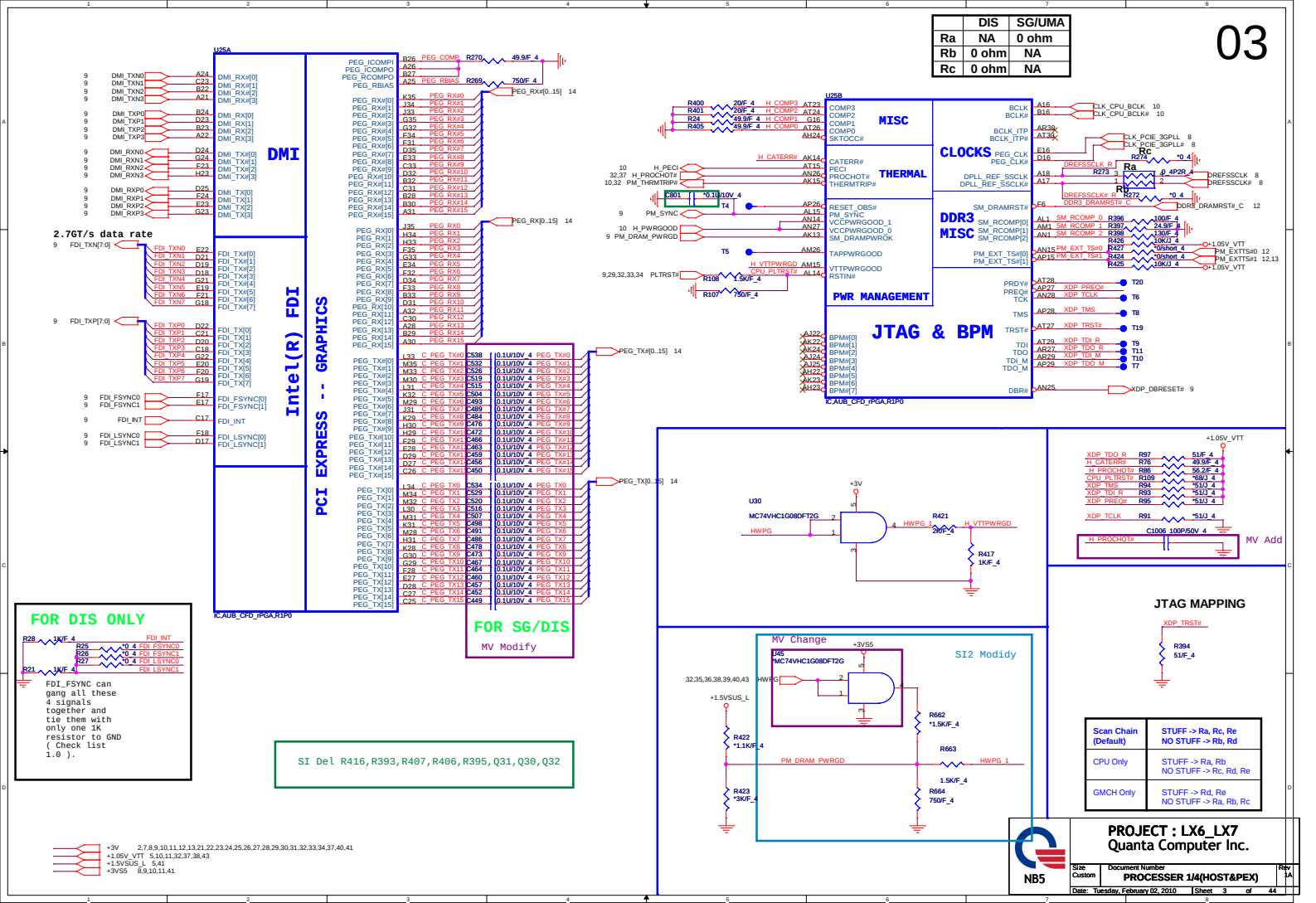
CPU

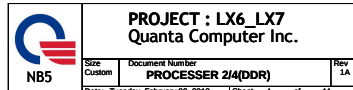


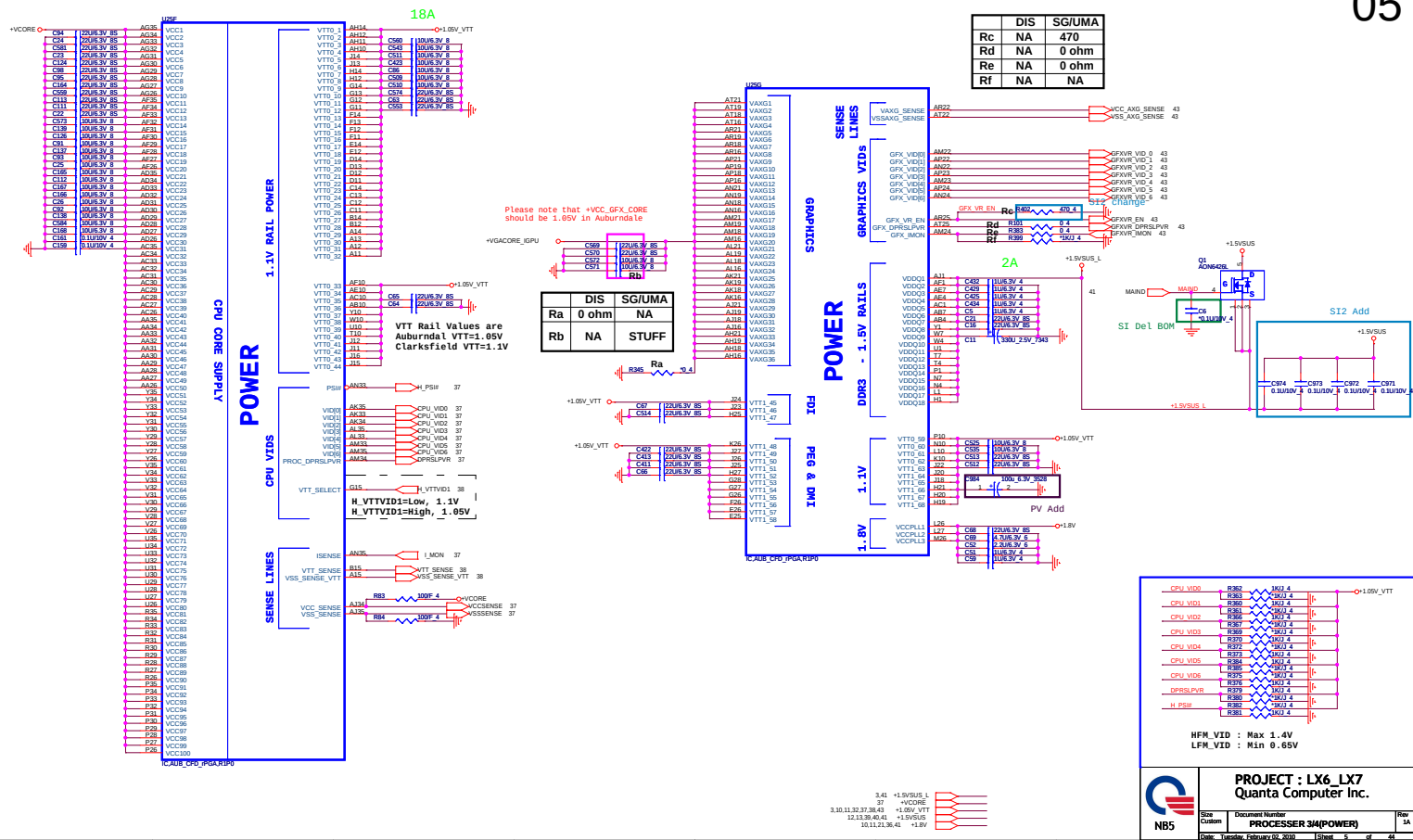
PROJECT : LX6 LX7
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Clock Gen(9LRS3197)HOLES	1A
Date: Tuesday, February 02, 2010	Sheet 2 of 44	

	DIS	SG/LMA
Ra	NA	0 ohm
Rb	0 ohm	NA
Rc	0 ohm	NA

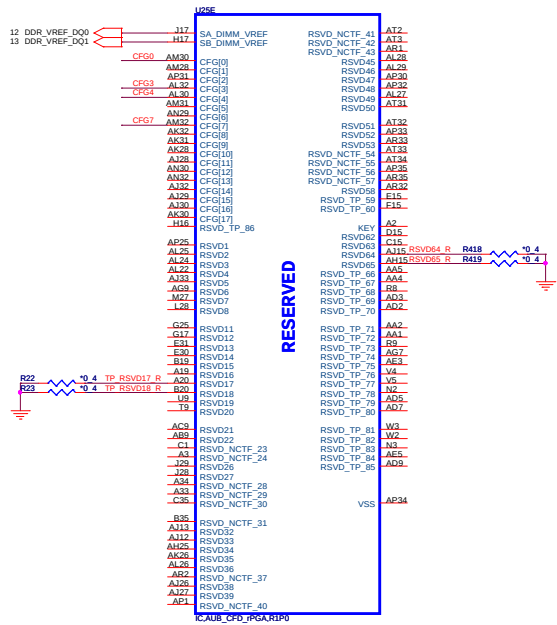
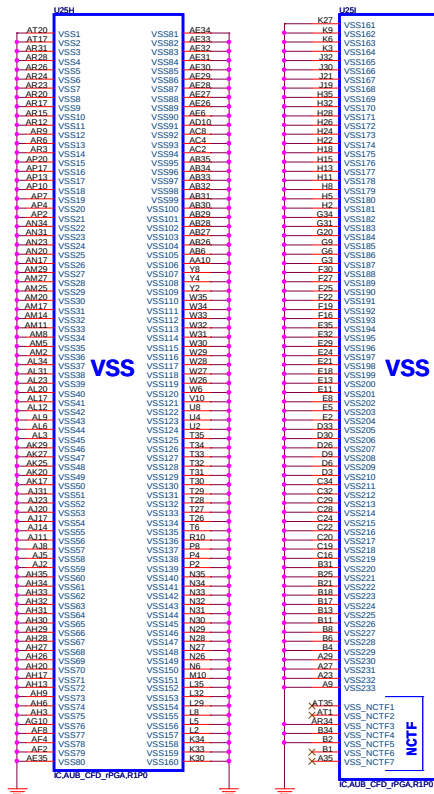






AUBURNDALE/CLARKSFIELD PROCESSOR (GND)

AUBURNDALE/CLARKSFIELD PROCESSOR(RESERVED, CFG)



The Clarkfield processor's PCI Express interface may not meet PCI Express 2.0 jitter specifications. Intel recommends placing a 3.01K +/- 5% pull down resistor to VSS on CFG[7] pin for both rPGA and BGA components. This pull down resistor should be removed when this issue is fixed.

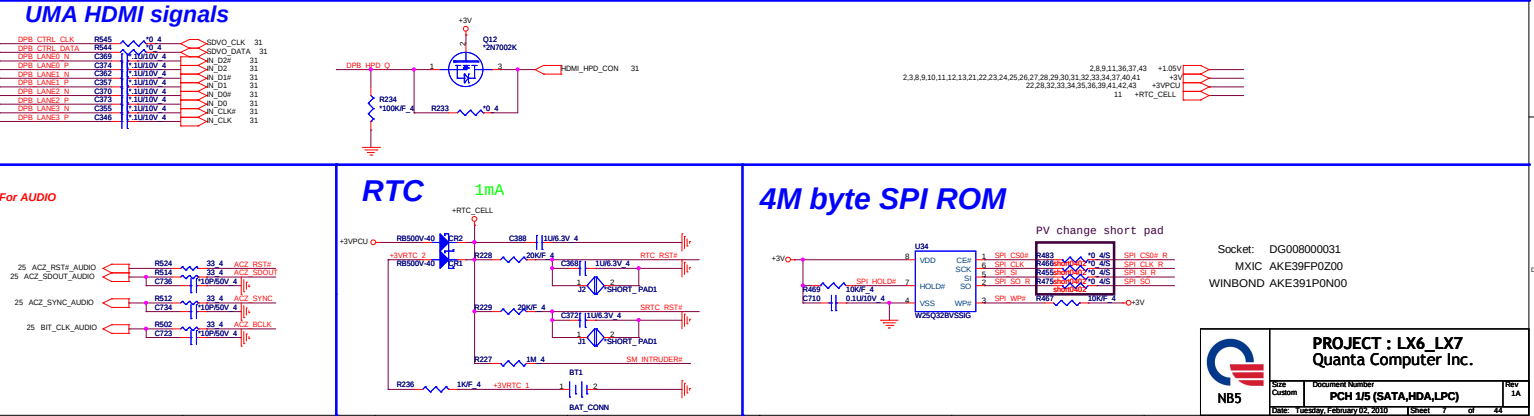
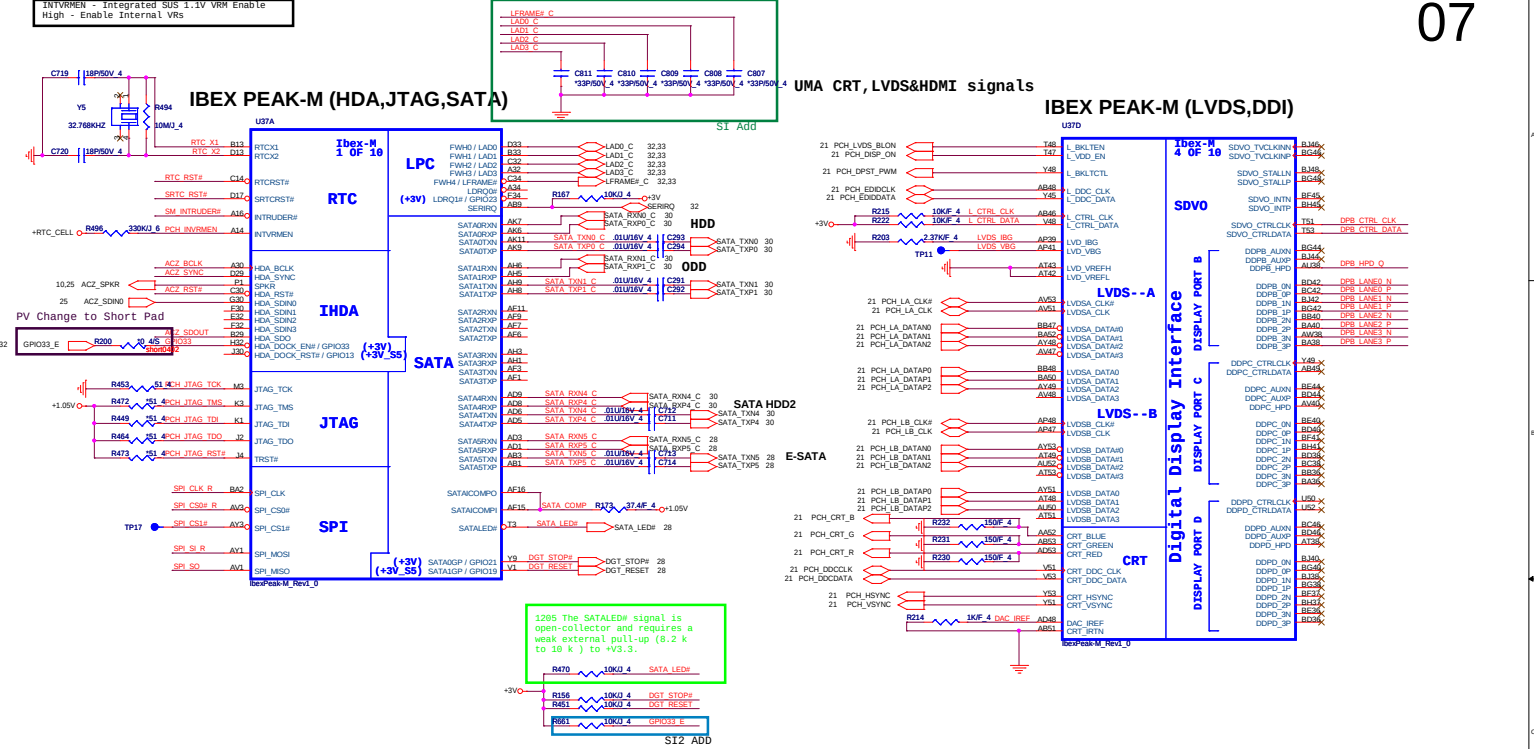
	1	0
CFG4 (Display Port Presence)	Disabled; No Physical Display Port attached to Embedded Display Port	Enabled; An external Display port device is connected to the Embedded Display port
CFG0 (PCI-Epress Configuration Select)	Single PEG	Bifurcation enabled
CFG3 (PCI-Epress Static Lane Reversal)	Normal Operation	Lane Numbers Reversed 15 -> 0, 14 -> 1

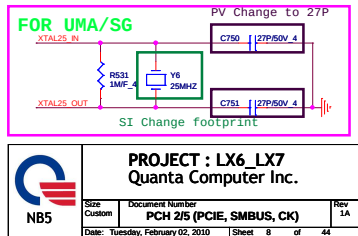
	UMA	SG/DIS
R90	NA	NA
R77	NA	3.01K
R79	NA	NA
R87	NA	NA

CFG[1:0] - PCI_Epress Configuration Select
 * 11= 1 x 16 PEG
 * 10= 2 x 8 PEG

PROJECT : LX6_LX7
 Quanta Computer Inc.

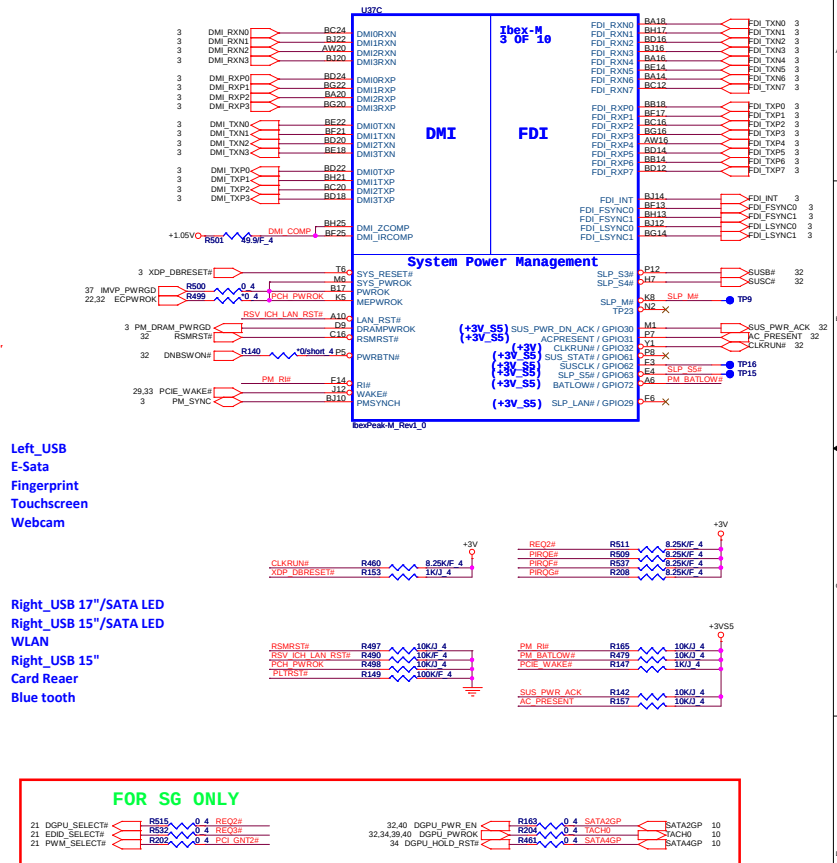
Size Custom Document Number
 Date: Tuesday, February 02, 2010 Sheet 6 of 44





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FOR SG ONLY

21 DGPU_SELECT# R515 0 4 REQ2# 32,40 DGPU_PWR_EN#
21 EDID_SELECT# R532 0 4 REQ3# 32,34,39,40 DGPU_PWRO#
21 PWM_SELECT# R202 0 4 PCI_GNT2# 34 DGPU_HOLD_RST#

2,3,7,8,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,34,37,40,41
2,7,8,11,36,37,43 +1.05V
+3V
3,8,10,11,41 +3VSS



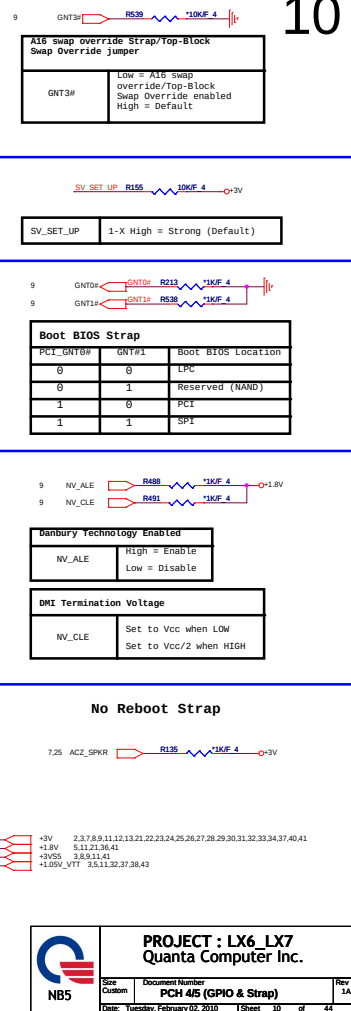
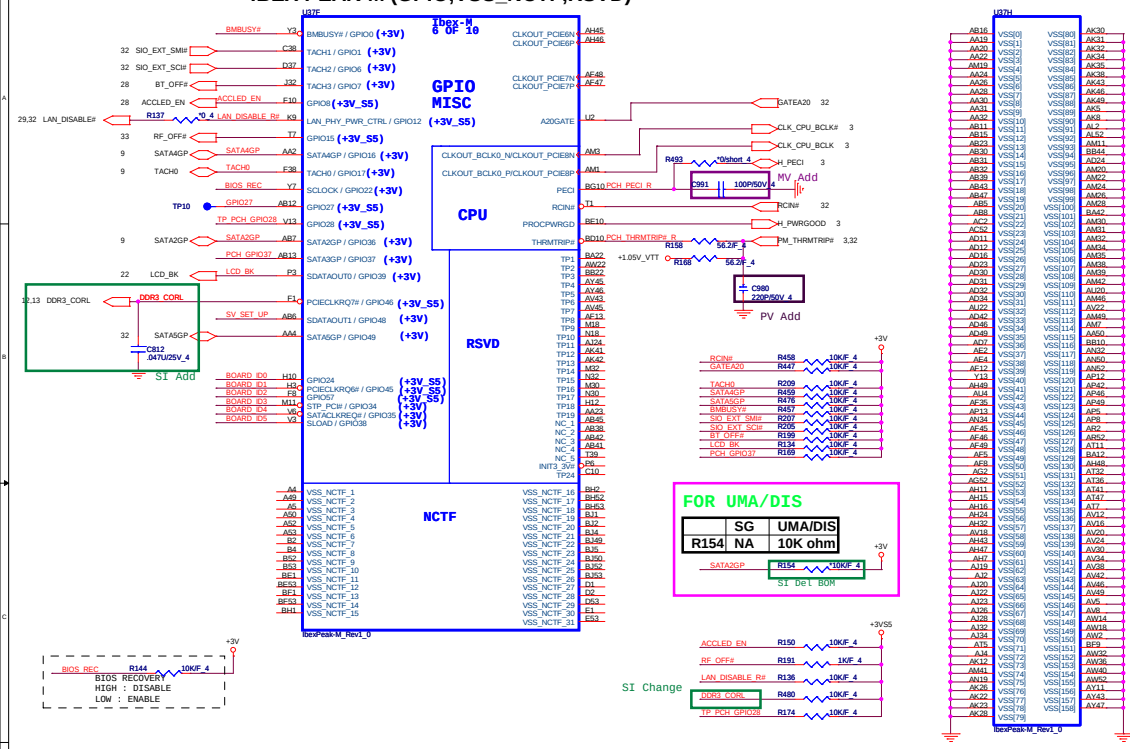
PROJECT : LX6_LX7
Quanta Computer Inc.

Size Custom	Document Number PCH 3/5 (PCI,ONFI,USB,DMI)	Rev 1A
Date: Tuesday, February 02, 2010 Sheet 9 of 44		

IBEX PEAK-M (GPIO,VSS_NCTF,RSVD)

IBEX PEAK-M (GND)

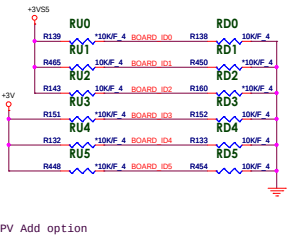
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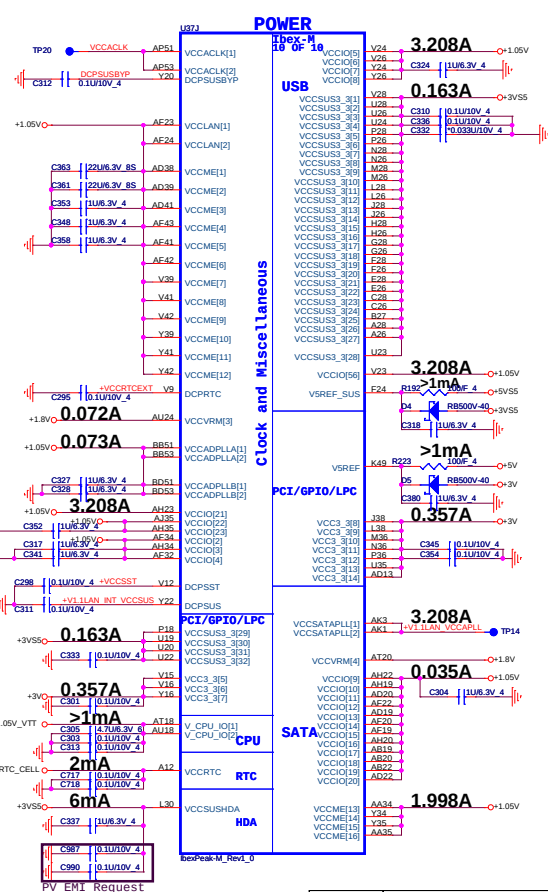
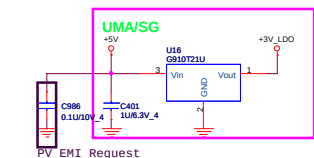
BOARD ID SETTING

Board ID	ID5	ID4	ID3	ID2	ID1	ID0
TBD	RD5 (0)	RD4 (0)	RD3 (0)	RD2 (0)	RD1 (0)	RD0 (1)
TBD	RD5 (0)	RD4 (0)	RD3 (0)	RD2 (0)	RD1 (1)	RD0 (1)
TBD	RD5 (0)	RD4 (0)	RD3 (0)	RD2 (1)	RD1 (1)	RD0 (1)
TBD	RD5 (0)	RD4 (0)	RD3 (0)	RD2 (1)	RD1 (0)	RD0 (1)
TBD	RD5 (0)	RD4 (0)	RD3 (0)	RD2 (1)	RD1 (1)	RD0 (1)
TBD	RD5 (0)	RD4 (0)	RD3 (0)	RD2 (1)	RD1 (1)	RD0 (1)
TBD	RD5 (0)	RD4 (0)	RD3 (0)	RD2 (1)	RD1 (1)	RD0 (1)

Board ID	ID0	ID1	ID2	ID3	ID4	ID5
LX6	0	1				
LX7	1	0				
Discrete			0	0		
UMA			0	1		
SG			1	0		
LX7_No Subwoofer	1	1				



Set	Document Number	Rev
NBS	PCN 405 (GPIO & Strap)	1A
Date	Tuesday, February 13, 2010	1 of 44



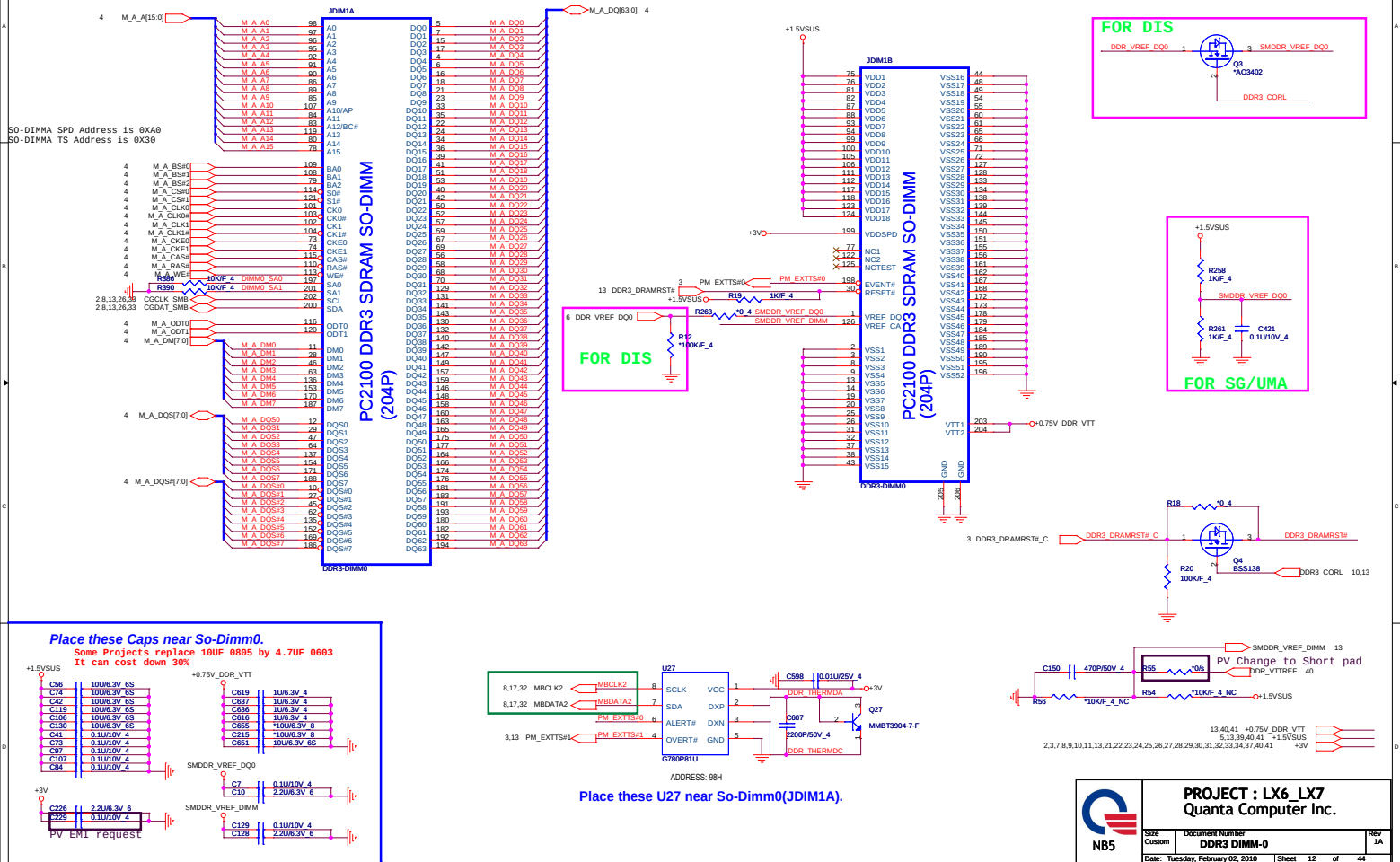
	DIS	SG/UMA
Ra	0 ohm	NA
Rb	NA	0 ohm
Rc	0 ohm	NA
Rd	NA	0 ohm

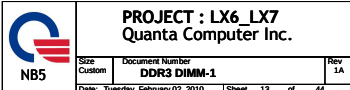
	2,7,8,9,36,37,43	+1.05V	
	3,5,10,32,37,38,43	+1.05V_VTT	
	5,10,21,36,41	+1.8V	
2,3,7,8,9,10,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,34,37,40,41	+3V		
	3,8,9,10,41	+3VSS	
	21,22,23,25,26,28,30,31,33,34,41	+3V	
	41	+5VSS	
7	+RTC_CELL		

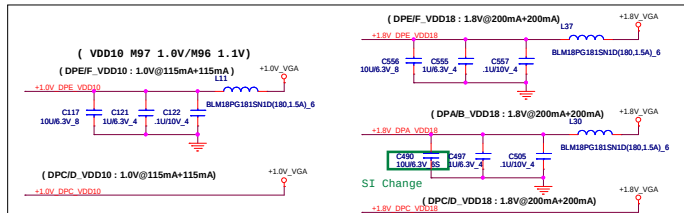
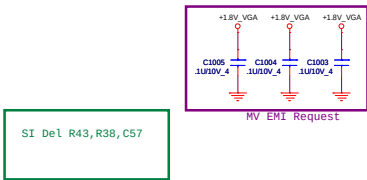
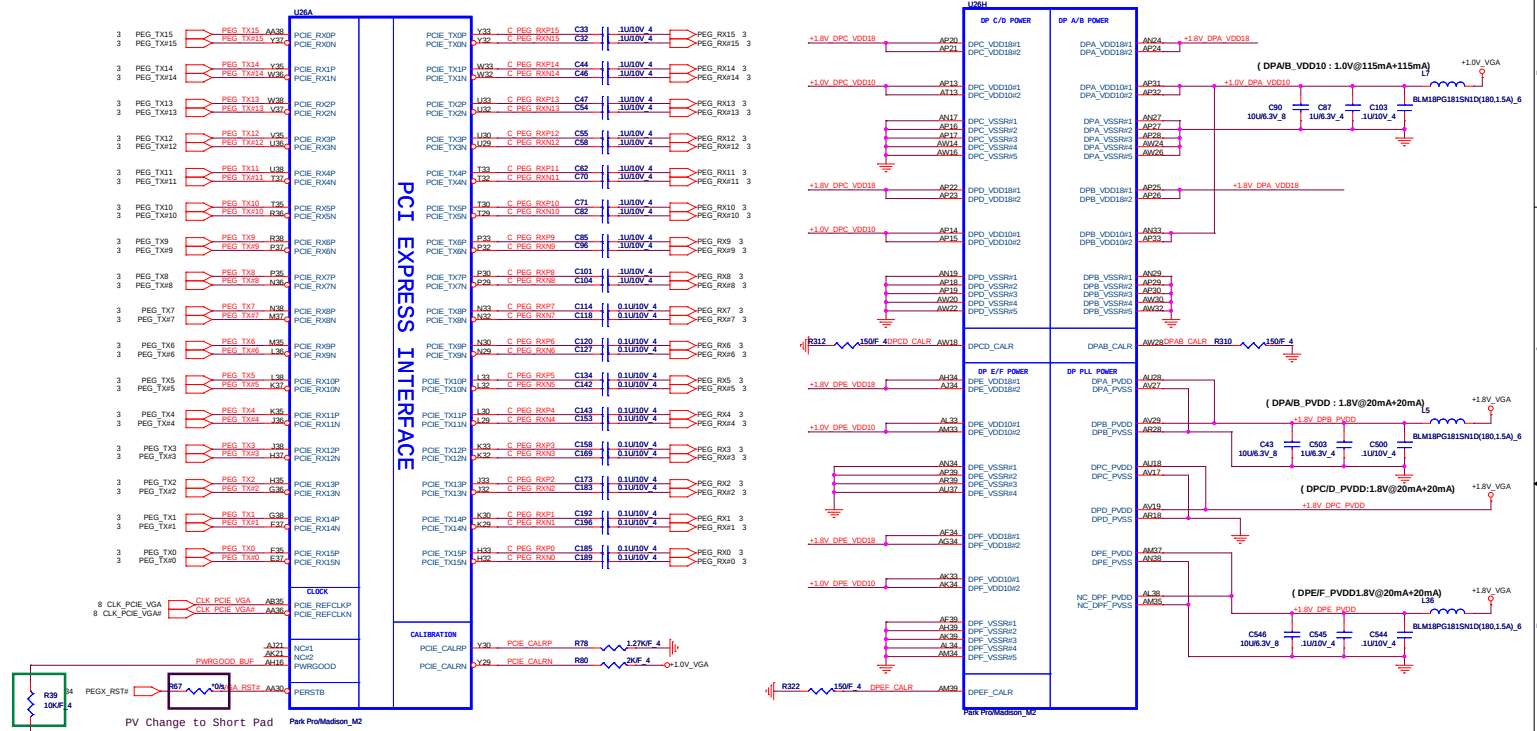


PROJECT : LX6_LX7
Quanta Computer Inc.

Size Custom	Document Number PCH 5/5 (POWER)	Rev 1A
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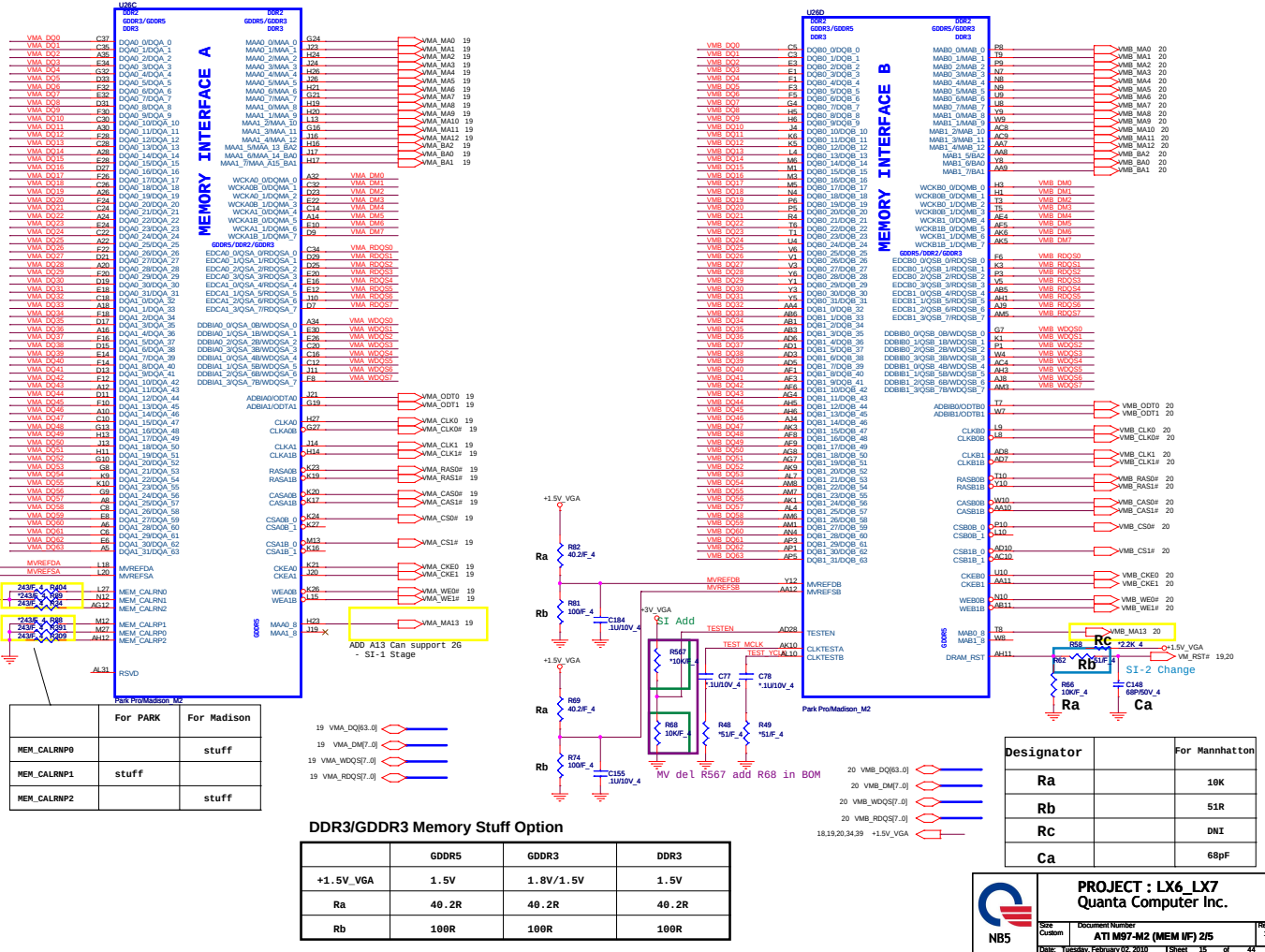






 +3V 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,34,37,40,41
 +1.0V_VGA 16,18,34,40
 +1.8V_VGA 16,18,34,39

 NB5	PROJECT : LX6_LX7 Quanta Computer Inc.		
	Size Custom	Document Number ATI M97-M2 (PCIE IF) 1/5	Rev 1A
Date: Tuesday, February 02, 2010 Sheet 14 of 44			

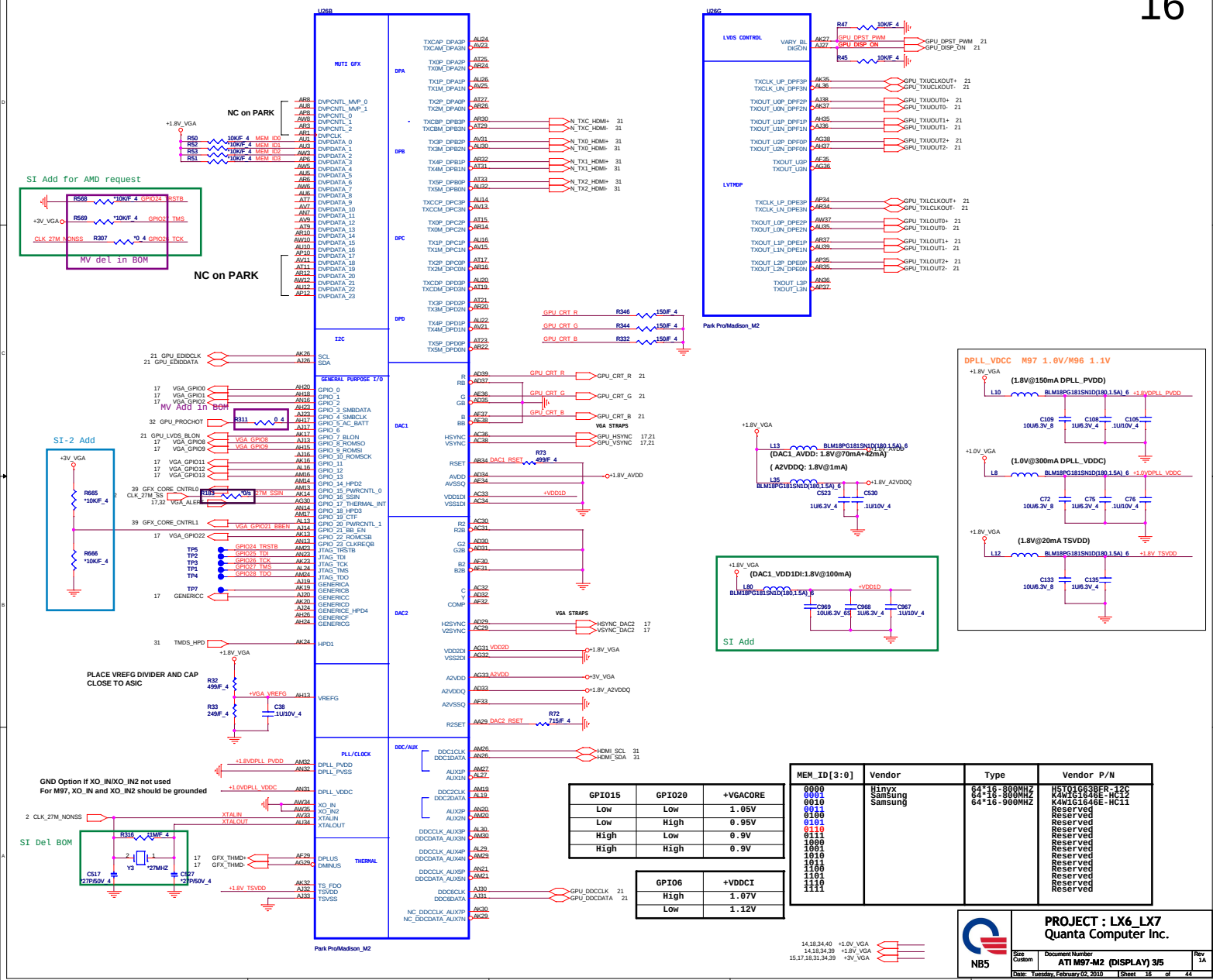


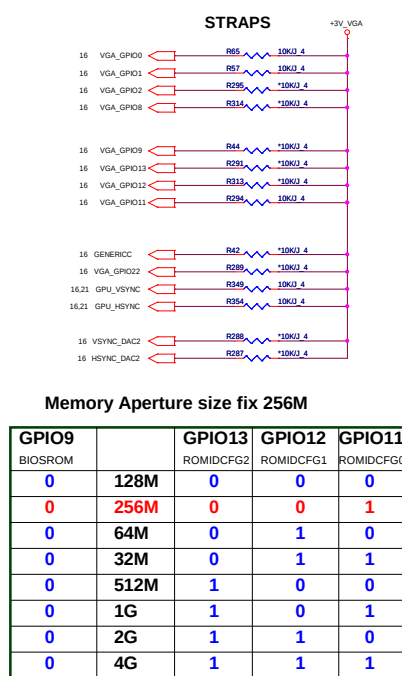
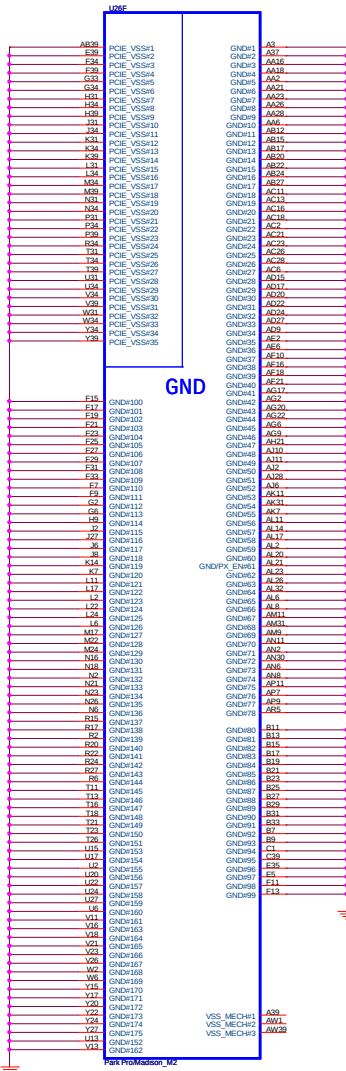
PROJECT : LX6_LX7
Quanta Computer Inc.

Rev | **Document Number** | **Rev**

Rev | **Document Number** | **Rev**

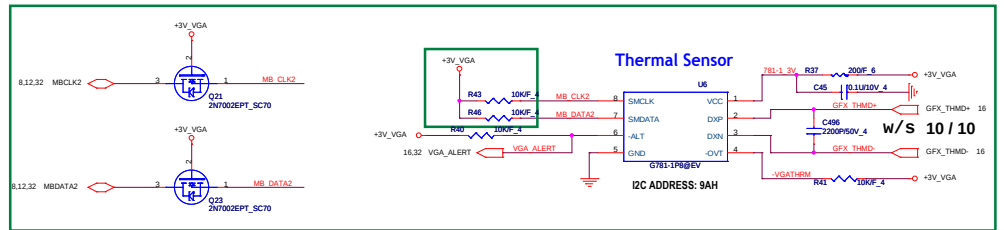
Use: Tuesday, February 02, 2010 | Sheet 15 of 44

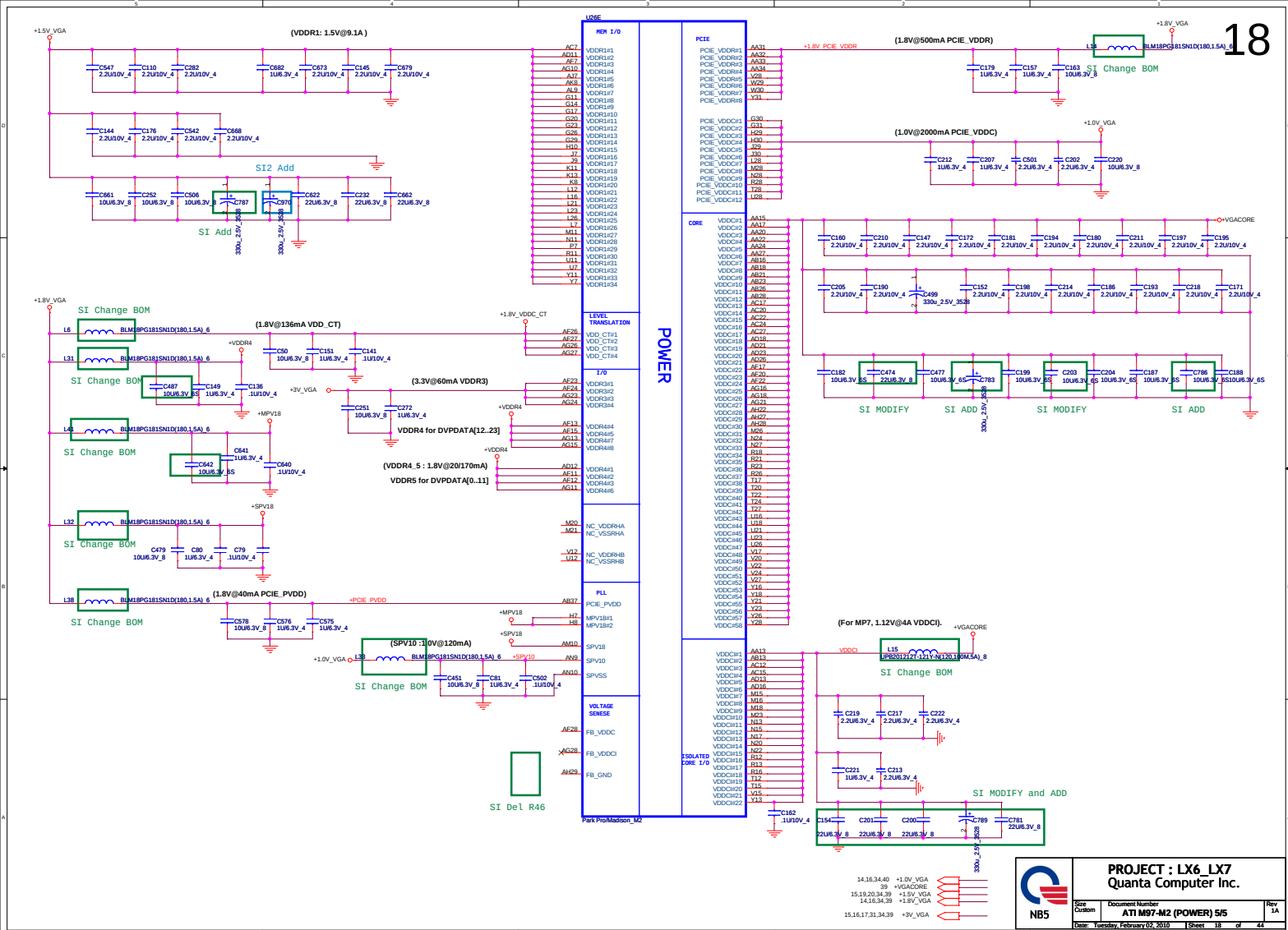




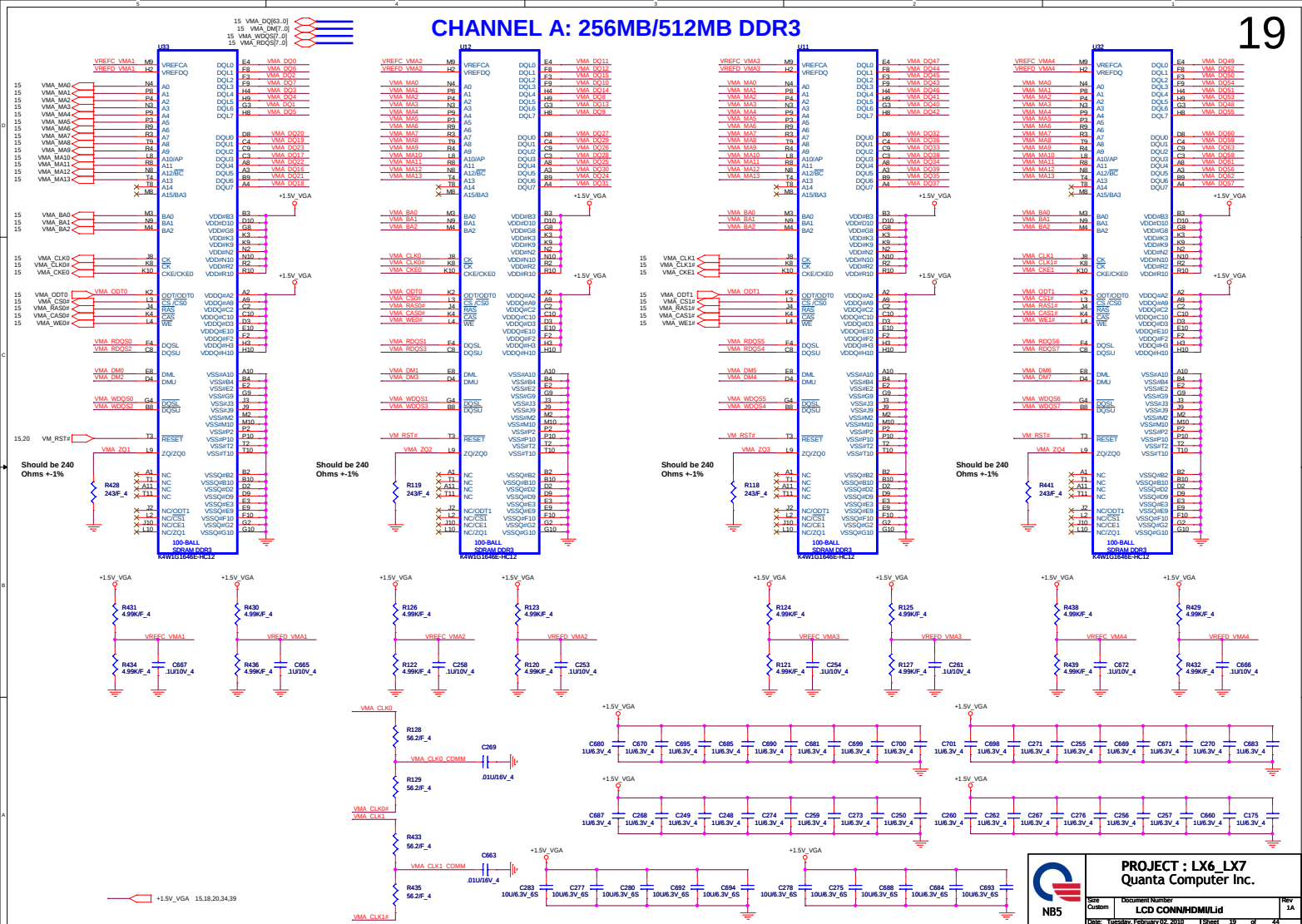
CONFIGURATION STRAPS			RECOMMENDED SETTINGS
ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET			0 = DO NOT INSTALL RESISTOR 1 = INSTALL 10K RESISTOR X = DESIGN DEPENDANT NA = NOT APPLICABLE
STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	
TX_PWRM_ENB	GPIO0	Transmitter Power Savings Enable 0 50% Tx output swing for mobile mode 1 Full Tx output swing (Default setting for Desktop)	1
TX_DEEMPH_EN	GPIO1	PCI Express Transmitter De-emphasis Enable 0 Tx de-emphasis disabled for mobile mode 1 Tx de-emphasis enabled (Default setting for Desktop)	1
BIF_GEN2_EN_A	GPIO2	0 = Advertises the PCI-E device as 2.5 GT/s capable at power-on. 1 = Advertises the PCI-E device as 5.0 GT/s capable at power-on. 1.5 GT/s capability will be controlled by software.	0
RSVD	GPIO8	VGA ENABLED	0
BF_VGA_DIS	GPIO9		0
RSVD	GPIO21		0
BIOS_ROM_EN	GPIO 22, ROMCFG8	ENABLE EXTERNAL BIOS ROM	0
ROMIDCFG2(9)	GPIO13(11)	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT	0 0 1
VIP_DEVICE_STRAP_ENA	V2SYN	IGNORE VIP DEVICE STRAPS	0
RSVD	GENERIC		0
AUD[0]	HSYN	AUD[0] AUD[0]	0
AUD[9]	VSYN	0 0 No audio function 0 1 Audio for DisplayPort and HDMI if dongle is detected 1 0 Audio for DisplayPort only 1 1 Audio for both DisplayPort and HDMI	11
AMD RESERVED CONFIGURATION STRAPS			
ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET			
HSYN			
PULLUP PADS ARE NOT REQUIRED FOR THESE STRAPS BUT IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET			
GPIO21_BB_EN			

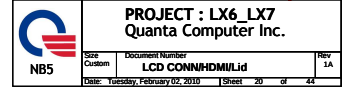
It is a shared pin strap with CONF16[2:0] if BIOS_ROM_EN is set to 0.

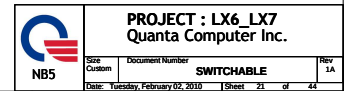
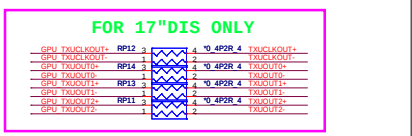
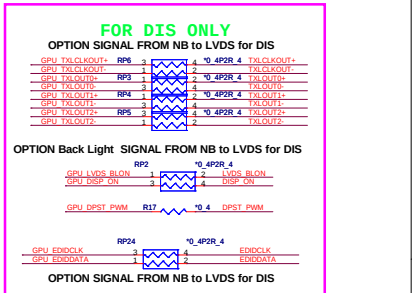
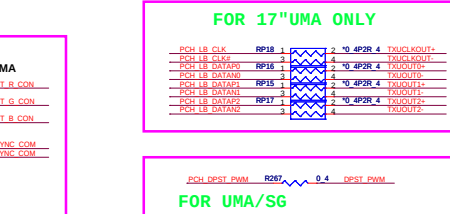
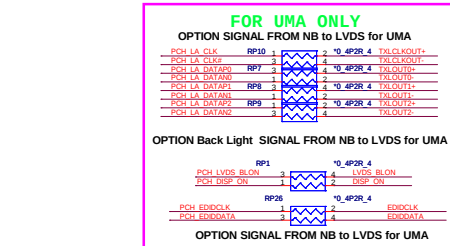
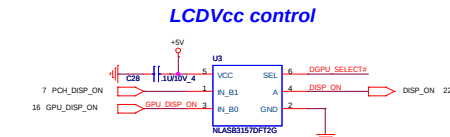
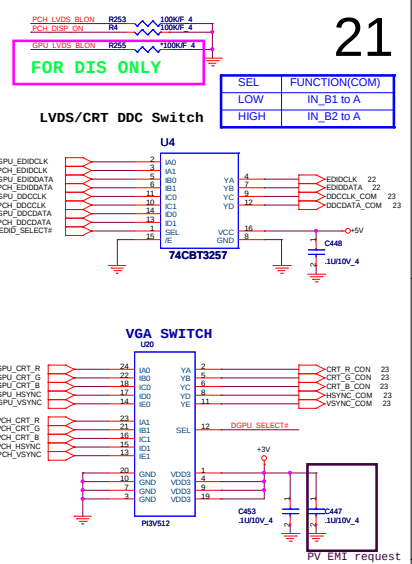
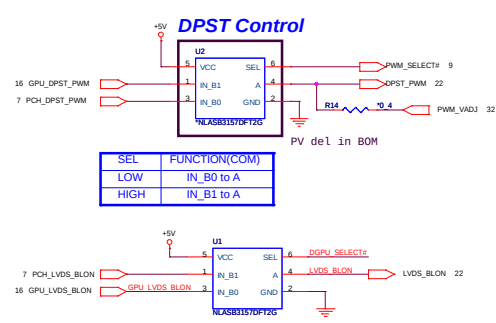
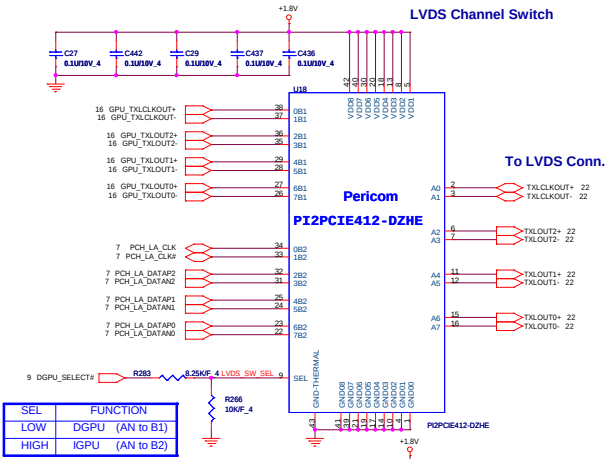


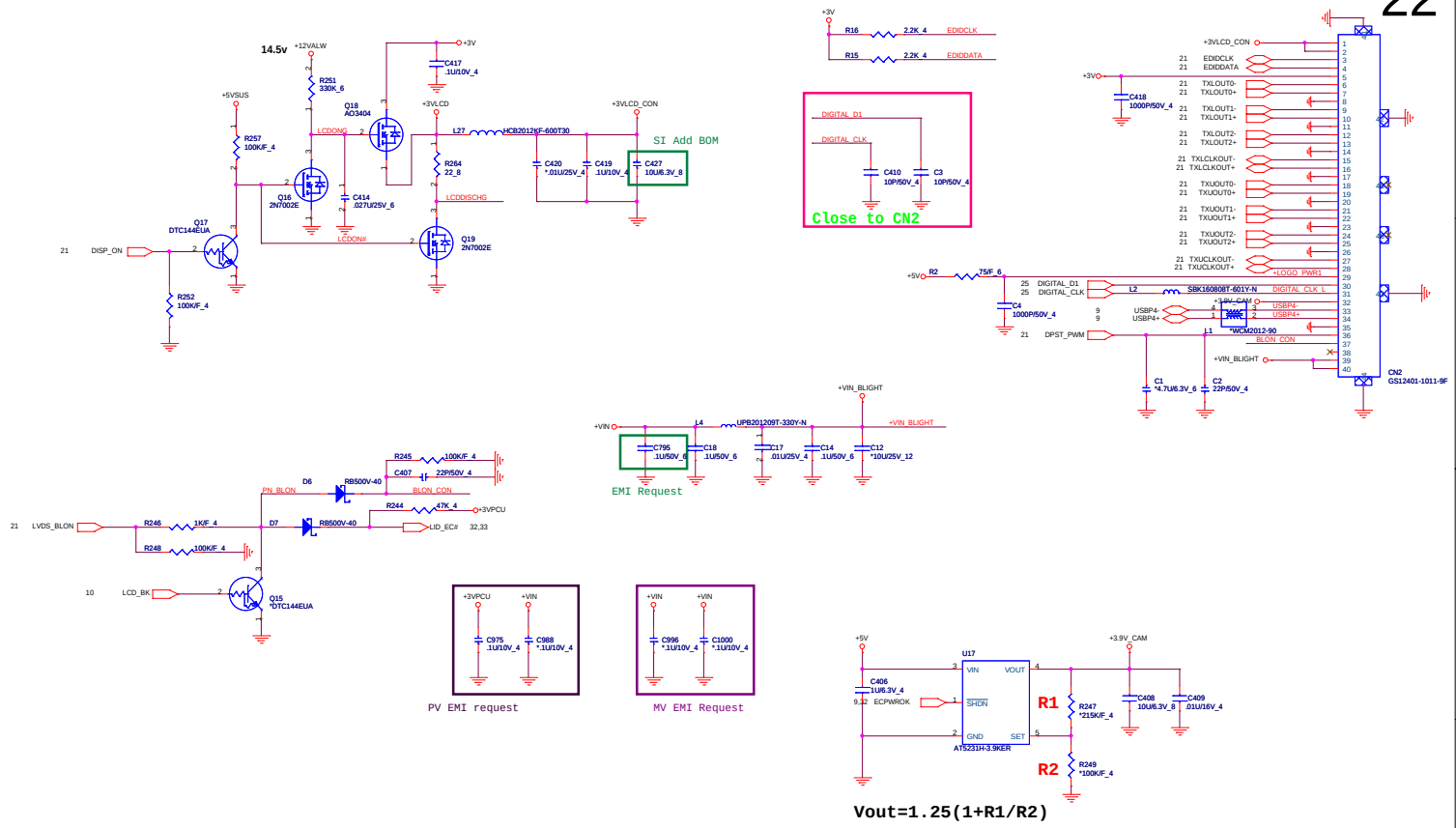


CHANNEL A: 256MB/512MB DDR3



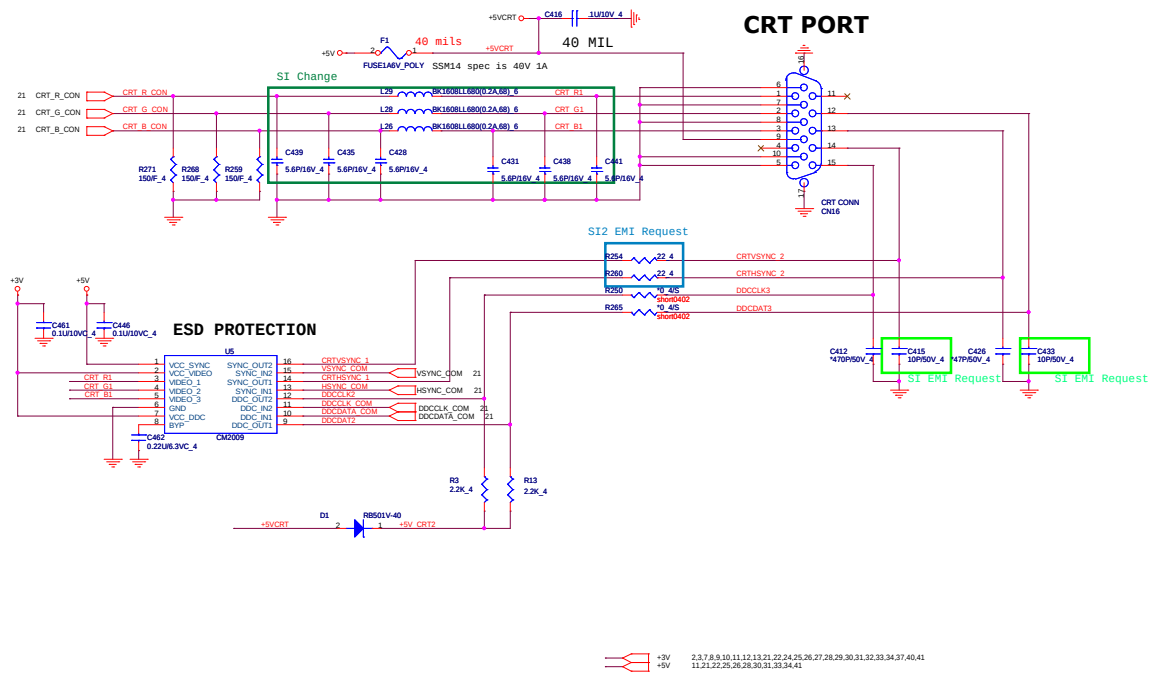


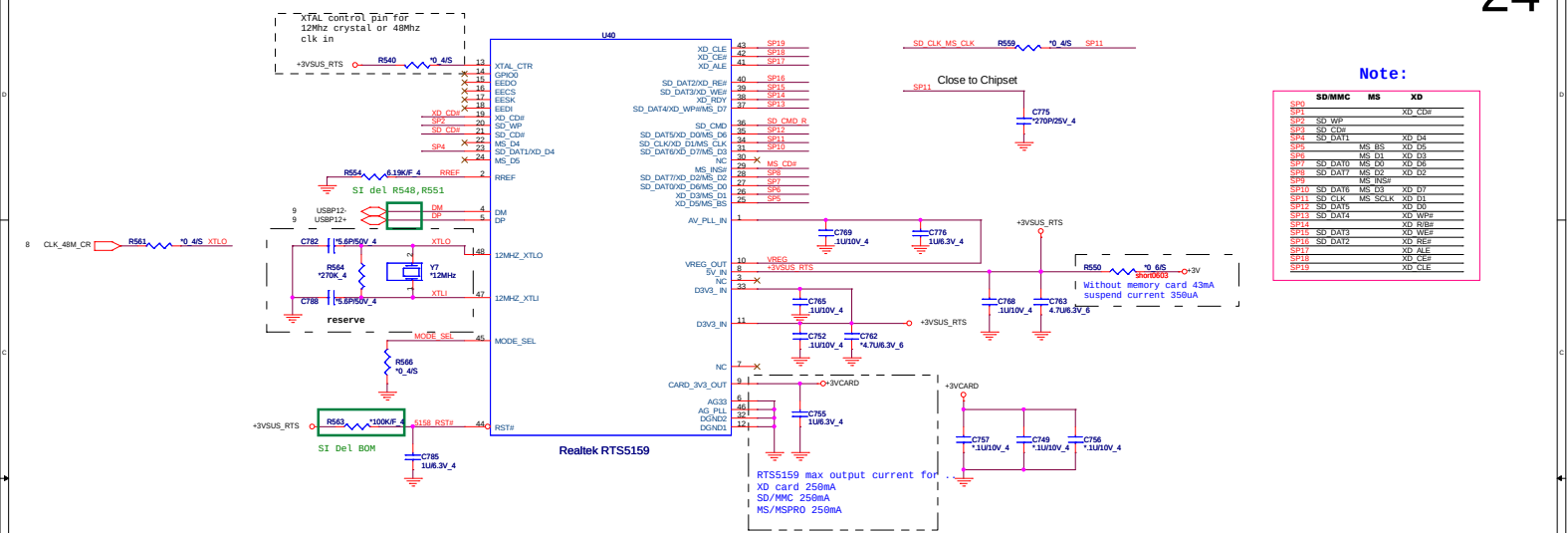




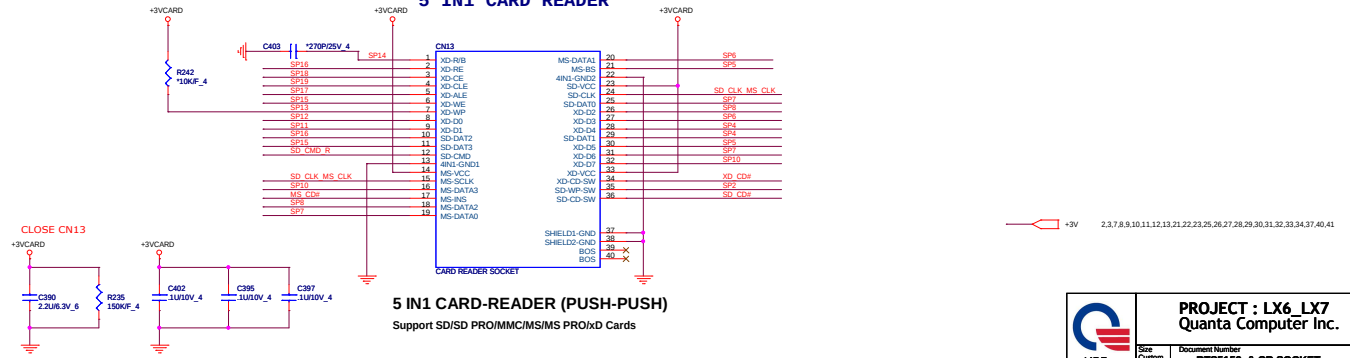
2,3,7,8,9,10,11,12,13,21,23,24,25,26,27,28,29,30,31,32,33,34,37,40,41 +3V
11,21,23,25,26,28,30,31,33,34,41 +5V
27,35,36,37,39,40,41,42,43 +VIN
7,28,32,33,34,35,36,39,41,42,43 +3VPCU
30,34,39,41 +12VALW

 NB5	PROJECT : LX6_LX7 Quanta Computer Inc.		
	Size Custom	Document Number LCD CONN1ID function	
Date: Tuesday, February 02, 2010		Sheet 22 of 44	





**XD, MMC/SD, MS/MSP
5 IN1 CARD READER**



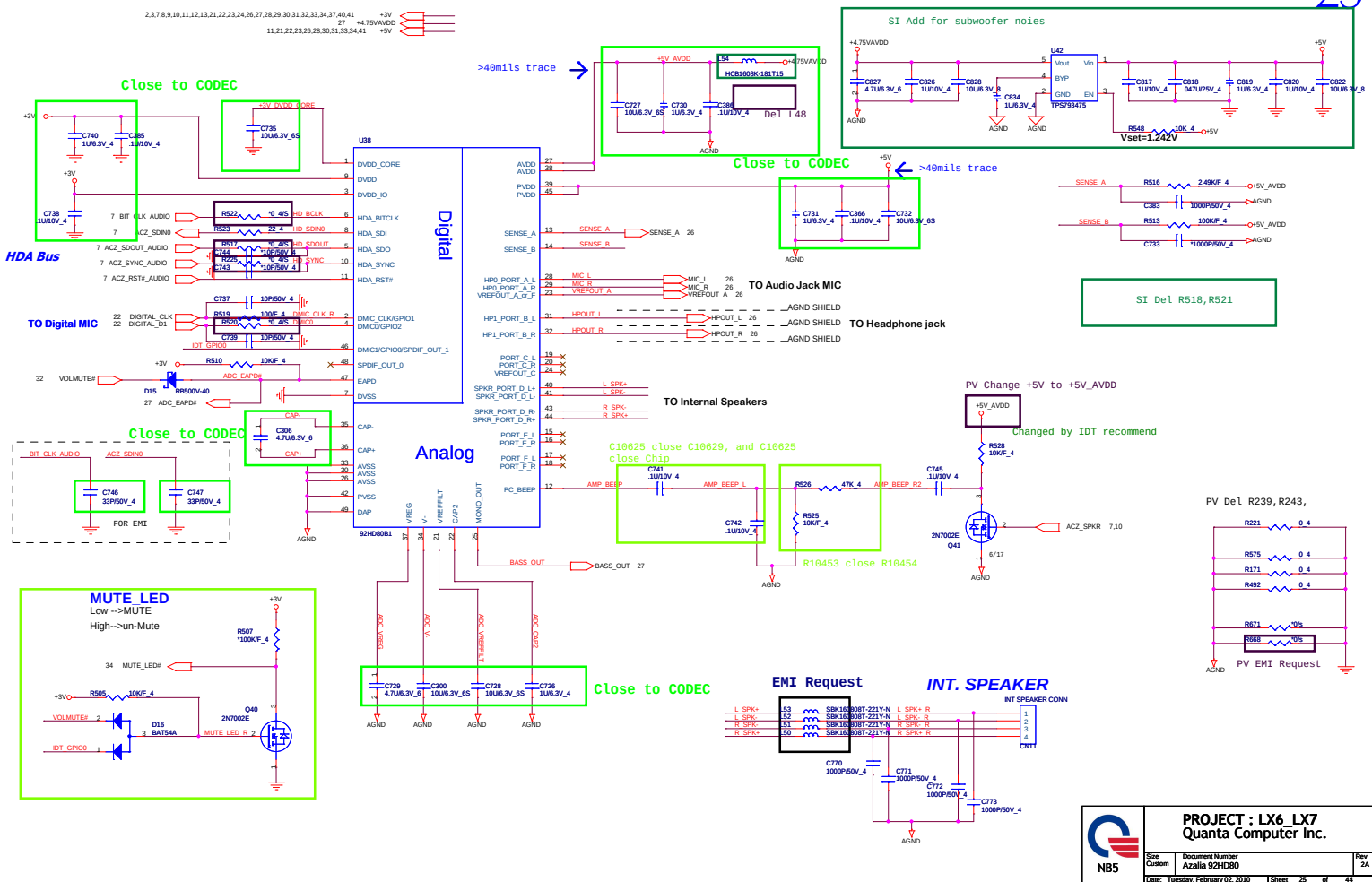
5 IN1 CARD-READER (PUSH-PUSH)
Support SD/SD PRO/MMC/MS/MS PRO/xD Cards



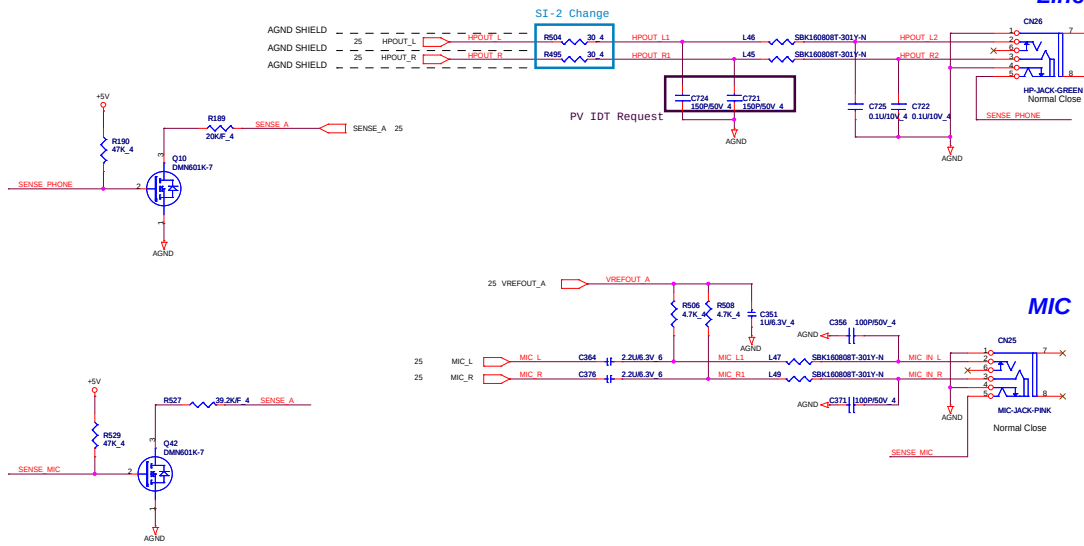
PROJECT : LX6_LX7
Quanta Computer Inc.

Size Custom	Document Number RTS5159 & CR SOCKET	Rev 1A
Date: Tuesday, February 02, 2010		Sheet 24 of 44

2,3,7,8,9,10,11,12,13,21,22,23,24,26,27,28,29,30,31,32,33,34,37,40,41 +3V
27 +4.75VAVDD
11,21,22,23,26,28,30,31,33,34,41 +5V

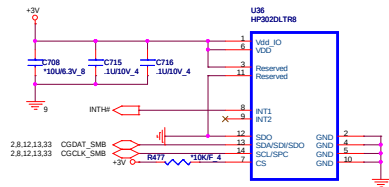


Line out



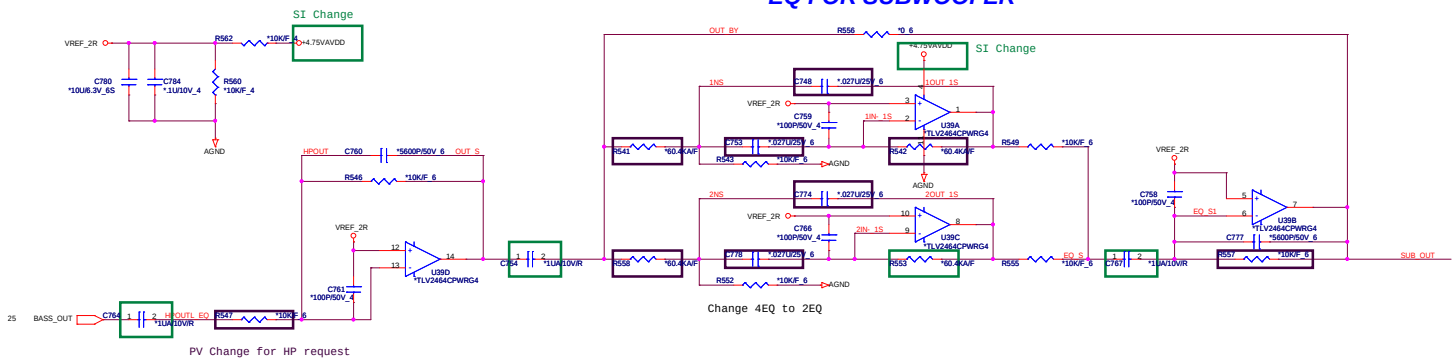
Accelerometer Sensor

SGT-LIS02DLTR interrupt pin default
is low / active Hi , BIOS need to
programming 22h to change status
from active Hi to low

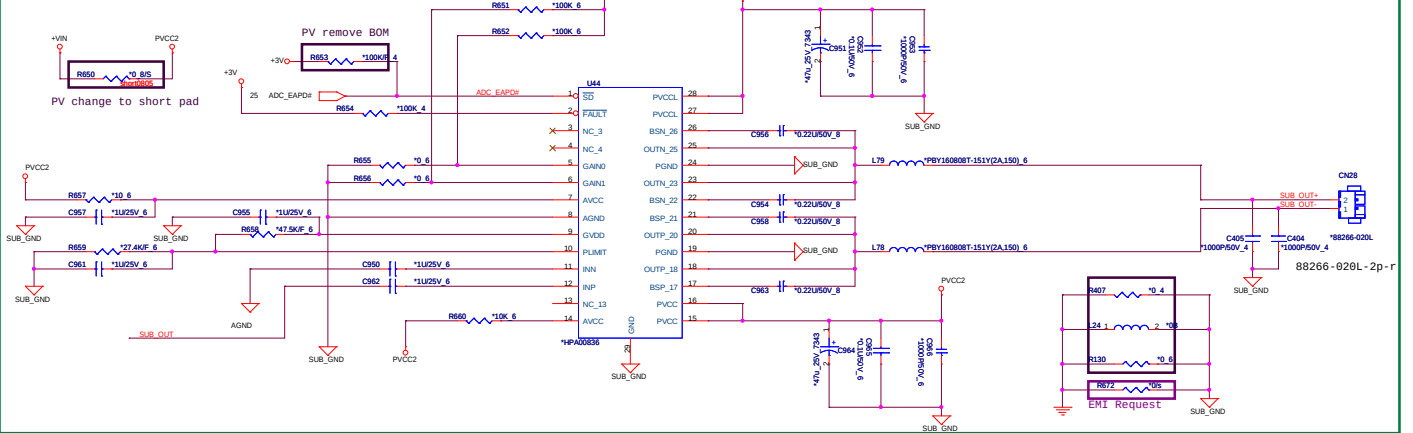


Pin 12: Low	38hex
Pin 12: unconnected/floating	3Ahex

 NB5	PROJECT : LX6_LX7 Quanta Computer Inc.		
	Size Custom	Document Number Audio Jack/Accelerometer	Rev 1A
Date: Tuesday, February 02, 2010 Sheet 26 of 44			



Sub-Woofers power



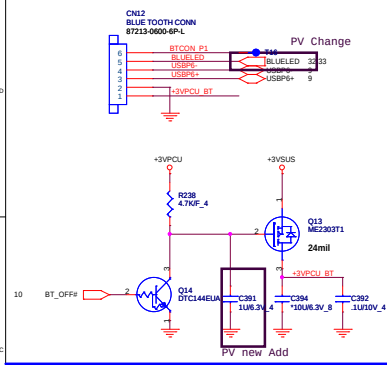
GAIN1	GAIN0	dB
0	0	12
0	1	18
1	0	23.6
1	1	36

PV Del R574,R241,R240

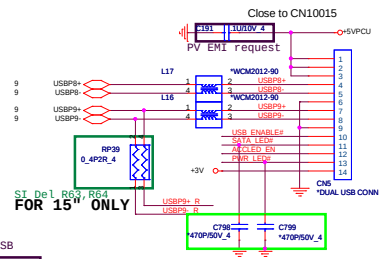


3V 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,28,29,30,31,32,33,34,37,40,41
 4.75VAVDD 25
 VIN 22,35,36,37,39,40,41,42,43

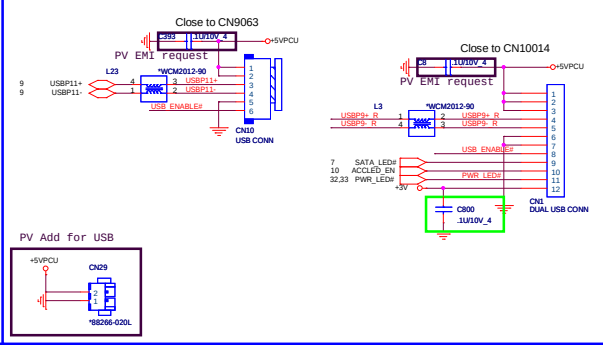
BLUETOOTH



RIGHT SIDE USBX2 for 17"



RIGHT SIDE USB for 15"



T: Stuffed for RTL8111DL(10/100/1000)

for RTL8111DL use: close Pin 44, 45

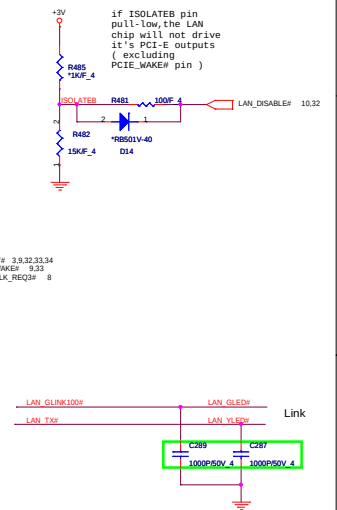
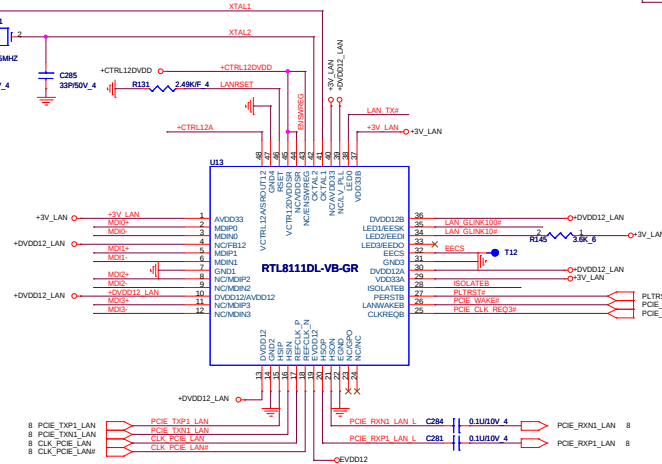
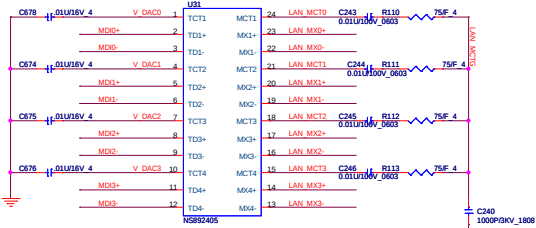
Remove R3571, R3573

R3571 and R3573 are used in RTL8111DL, remove R3573 if switching regulator is enable, Remove R3571 external power is used.

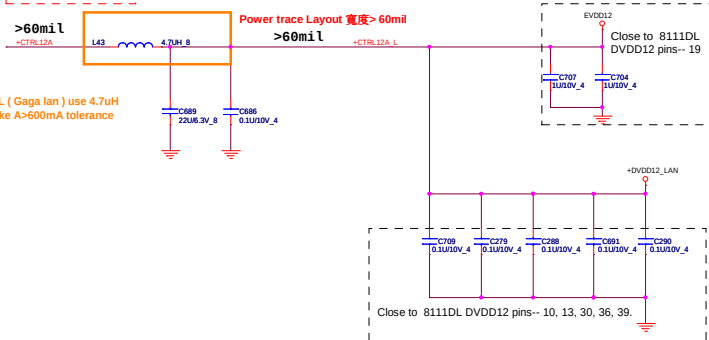
SI del R130
+3V_LANVCC
C284 0.1uF/0V_4
C285 0.1uF/0V_4
C283 0.1uF/0V_4
C286 0.1uF/0V_4
Close to PIN 1

+3V
if ISOLATEB pin pull-low, the LAN chip will not drive it's PCI-E outputs (excluding PCI-E_WAKE# pin)
R485 10K/4
R481 100F/4
R482 15K/4
LAN_DISABLE# 10,32

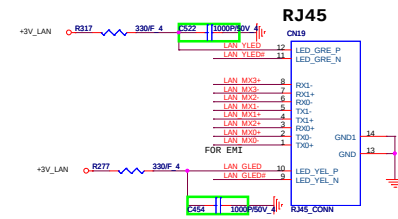
AL08111DB00 RTL8111DL-GR



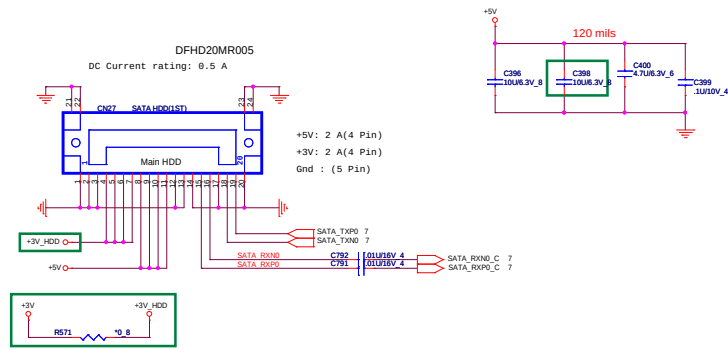
NS892402:GIGABIT



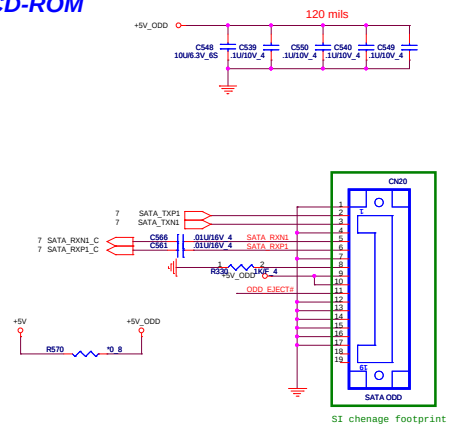
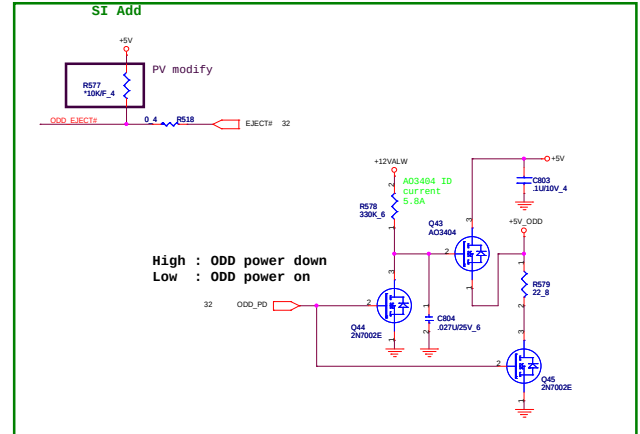
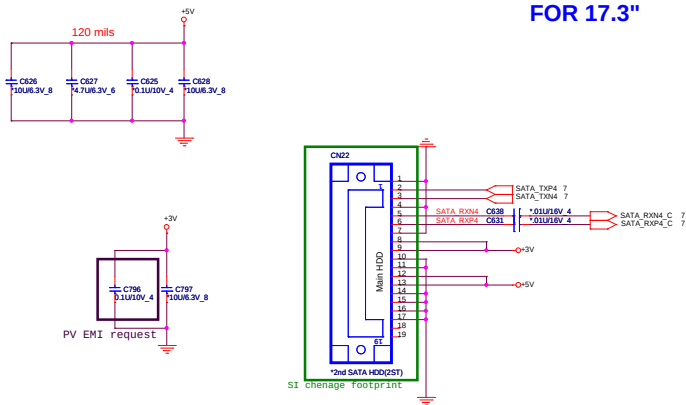
L23
RTL8111DL (Giga lan) use 4.7uH
power choke A>600mA tolerance
±15%



SATA HDD CONNECTOR

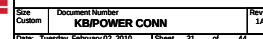


SATA CD-ROM

SATA_2 CONNECTOR
FOR 17.3"

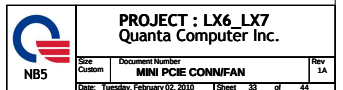
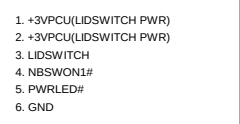
+3V 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,31,32,33,34,37,40,41
+5V 11,21,22,23,25,26,28,31,33,34,41
+12VALW 22,34,39,41

PROJECT : LX6_LX7 Quanta Computer Inc.			
NB5	Doc Number	Document Number	Rev
	Doc	000HDDDIANT	1A
Date: Tuesday, February 03, 2010 Sheet 30 of 44			

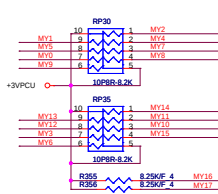




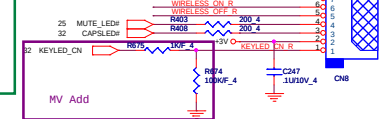
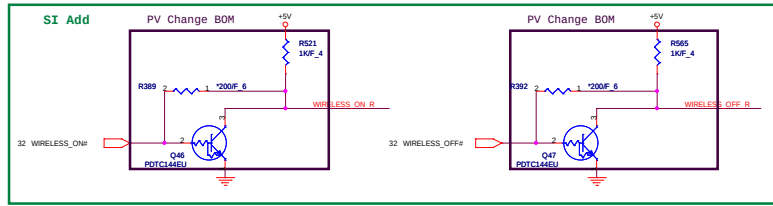
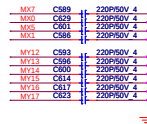
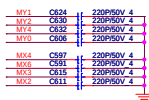
POWER BOTTON CONNECT



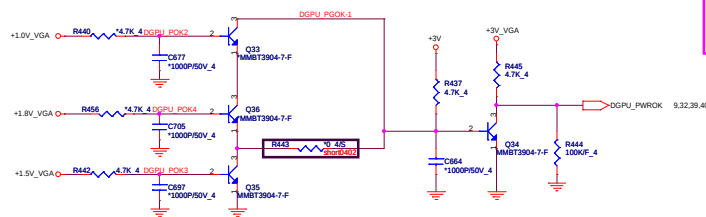
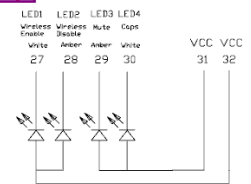
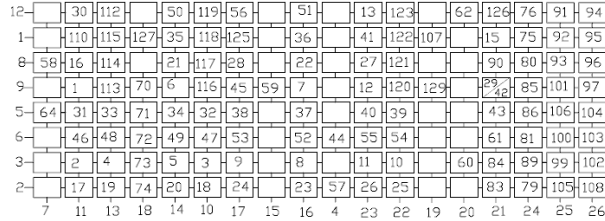
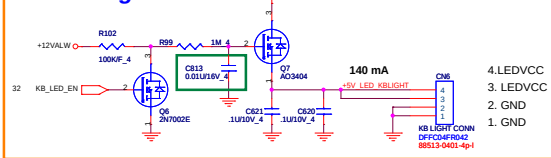
KEYBOARD PULL-UP



```
clear ABS 758 resin for key cap.  
7 LEDs for 15.4" (total LED current 140mA)  
11 LEDs for 17" (Total LED current 220mA)
```



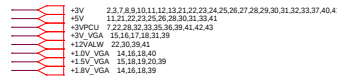
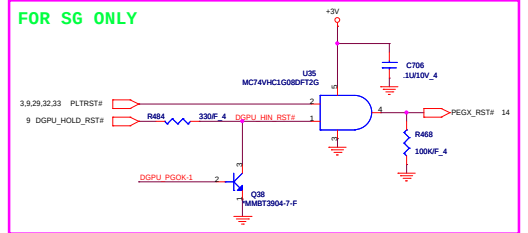
KB backlight for 15"



FOR DIS ONLY



FOR SG ONLY

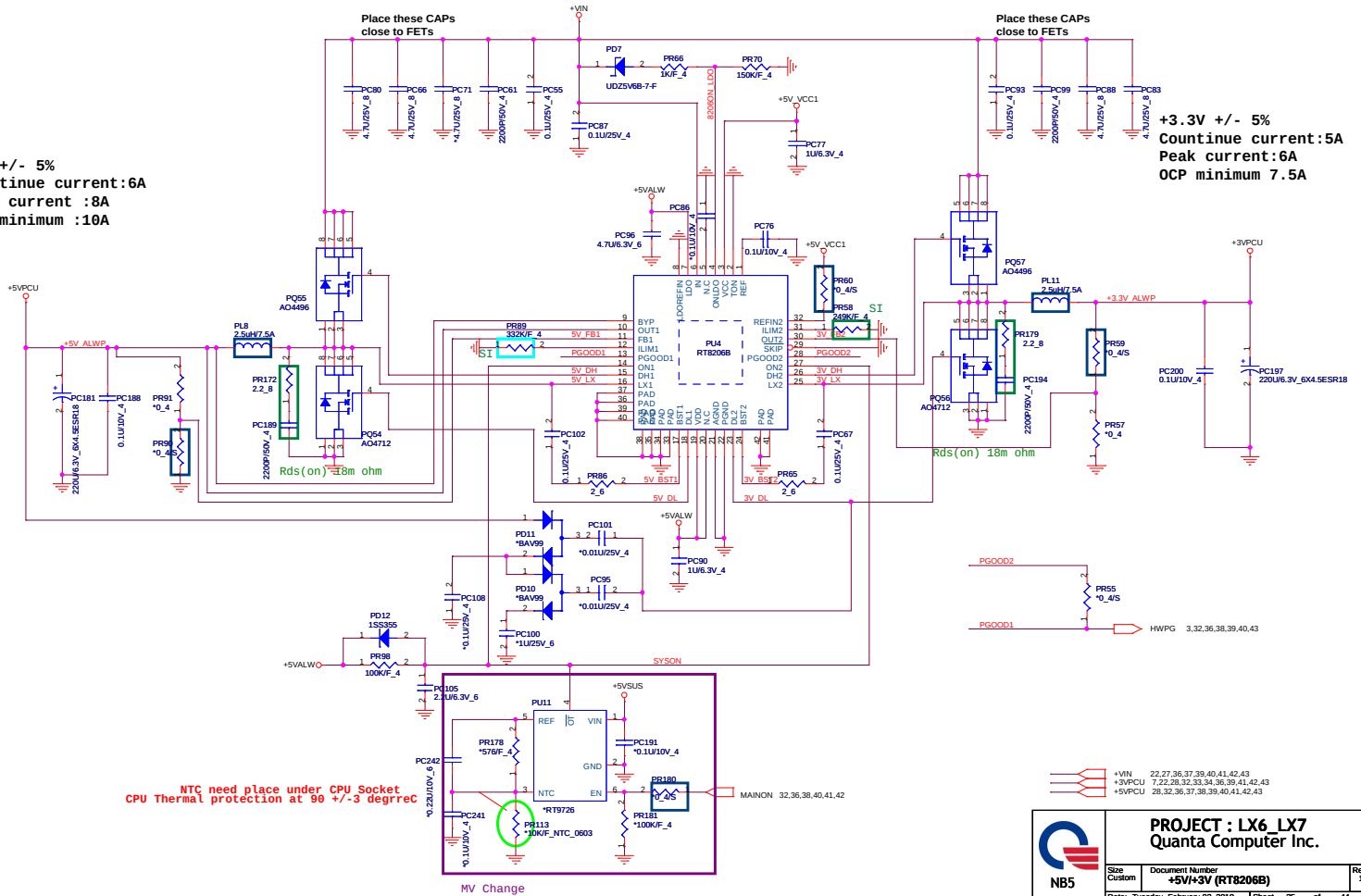


PROJECT : LX6_LX7
Quanta Computer Inc.

Size Custom	Document Number KB	Rev 1A
Date: Tuesday, February 02, 2010		Sheet 34 of 44

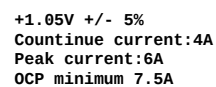
+5V +/- 5%
 Countinue current:6A
 Peak current :8A
 OCP minimum :10A

+3.3V +/- 5%
 Countinue current:5A
 Peak current:6A
 OCP minimum 7.5A



PROJECT : LX6_LX7
 Quanta Computer Inc.

Size	Document Number	Rev
Custom	+5V/+3V (RT8206B)	1A
Date: Tuesday, February 02, 2010		Sheet 35 of 44



+1.8V Volt +/- 5%
Continue current:1A
Peak current:2A
OCP minimum 3A

$$V_o = 0.75(R_1 + R_2)/R_2$$

$$V_0 = 0.75(R_1 + R_2)/R_2$$

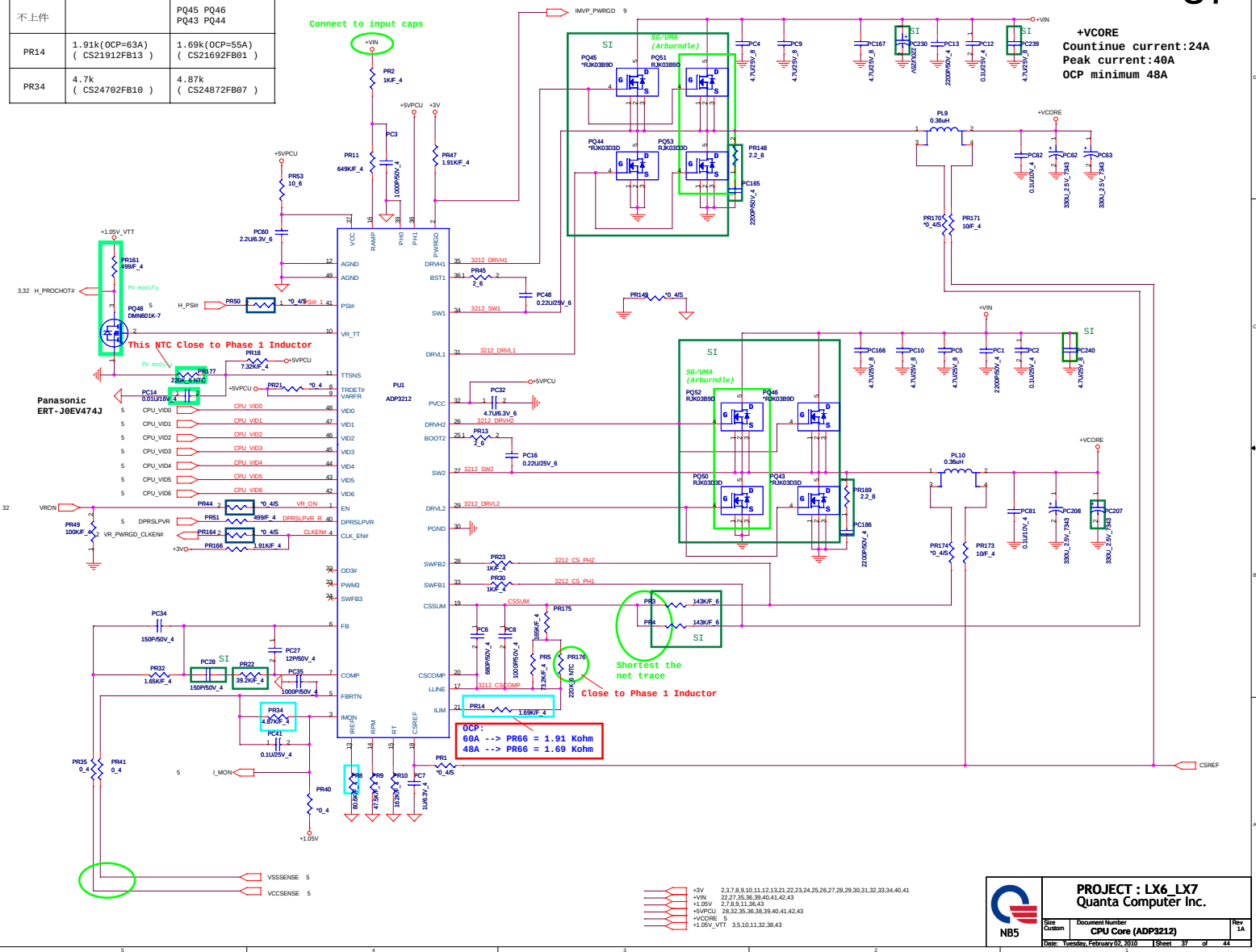
+1.8V	5,10,11,21,41
+VIN	22,27,35,37,39,40,41,42,43
+1.05V	2,7,8,9,11,37,43
+3VPCU	7,22,28,32,33,34,35,39,41,42,43
+5VPCU	28,32,35,37,38,39,40,41,42,43



PROJECT : LX6_LX7
Quanta Computer Inc.

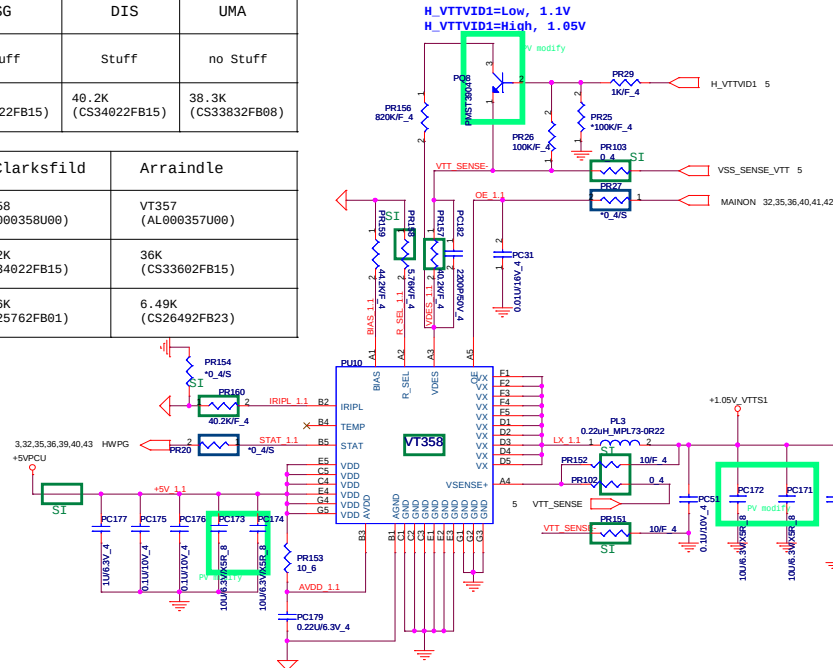
Size Custom	Document Number +1.05V/+1.8V (RT8204C)	Rev 1A
Date: Tuesday, February 02, 2010		Sheet 36 of 44

	DIS (Clarksfild)	SG/UMA (Arburndle)
不上件		PQ45 PQ46 PQ43 PQ44
PR14	1.91k (0CP=63A) (CS21912FB13)	1.69k (0CP=55A) (CS21692FB01)
PR34	4.7k (CS24702FB10)	4.87k (CS24872FB07)



	SG	DIS	UMA
PR156 PR26 PR29 PQ8	Stuff	Stuff	no Stuff
PR157	40.2K (CS34022FB15)	40.2K (CS34022FB15)	38.3K (CS33832FB08)

	Clarksfild	Arraindle
PU10	VT358 (AL000358U00)	VT357 (AL000357U00)
PR160	40.2K (CS34022FB15)	36K (CS33602FB15)
PR158	5.76K (CS25762FB01)	6.49K (CS26492FB23)



+1.05V_VTT +/- 5%
Countinue current:12A
Peak current:15A
OCP minimum 18A



PROJECT : LX6_LX7
Quanta Computer Inc.

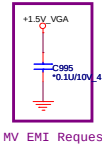
Size	Document Number	Rev
Custom	+1.05V_VTT (VT358)	1A
Date: Tuesday, February 02, 2010	Sheet 38 of 44	

+VGACORE +/- 5%
Countinue current:19A
Peak current:21A
OCP minimum 26A

Park XT Setting		
High	1.12V	
Low	0.90V	
CTRL0	CTRL1	Vo
0	0	1.12
0	1	0.9
不上件		
改料	PR187=0.81k (CS26812F801) PR188=0.89k (CS26812F802) PQ58=RJK63030 (BAW63030000) PQ59=RJK63030 (BAW63030000)	

Madison Pro Setting		
High	1.00V	
Medium	0.95V	
Low	0.90V	
CTRL1	CTRL0	Vo
0	0	1
0	1	0.95
1	0	0.9
1	1	0.9
不上件		
改料	PC120 PC121=CH733R8831	

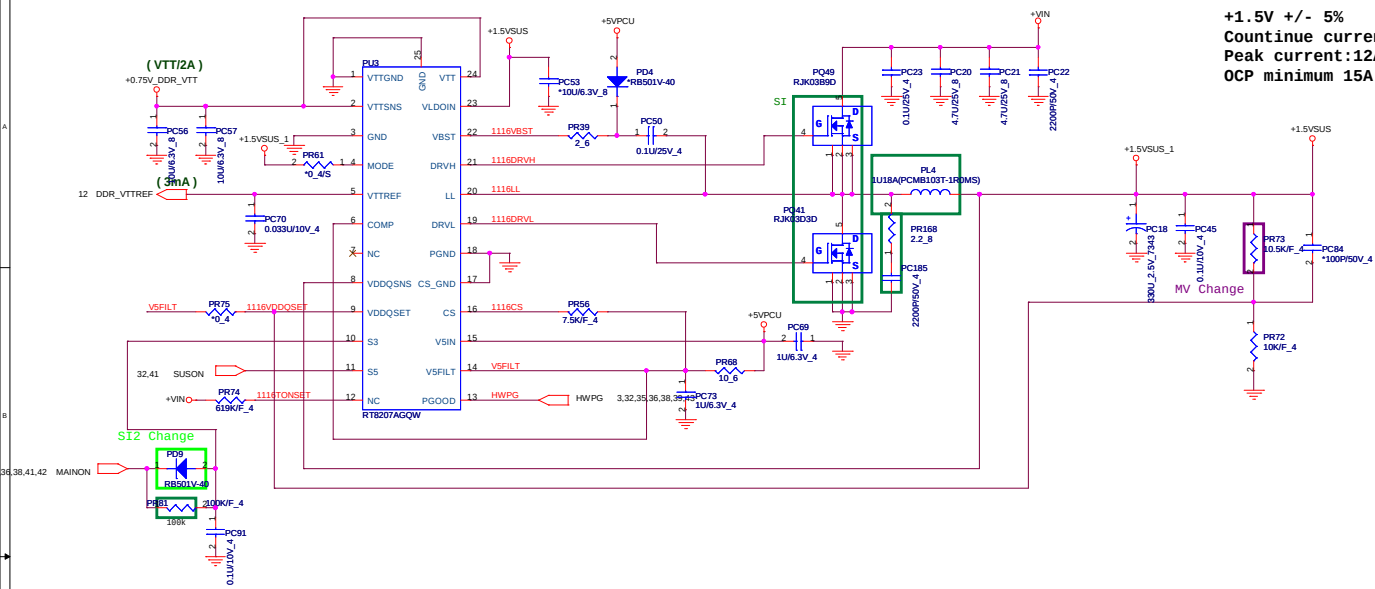
+1.8V +/- 5%
Countinue current:1.2A
Peak current:3A



MV EMI Request

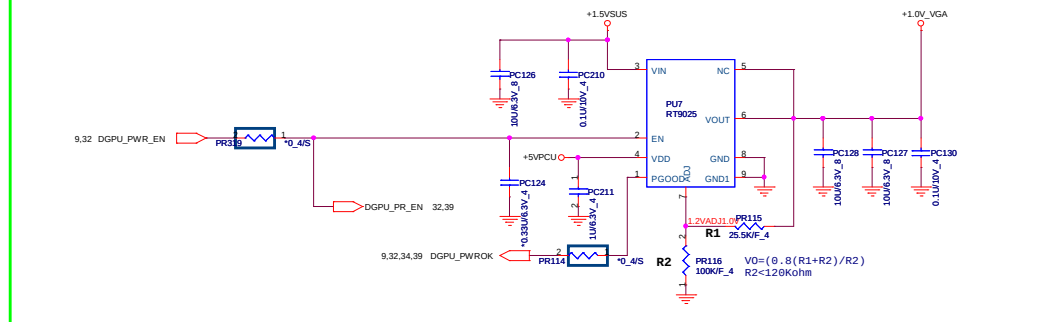
PROJECT : LX6_LX7 Quanta Computer Inc.			
Size	Document Number	Rev	1A
NB5		GPU CORE(ISL6264)	
Date	Tuesday, February 02, 2010	Sheet	39 of 44

+1.5V +/- 5%
Countinue current:6A
Peak current:12A
OCp minimum 15A

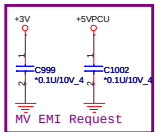


SG & Discrete Only

+1.0V +/- 5%
Countinue current:1.7A
Peak current:3A



PV EMI Request

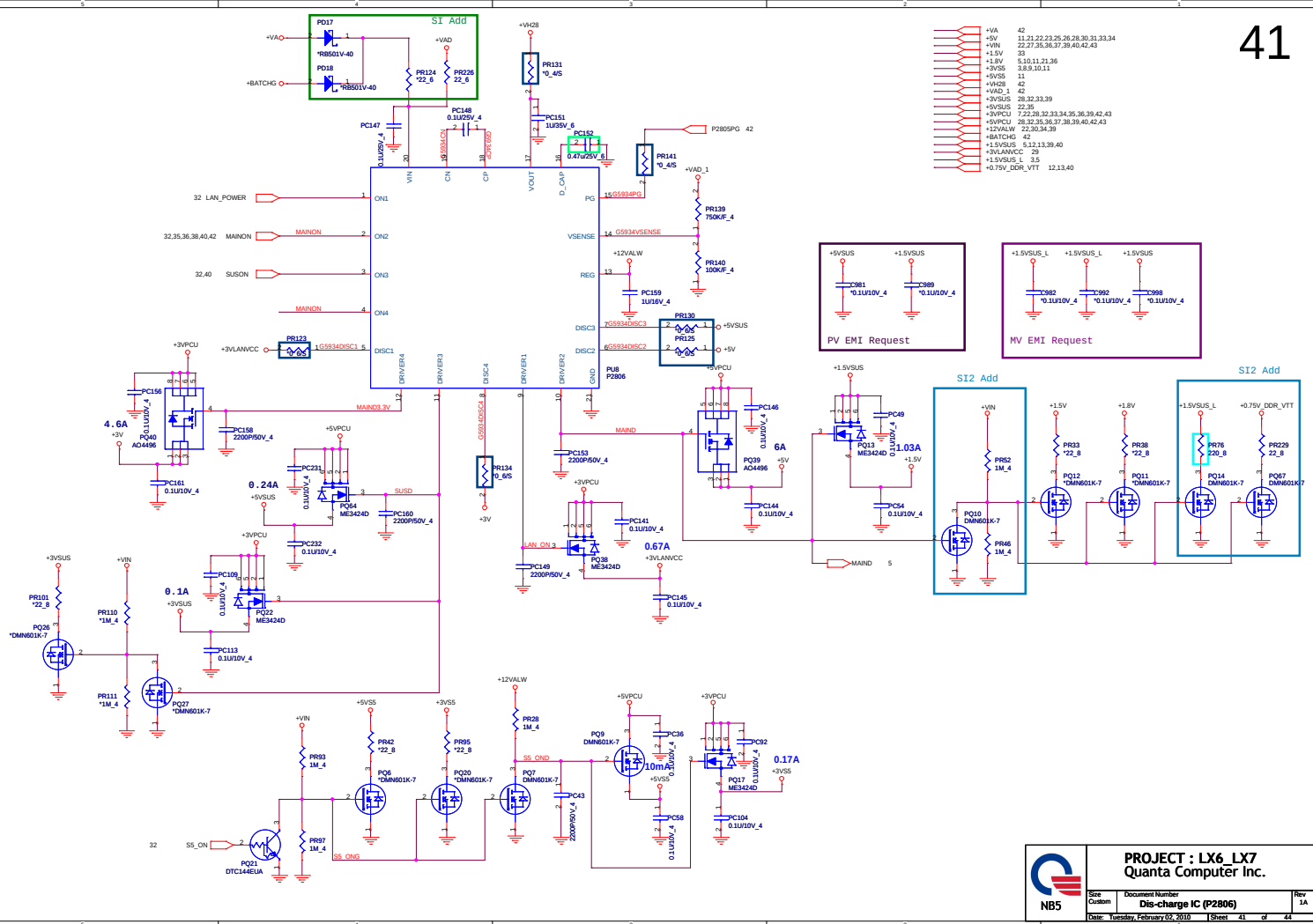


+3V 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,34,37,41
 +VIN 22,27,35,36,37,39,41,42,43
 +5VPCU 28,32,35,36,37,38,39,41,42,43
 +1.5VSUS 5,12,13,39,41
 +1.0V_VGA 14,16,18,34
 +0.75V_DDR_VTT 12,13,41

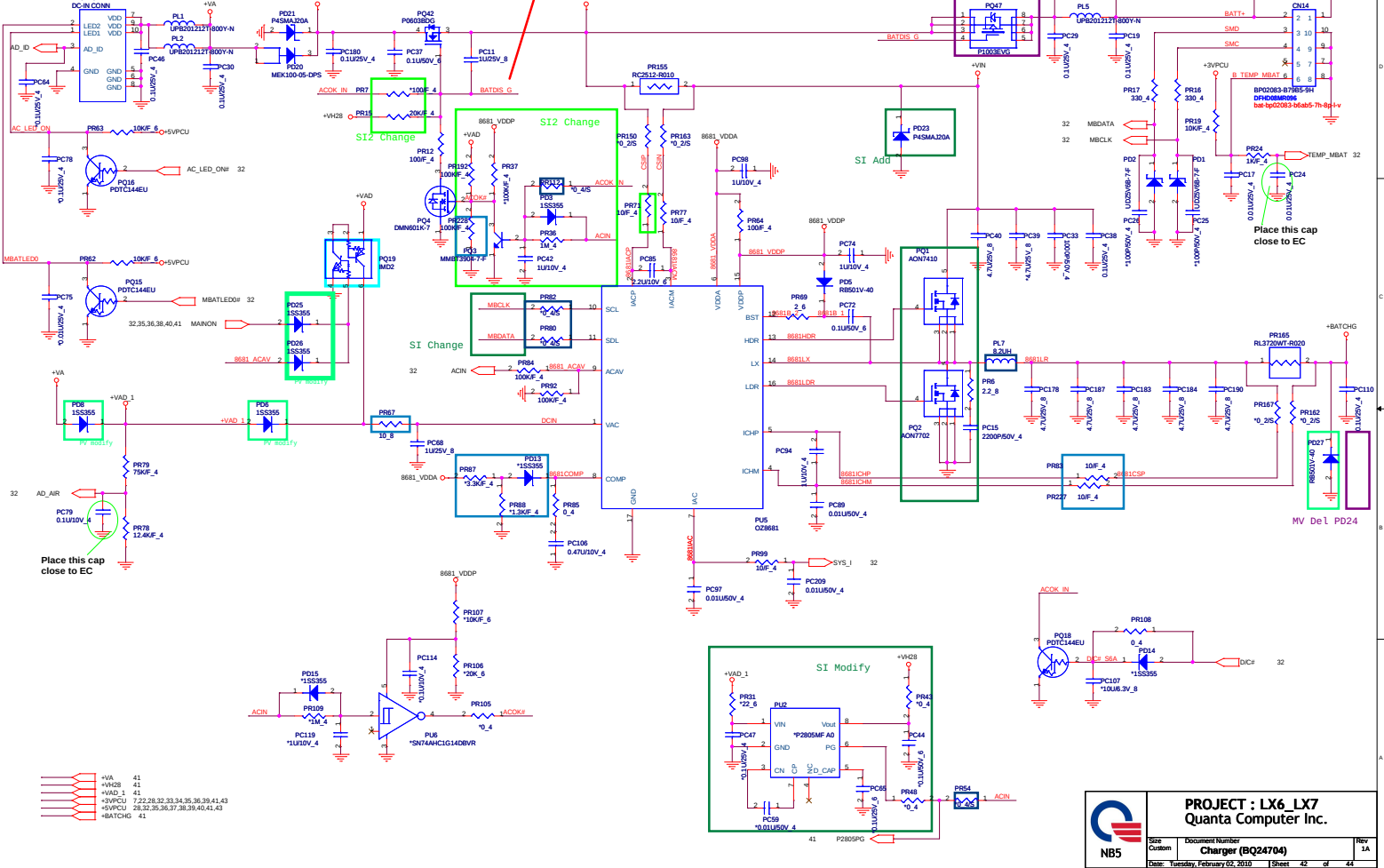


PROJECT : LX6_LX7
Quanta Computer Inc.

Size	Document Number	Rev
Custom	DDR3 (RT8207)	1A
Date: Tuesday, February 02, 2010	Sheet 40 of 44	



Do Not add test pad on BATDIS_G signal



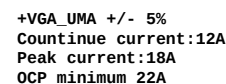
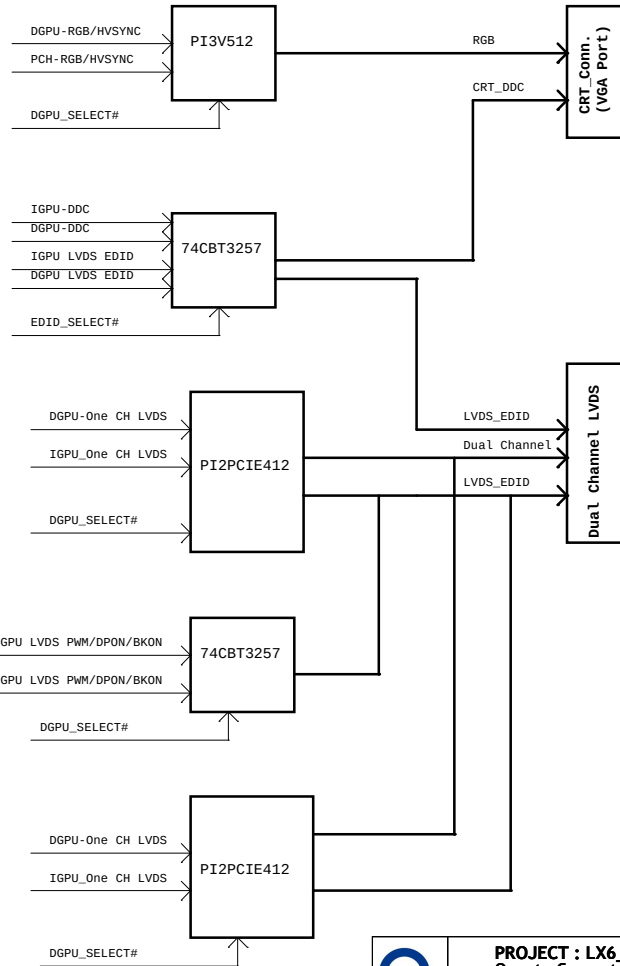


Diagram illustrating a pull-up resistor (PR213, 1K/Ω) connected to +1.05V_VTT. A callout box highlights the capacitor value (C994, 0.1u/10V_4) and the VTT label.



 NB5	PROJECT : LX6_LX7 Quanta Computer Inc.		
	Size Custom	Document Number Switch Blockdiagram	Rev 1A
	Date: Tuesday, February 02, 2010 Sheet 44 of 44		