

Tango/Ballet BLOCK DIAGRAM

01

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : BOT

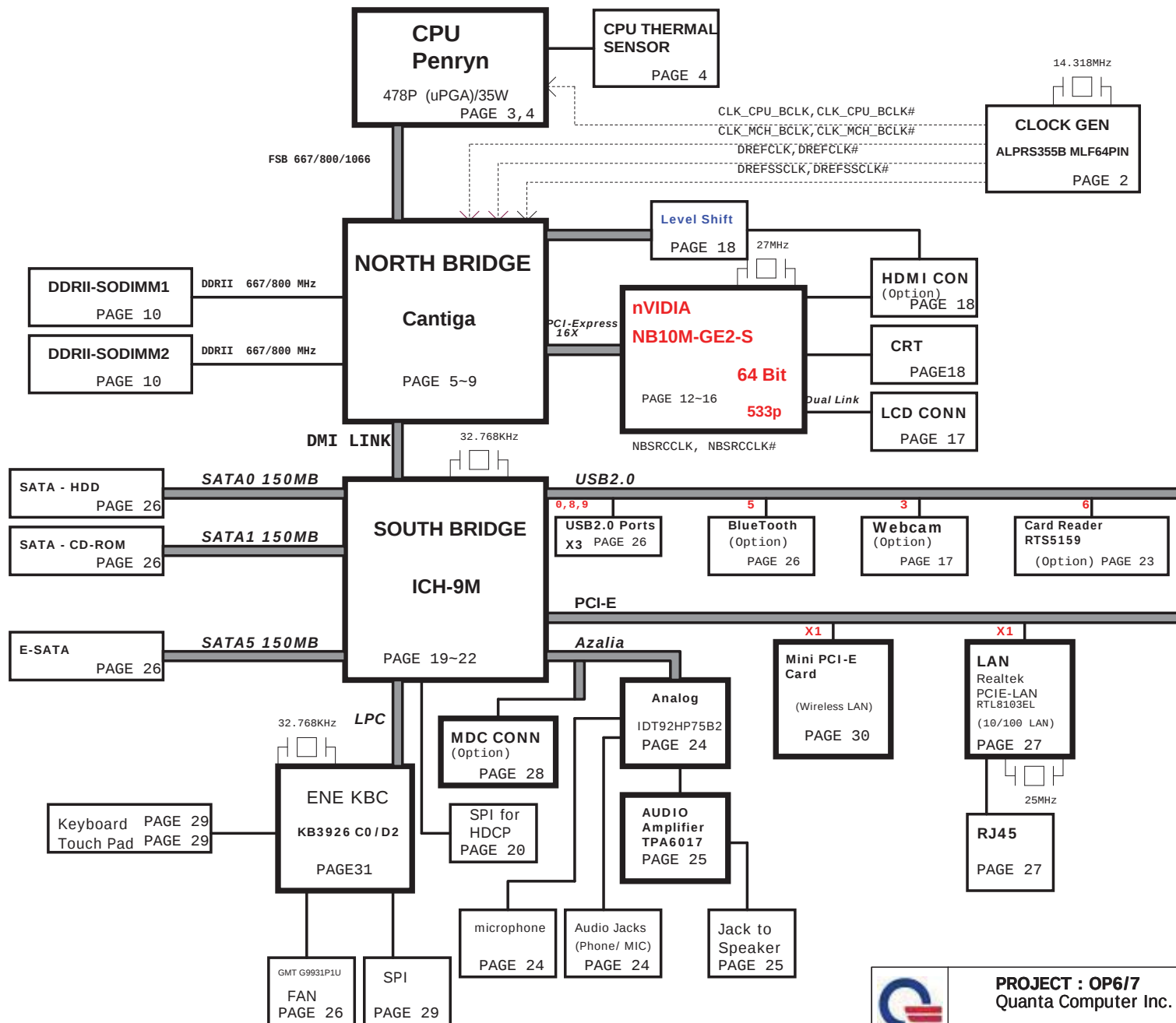
SYSTEM CHARGER(ISL6251AHAZ-T)
PAGE 31

SYSTEM POWER ISL6237IRZ-T
PAGE 32

DDR II SMDDR_VTERM
1.8V/1.8VSUS(TPS51116REGR)
PAGE 36

VCCP +1.5V AND GMCH
1.05V(RT8204)
PAGE 33

CPU CORE ISL6266A
PAGE 34

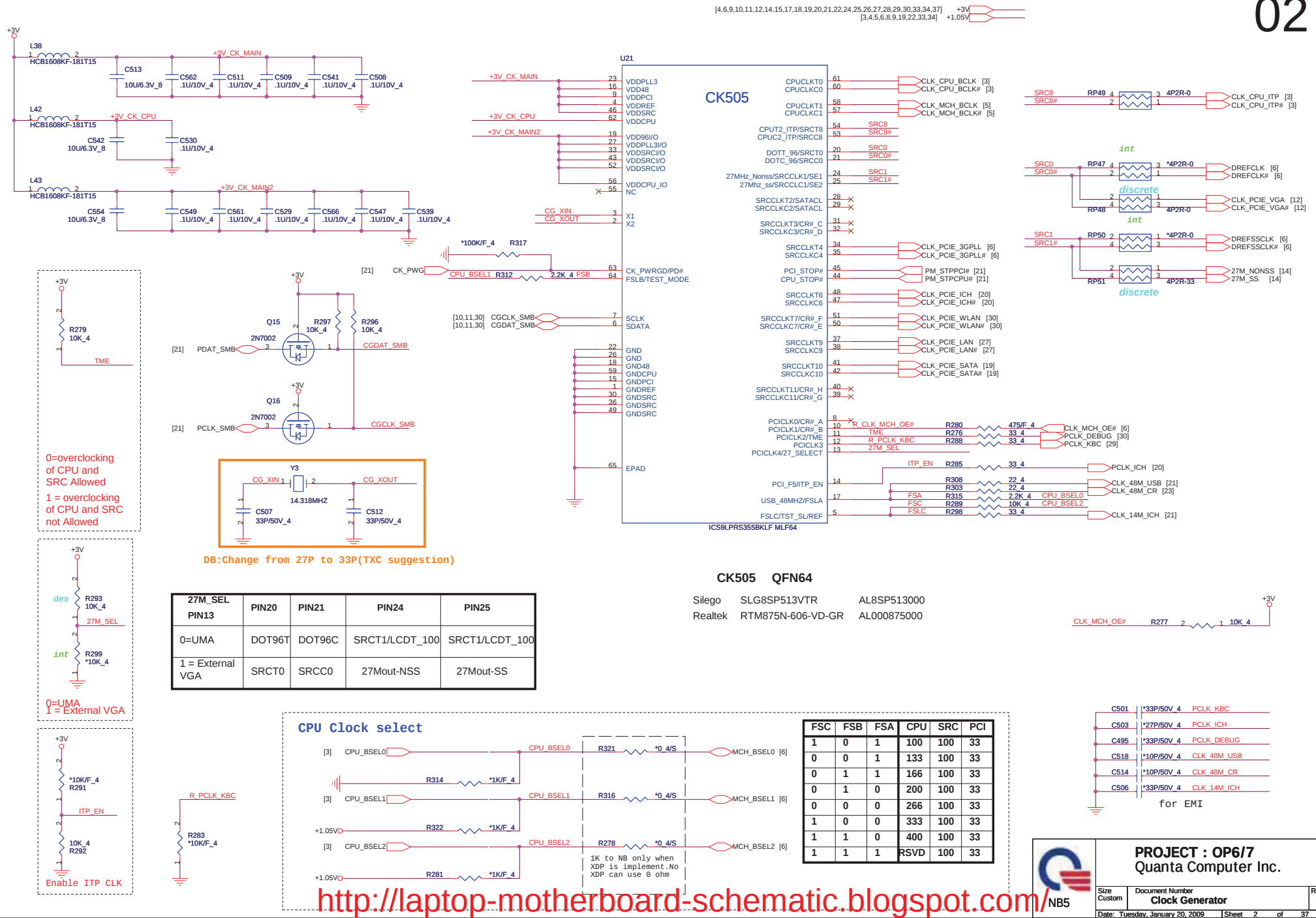


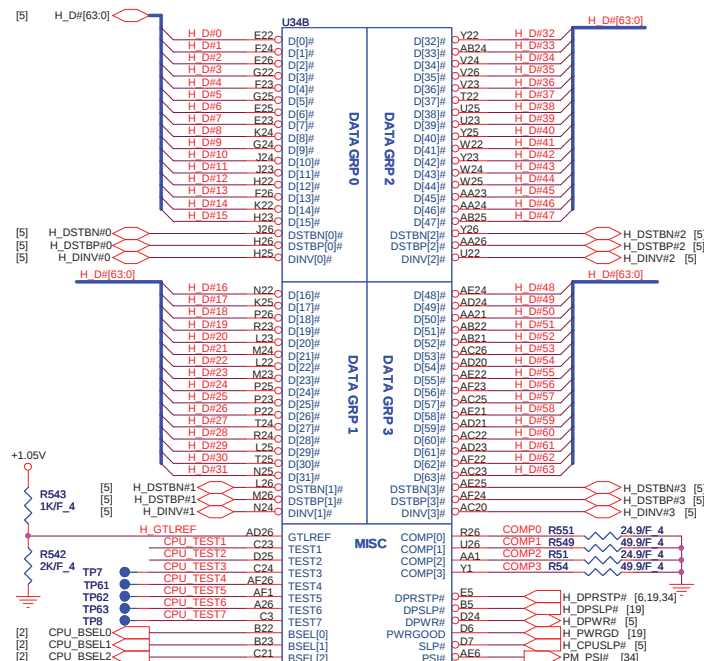
<http://laptop-motherboard-schematic.blogspot.com>



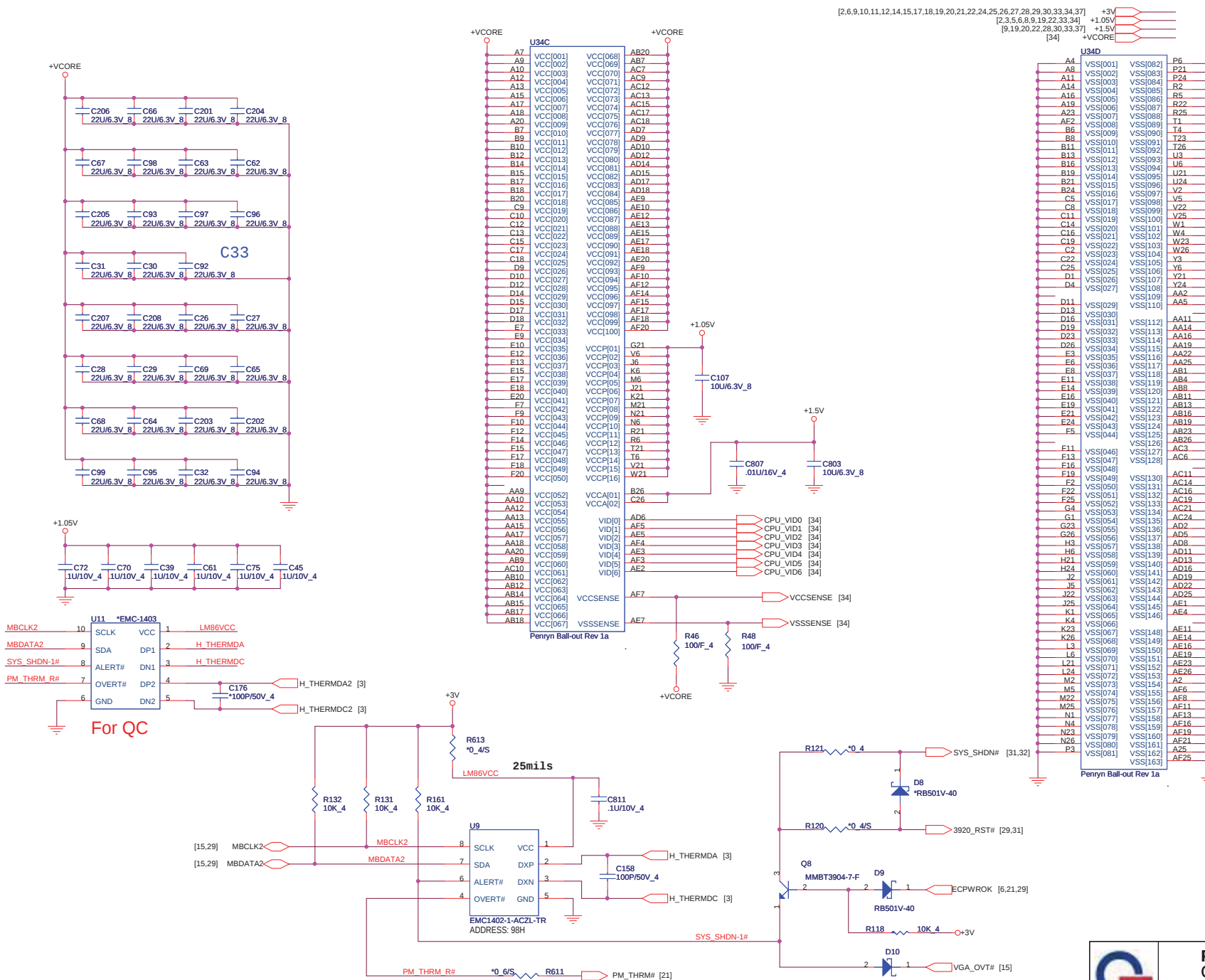
PROJECT : OP6/7
Quanta Computer Inc.

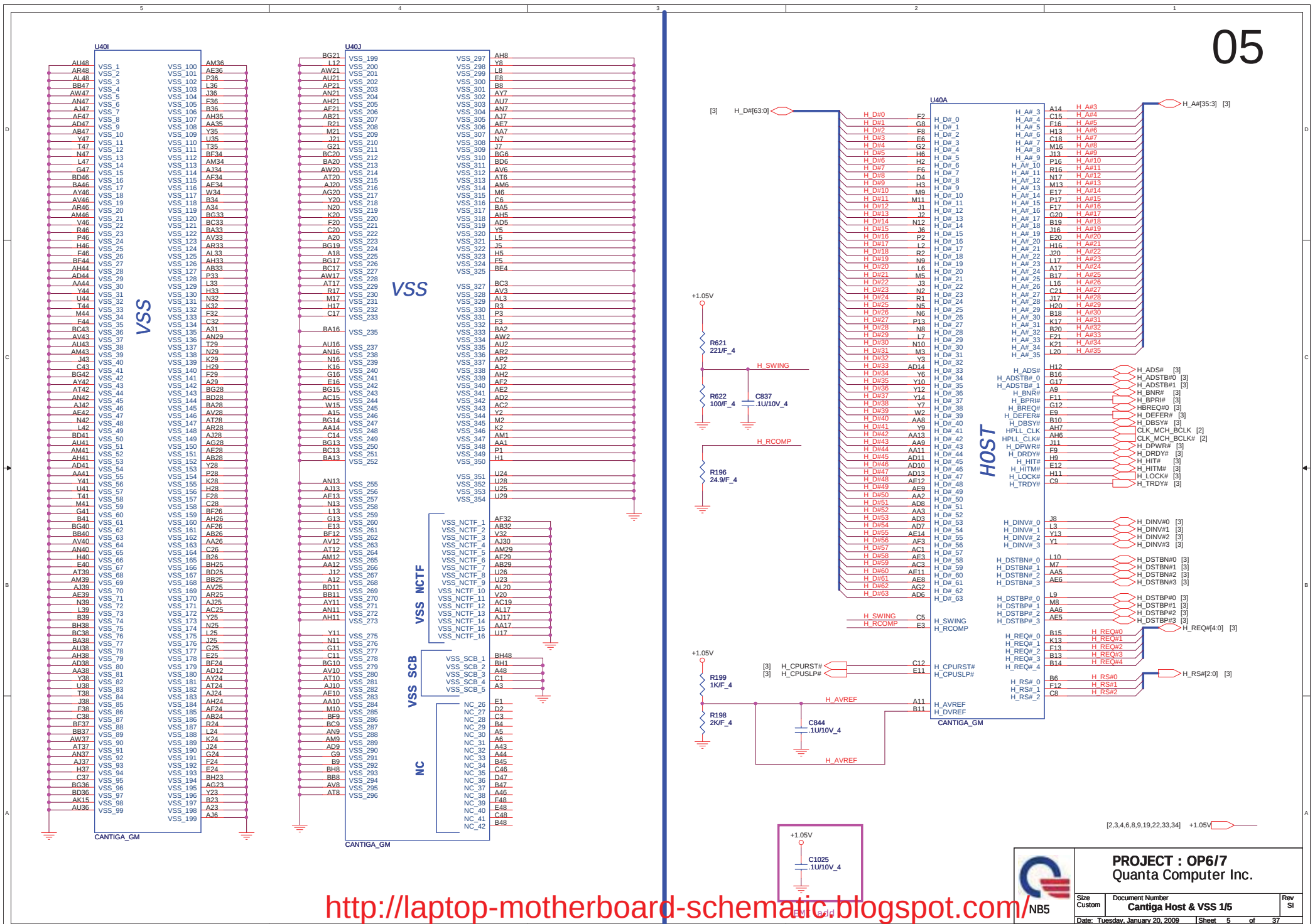
Size: Custom
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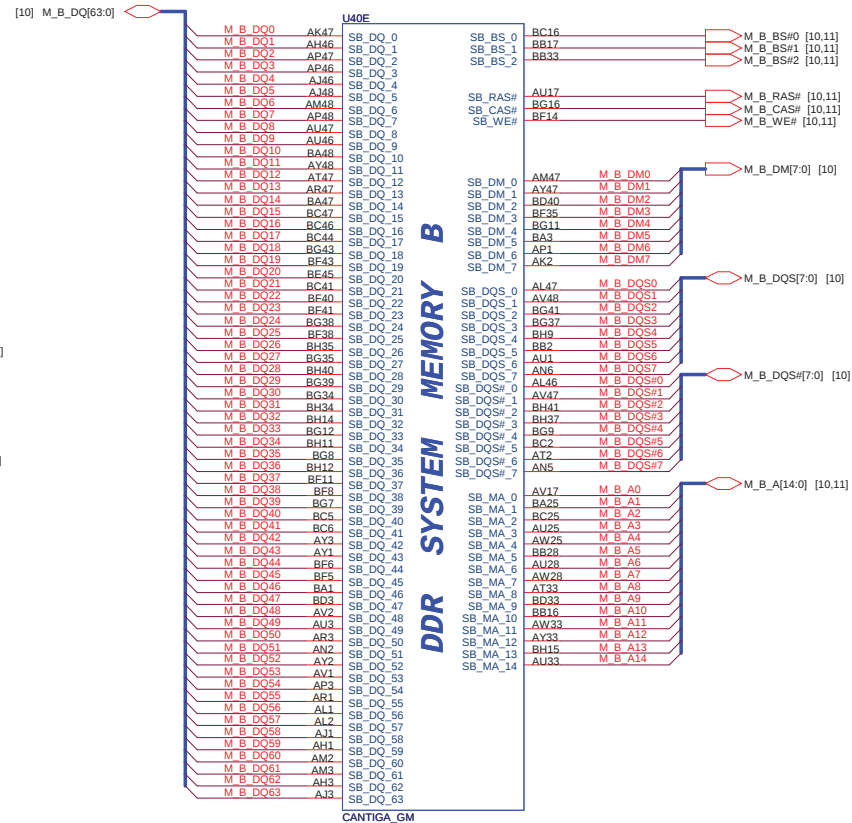
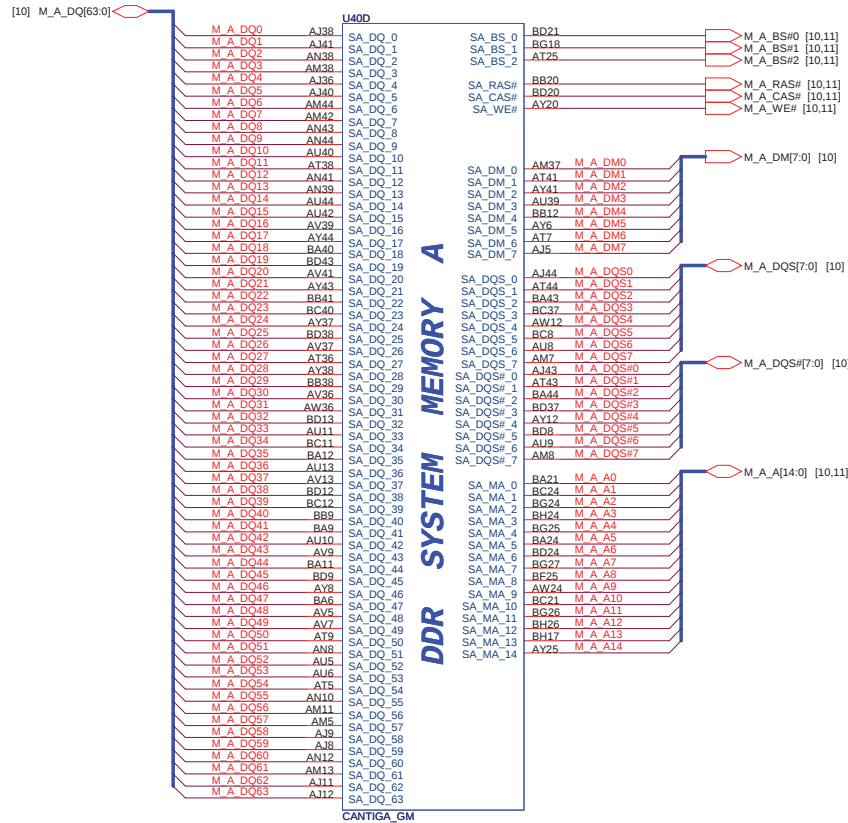




<http://laptop-motherboard-schematic.blogspot.com/>



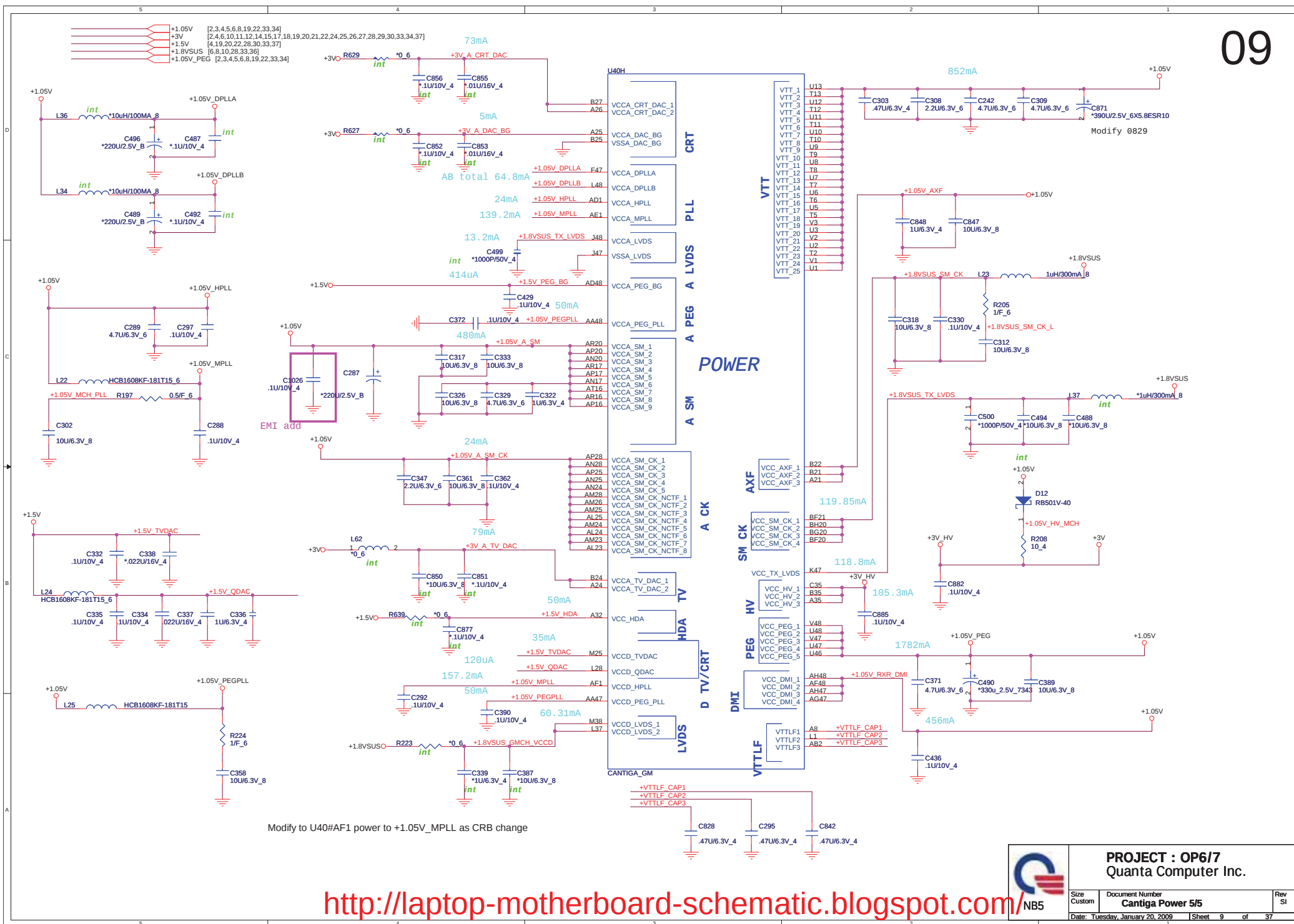


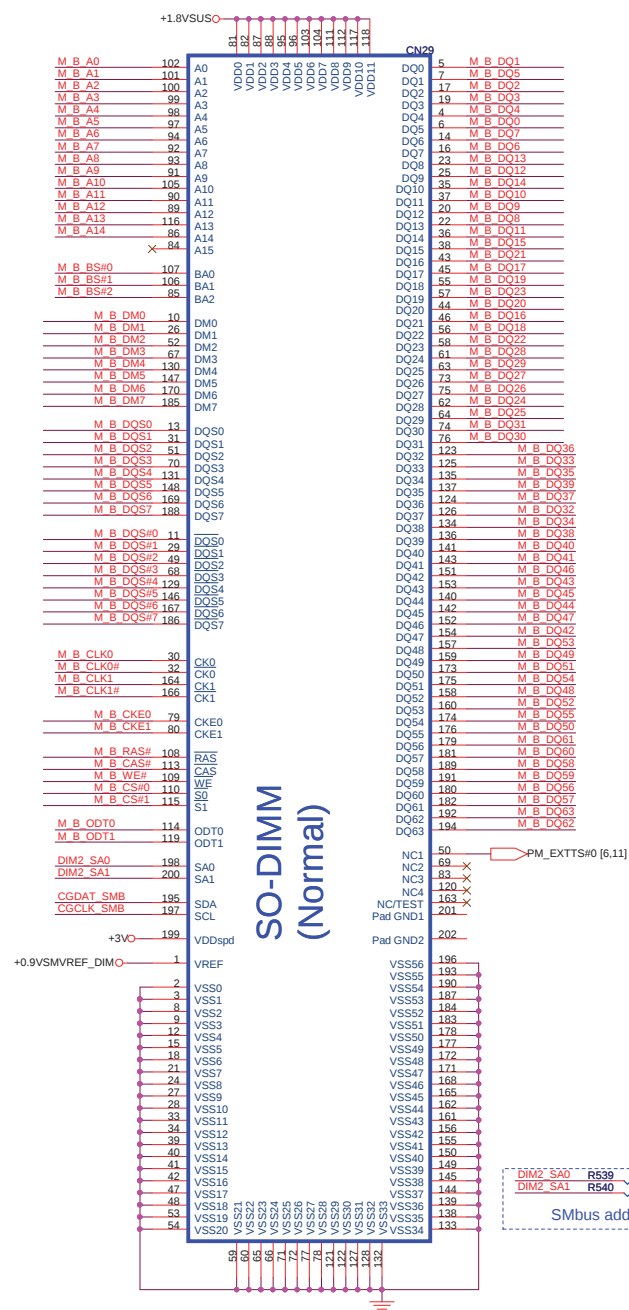




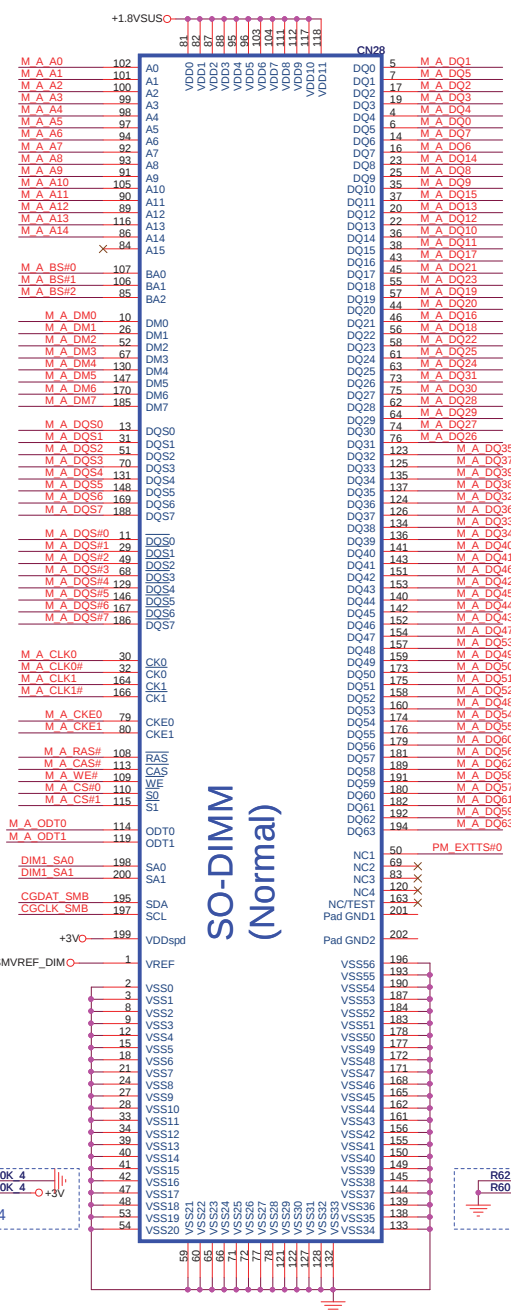
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Quanta Computer Inc.

Size Custom	Document Number Cantiga Vcc 4/5	Rev S
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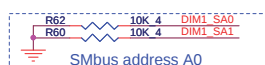
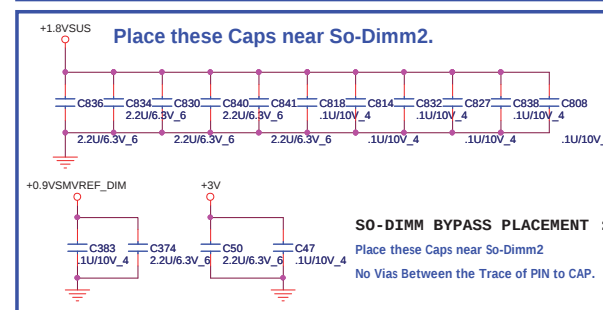
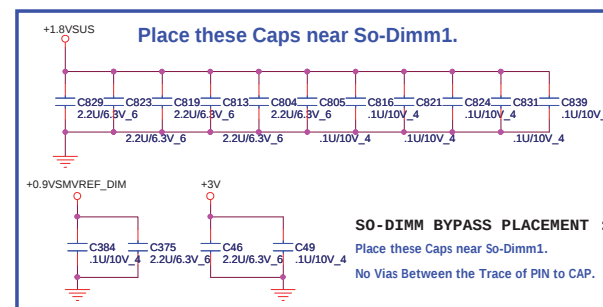
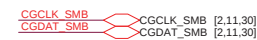
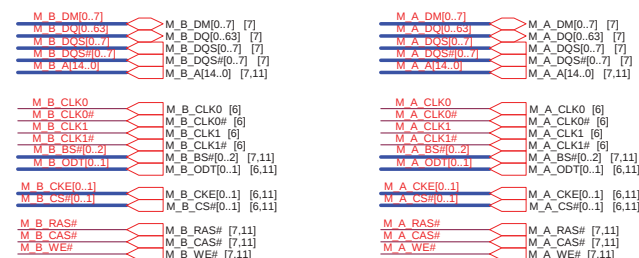




H 9.2

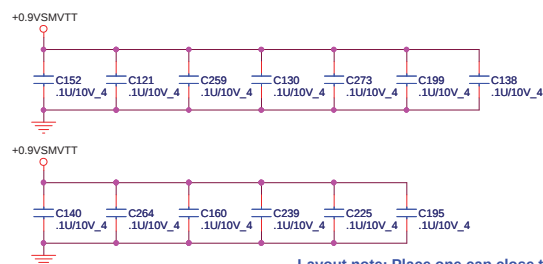


H 5.2



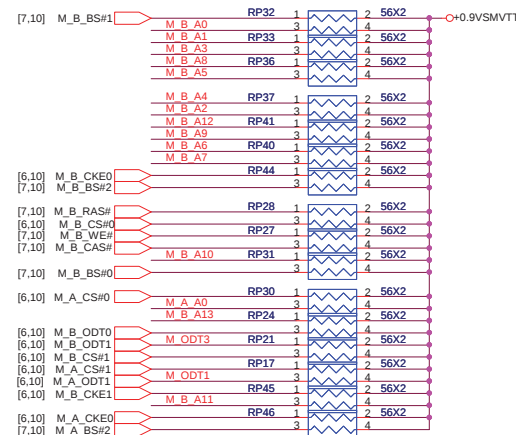
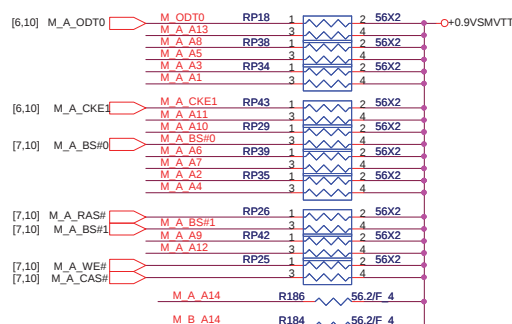
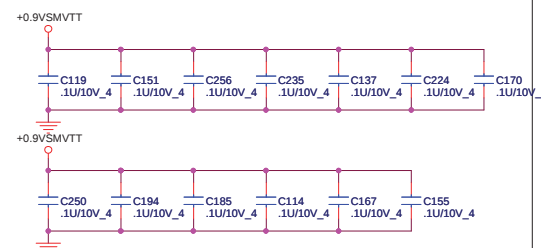
DDRII DUAL CHANNEL A, B.

DDRII A CHANNEL

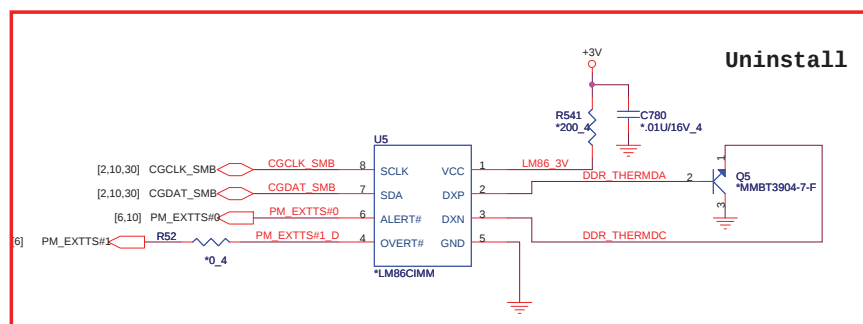


Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM

DDRII B CHANNEL



M_B_A[14.0] M_B_A[14.0] [7.10]
M_A_A[14.0] M_A_A[14.0] [7.10]



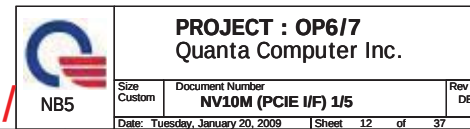
+0.9VSMVTT [36]
+3V [2,4,6,9,10,12,14,15,17,18,19,20,21,22,24,25,26,27,28,29,30,33,34,37]

<http://laptop-motherboard-schematic.blogspot.com/>

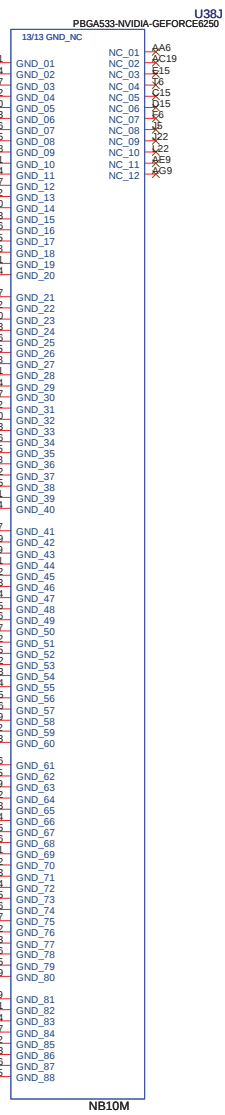
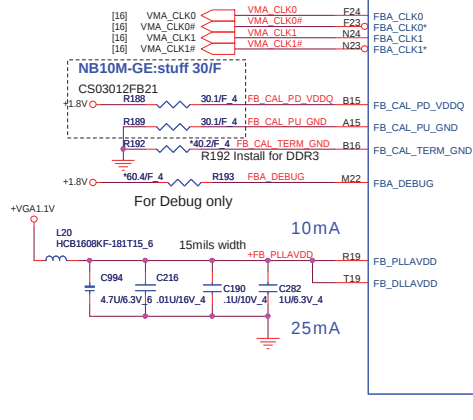
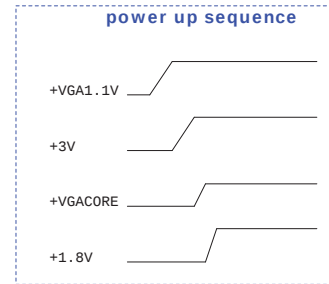
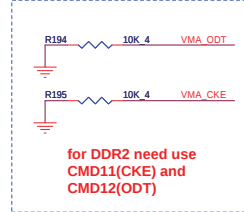
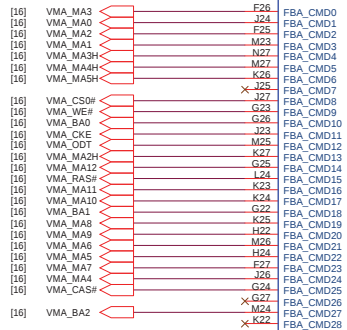
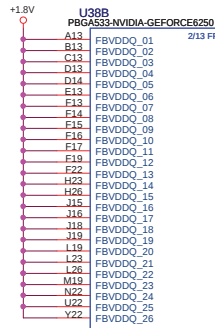


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Custom	DDR2 termination	SI
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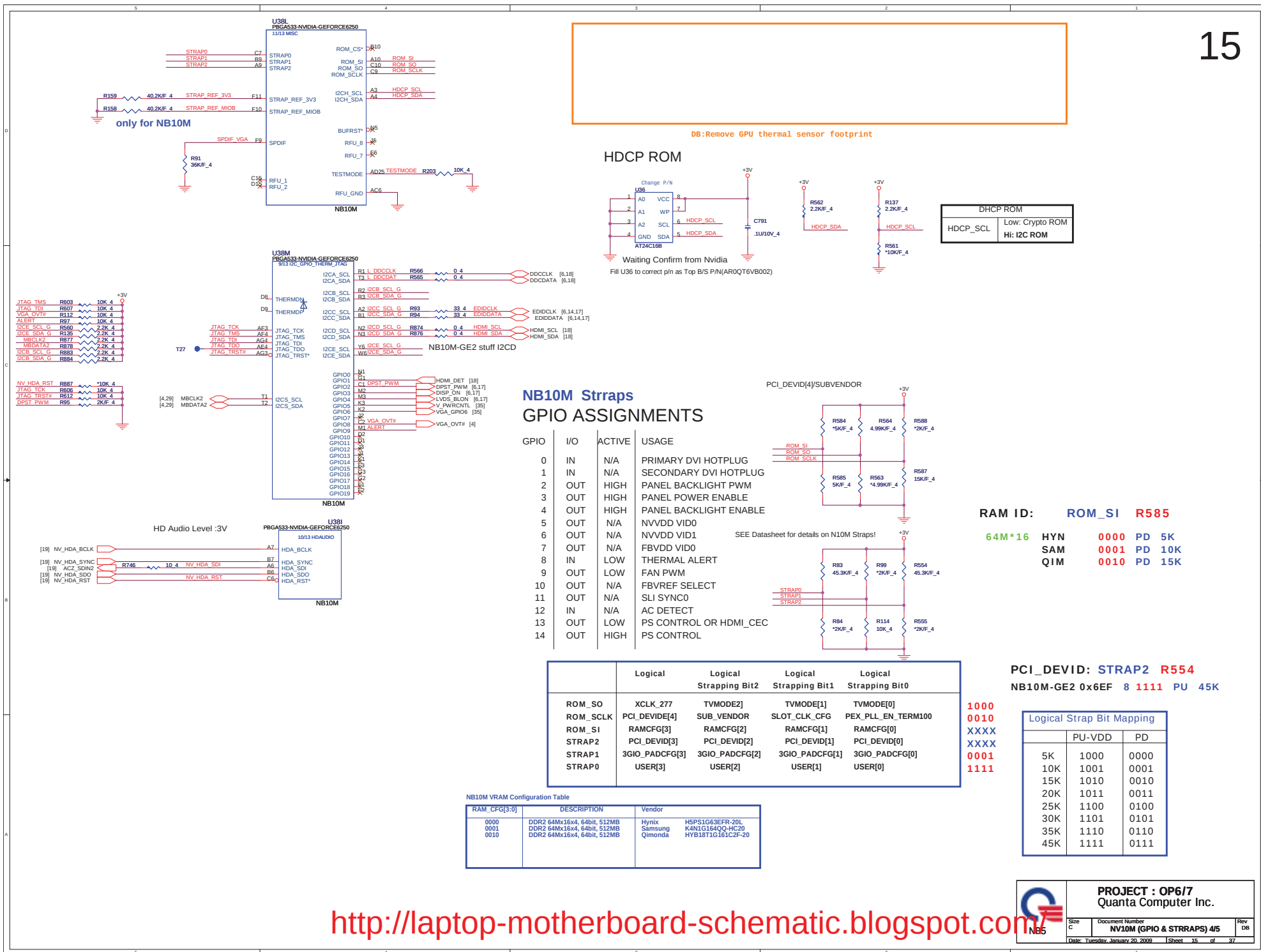


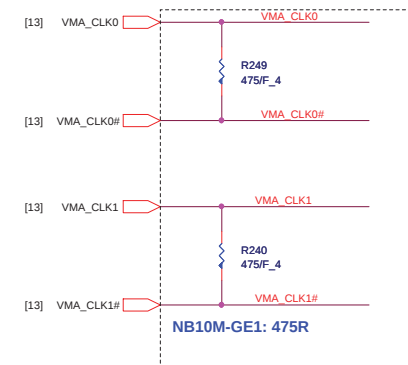
1.75A



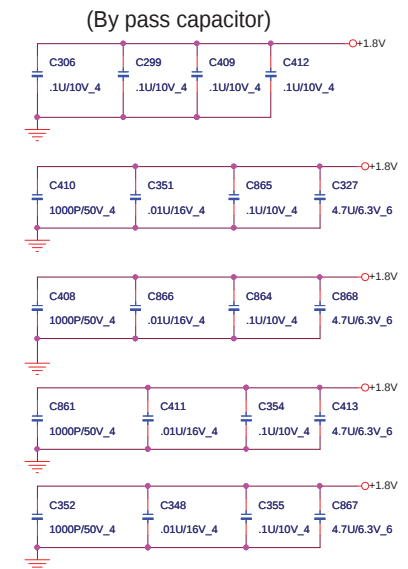
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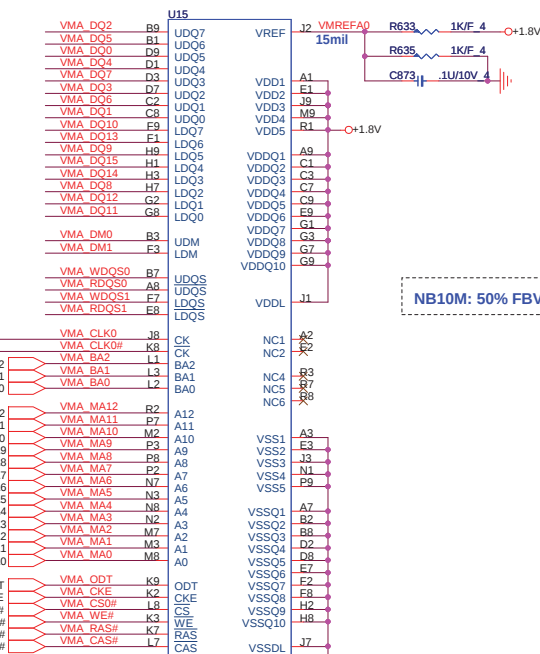
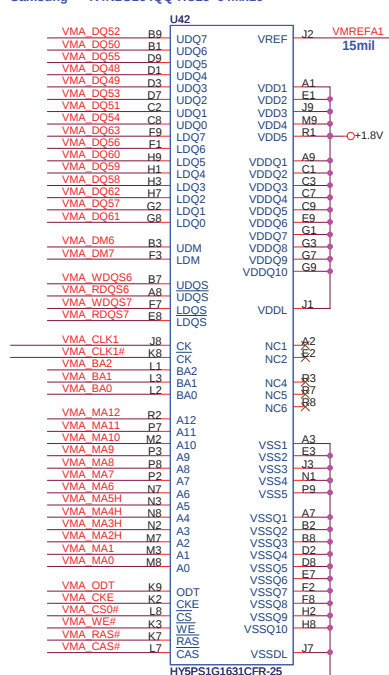
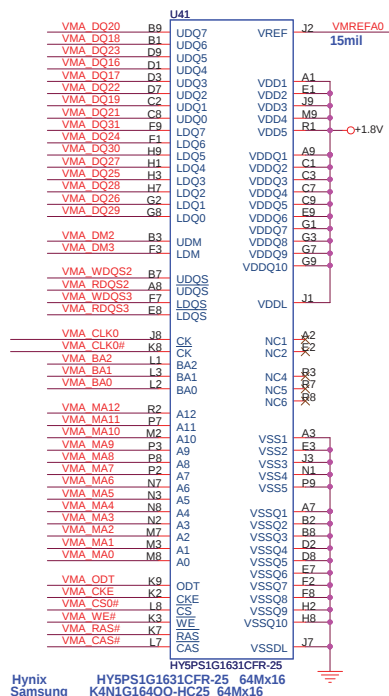


CS14752FB11 RES CHIP 475 1/16W +-1%(0402)

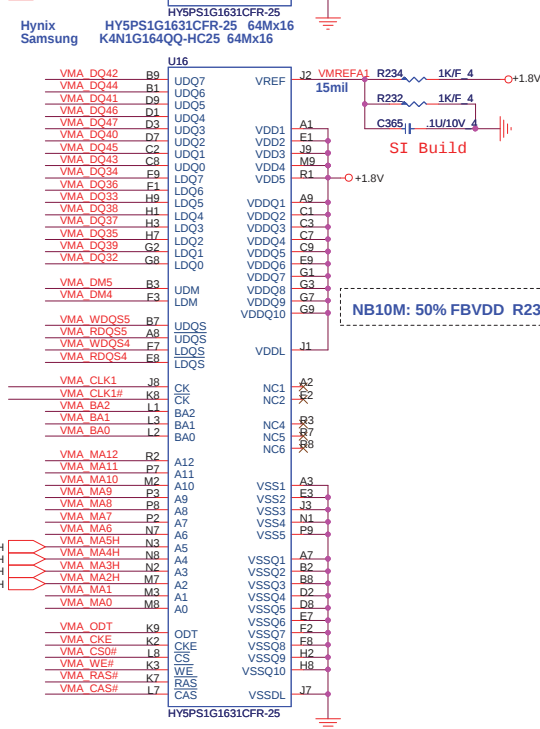


[13] VMA_DQ[63.0]  
[13] VMA_DM[7..0]  
[13] VMA_WDQS[7..0]  
[13] VMA_RDQS[7..0]  

256Mb : AKD5JGAT^05
512Mb : AKD59G-T^01



NB10M: 50% FBVDD R232 (1K)



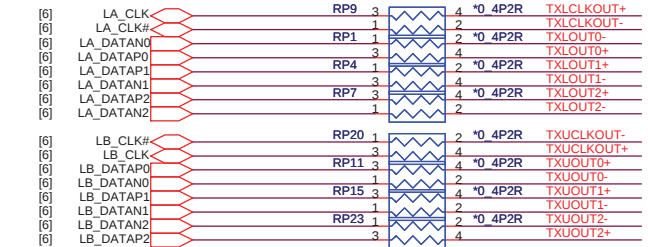
NB10M: 50% FBVDD R232 (1K)

SI Build

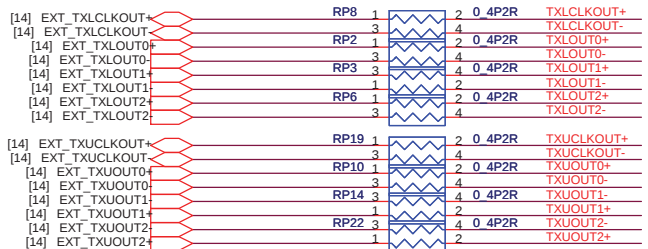
<http://laptop-motherboard-schematic.blogspot.com>

1. If LCD connector near GPU, then place these series Resistors near GPU
2. If LCD connector near N/B, then place these series Resistors near N/B

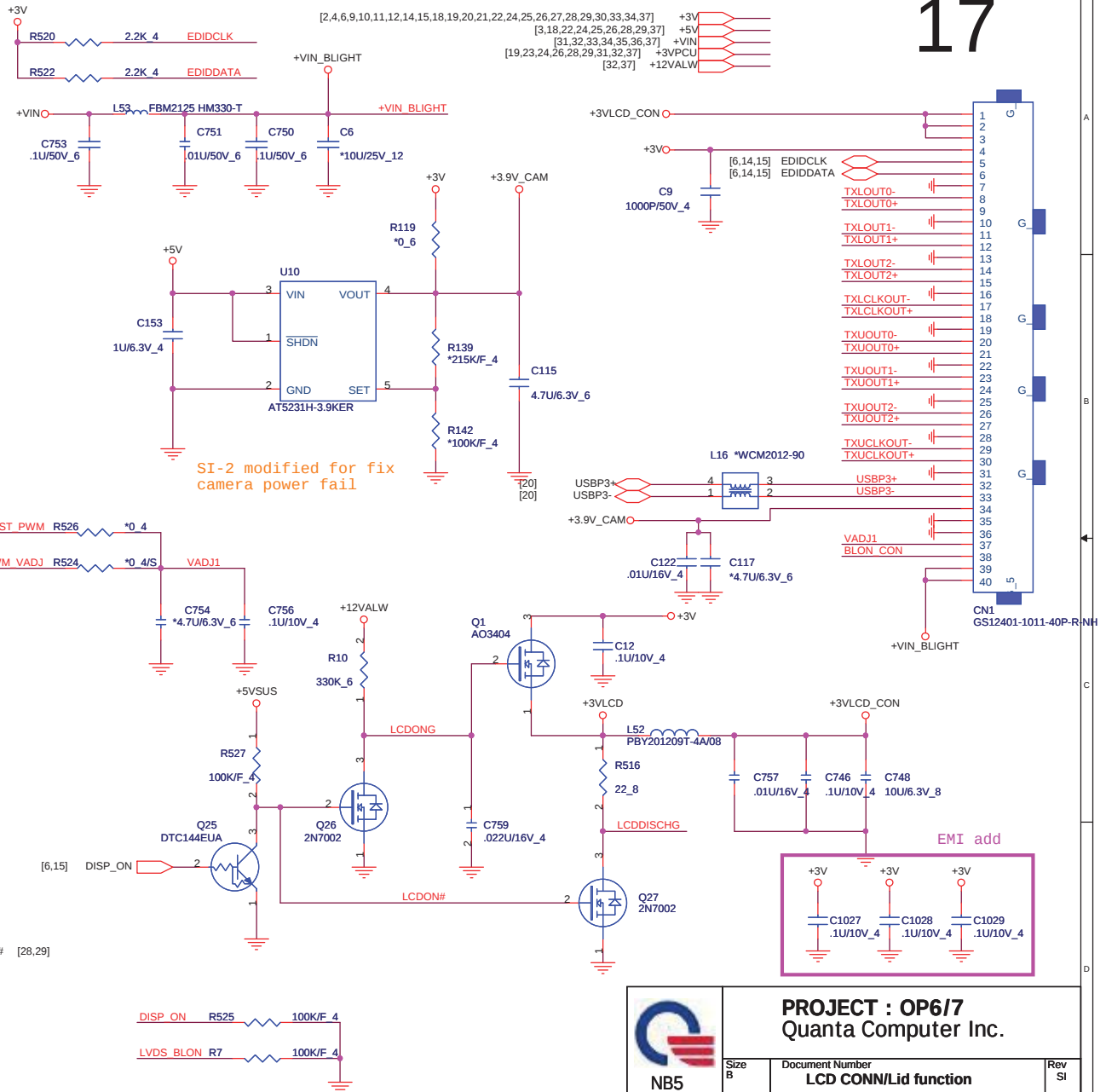
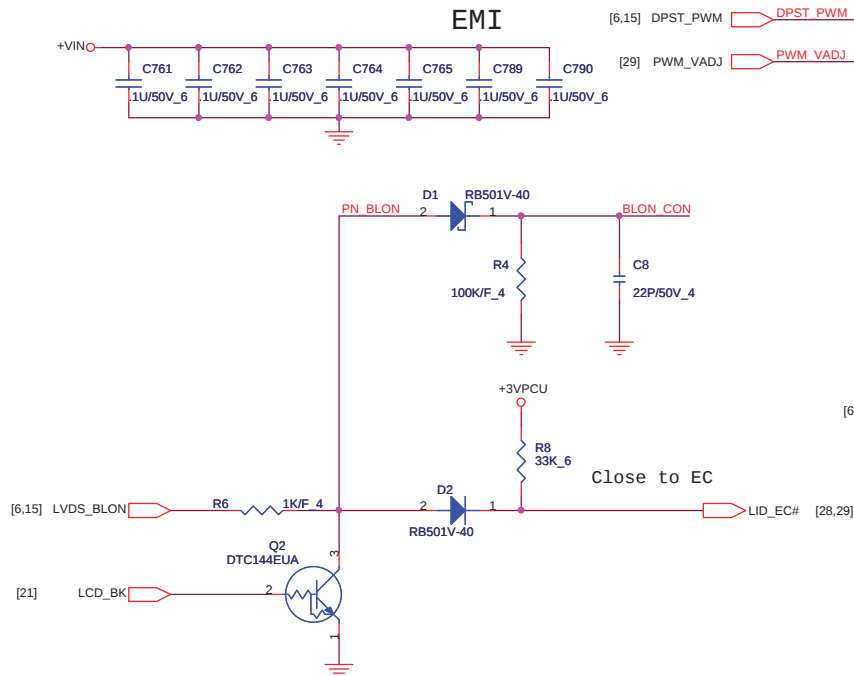
OPTION SIGNAL FROM NB FOR UMA VGA

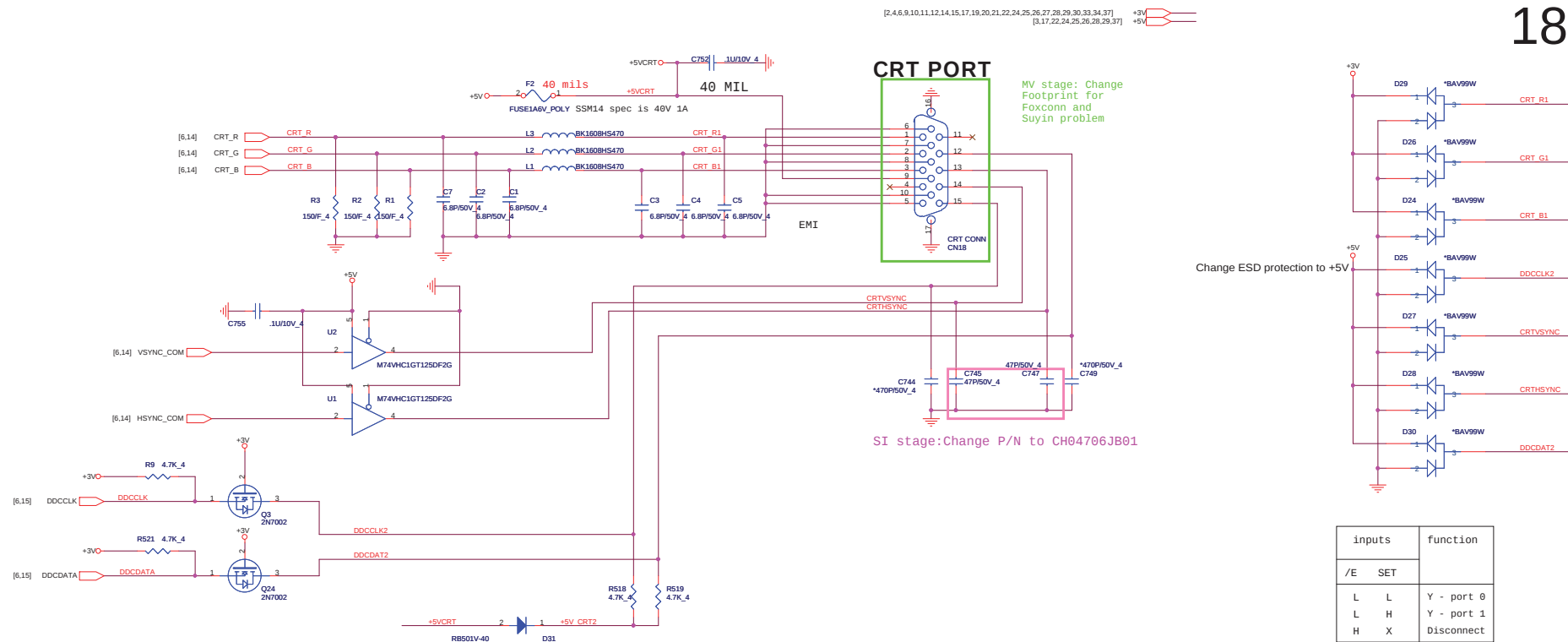


OPTION SIGNAL FROM Nvidia to VGA

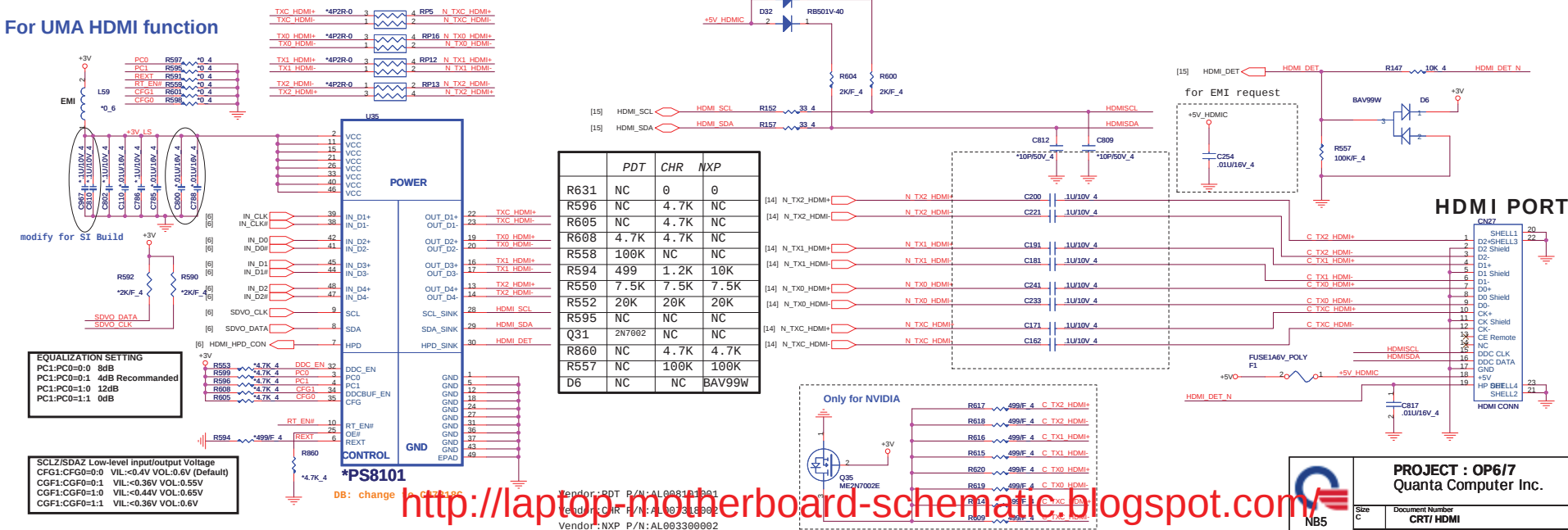


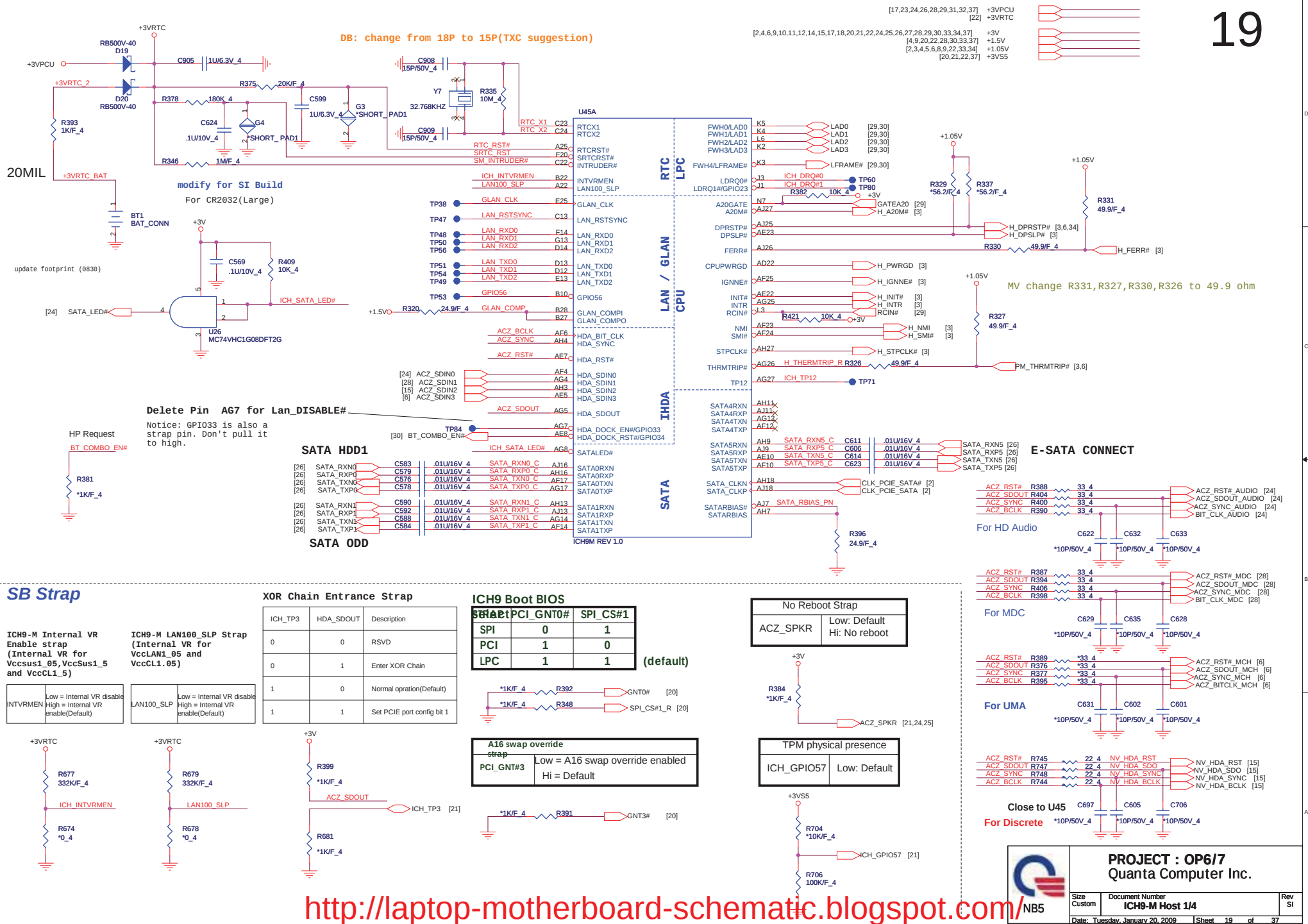
EMI





For UMA HDMI function







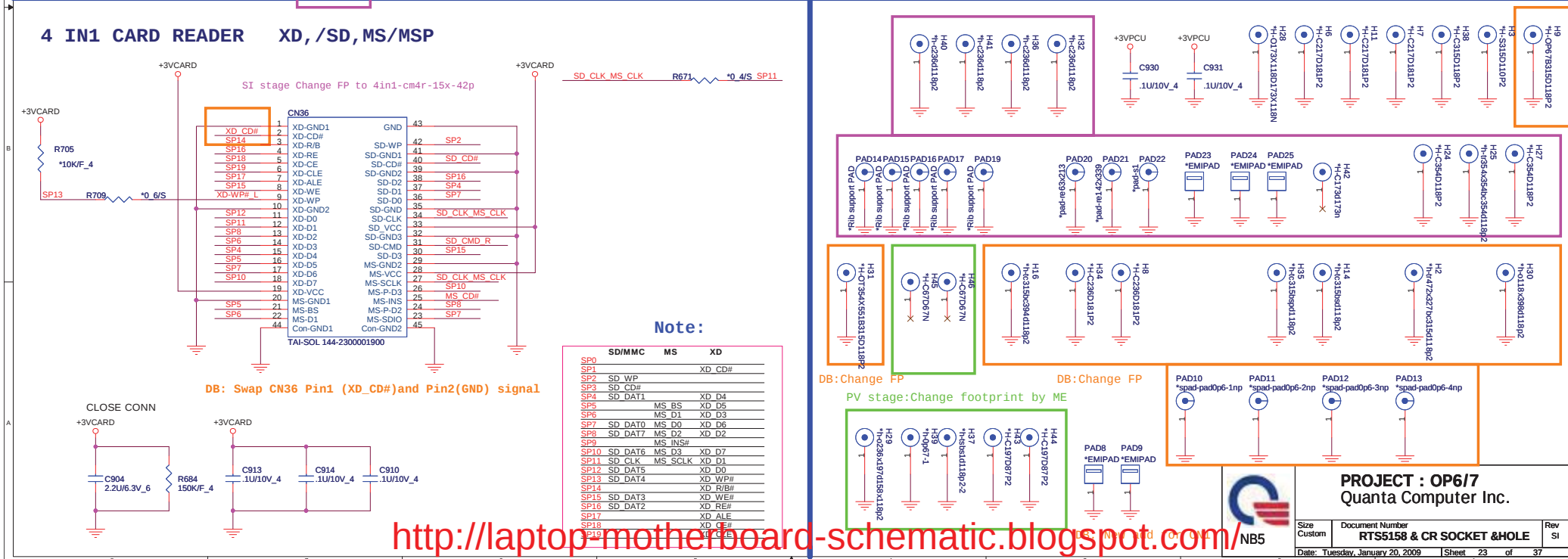
Board ID	ID0 GPIO19	ID1 GPIO21	ID2 GPIO22	ID3 GPIO36	ID4 GPIO37	ID5 GPIO17
UMA/DIS	0=UMA 1=Dis.					
CardReader		0=No Sup 1=Support				
HDMI			0=No Sup 1=Support			
Bluetooth						
Camera						
Modem						

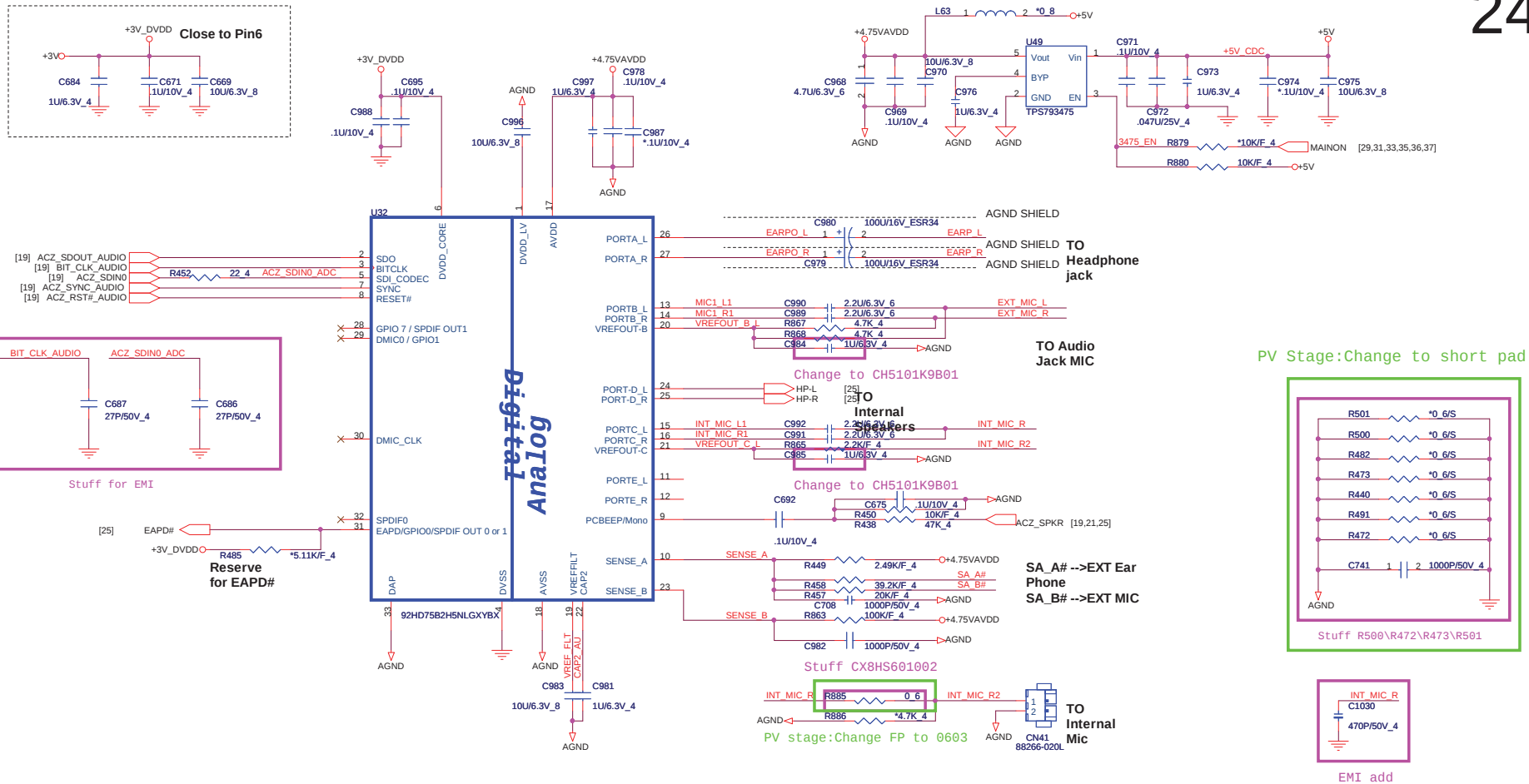


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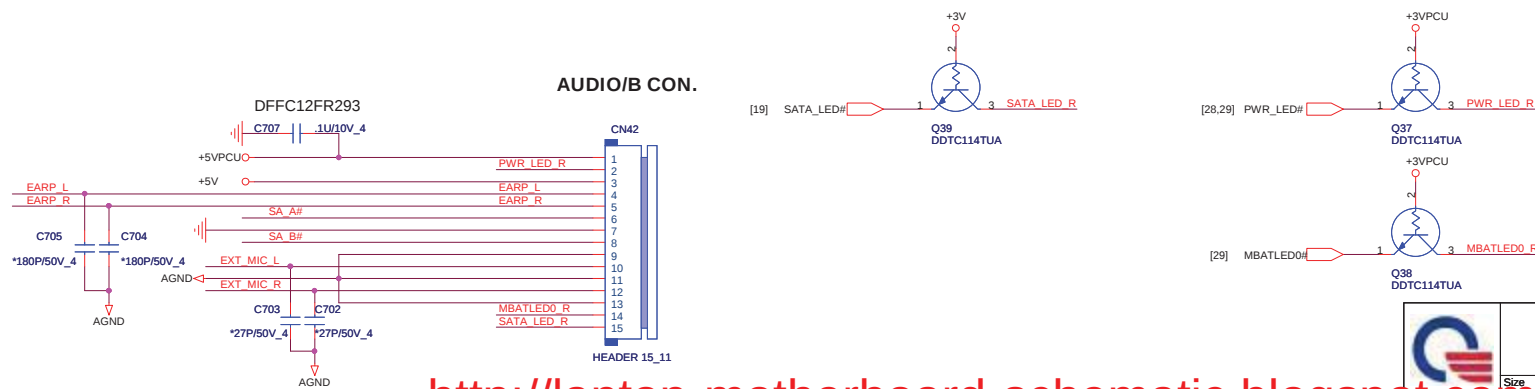
Size Custom	Document Number ICH9-M GPIO 3/4
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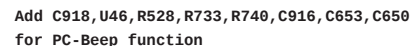
AUDIO/B CON.



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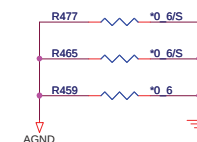
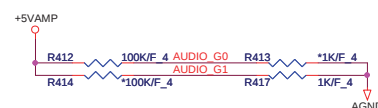
25

Change R468.R469 from 20K to 0 ohm as HP request

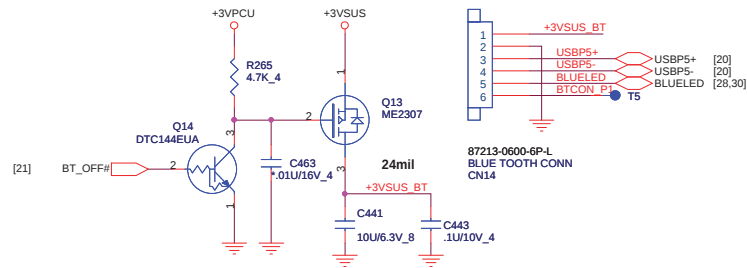


R528,C916 change to no-stuff for SI-2 test -->12/6

GAIN0	GAIN1	AV	RIN
0	0	6dB	90K
0	1	10dB	70K
1	0	15.6dB	45K
1	1	21.6dB	25K

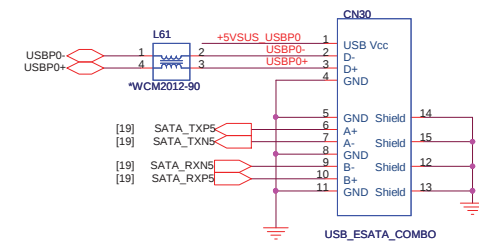
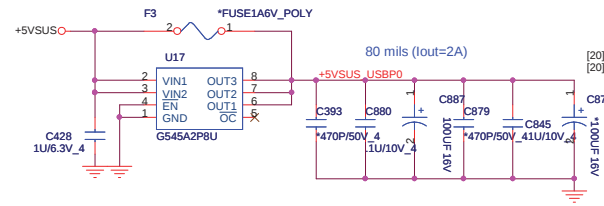


BLUETOOTH

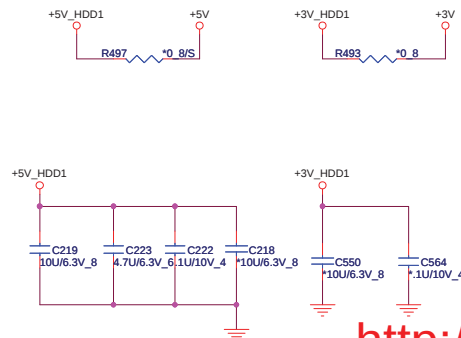
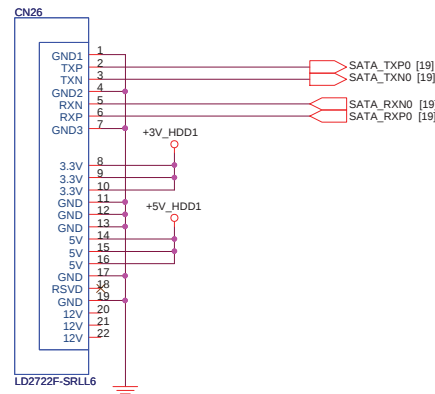


LEFT SIDE USBX1 and E-SATA/USB COMBO

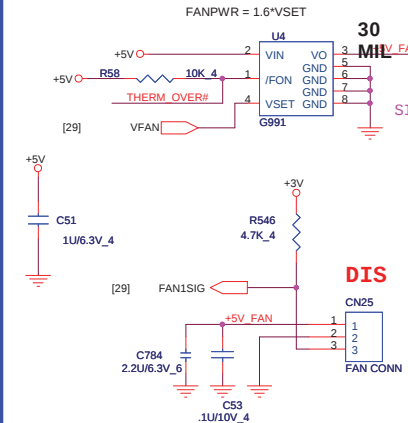
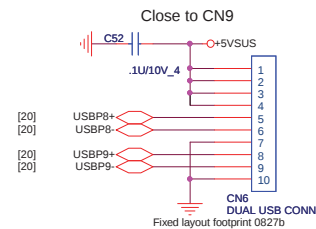
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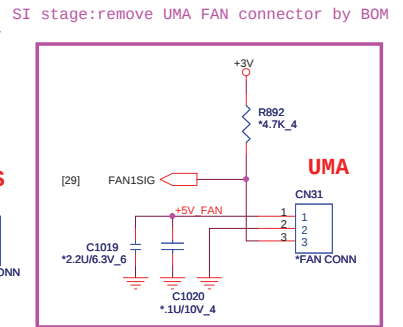
SATA_1 CONNECTOR



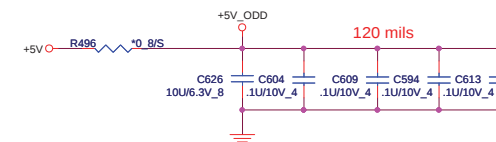
RIGHT SIDE USBX2



CPU FAN

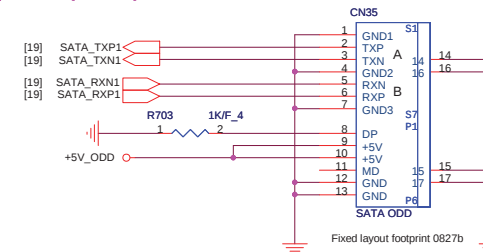


SATA CD-ROM



CN35 part number

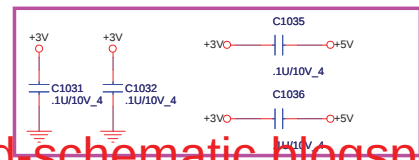
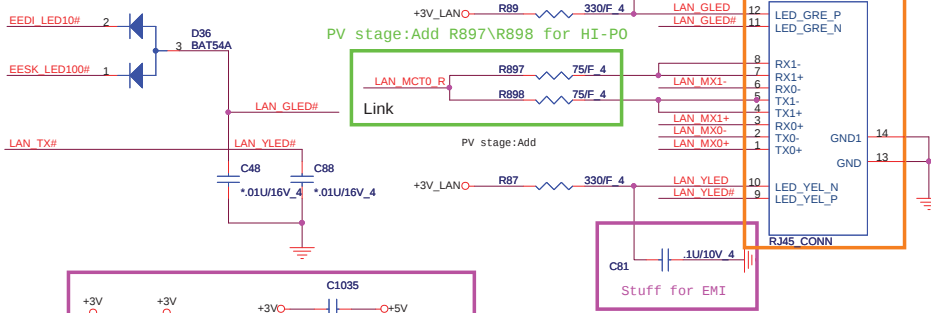
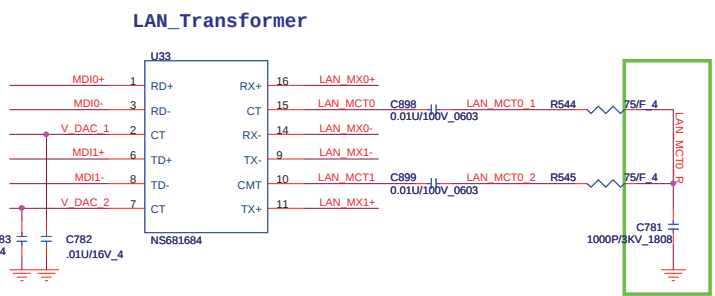
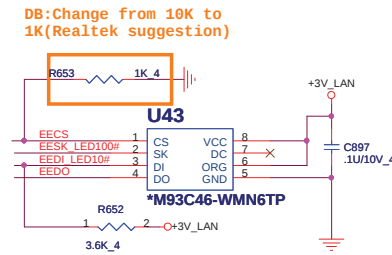
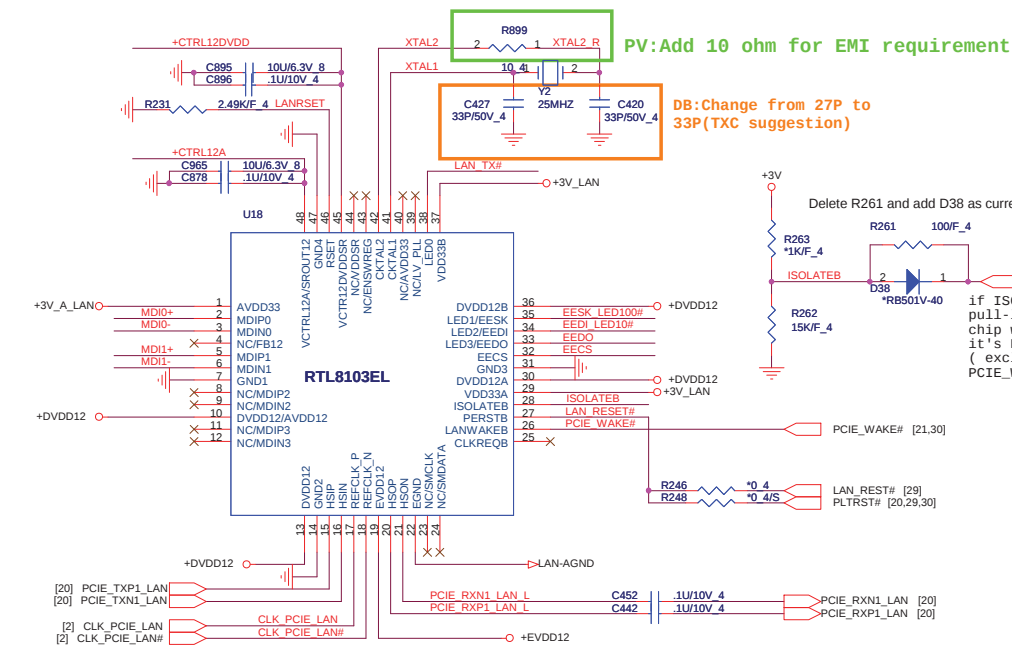
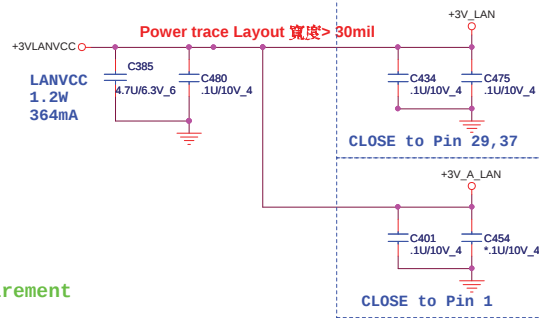
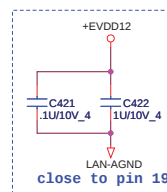
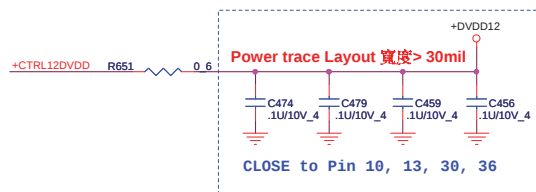
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	DFHD13MR008
	DFHD13MR009
UT5	DFHS13FR030
	DFHS13FR040



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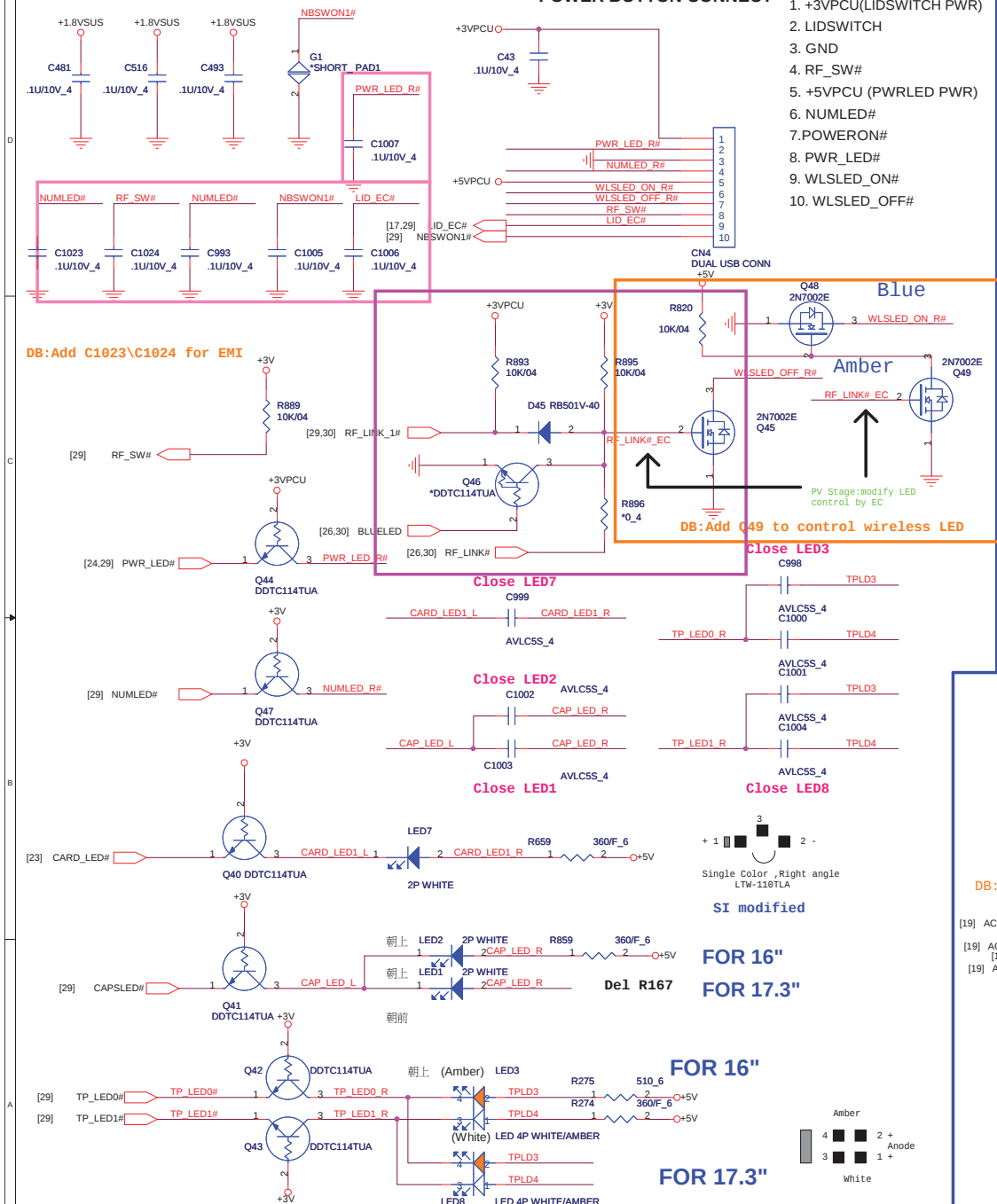
Size Custom	Document Number BT/ESATA/USB/ODD/HDD	Rev SI
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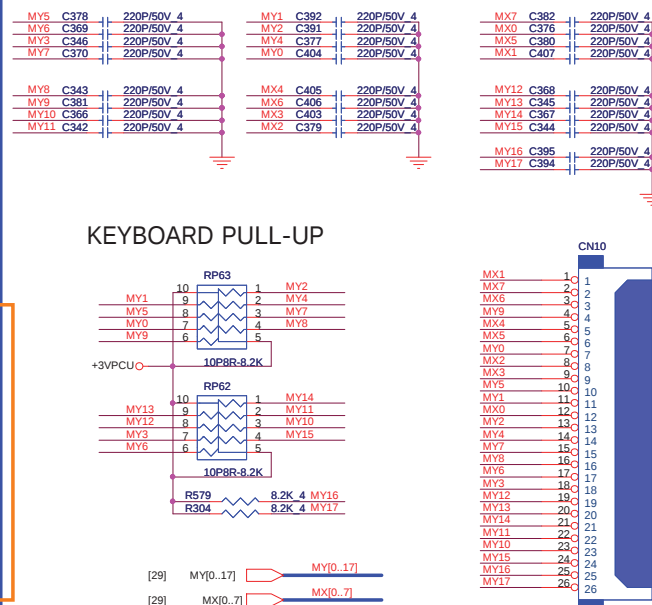


POWER BOTTON CONNECT

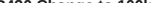
1. +3VPCU(LIDSWITCH PWR)
2. LIDSWITCH
3. GND
4. RF_SW#
5. +5VPCU (PWRLED PWR)
6. NUMLED#
- 7.POWERON#
8. PWR_LED#
9. WLSLED_ON#
10. WLSLED_OFF#

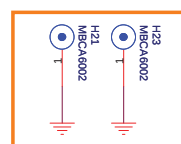


KEYBOARD PULL-UP

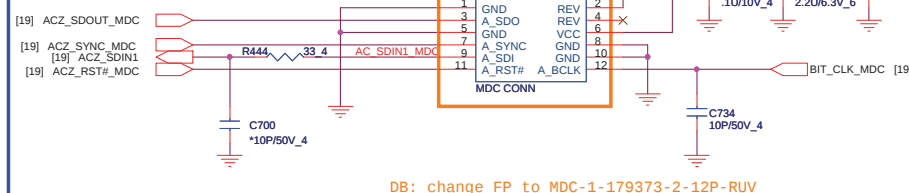


Footprint: "qb1rf260-1253-7f-26p-1

 R430 Change to 100k ohm ,pull low [26,30]



DB: change FP to H-C217D147P2

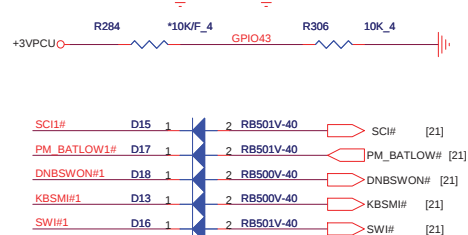
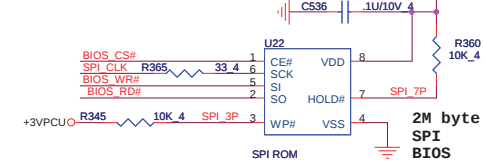
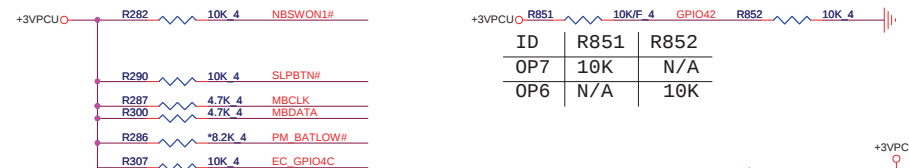
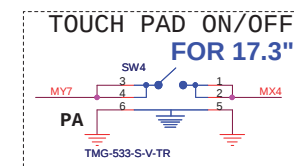
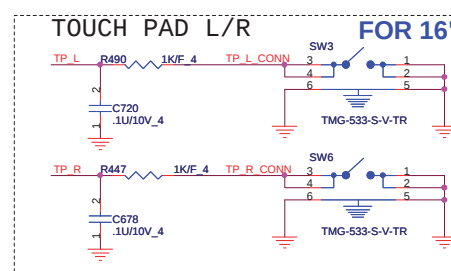
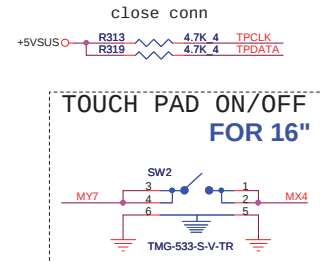
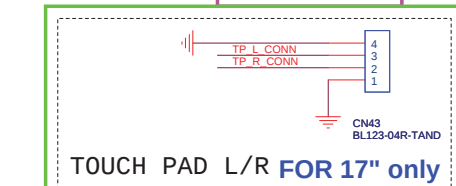
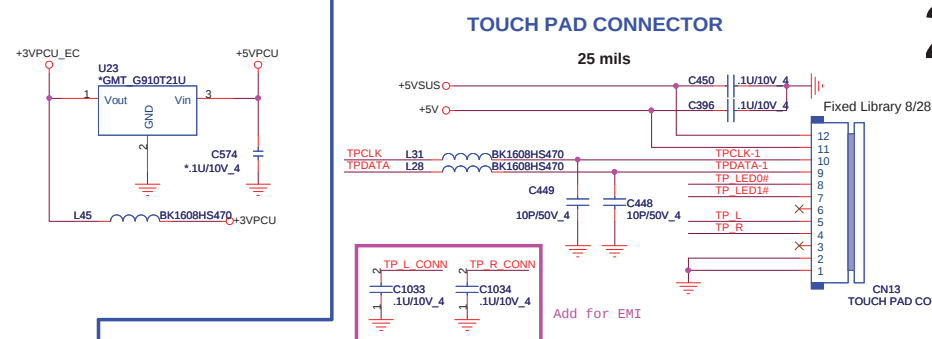
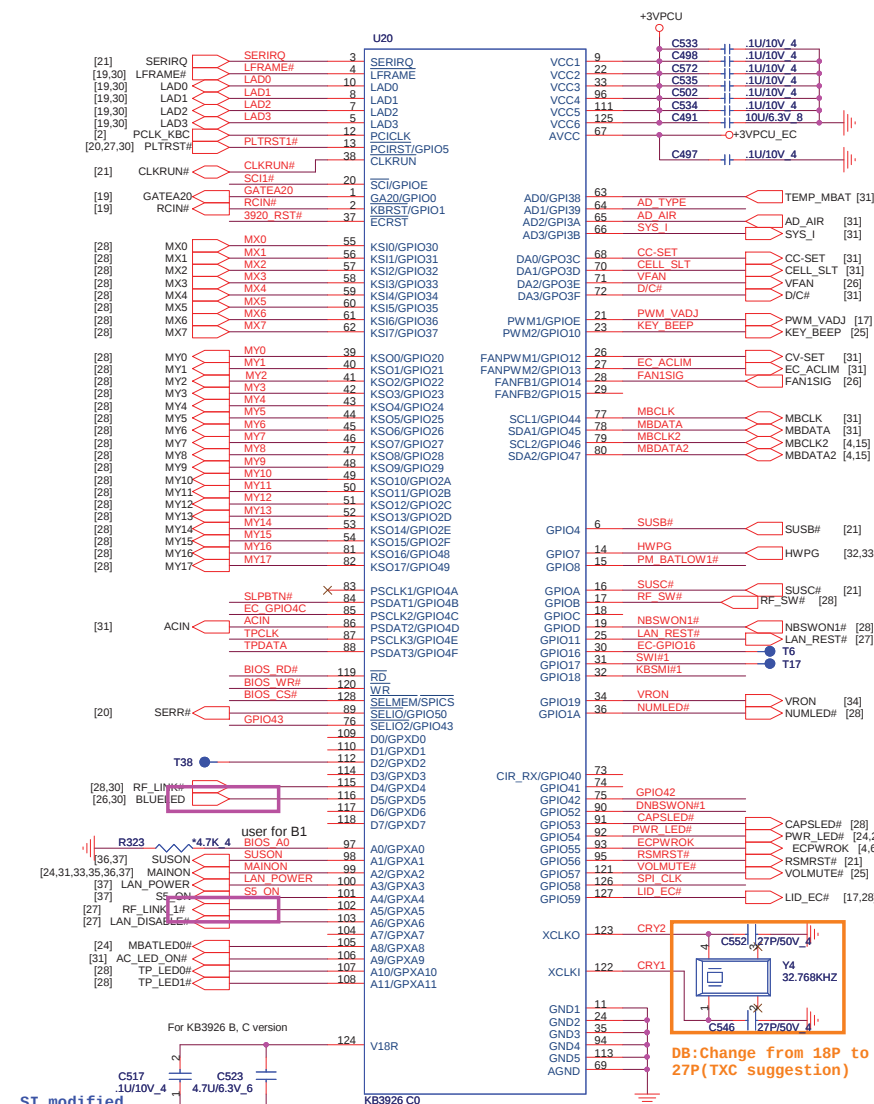


DB: change FP to MDC-1-179373-2-12P-RUV

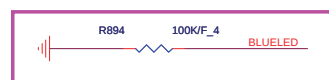


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Size Custom	Document Number KB/LED/POWER CONN/MDC
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Hi ==> 120W
Low ==> 65W/90W



Add Pin 117,103 for DSM, Pin 116 for Bluetooth, Pin 23 for Key Beep to Amplifier
Delete T37, T38, T39 for EC
Delete T10 and tie pin 117 from Lan for DSM

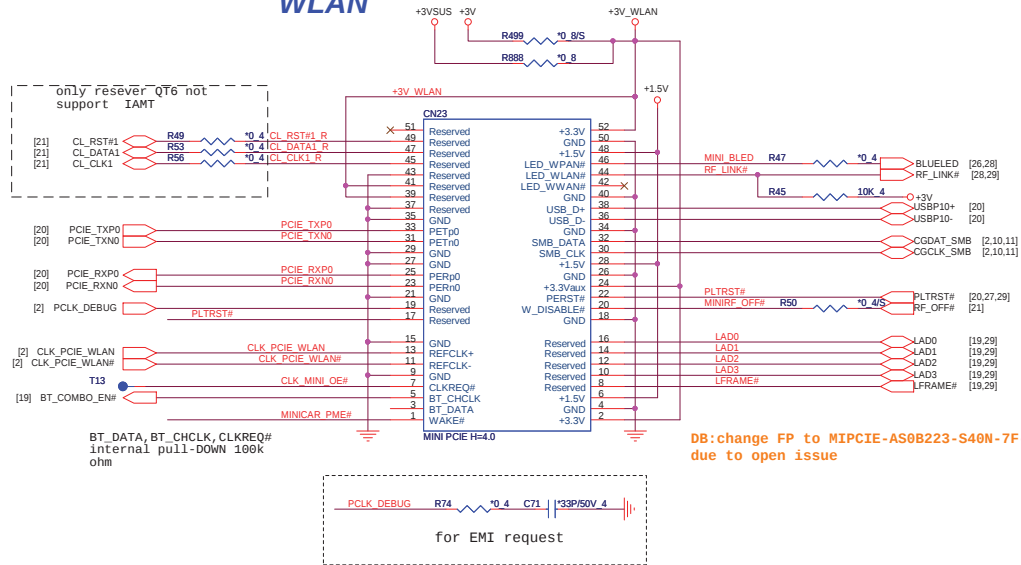
<http://laptop-motherboard-schematic.blogspot.com/>



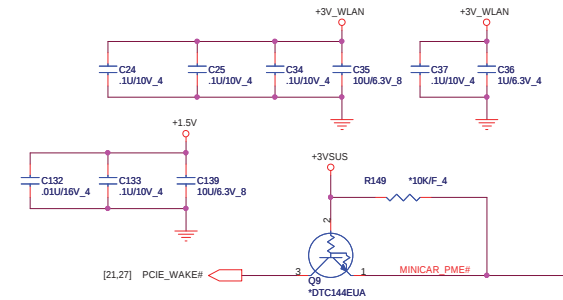
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Size: 29.5 x 21.5 cm
Document Number: KB3926/ROM/TP
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Mini PCI-E Card 1 WLAN



INTEL WLAN
CARD PIN 20
W_DISABLE#
have
internal
pull-up 110k
ohm



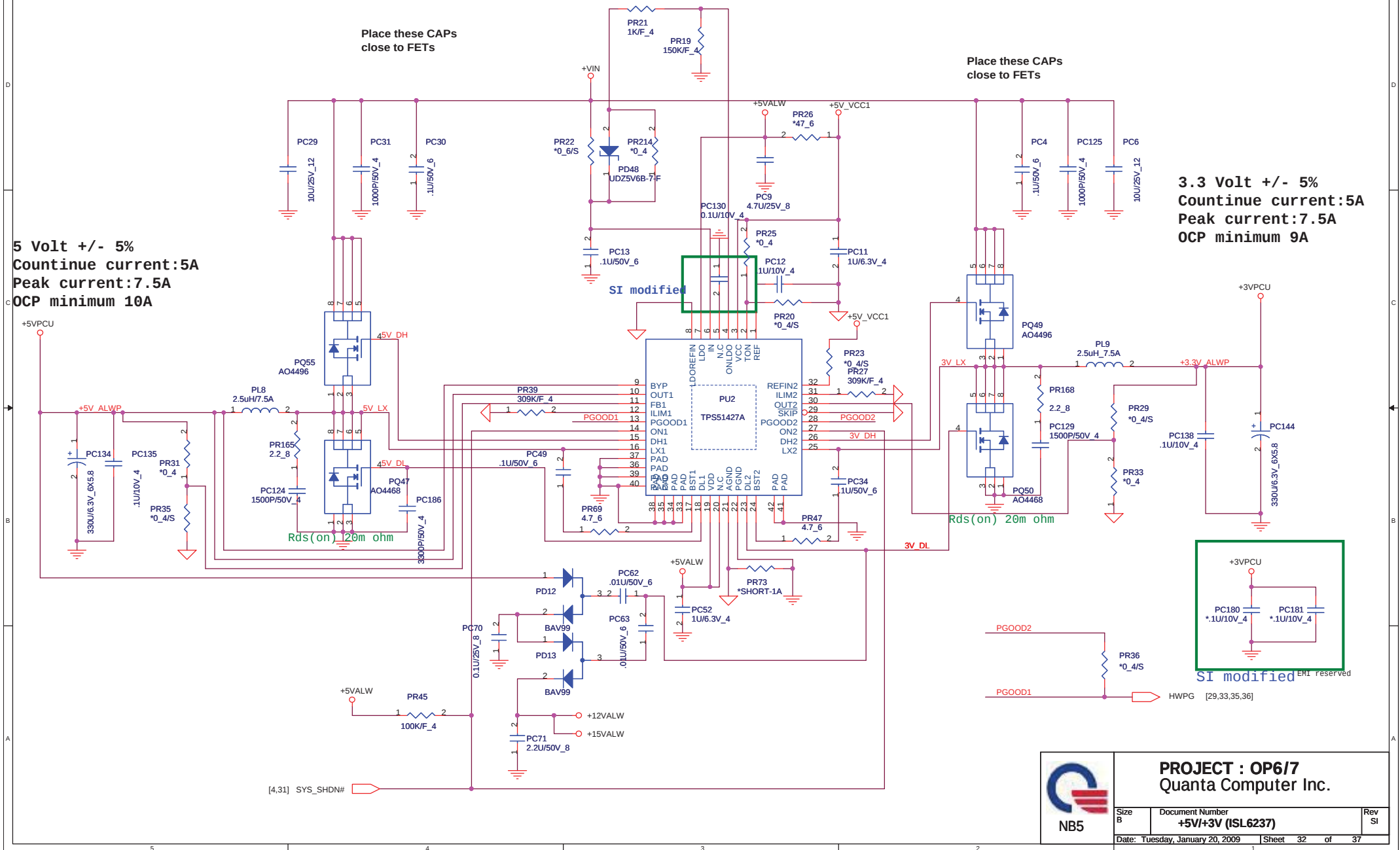
DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V_ALW

Place these CAPS
close to FETs

Place these CAPS
close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

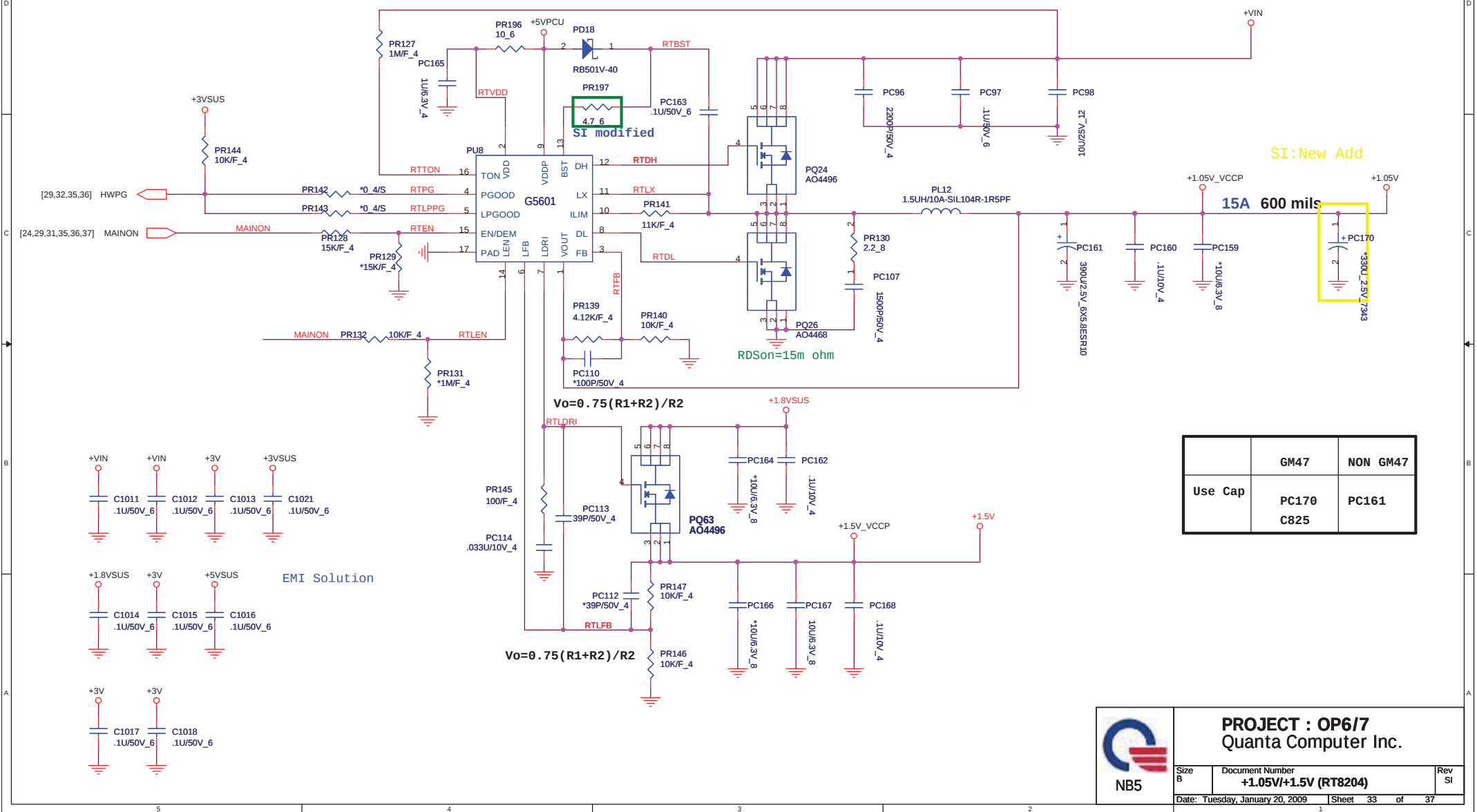
3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

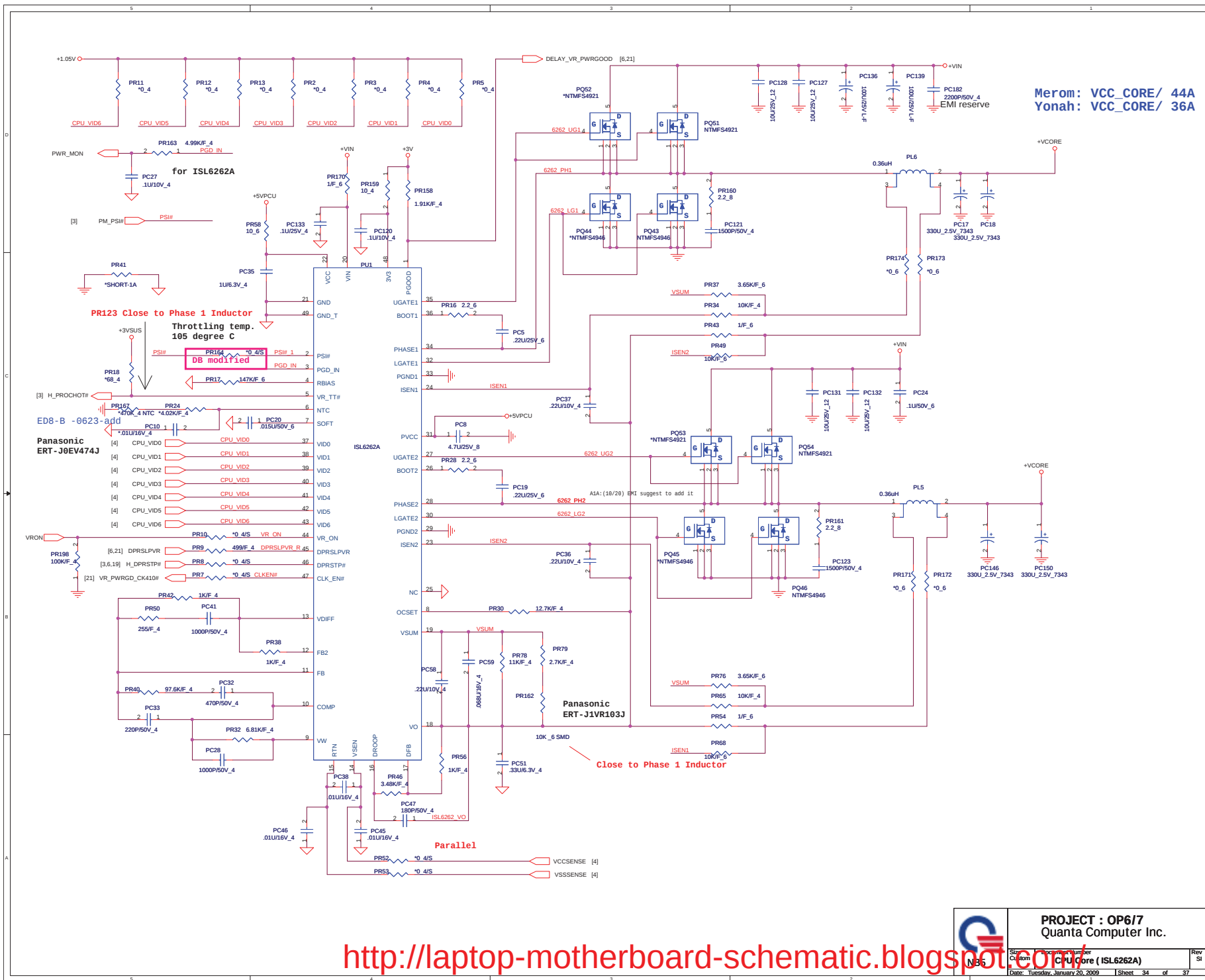


			PROJECT : OP6/7 Quanta Computer Inc.		
Size B	Document Number +5V/+3V (ISL6237)				Rev SI
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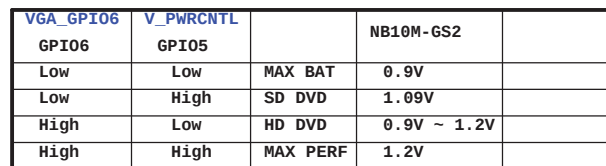
VCCP1.05V & +1.5V

+1.05Volt +/- 5%
Countinue current:7.5A
Peak current:10A
OCP minimum 15A

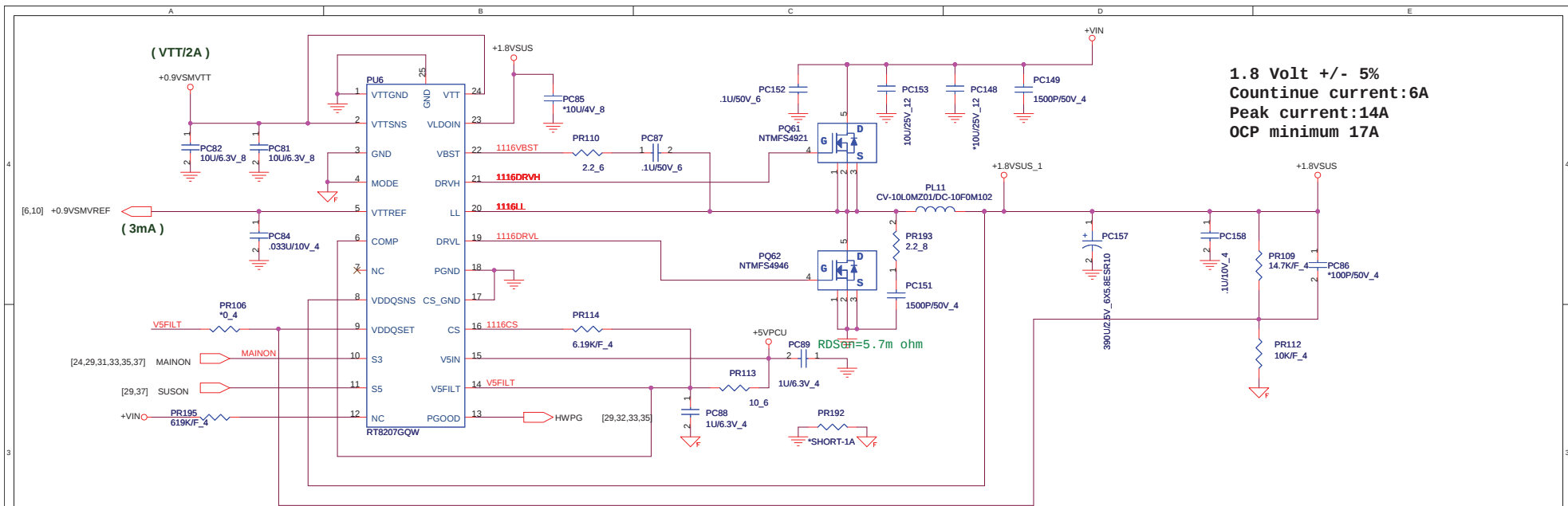




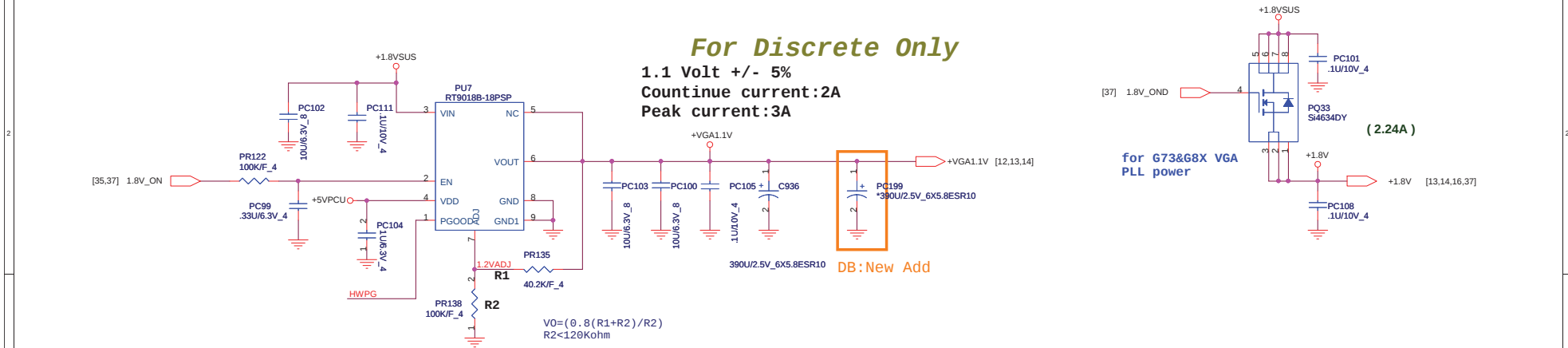
+1.1Volt +/- 5%
Continue current:17.54A
Peak current:22.8A
OCP minimum 23A



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Change PR122 tied to 1.8V_ON as power sequence reuqest



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Size	Document Number	Rev
Standard	1.8V/DDR_VTER/+1.8v/+1.1V	SI
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