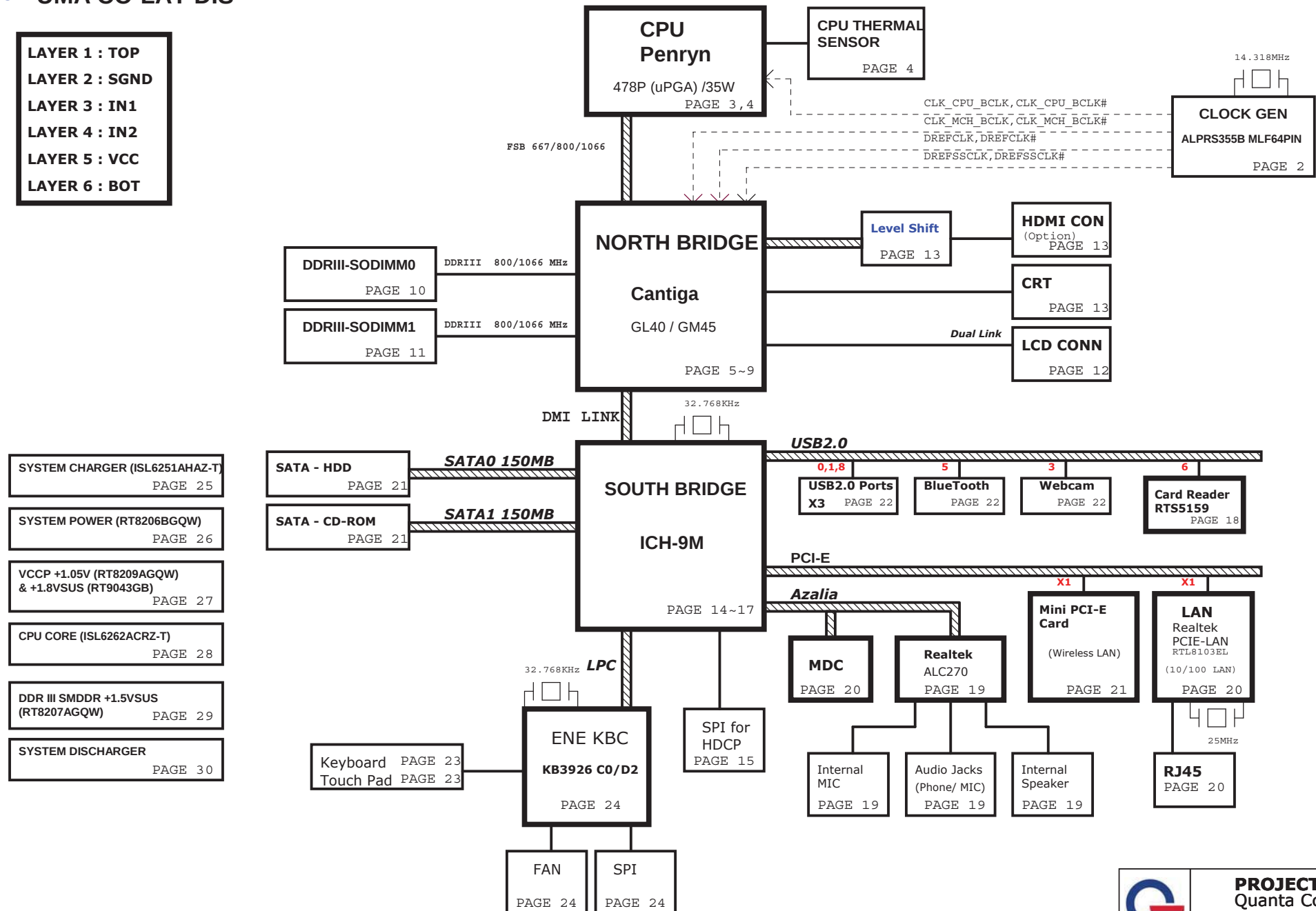
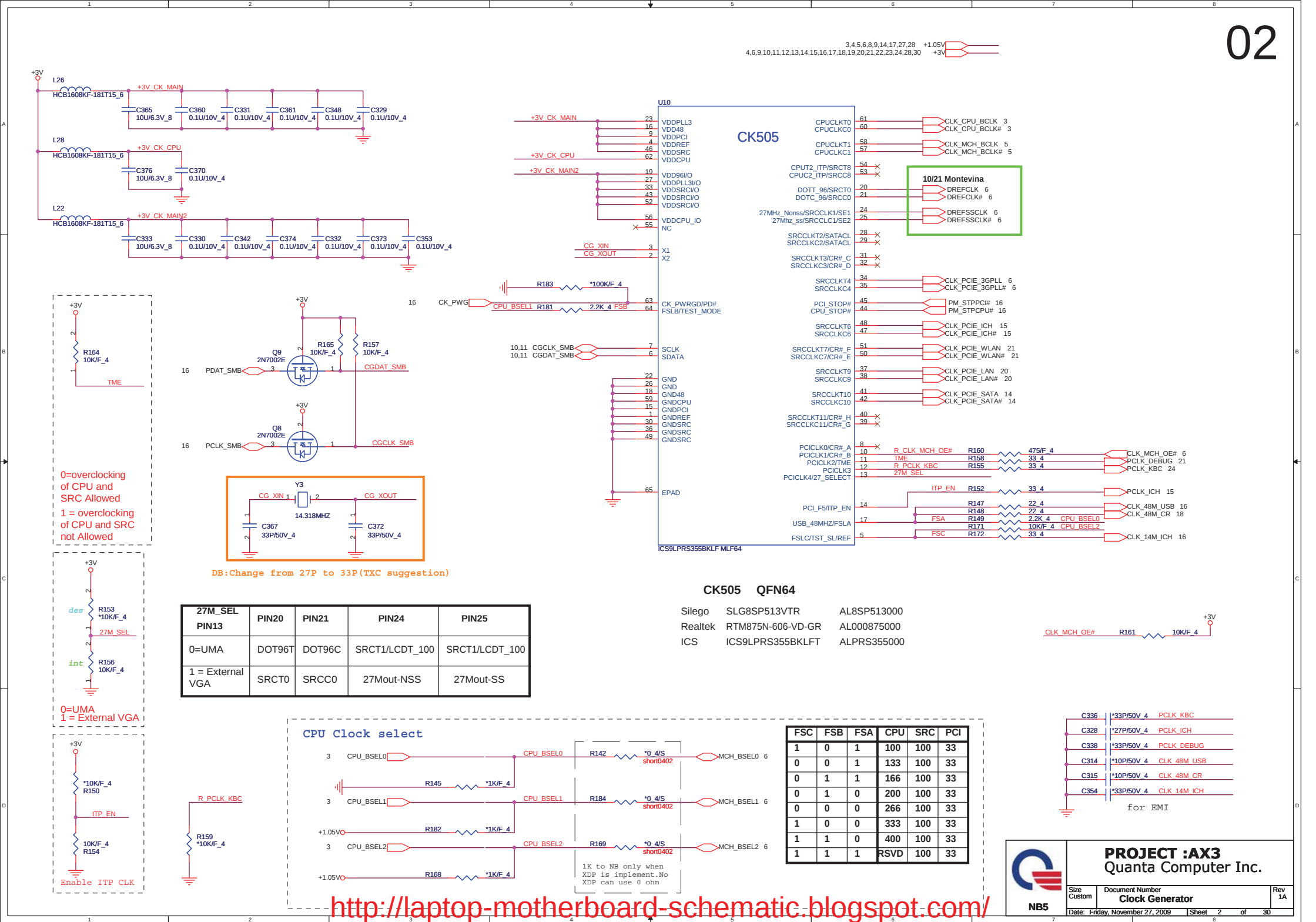
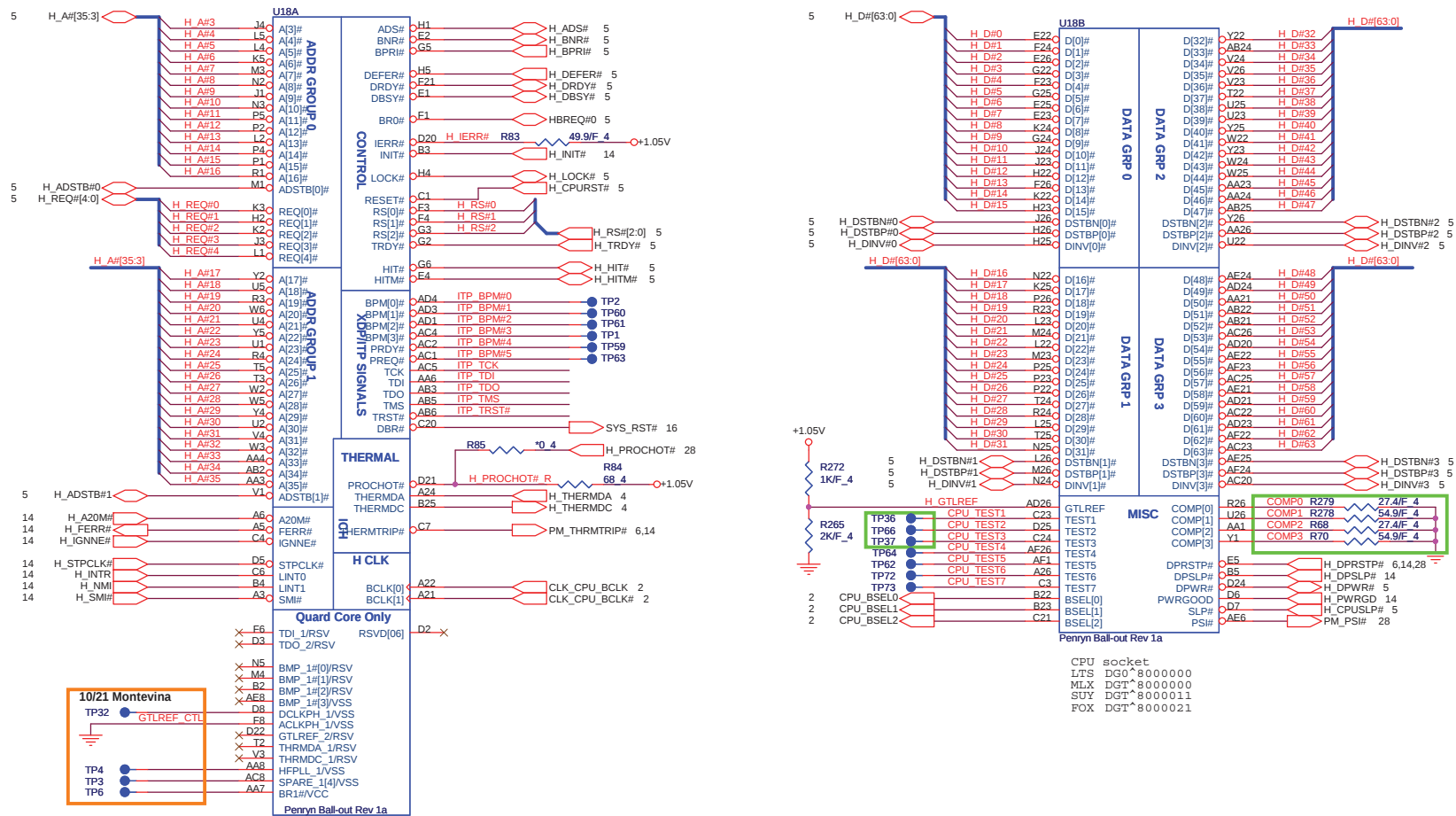


LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : BOT

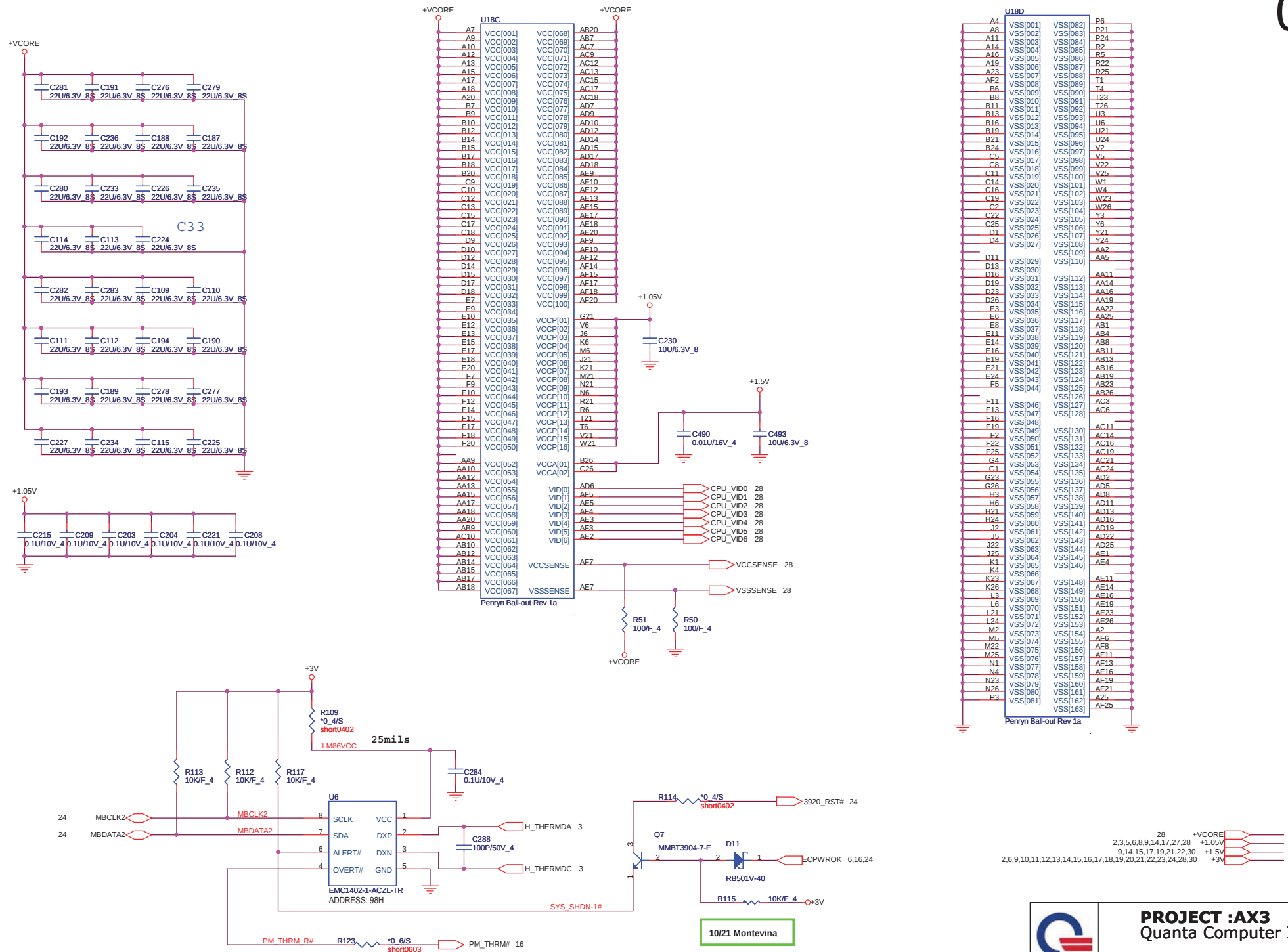


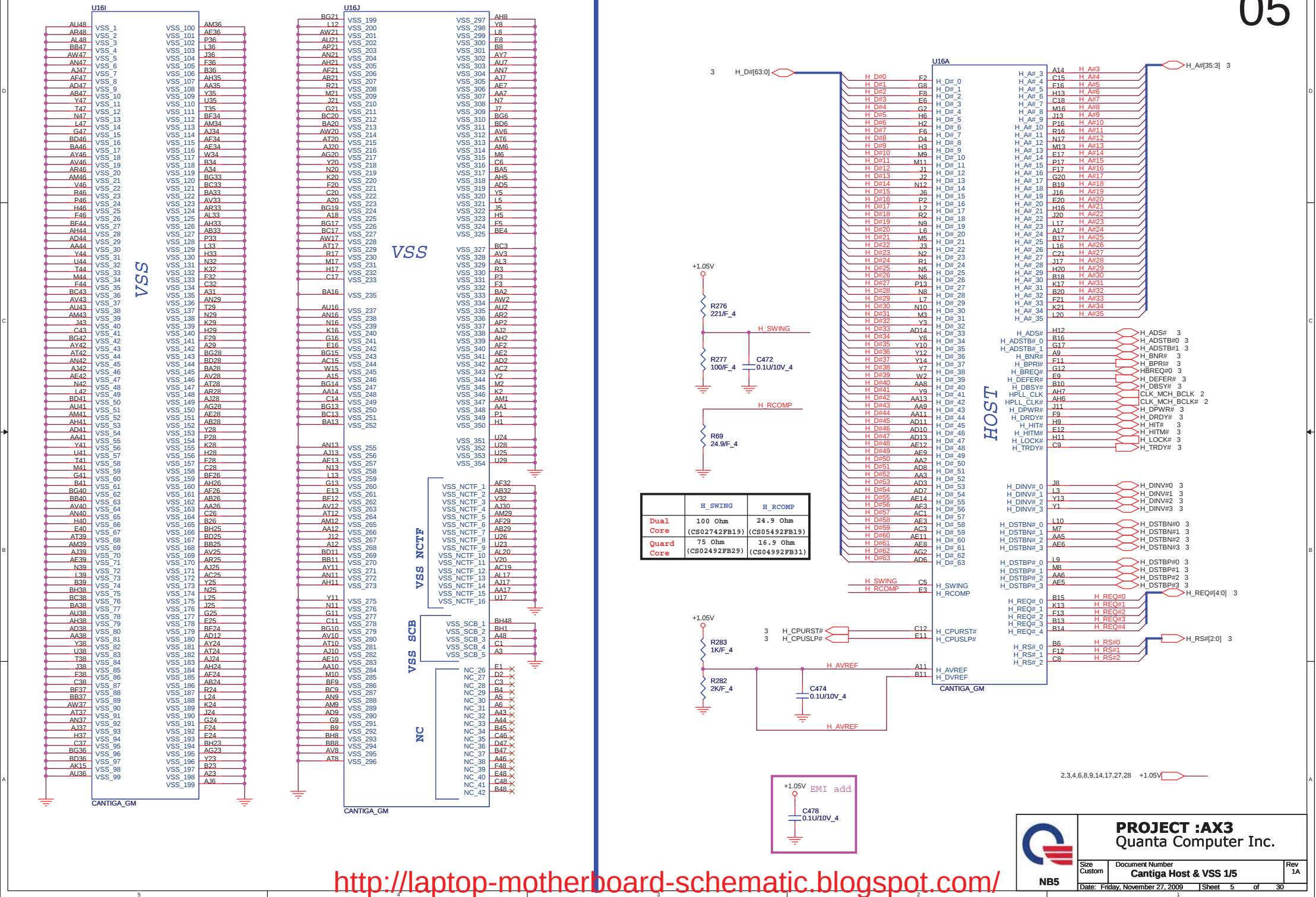




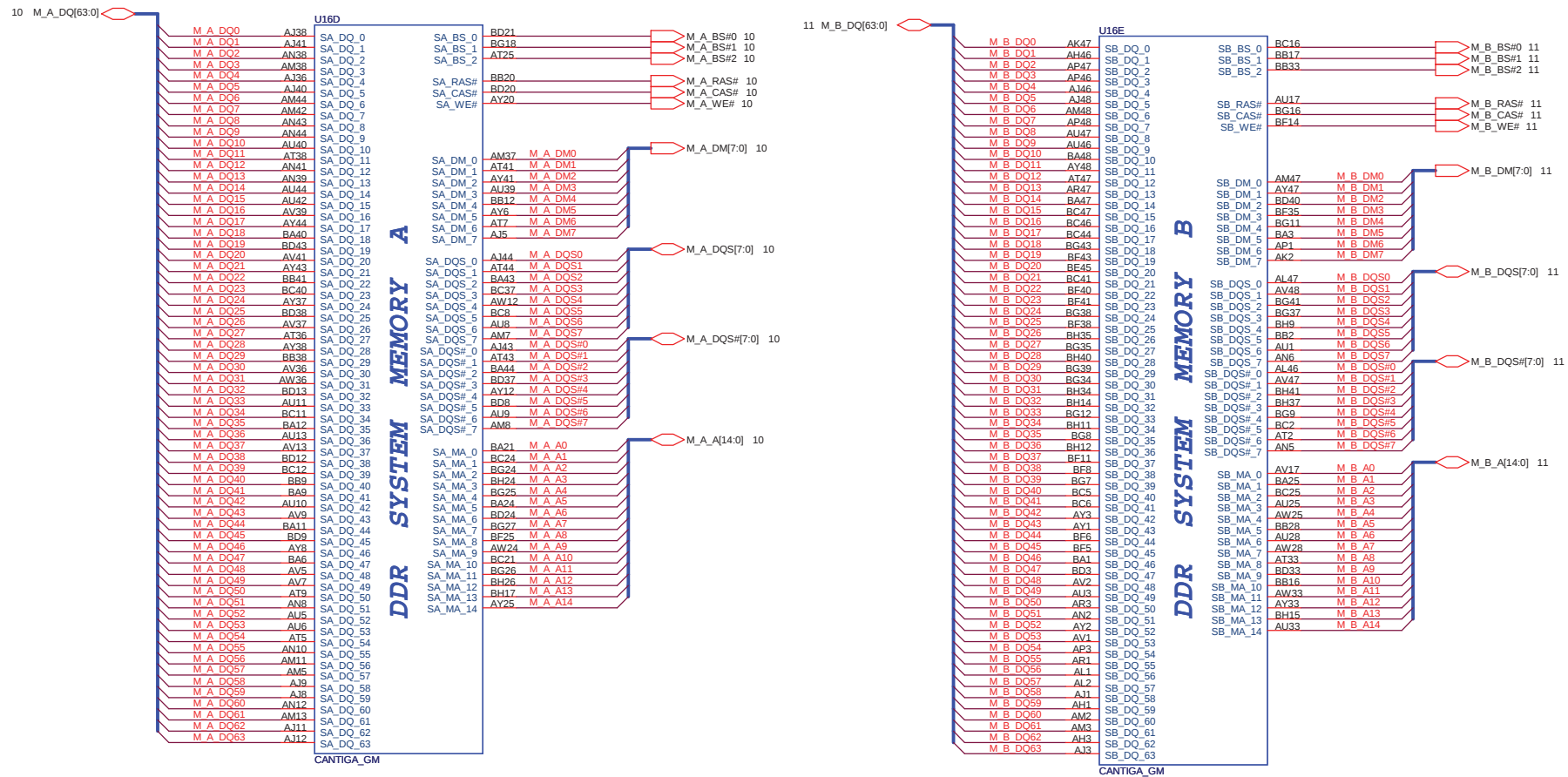
	COMP0/2	COMP1/3
Dual Core	27.4 Ohm (C802742FB19)	54.9 Ohm (C805492FB19)
Quad Core	24.9 Ohm (C802492FB29)	49.9 Ohm (C804992FB31)

2,4,5,6,8,9,14,17,27,28 +1.05V

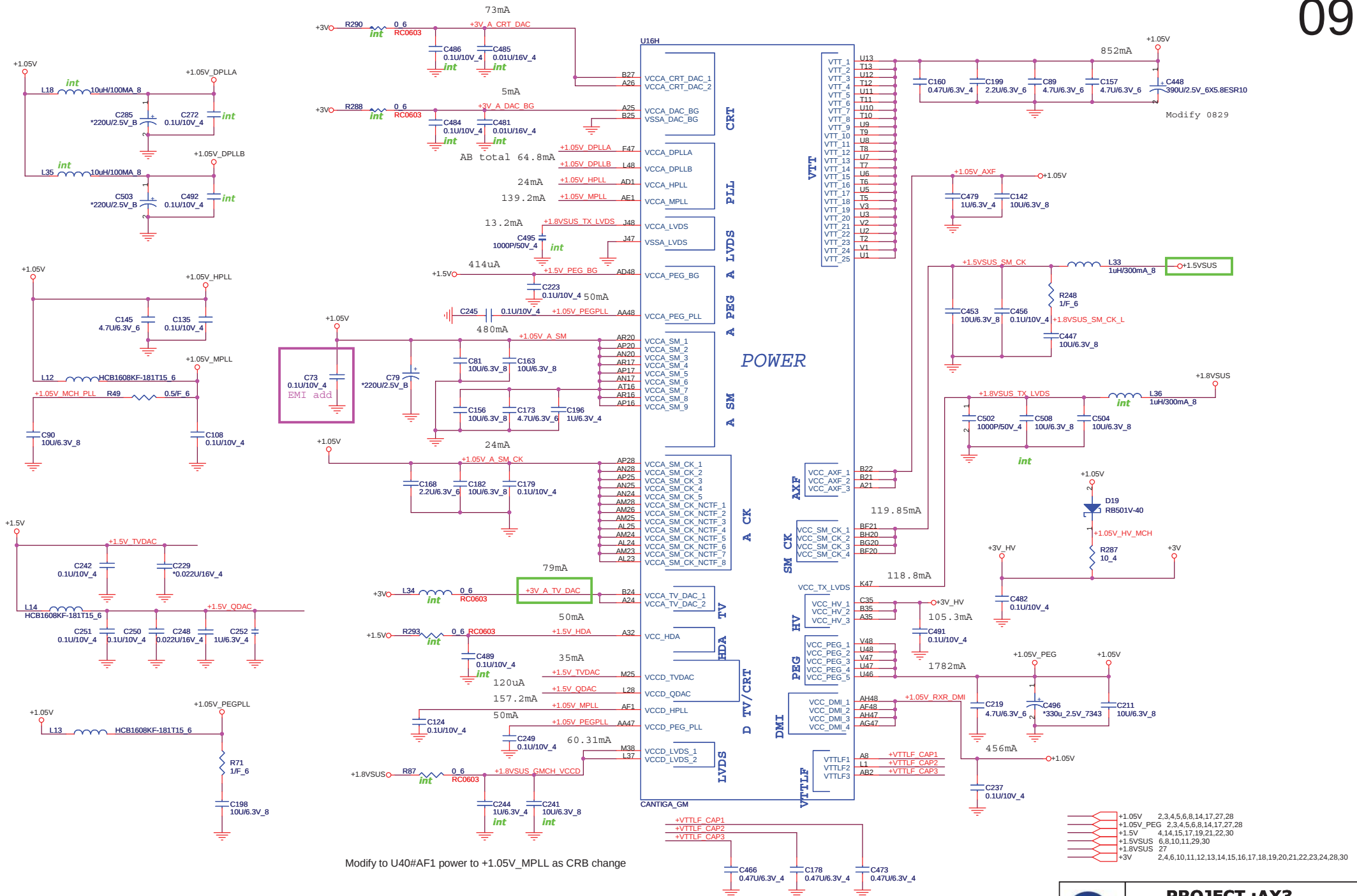


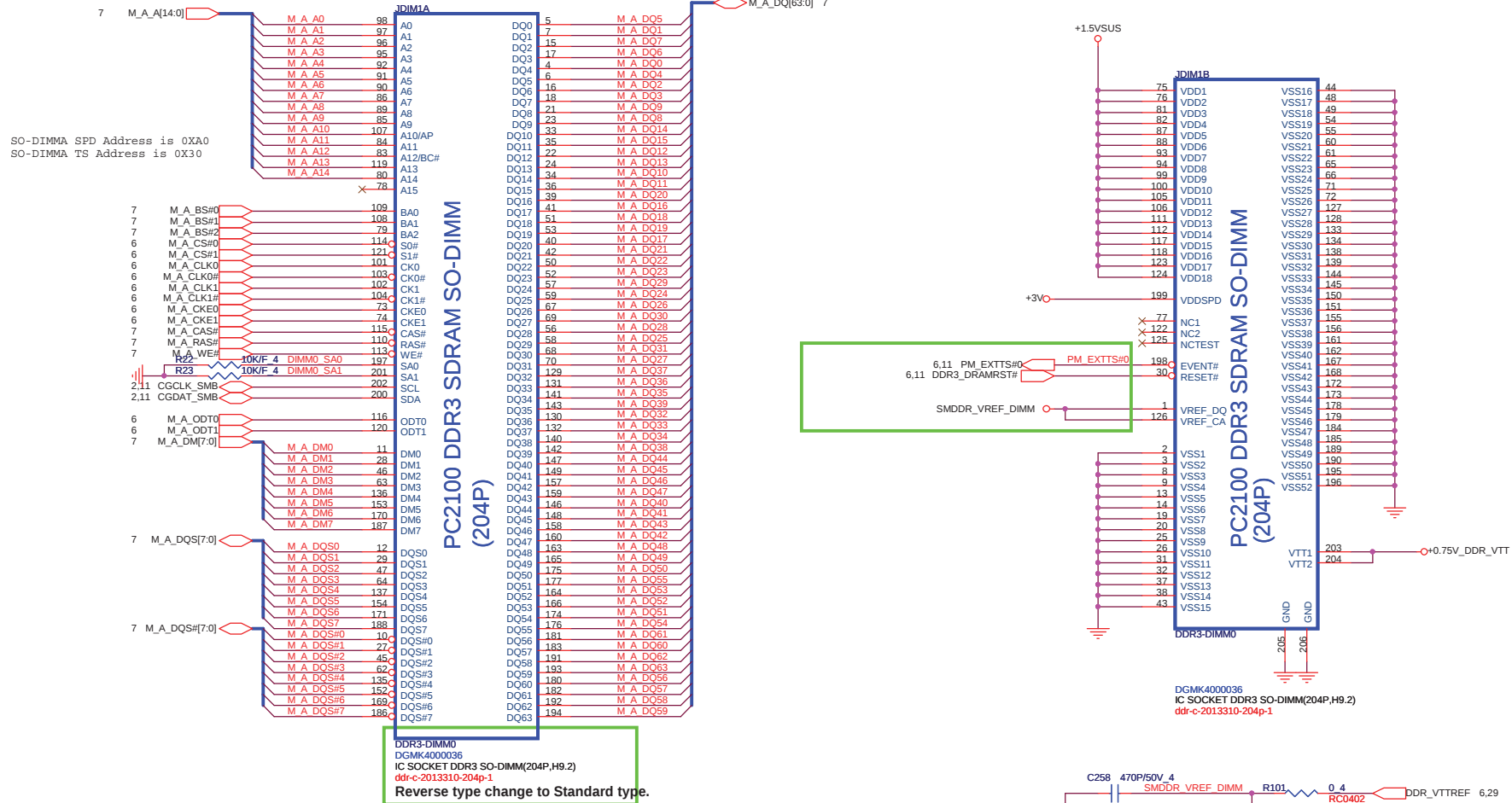






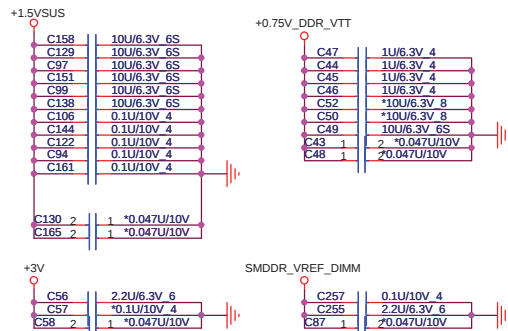


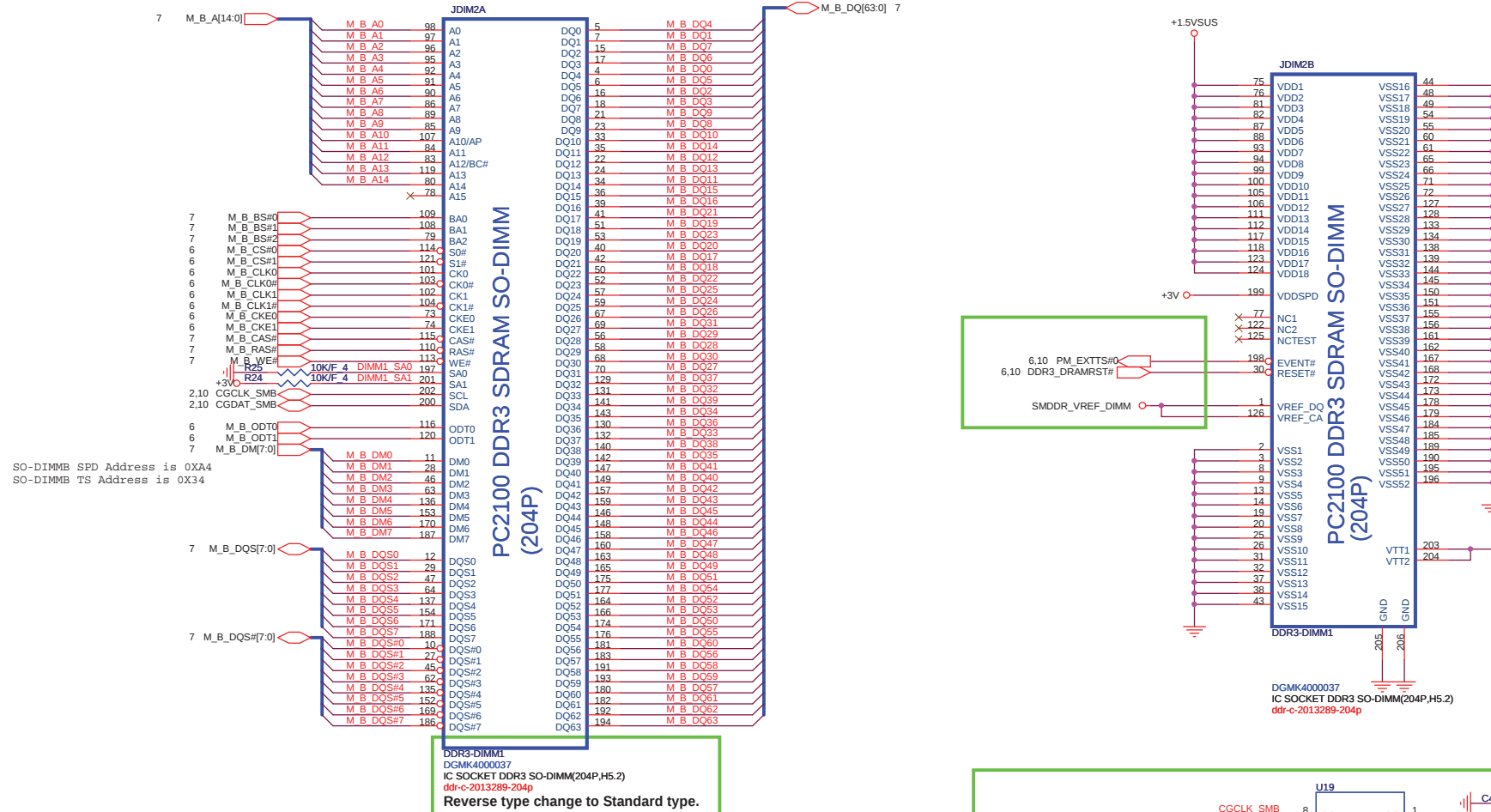




Place these Caps near So-Dimm0.

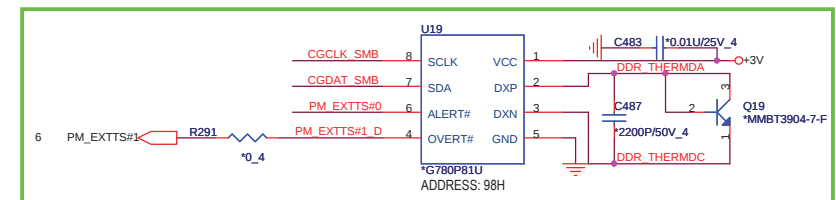
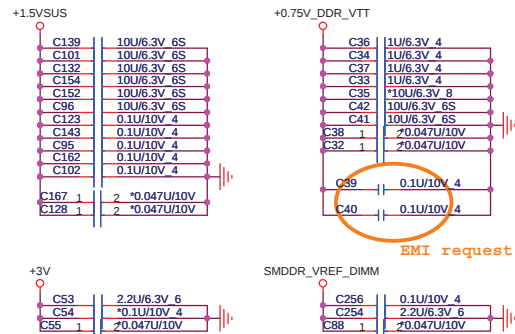
Some Projects replace 10UF 0805 by 4.7UF 0603
It can cost down 30%





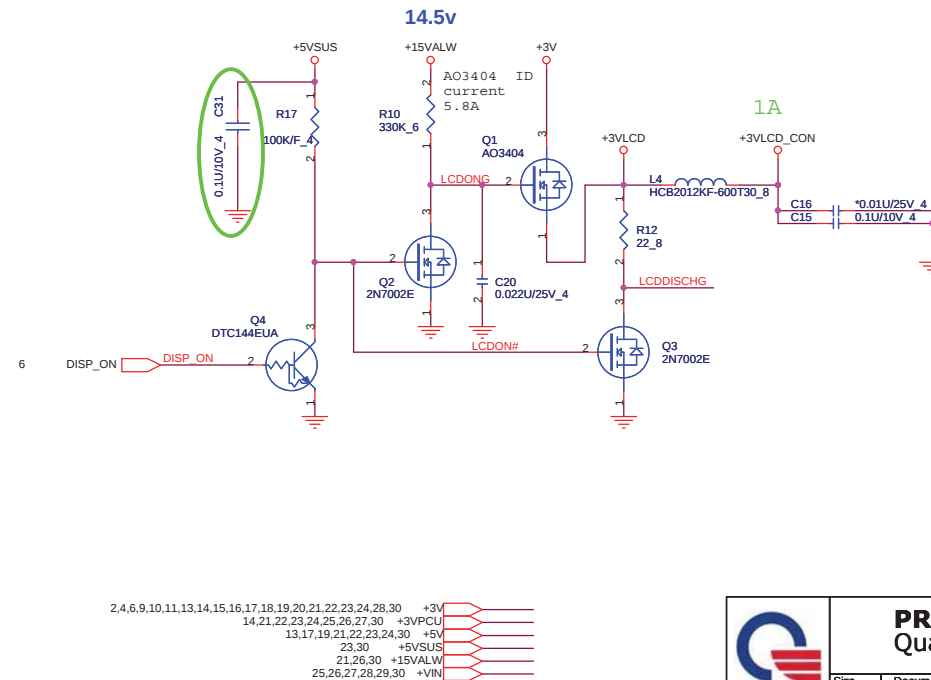
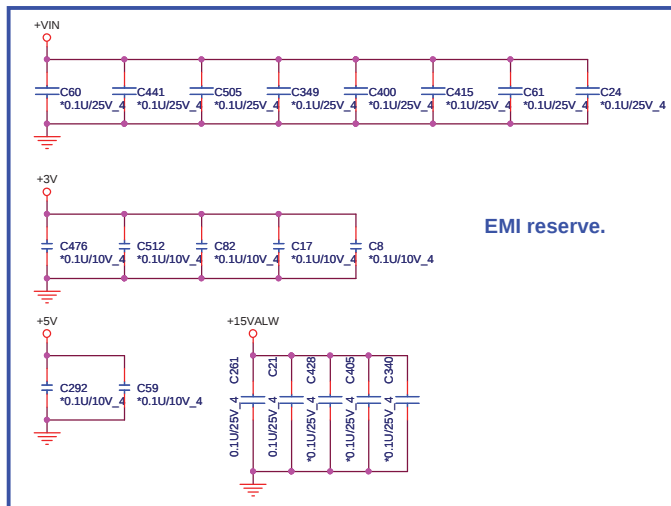
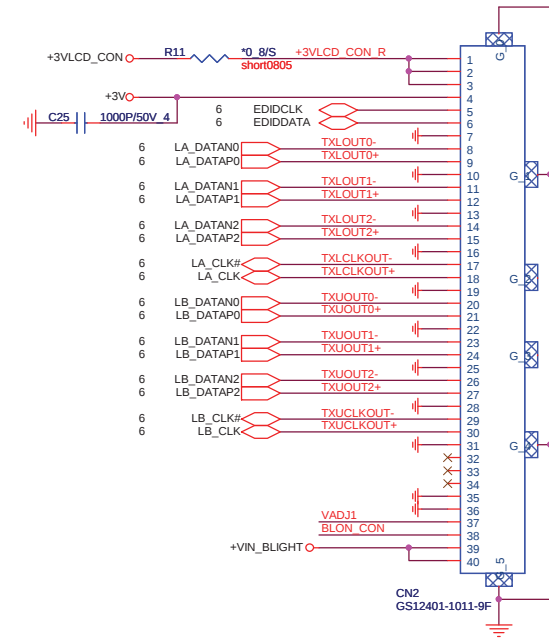
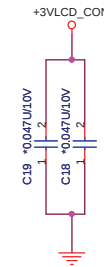
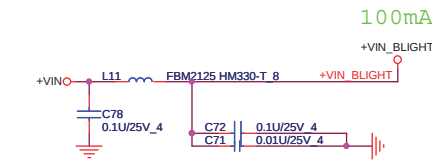
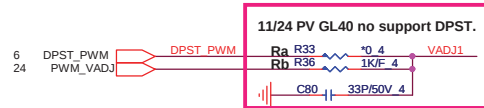
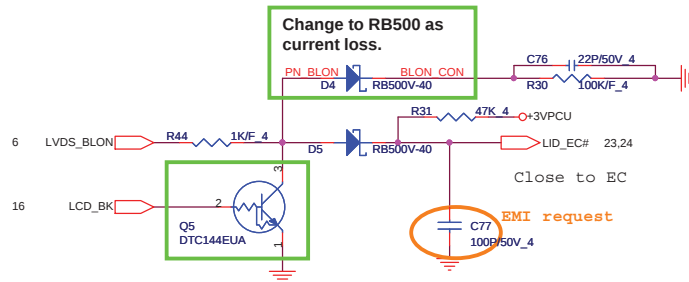
Place these Caps near So-Dimm1.

Some Projects replace 10UF 0805 by 4.7UF 0603
It can cost down 30%

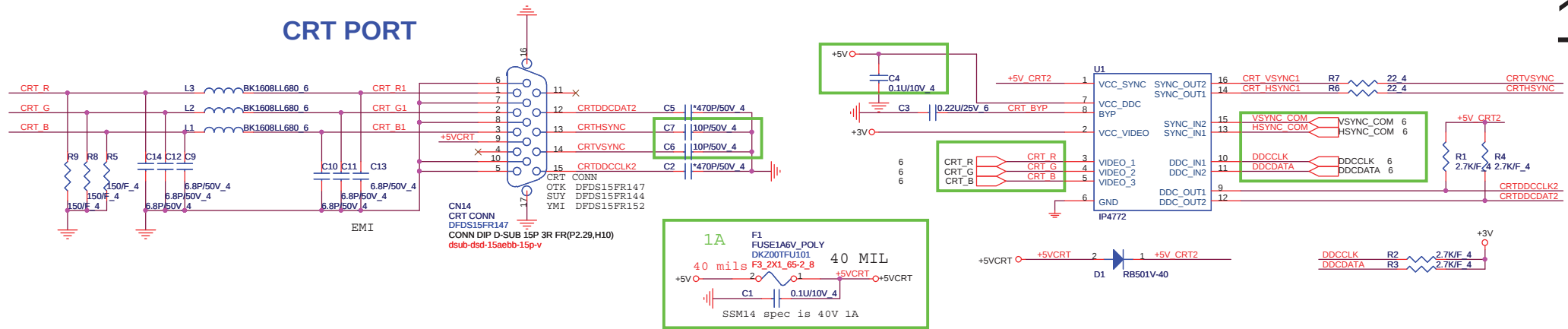


10,29 +0.75V_DDR_VTT
6,8,9,10,29,30 +1.5VSUS
2,4,6,9,10,12,13,14,15,16,17,18,19,20,21,22,23,24,28,30 +3V

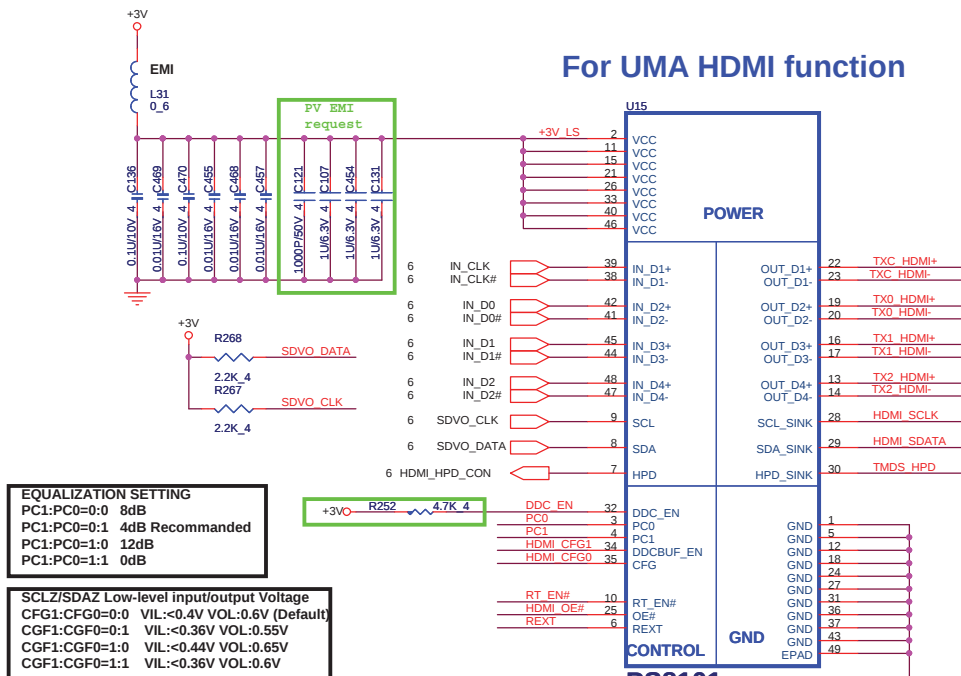
LID Switch



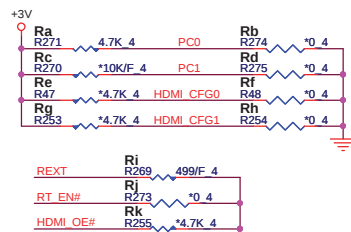
CRT PORT



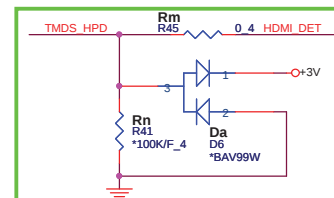
For UMA HDMI function



Signals	R	PDT	CHR	PIM
PC0	Ra	4.7K	NC	NC
PC1	Rb	NC	NC	0
PC1	Rc	NC	10K	NC
PC1	Rd	NC	NC	0
HDMI_CFG0	Re	NC	10K	NC
HDMI_CFG0	Rf	NC	NC	0
HDMI_CFG1	Rg	NC	10K	NC
HDMI_CFG1	Rh	NC	NC	0
REXT	Ri	499	1.2K	0
RT_EN#	Rj	NC	NC	0
HDMI_OE#	Rk	NC	0	0



Detect	R	PDT	CHR	PIM
IC	Rm	0	20K	0
IC	Rn	NC	47K	NC
IC	Da	NC	Stuff	NC
NB (Page6)	Ro	NC	20K	20K
NB (Page6)	Rp	NC	7.5K	7.5K
NB (Page6)	Rq	0	0	0

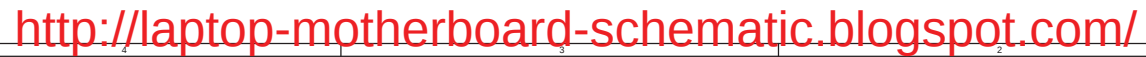


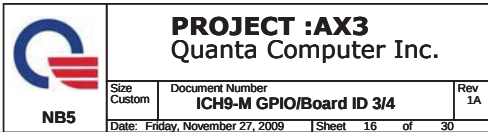
2,4,6,9,10,11,12,14,15,16,17,18,19,20,21,22,23,24,28,30
12,17,19,21,22,23,24,30 +3V
+5V



PROJECT :AX3
Quanta Computer Inc.

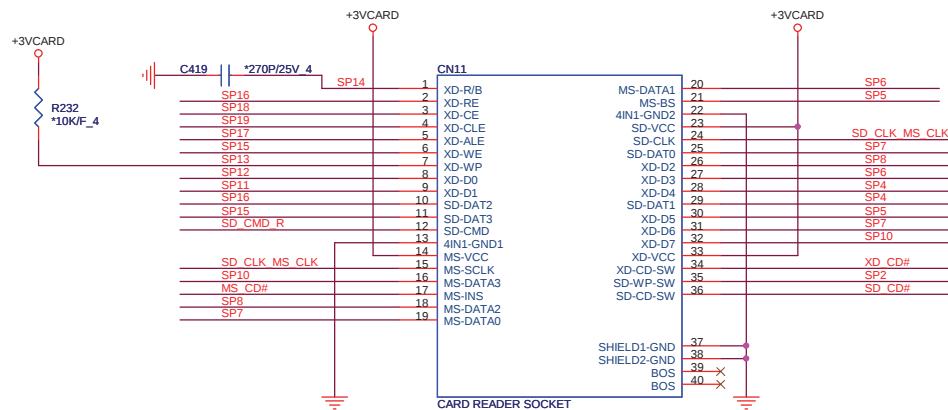
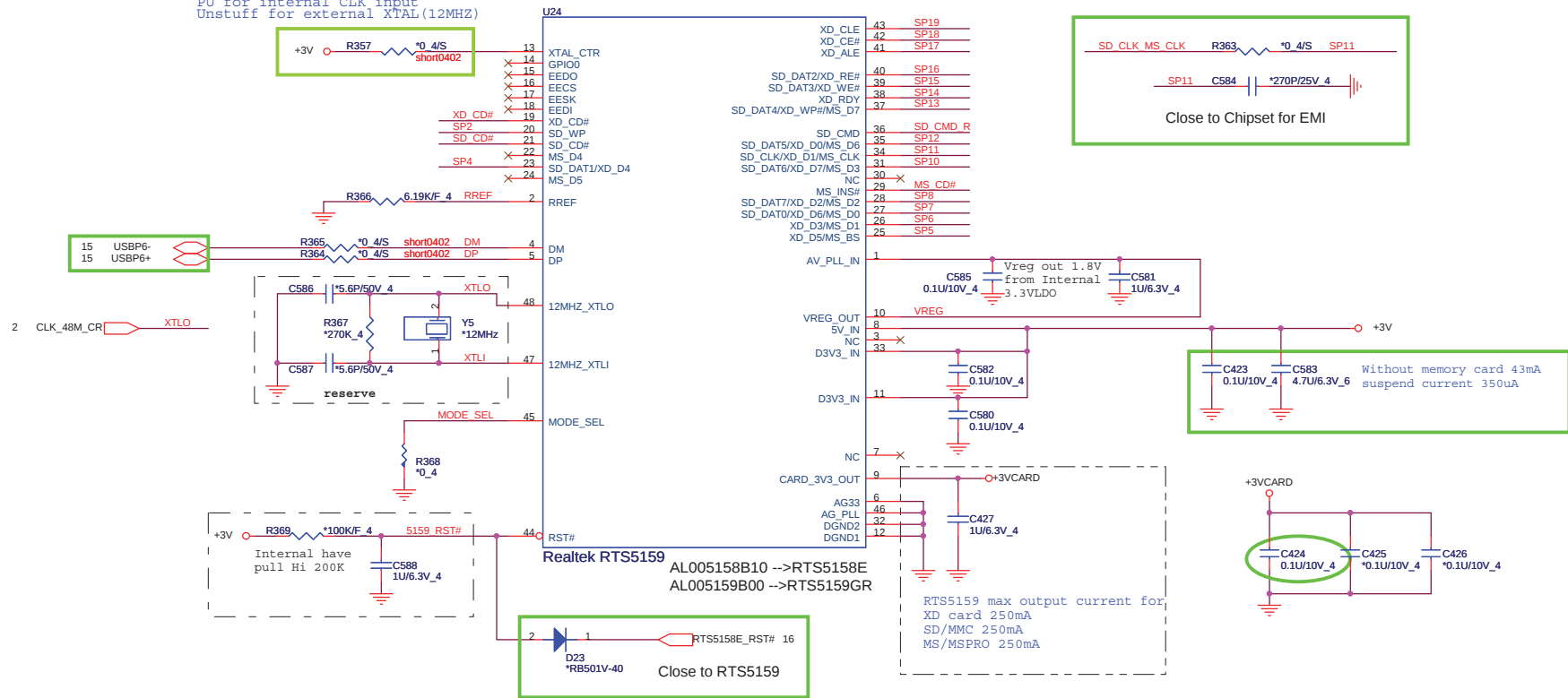
Size Custom Document Number CRT/HDMI Conn Rev 1A
Date: Friday, November 27, 2009 Sheet 13 of 30





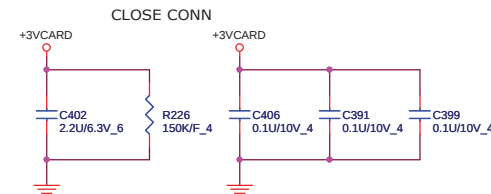


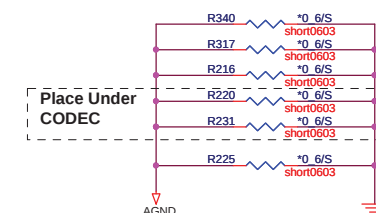
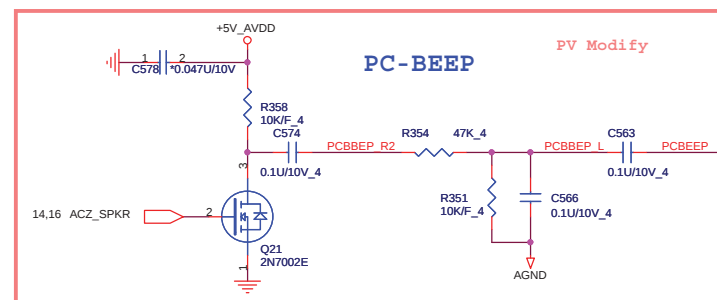
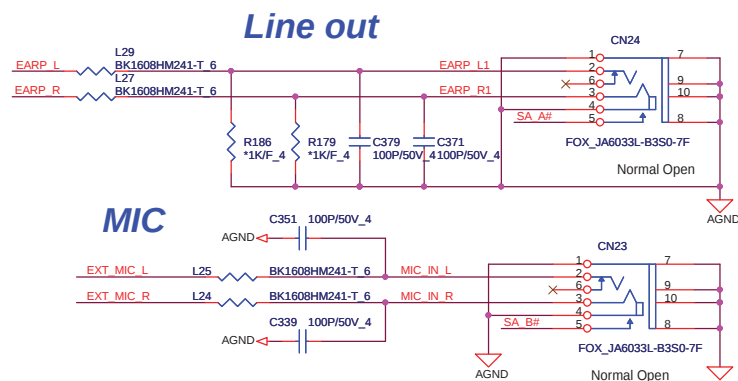
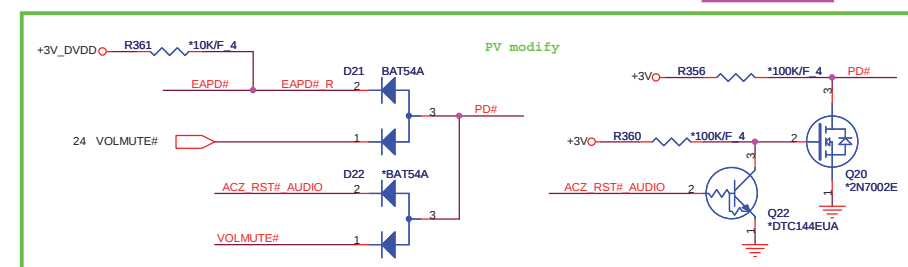
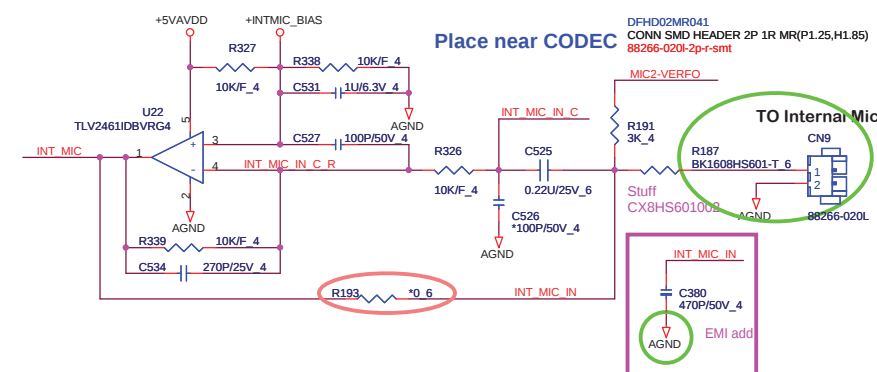
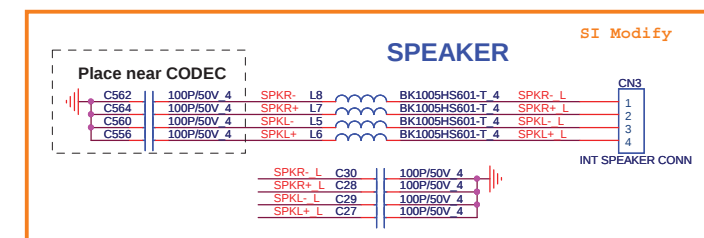
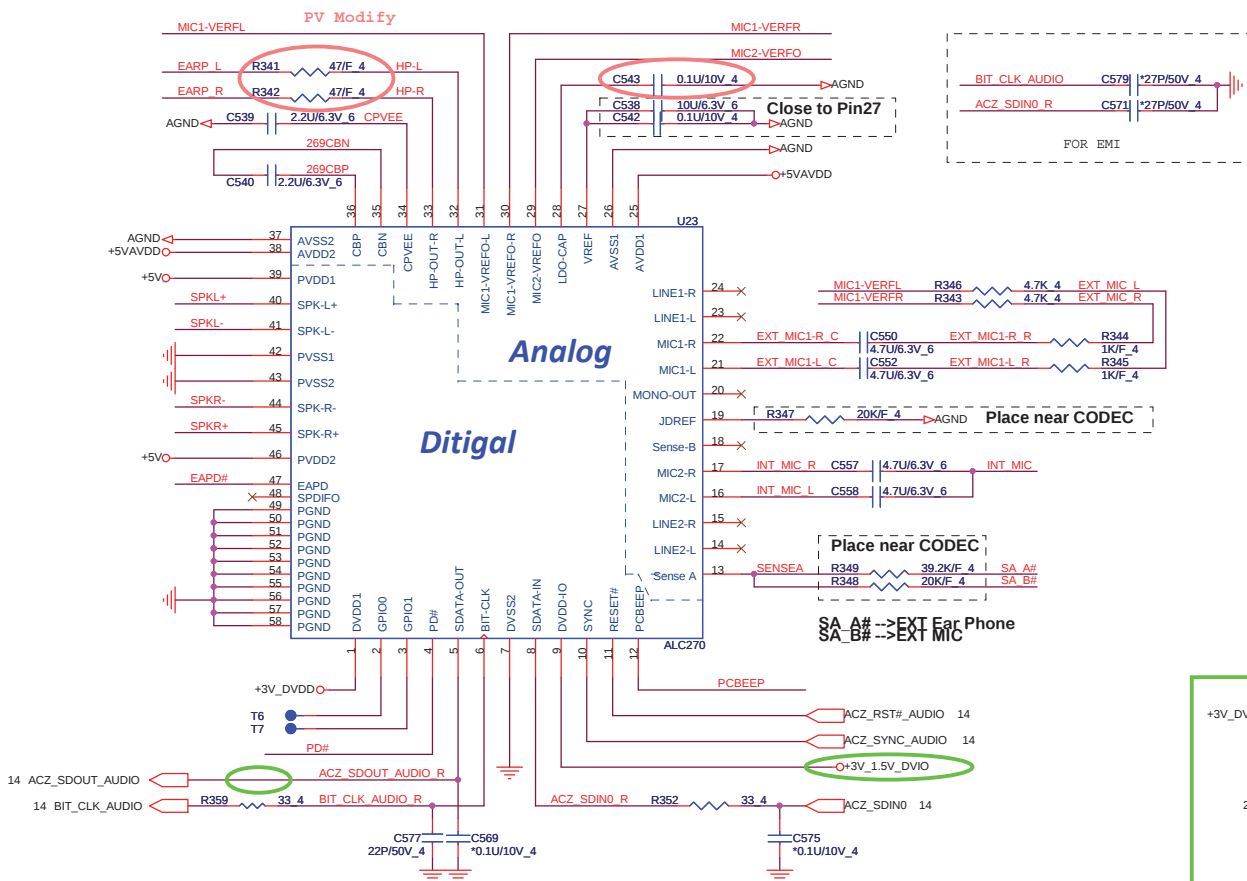
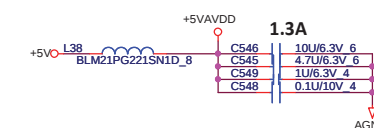
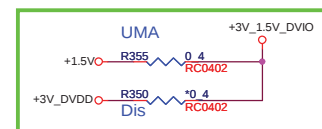
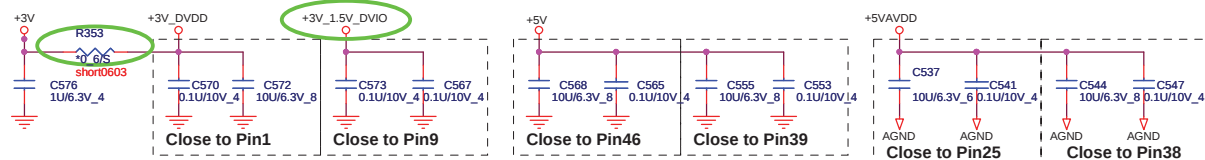
PU for internal CLK input
Unstuff for external XTAL(12MHZ)



Note:

SD/MMC	MS	XD
SP0		XD CD#
SP1	SD WP	
SP2	SD CD#	
SP3	SD DAT1	XD D4
SP4	SD DAT1	XD D4
SP5	MS BS	XD D5
SP6	MS D1	XD D3
SP7	SD DAT0	XD D6
SP8	SD DAT7	XD D2
SP9	MS INS#	
SP10	SD DAT6	XD D7
SP11	SD CLK	MS SCLK
SP12	SD DAT5	XD D0
SP13	SD DAT4	XD WP#
SP14	SD DAT4	XD R/B#
SP15	SD DAT3	XD WE#
SP16	SD DAT2	XD RE#
SP17		XD ALE
SP18		XD CE#
SP19		XD CLE

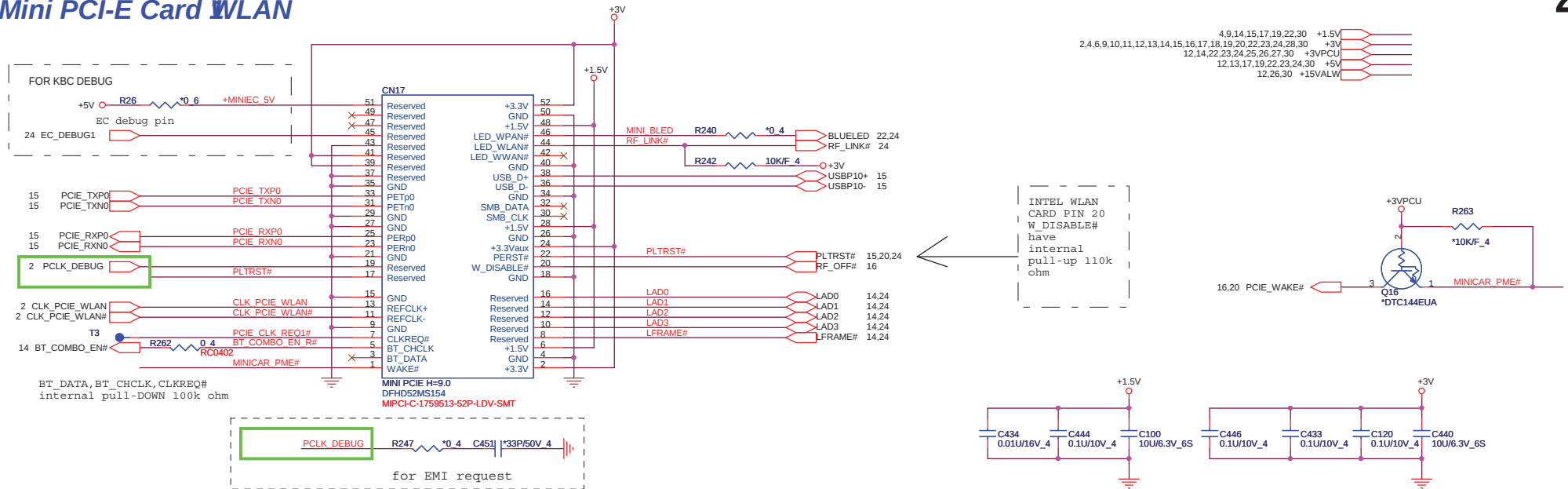




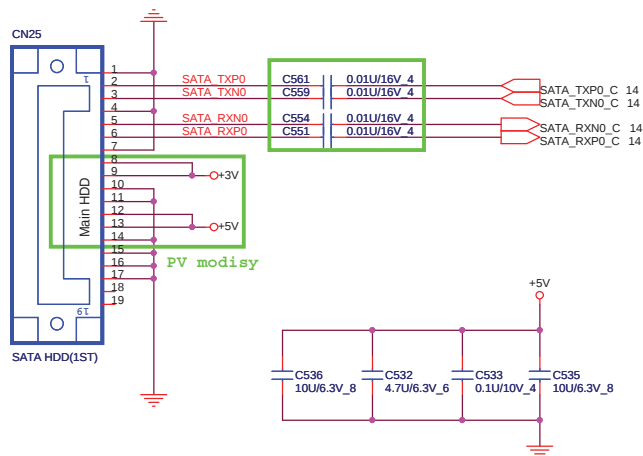




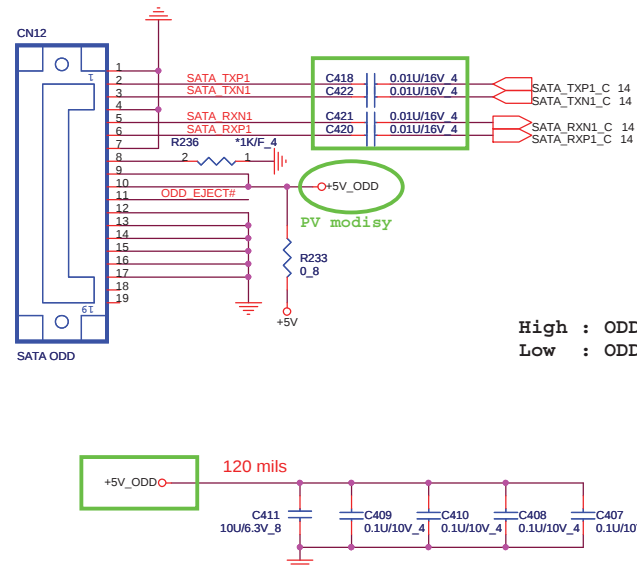
Mini PCI-E Card WLAN



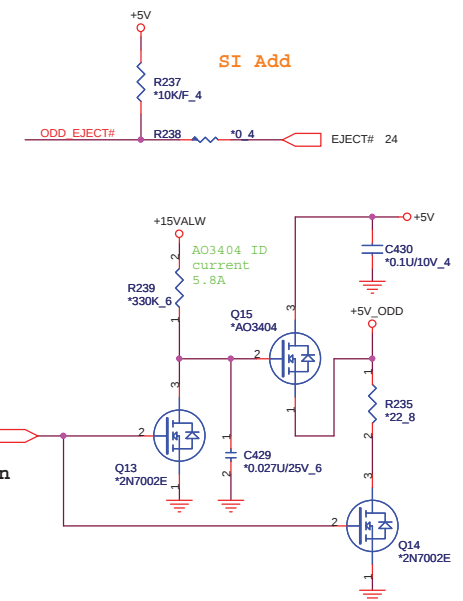
SATA HDD CONNECTOR



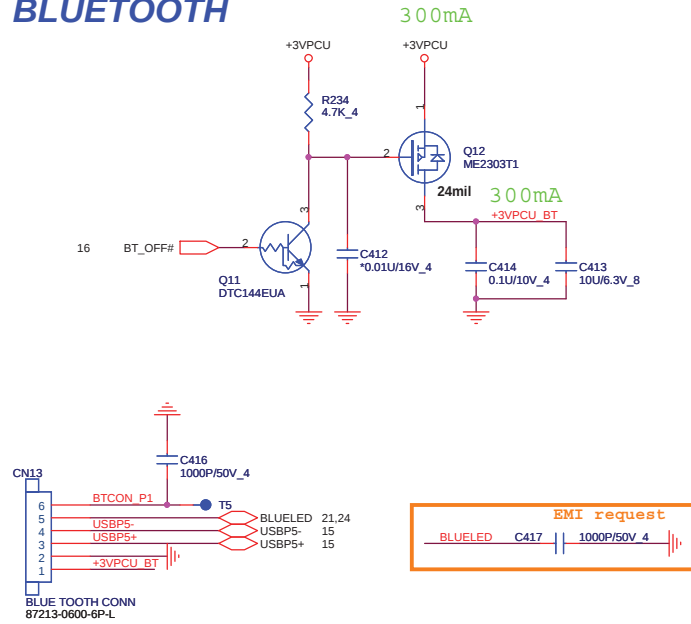
SATA ODD CONNECTOR



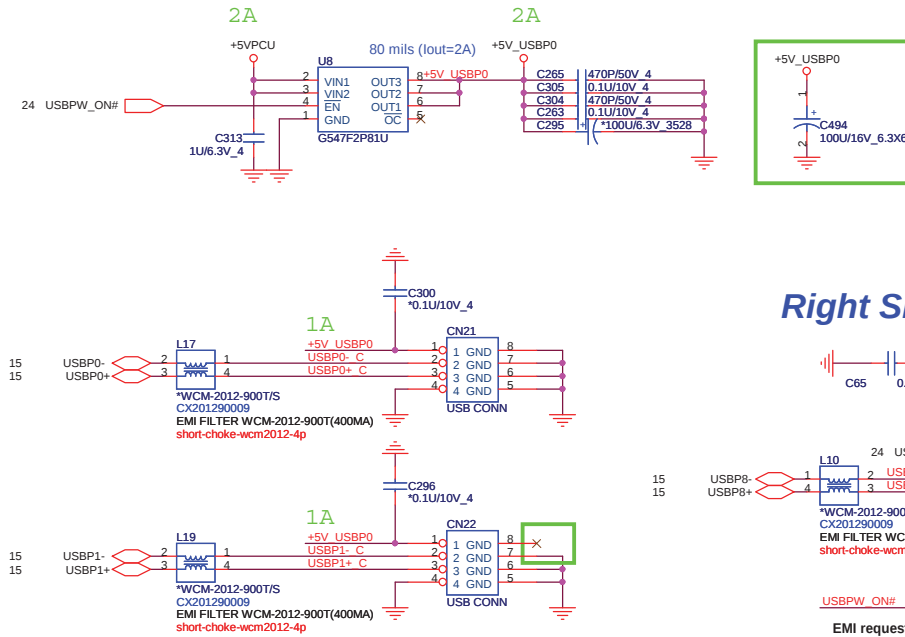
High : ODD power down
Low : ODD power on



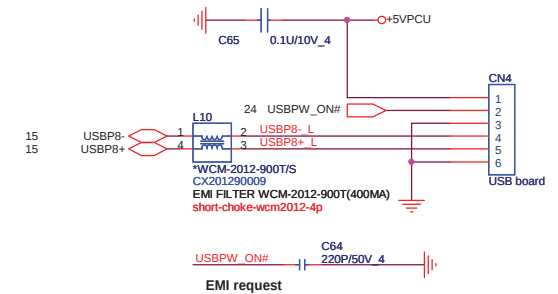
BLUETOOTH



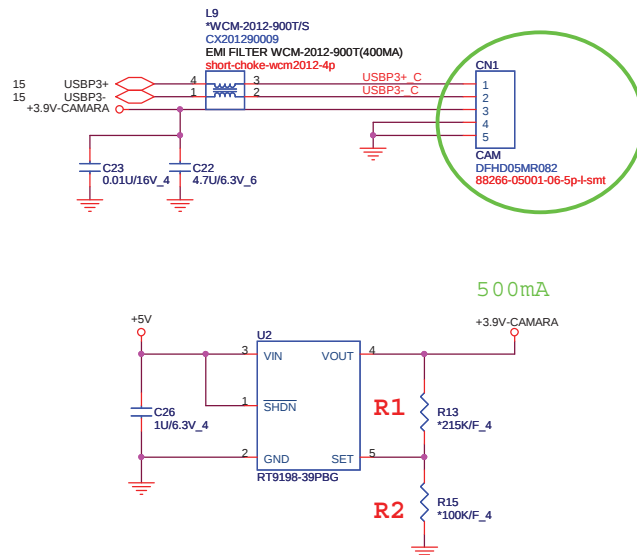
LEFT SIDE USBX1



Right SIDE USBX1

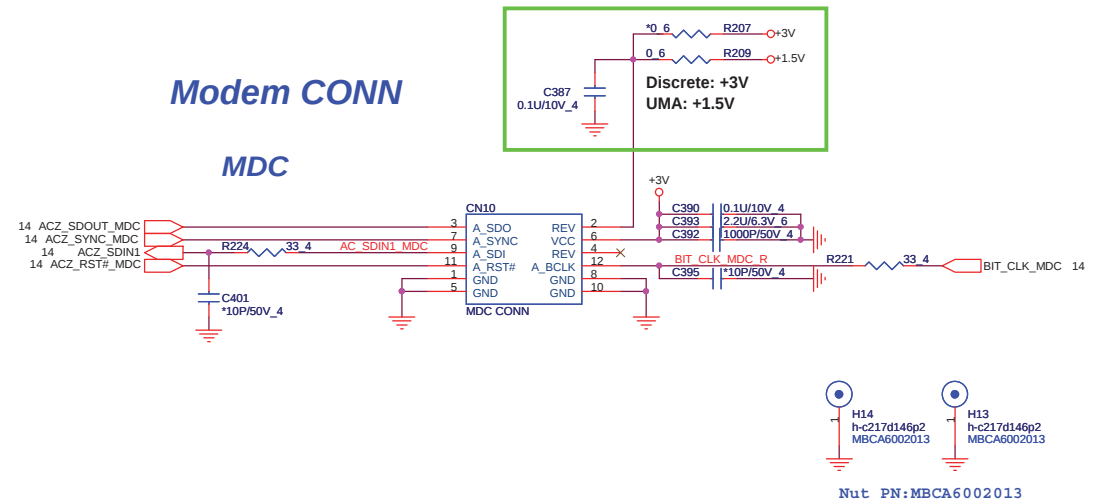


CAMERA

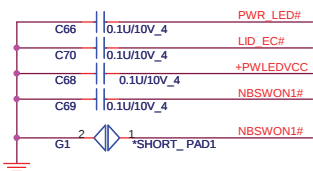
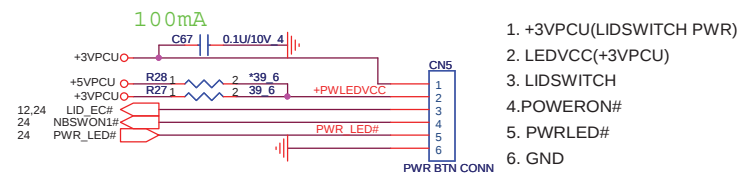


Modem CONN

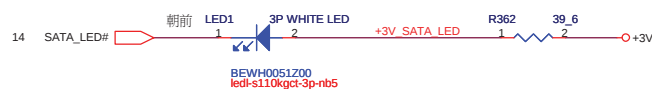
MDC



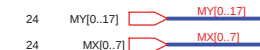
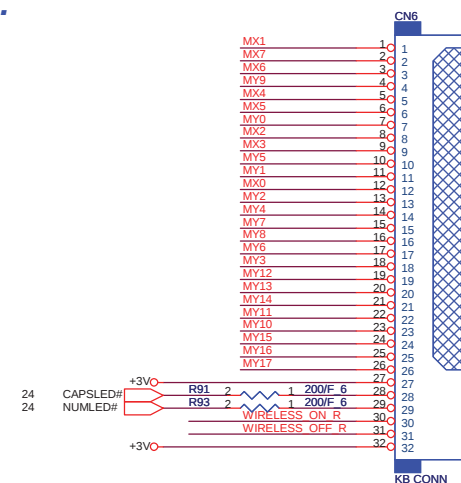
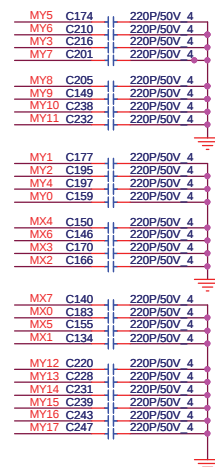
POWER BOTTON CONNECT



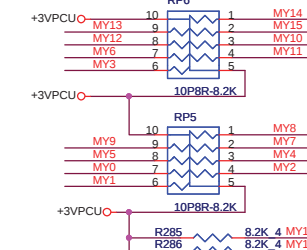
SATA LED



KEYBOARD Con.

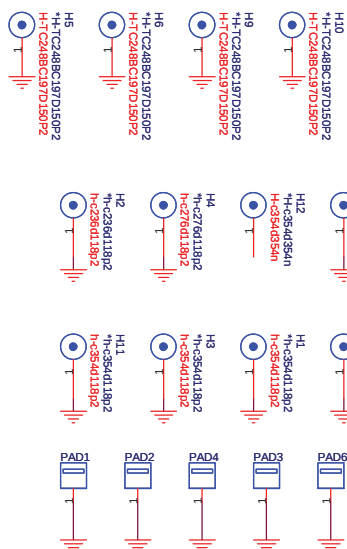


KEYBOARD PULL-UP

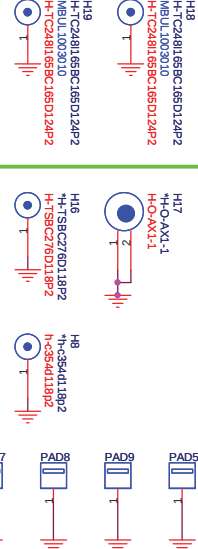


HOLE & PAD

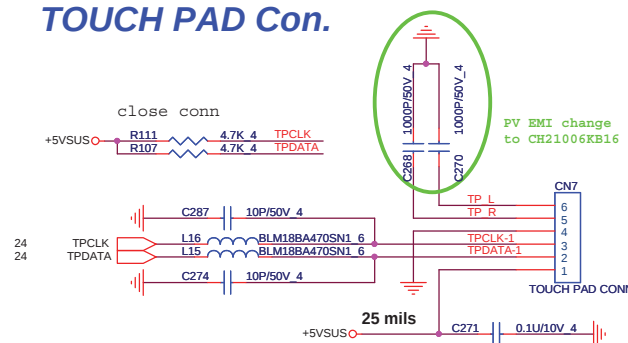
CPU



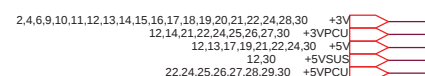
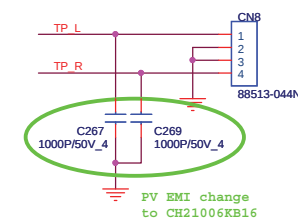
MCH

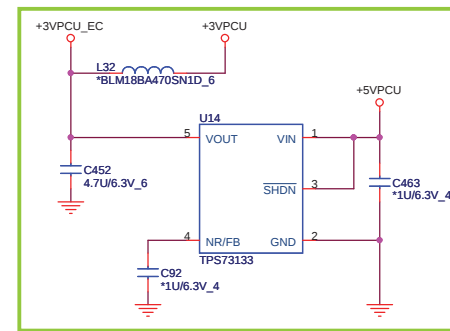
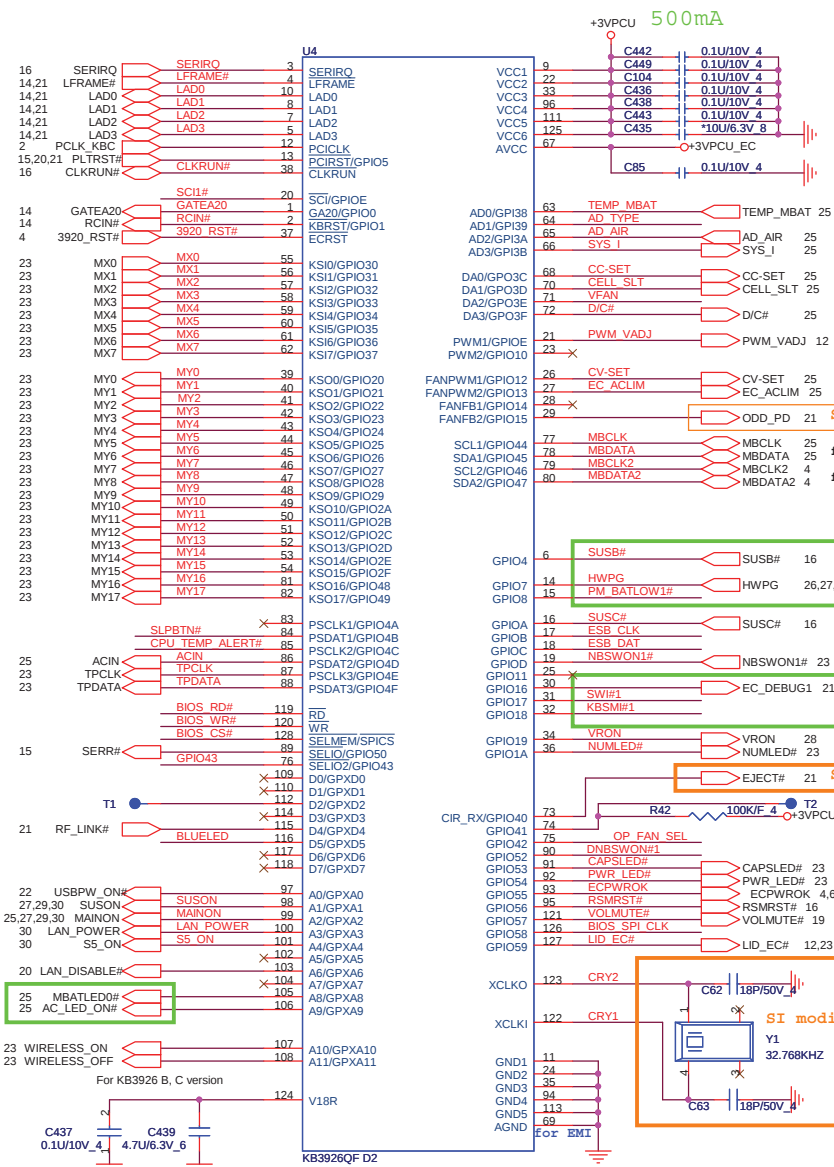


TOUCH PAD Con.

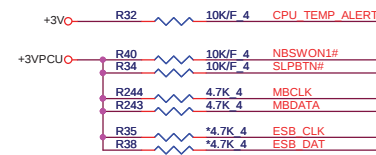
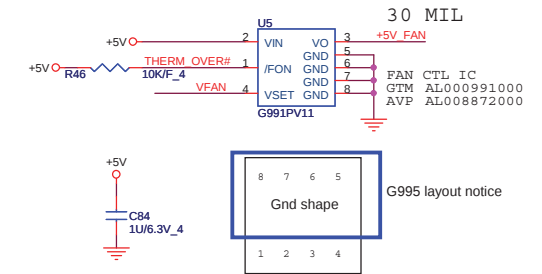


To TOUCH PAD SW board

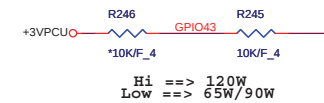




CPU FAN



Adapter select for EC



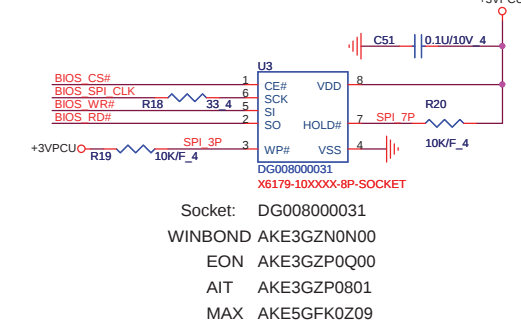
GPIO42 control fan table

11/24 PV modify board ID.

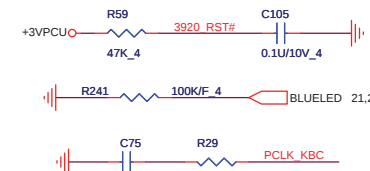
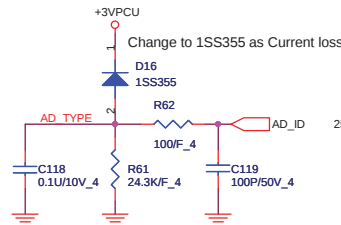
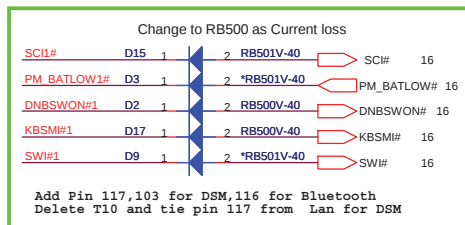


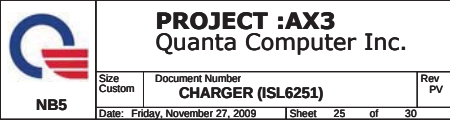
Project Model	GPIO42
AX 14"	High
AX 15.6"	Low
AX 17.3"	Middle (1.5V)

1M byte SPI EC ROM



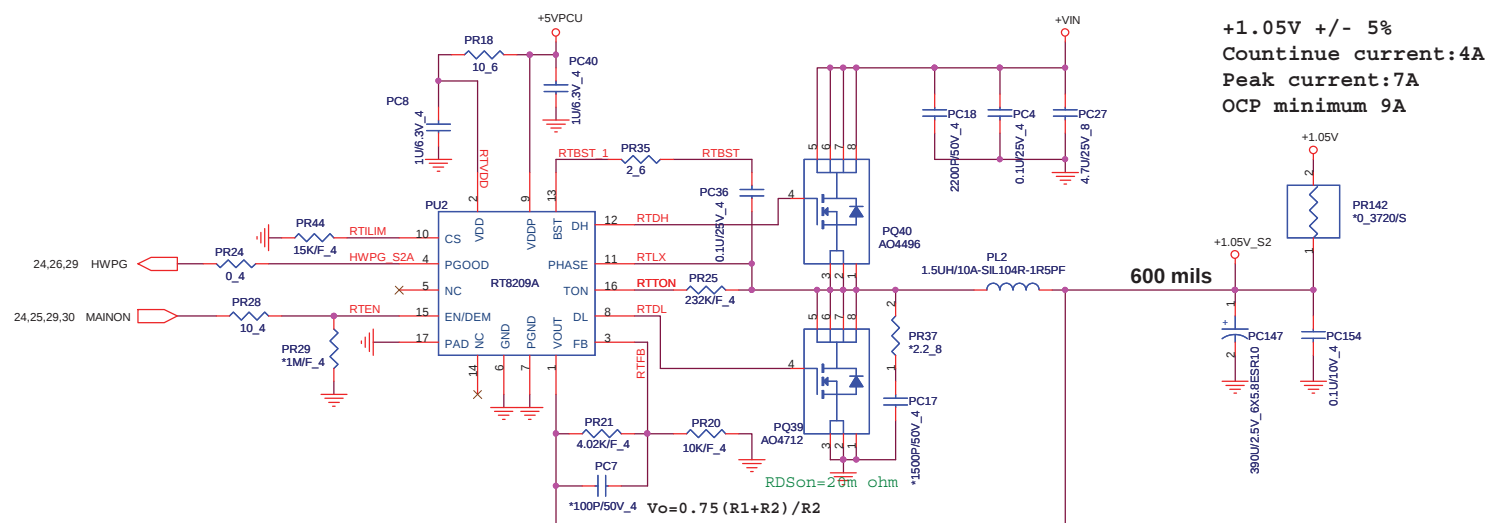
adapter Type check



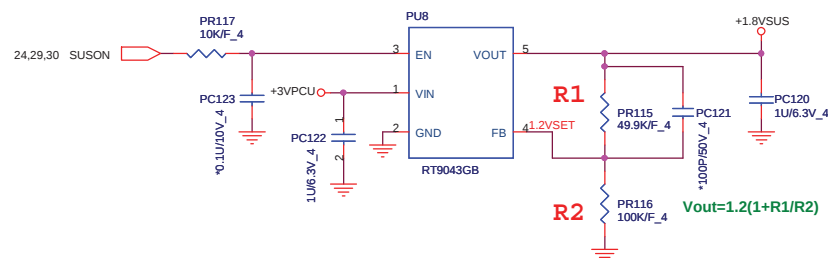


+3.3V +/- 5%
Continue current:5A
Peak current:6A
OCP minimum 7.5A





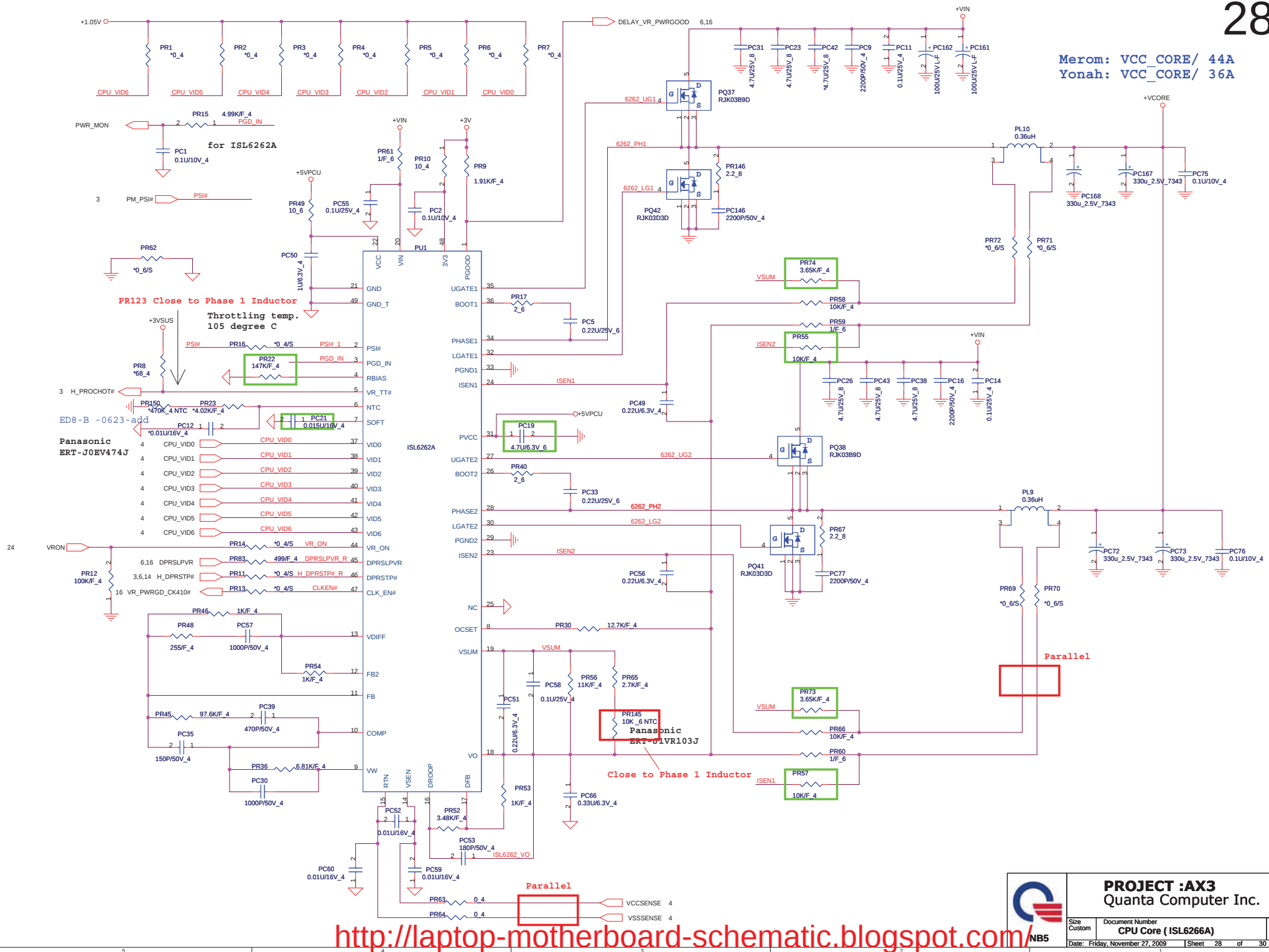
+1.8 Volt +/- 5%
Countinue current:150mA
Peak current: 600mA

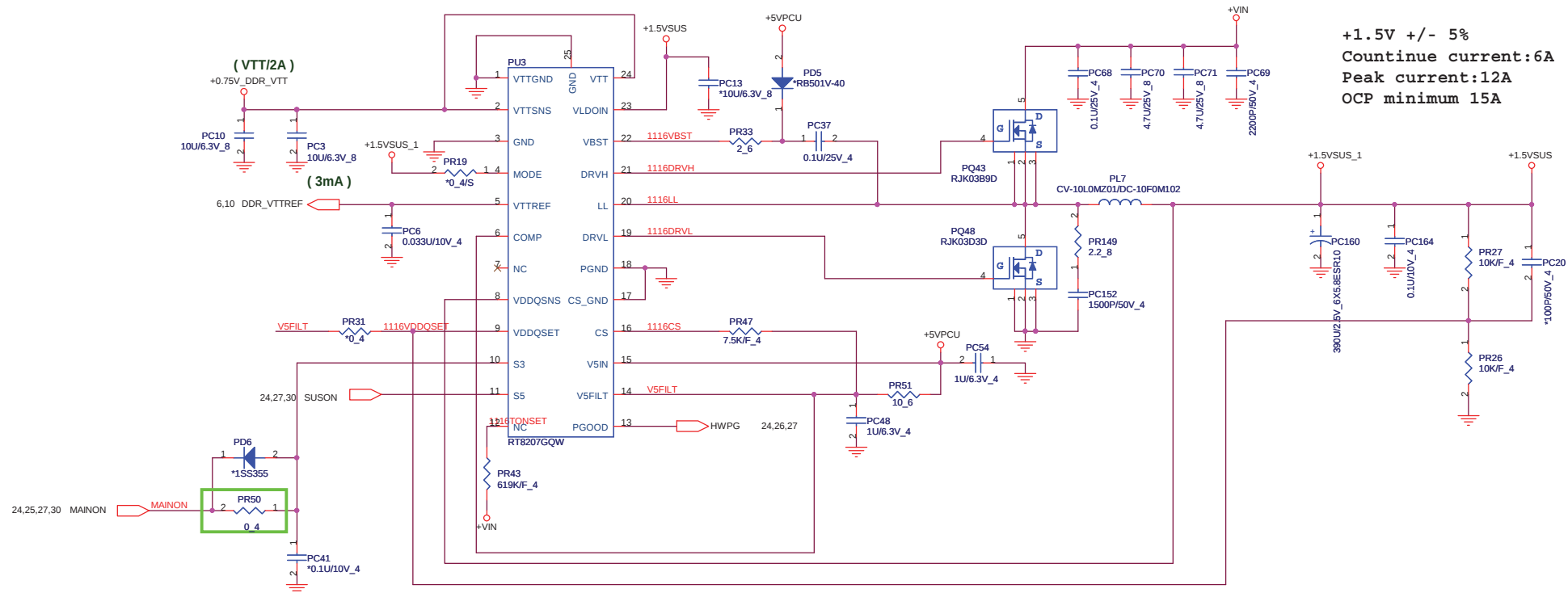


PROJECT :AX3
Quanta Computer Inc.

Size Custom	Document Number +1.05V/+1.8V (RT8204C)	Rev PV
Date: Friday, November 27, 2009	Sheet 27 of 30	

Merom: VCC_CORE/ 44A
Yonah: VCC_CORE/ 36A





PROJECT :AX3
Quanta Computer Inc.

Size Custom	Document Number DDR3 (RT8207)	Rev PV
Date: Friday, November 27, 2009	Sheet 29 of 30	

