

GIGABYTE GA-6IEML Schematics

Revision:1.1B

SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	SOCKET370 CPU-1
04	SOCKET370 CPU-2
05	SOCKET370 CPU-3
06	N/B 815E SGMCH-1
07	N/B 815E SGMCH-2
08	N/B 815E SGMCH-3
09	SDRAM SLOT1
10	SDRAM SLOT2
11	SDRAM SLOT3
12	AGP
13	S/B ICH2-1
14	S/B ICH2-1
15	CLOCK GEN.
16	PCI1-2
17	PCI3
18	IDE
19	FWH
20	CODEC
21	AUDIO-J1
22	AUDIO-J2

SHEET	TITLE
23	CNR
24	FRONT PANEL
25	LPCIO
26	COM&LPT
27	KB_PS2
28	H/W MONITOR
29	TOTAL POWER
30	PWM (FAN5071&FAN5056 DUAL LAYOUT)
31	KINNERETH562&USB CONNECT
32	ON-BOARD VGA
33	
34	
35	
36	
37	
PCB	„ s ² » , “ ´ I , ² İ .
DESIGNER	WINFRED-LIN

COMPONENT SIDE
(0.5 oz. Copper)
VCC SIDE
(1 oz. Copper)
GND SIDE
(1 oz. Copper)
SOLDER SIDE
(0.5 oz. Copper)

GIGABYTE

Title

COVER SHEET

Size Custom

Document Number

6IEML

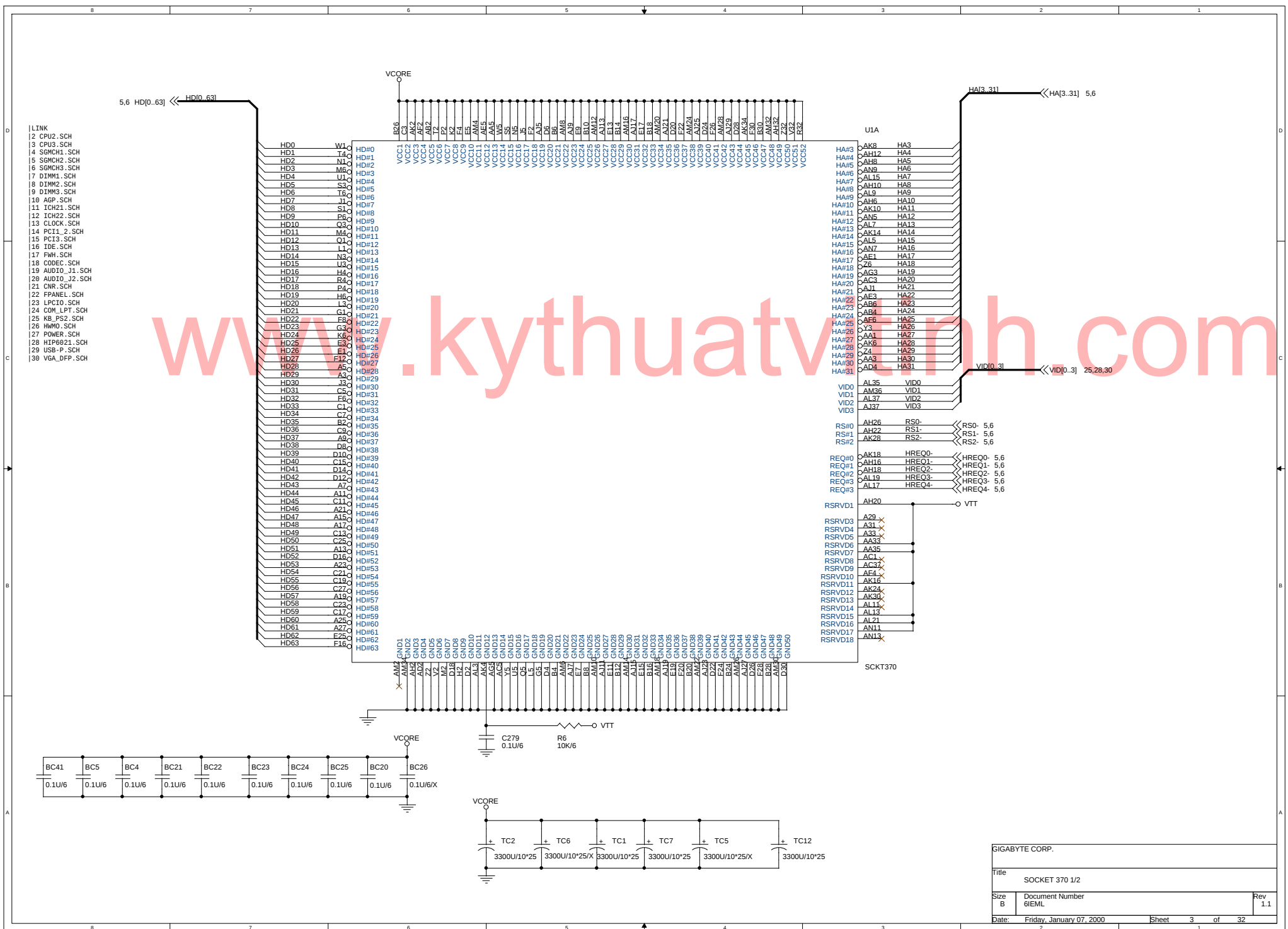
Date: Friday, January 07, 2000

Sheet 1 of 32

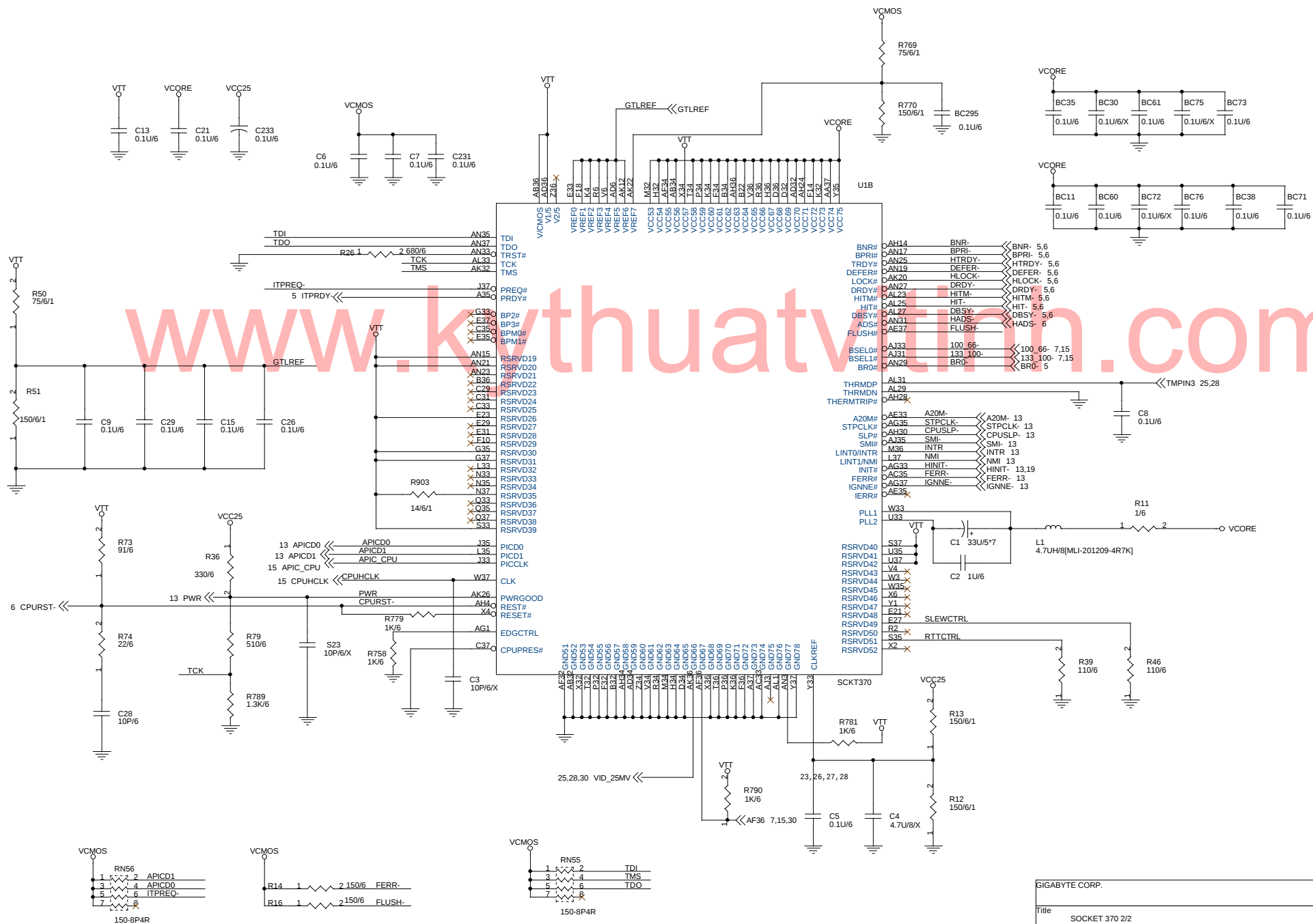
Rev 1.1

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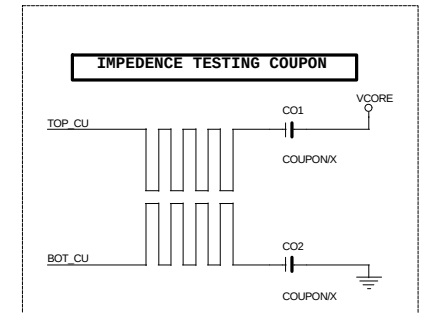
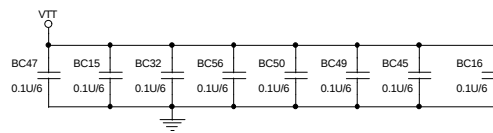
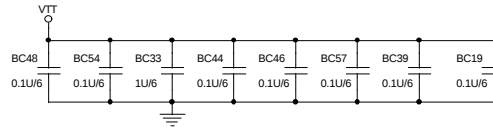
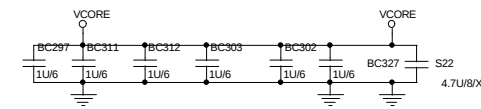
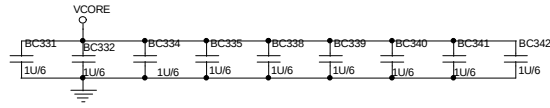
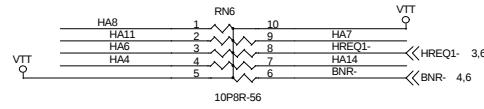
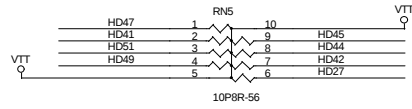
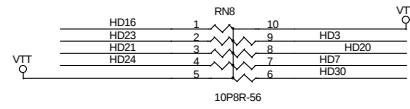
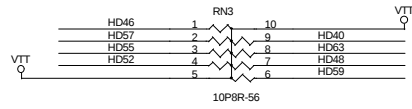
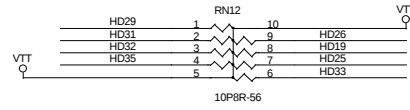
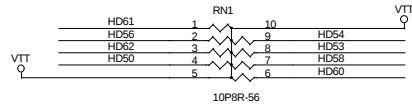
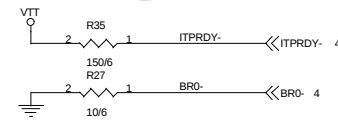
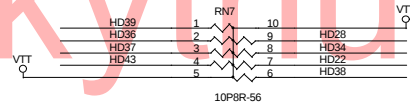
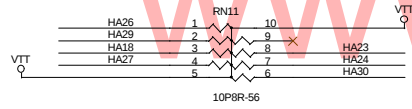
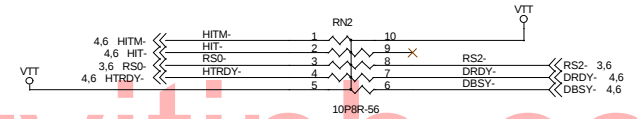
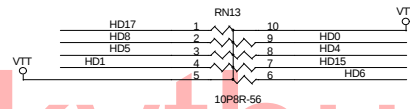
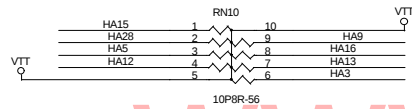
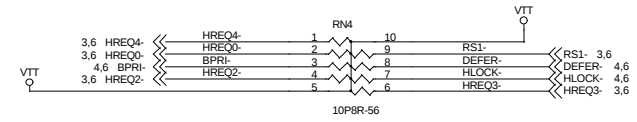
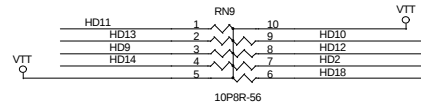
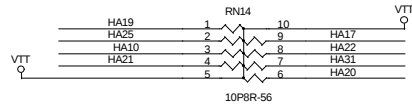
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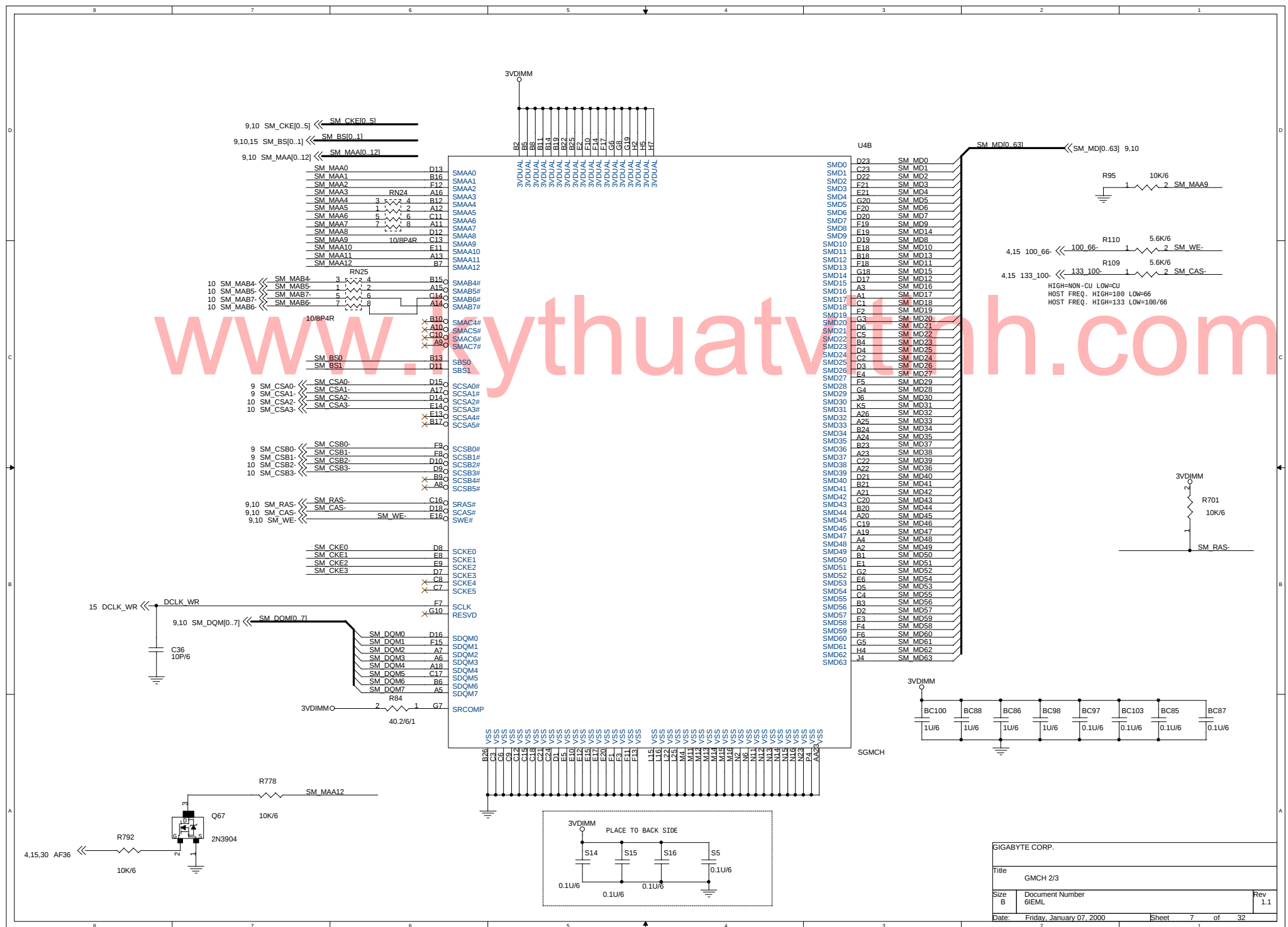
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HA[3..31] << HA[3..31] 3.6

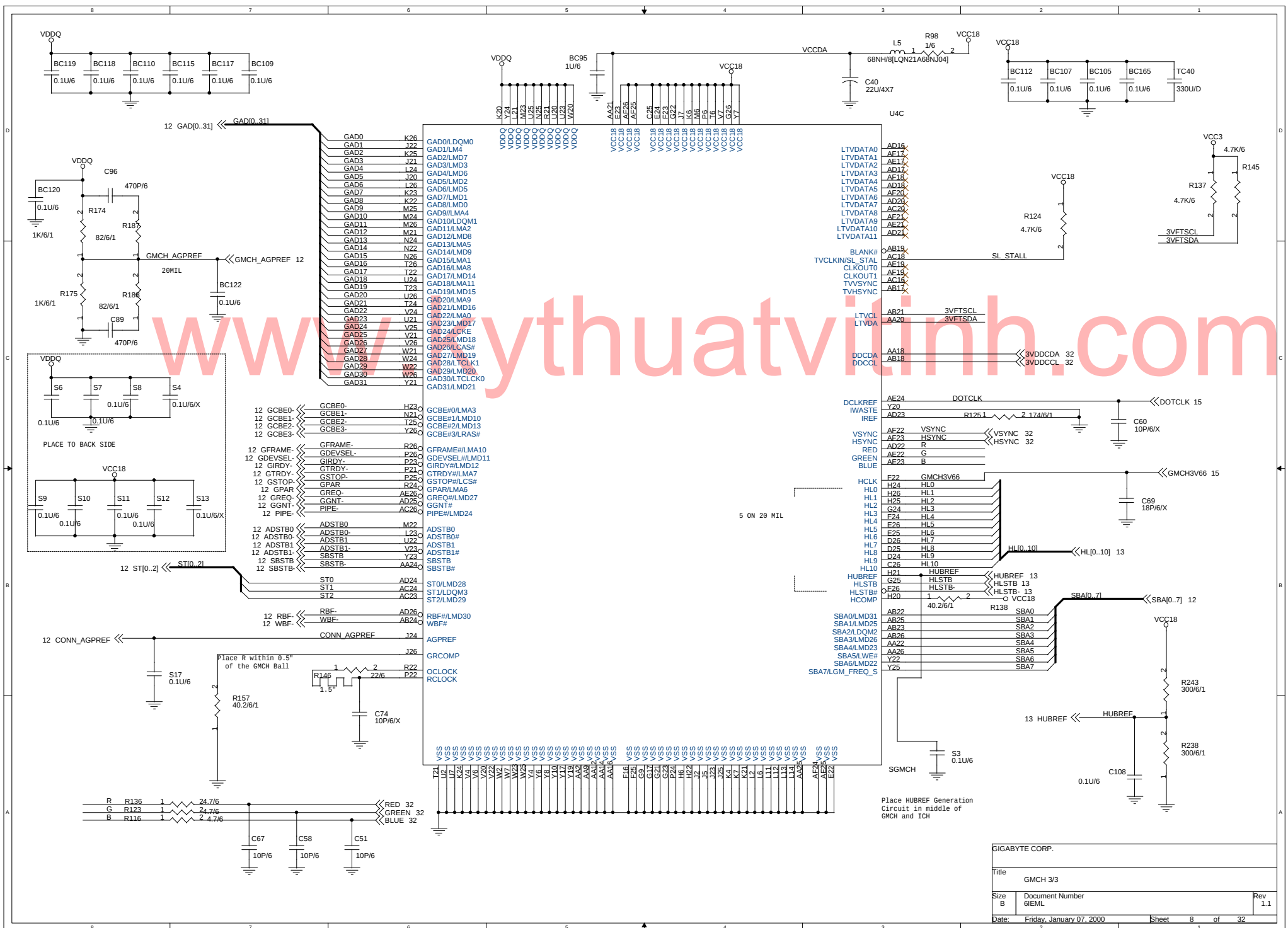


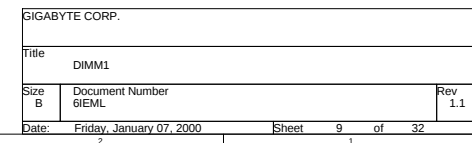
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N/B - n¥[; 3 ©T©w □ Ō □ § HEAT S1

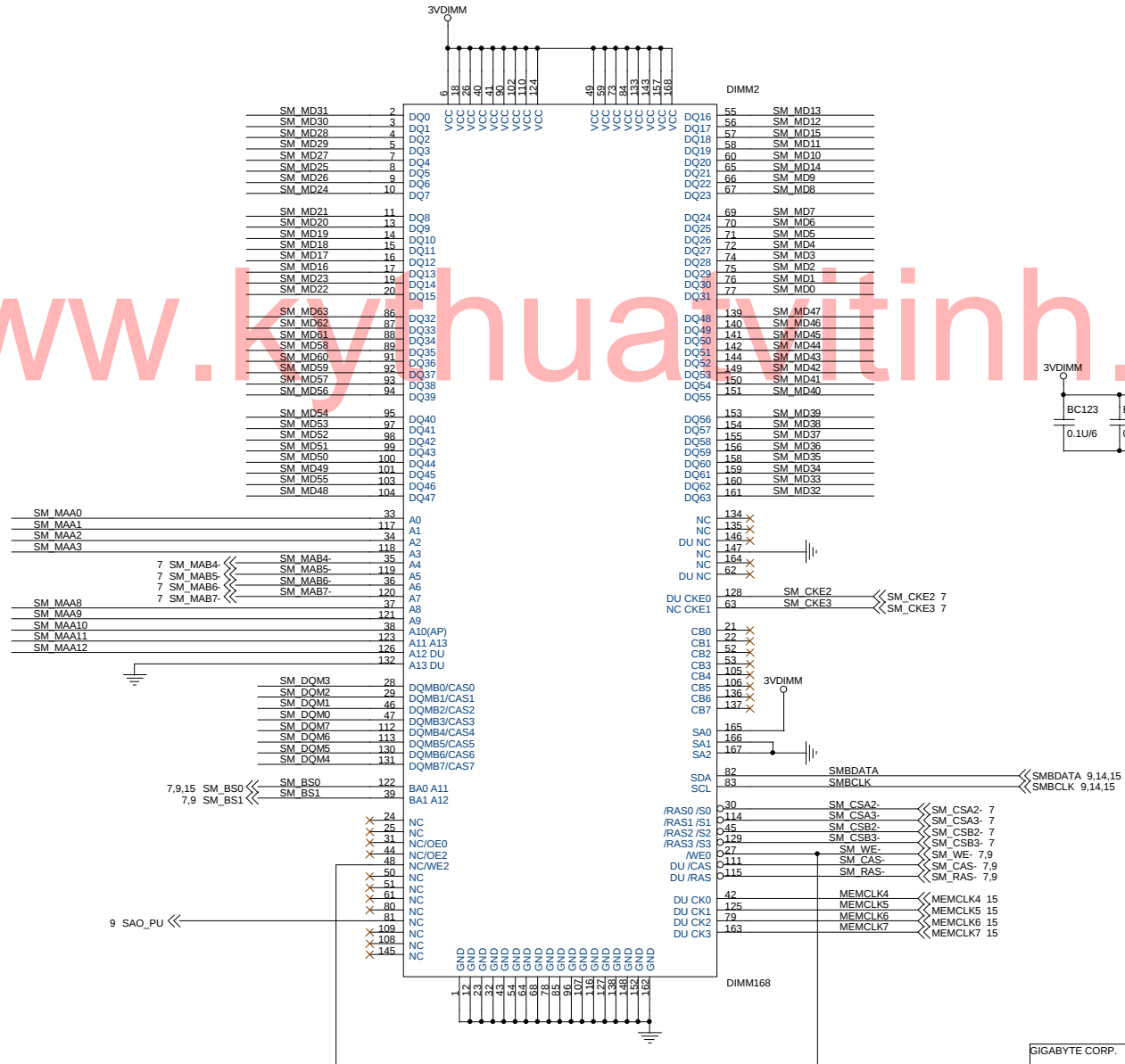
Title		GMCH 1/3	
Size	Document Number	Rev	
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7,9 SM_MAA[0..12] << SM_MAA[0..12]
7,9 SM_DQM[0..7] << SM_DQM[0..7]



GIGABYTE CORP.

Title
DIMM2

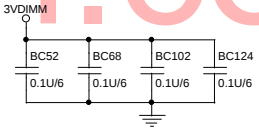
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B Document Number
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Rev
1.1

Date: Friday, January 07, 2000

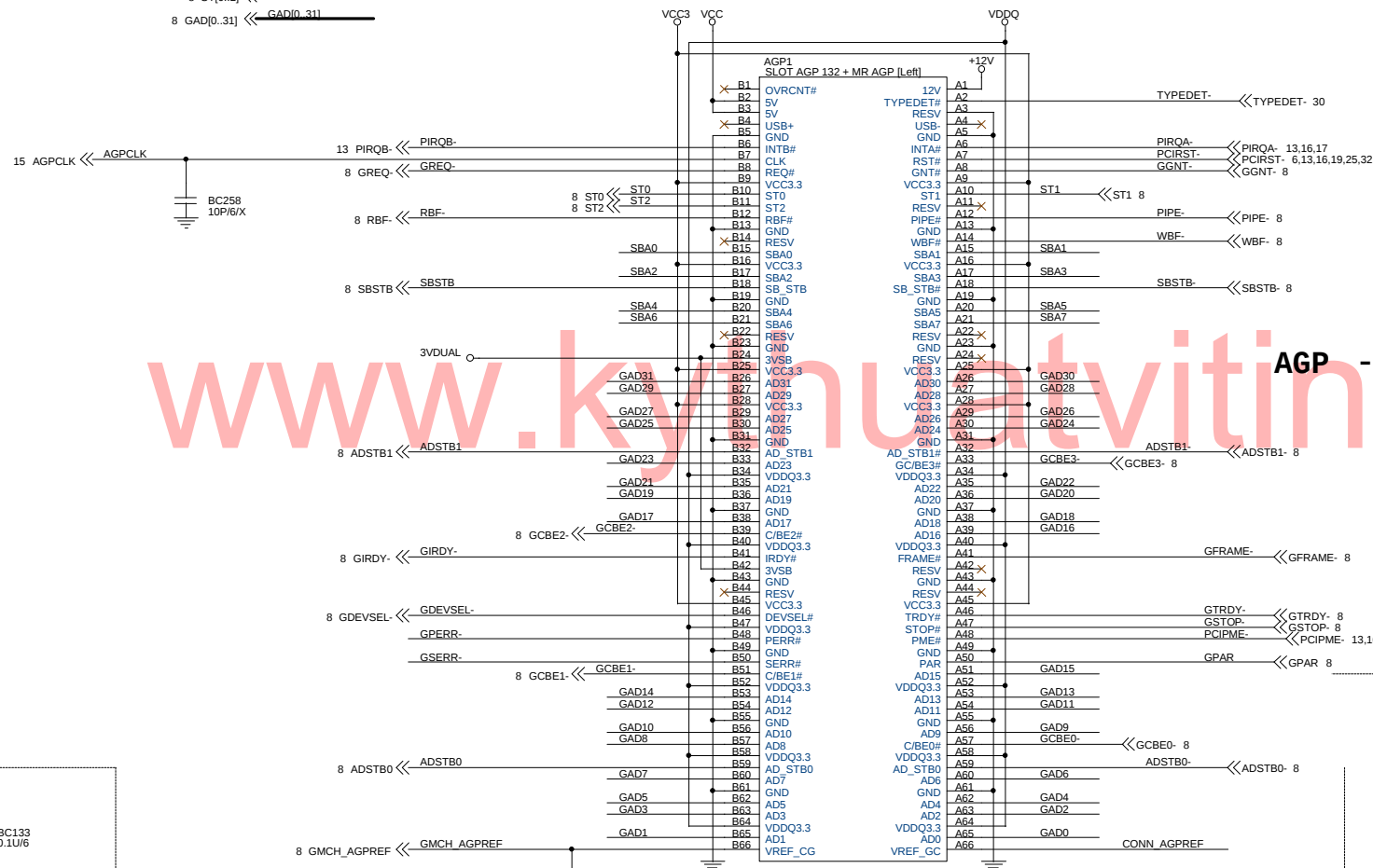
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GIGABYTE CORP.			
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Size	Document Number		Rev
B	6IEML		1.1
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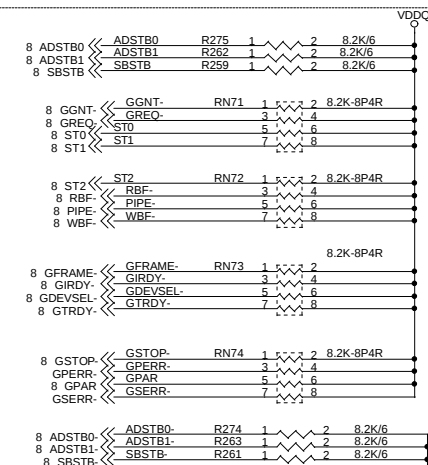
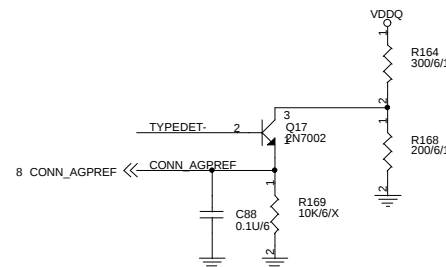
8 SBA[0..7] << SBA[0..7]
8 ST[0..2] << ST[0..2]
8 GAD[0..31] << GAD[0..31]



AGP - n ¥ [RETENTION MODULE

ADD RM

PLACE CLOSE TO GMCH



GIGABYTE CORP.

Title
AGP SLOT

Size
B

Document Number
6IEM1

Date:

Friday, January 07, 2000

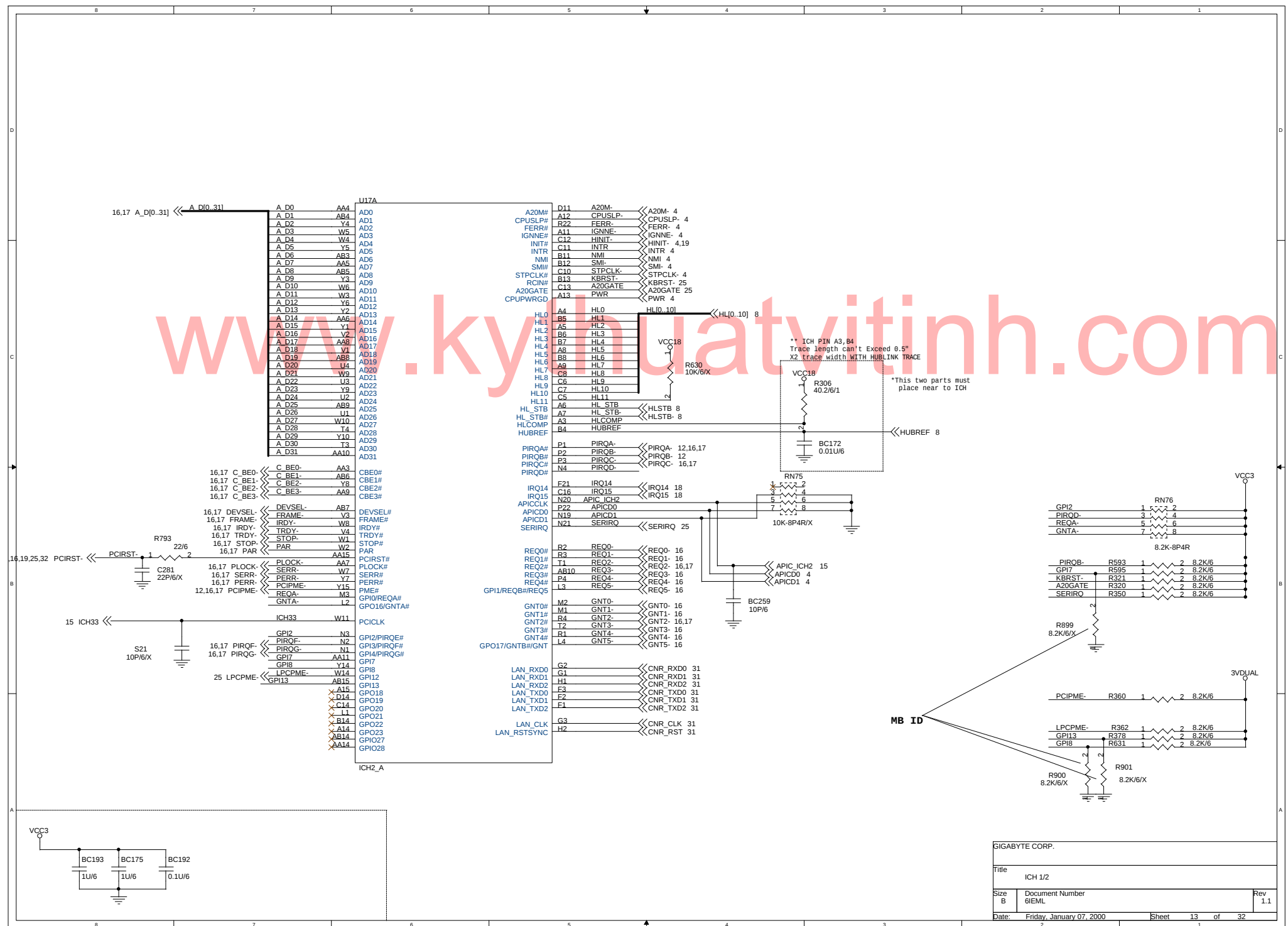
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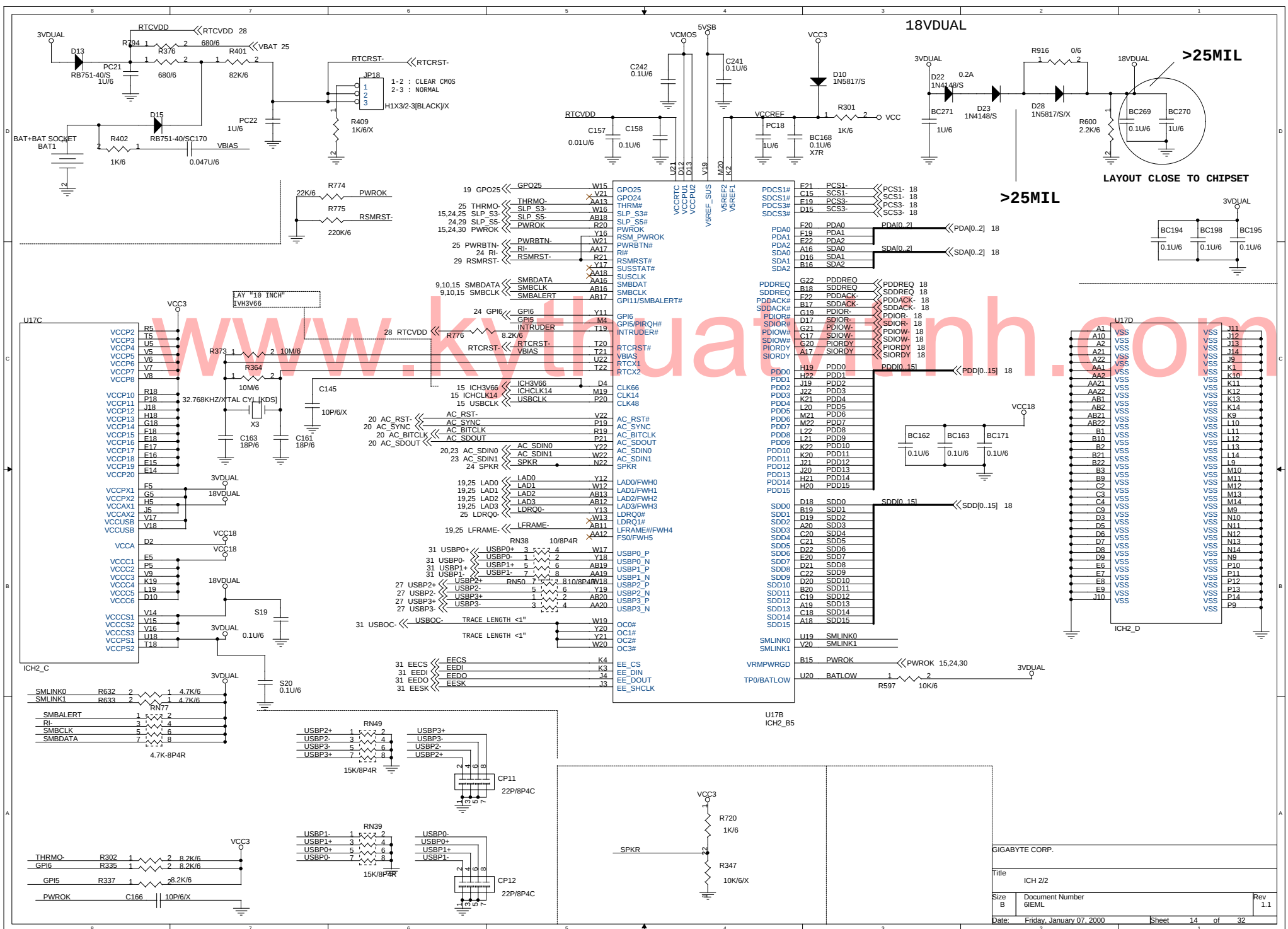
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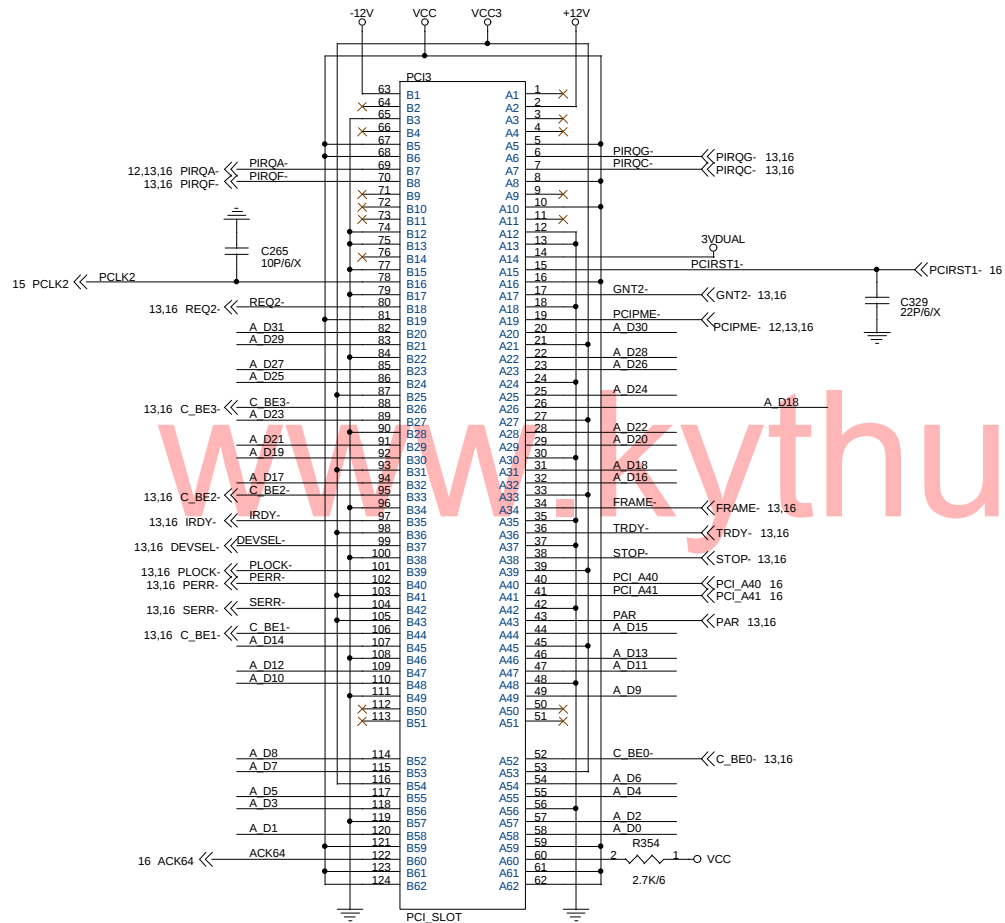
of

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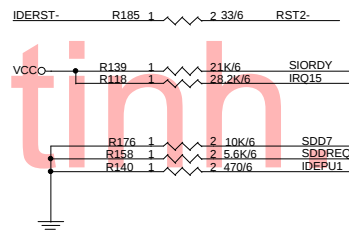
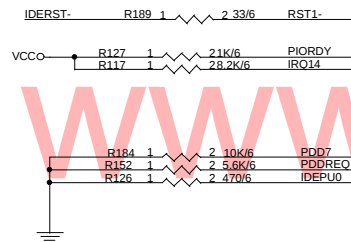
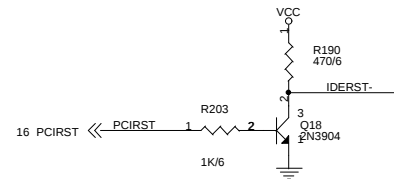
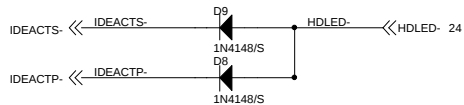




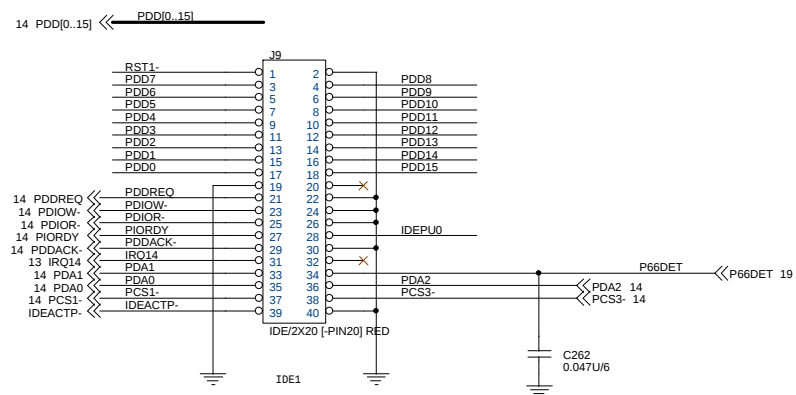


13.16 A_D[0..31] << A_D[0..31]

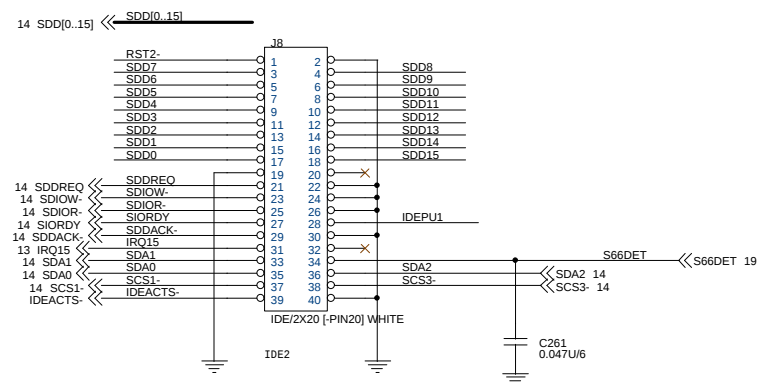
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Title			
PCI SLOT 3			
Size	Document Number		Rev
B	6IEML		1.1
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PRIMARY IDE CONNECTOR



SECONDARY IDE CONNECTOR



GIGABYTE CORP.

Title
IDE CONNECTOR

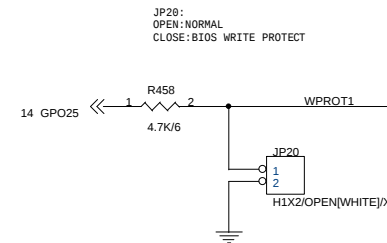
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Document Number
6IEML

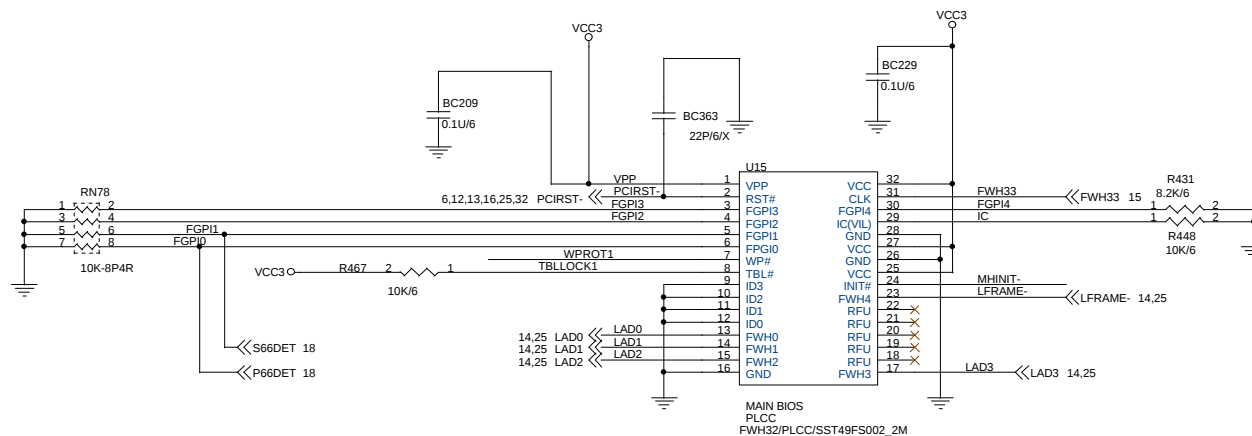
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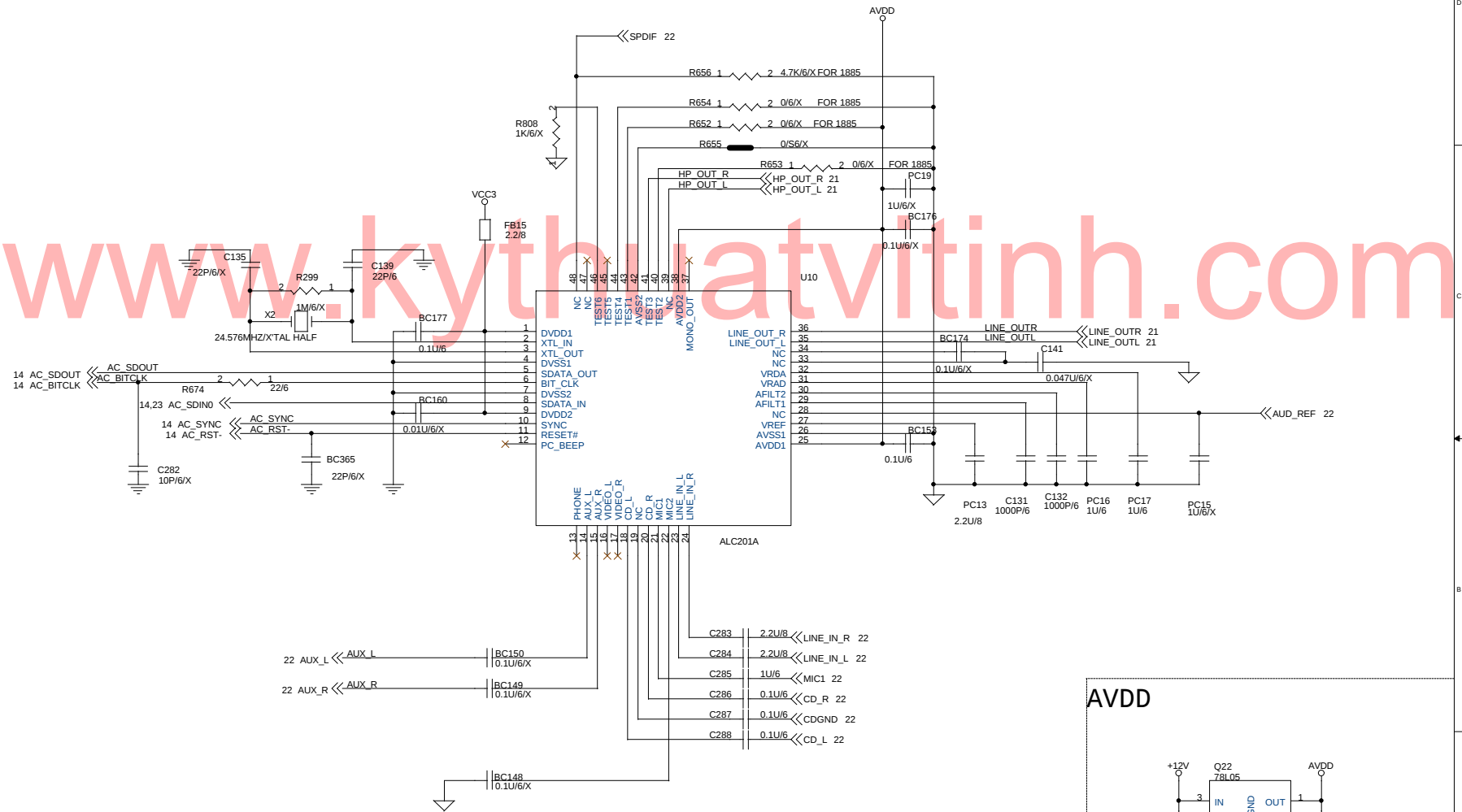
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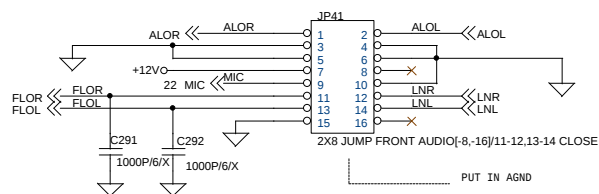
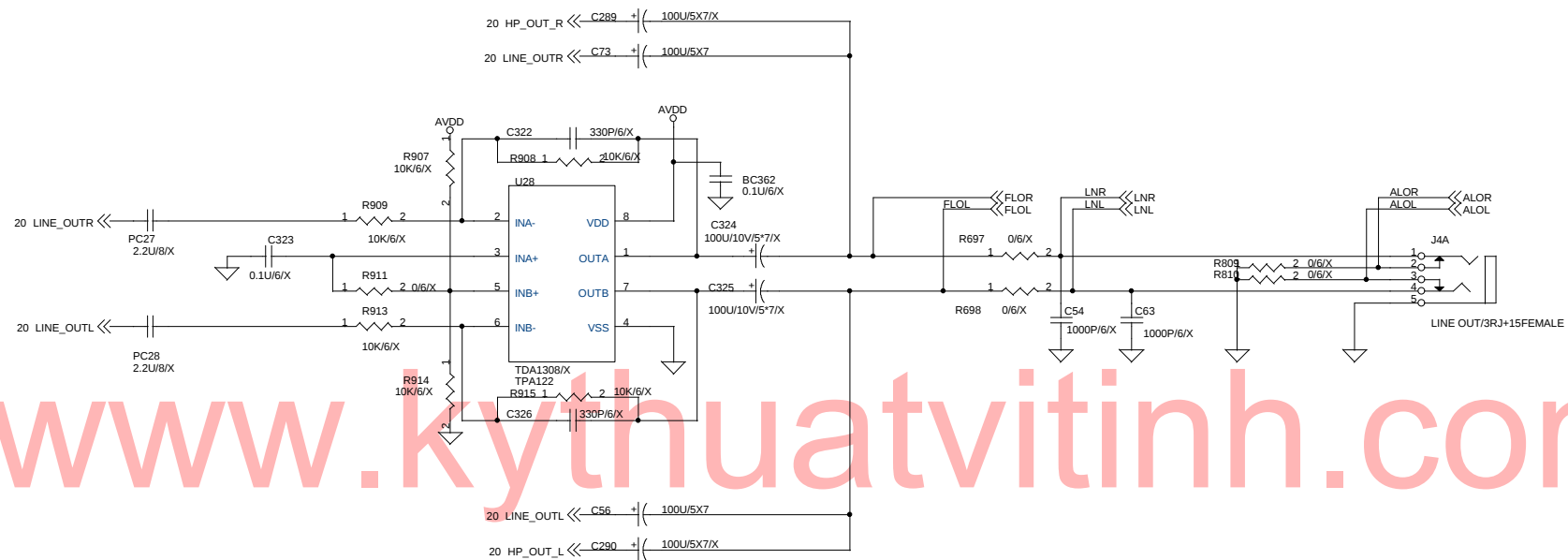
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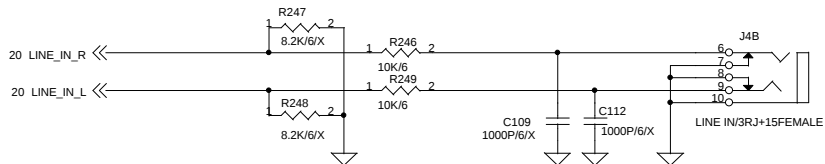


AVDD

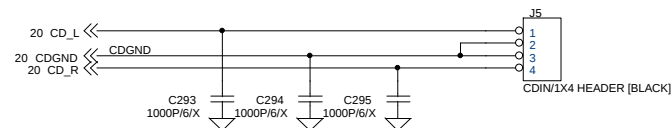
GIGABYTE CORP.			
Title AC97			
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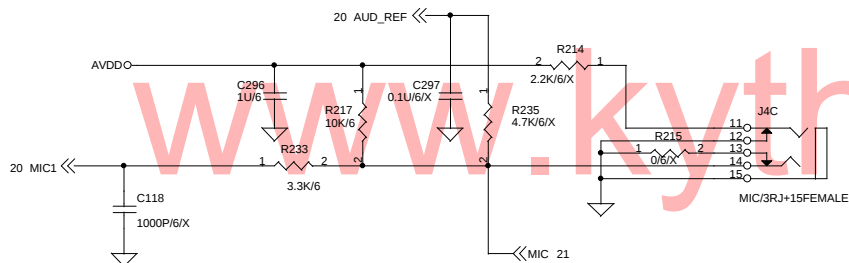
LINE-IN



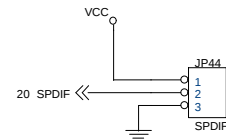
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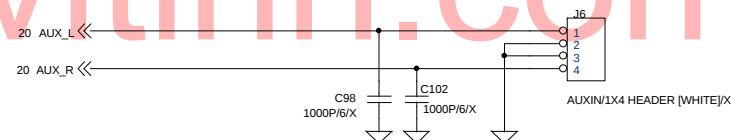
MIC



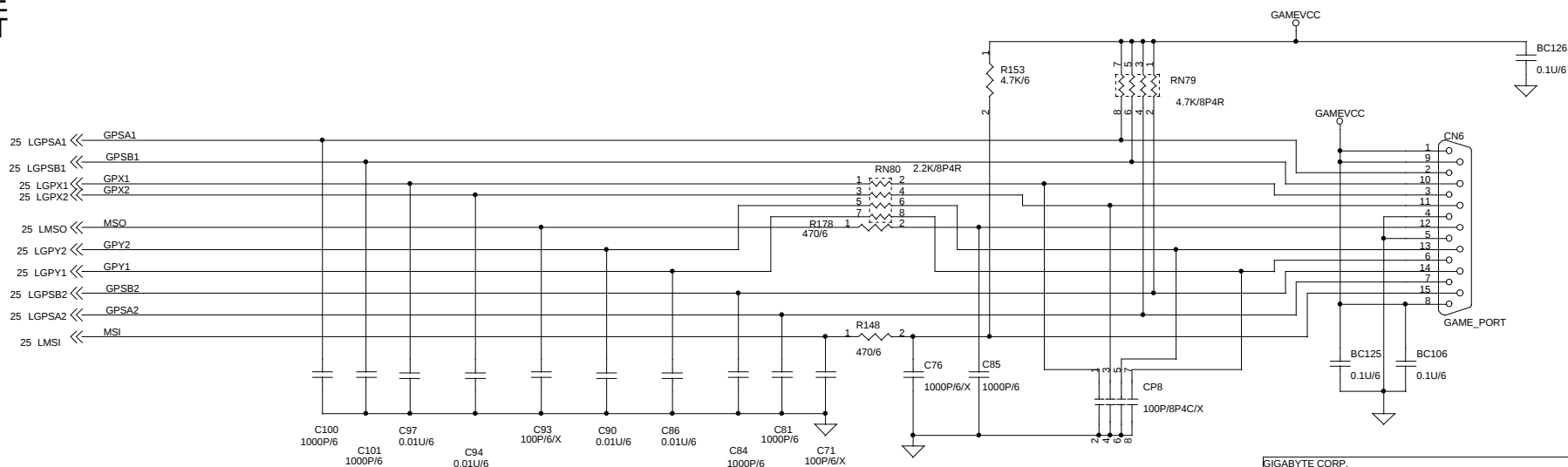
SPDIF



AUXIN



GAME PORT



GIGABYTE CORP.

Title
PHONE JACK, GAME PORT

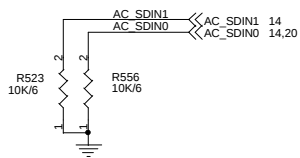
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6IEMI

Date: Friday, January 07, 2000

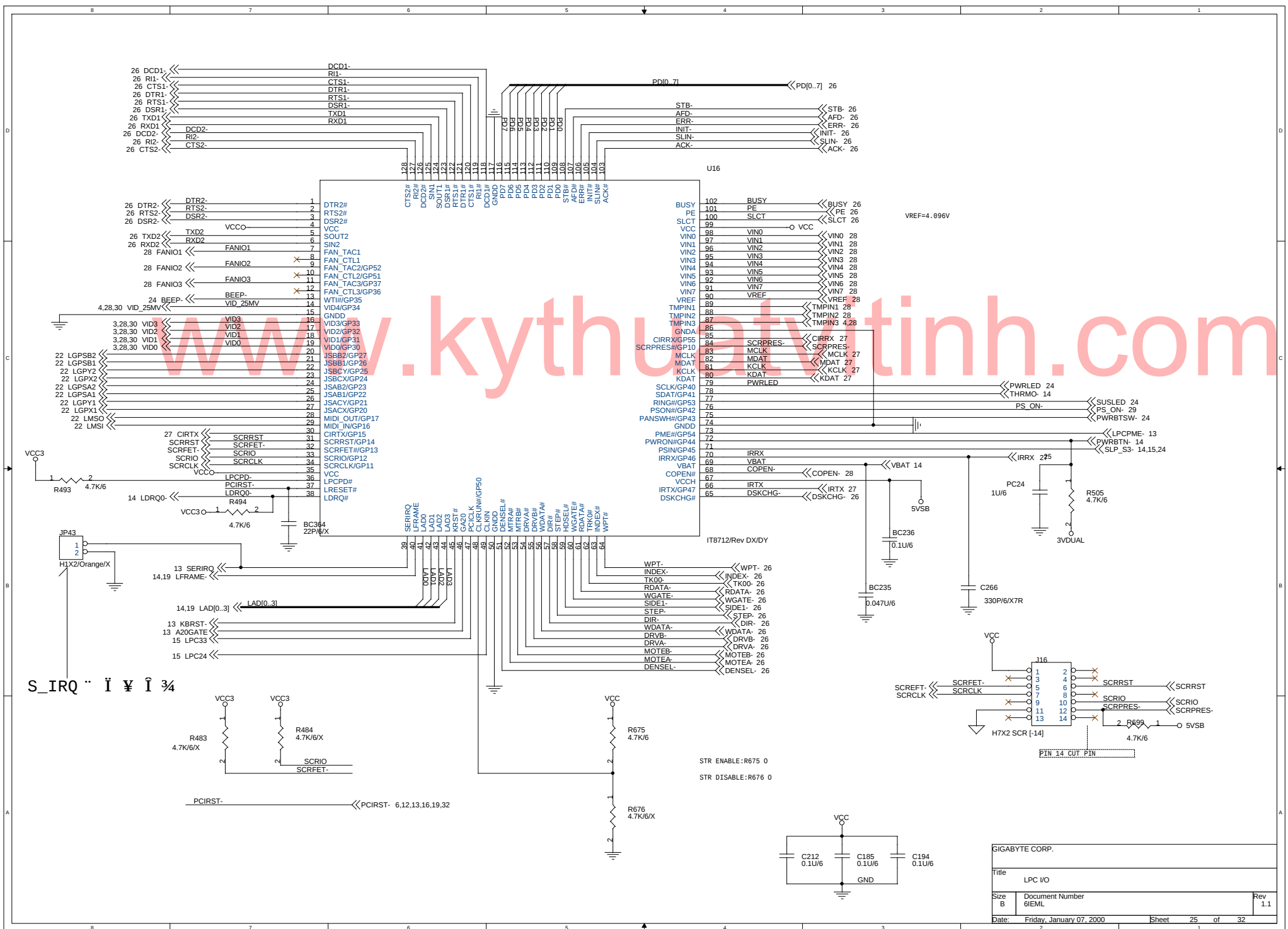
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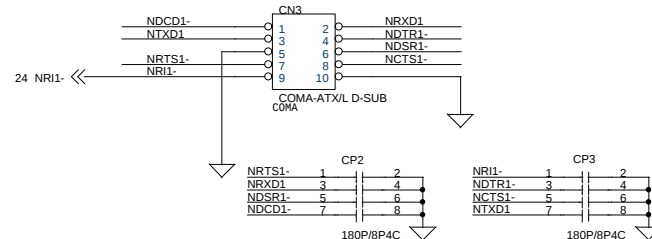
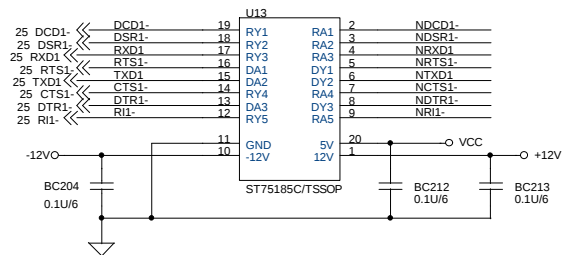
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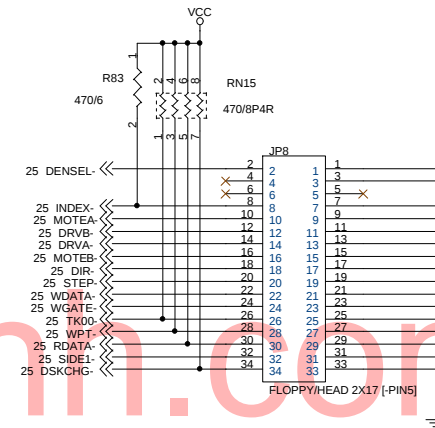
GIGABYTE			
Title			
CNR,KINNERETH			
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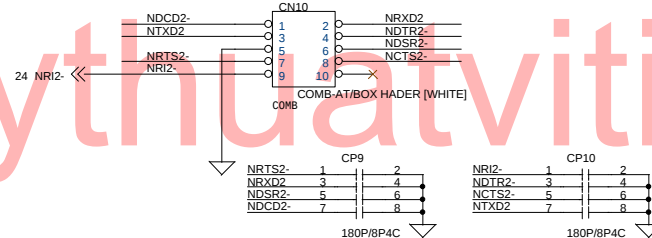
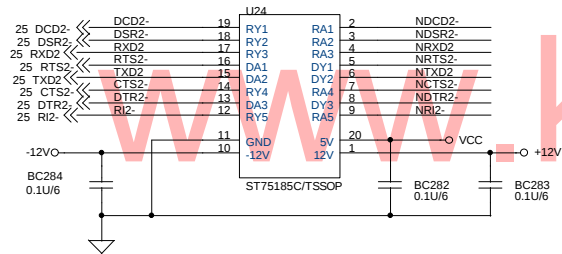
COMA



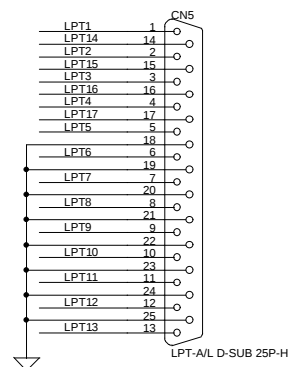
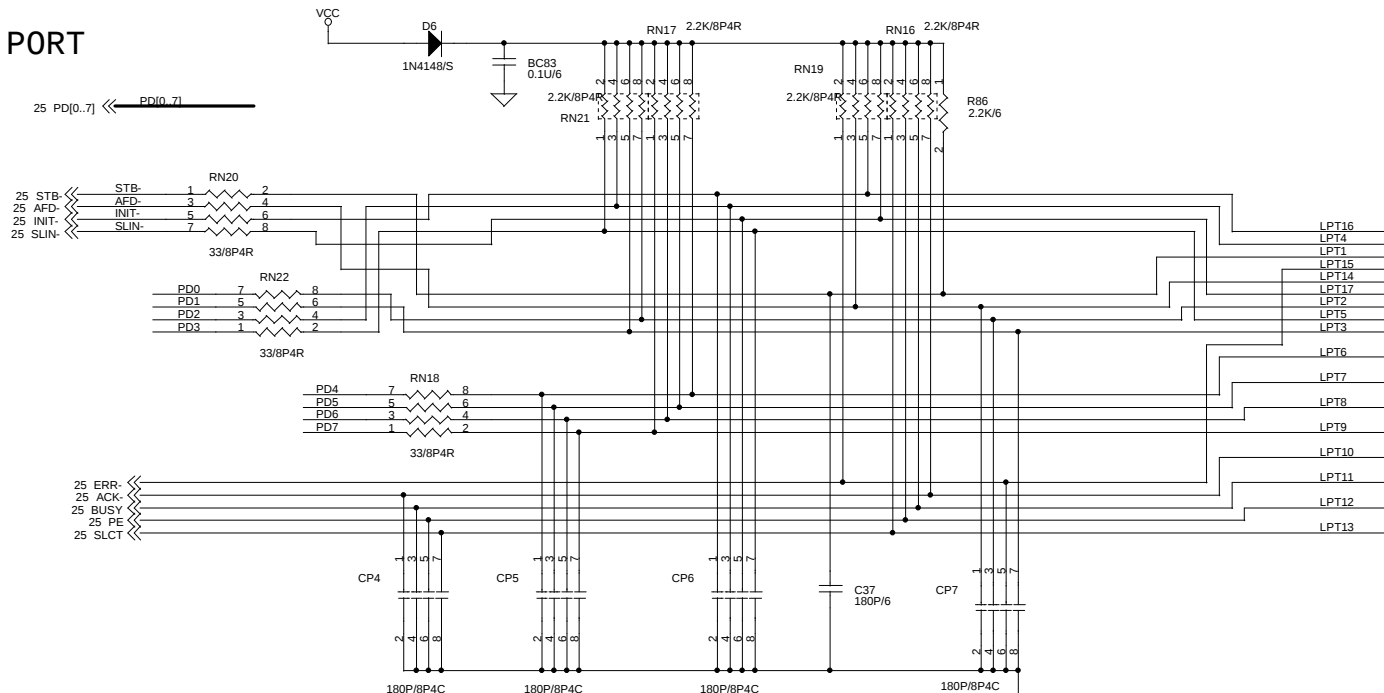
FLOPPY



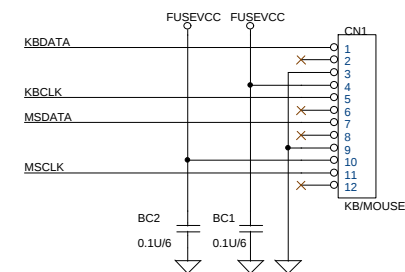
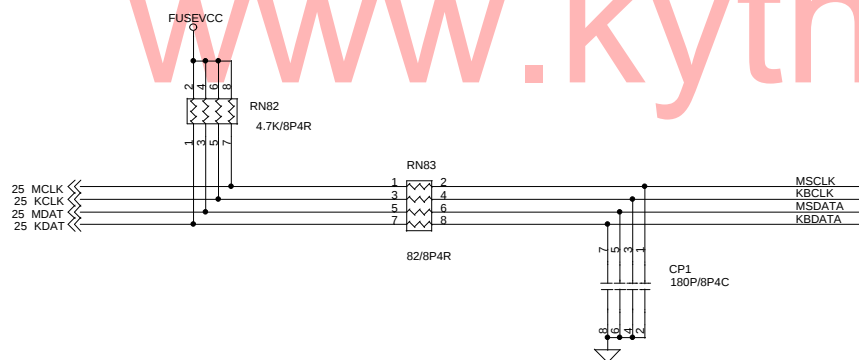
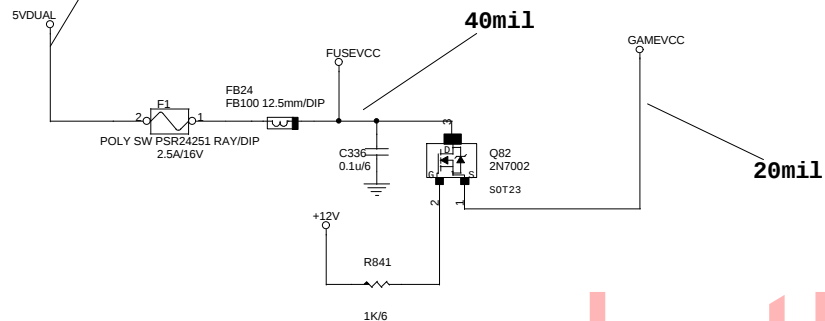
COMB



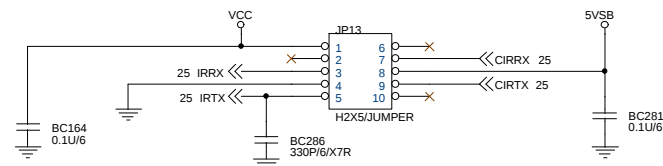
PRT PORT



Layout 40mil

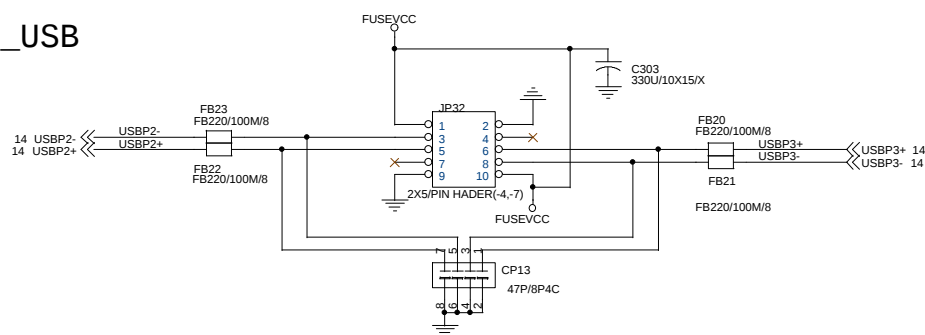


IR/CIR CONNECTOR



PIN TYPE MUST MATCH PHYSICAL LAYOUT
THE KBVCC OF PIN 8 IS FOR CIR WAKE-UP FUNCTION.

F_USB



GIGABYTE CORP.			
Title			
KB & PS2 MOUSE & IR			
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CPU Voltage ID output

VCC

RN44 1K/8P4R

1 2 VID3 3,25,30

3 4 VID2 3,25,30

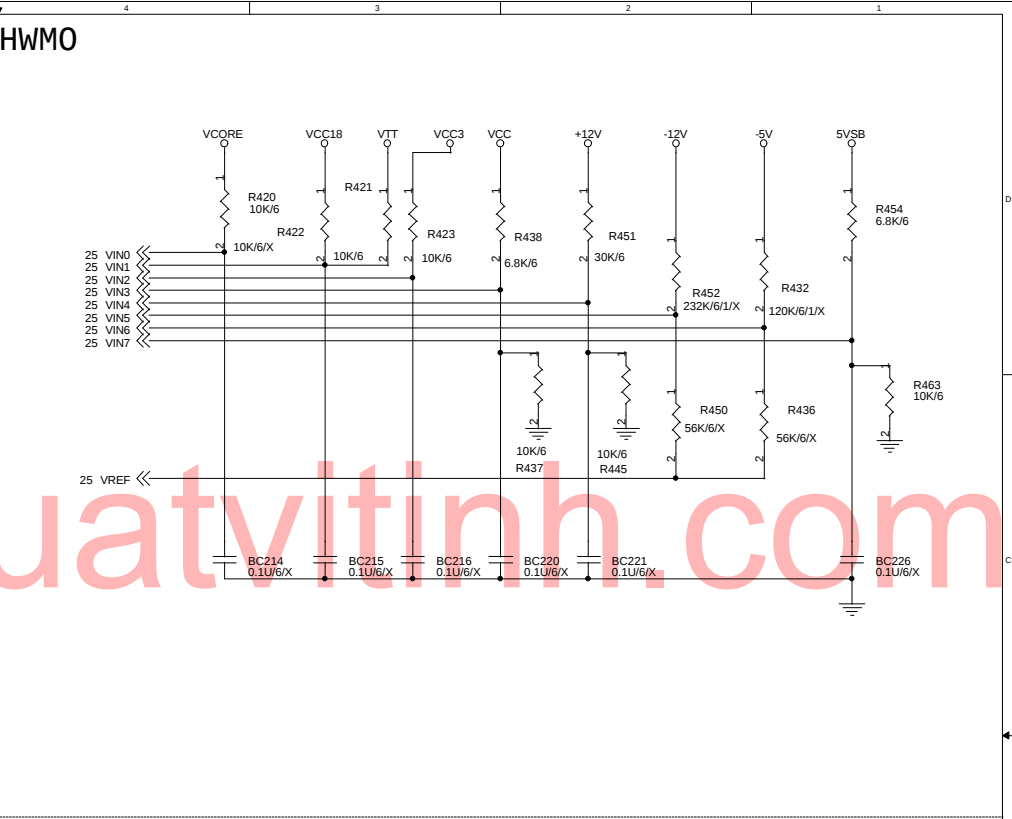
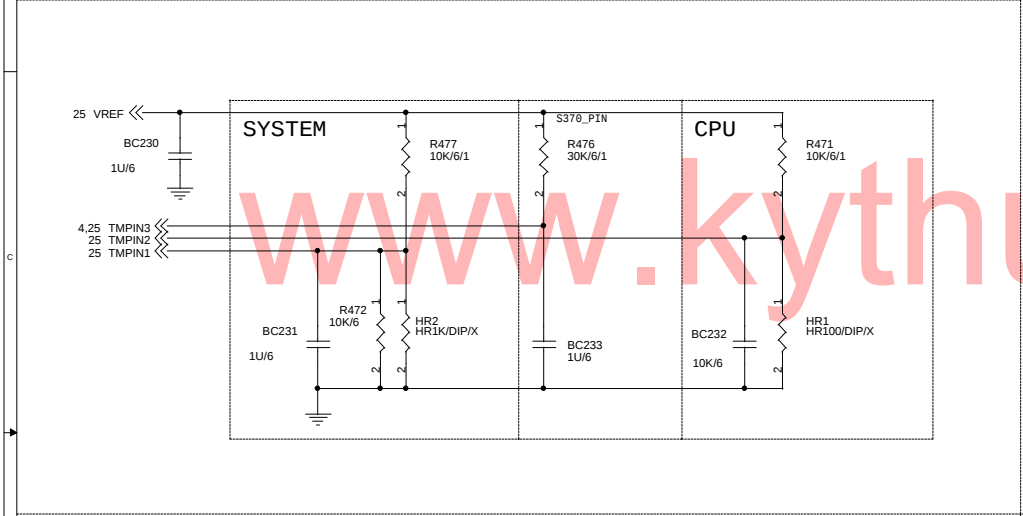
5 6 VID1 3,25,30

7 8 VID0 3,25,30

9 VID_25MV 4,25,30

R470 1K/6

1 2 VID_25MV 4,25,30



CPU_FAN

B

VCC

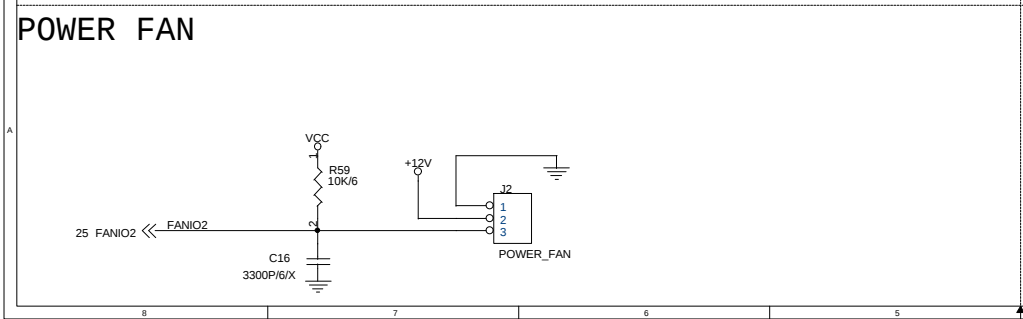
R41
10K/6

+12V

J1
1
2
3
CPU_FAN

25 FANIO1 << FANIO1

C10
3300P/6/X



SYSTEM_FAN

25 FANIO3 << FANIO3

VCC

R507
10K/6

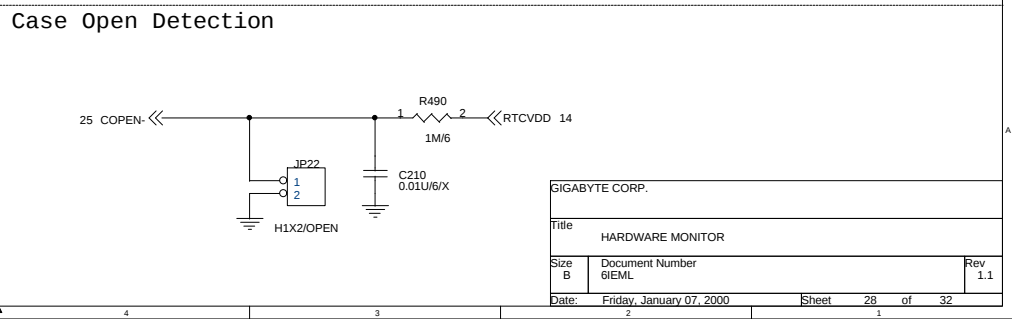
+12V

J14

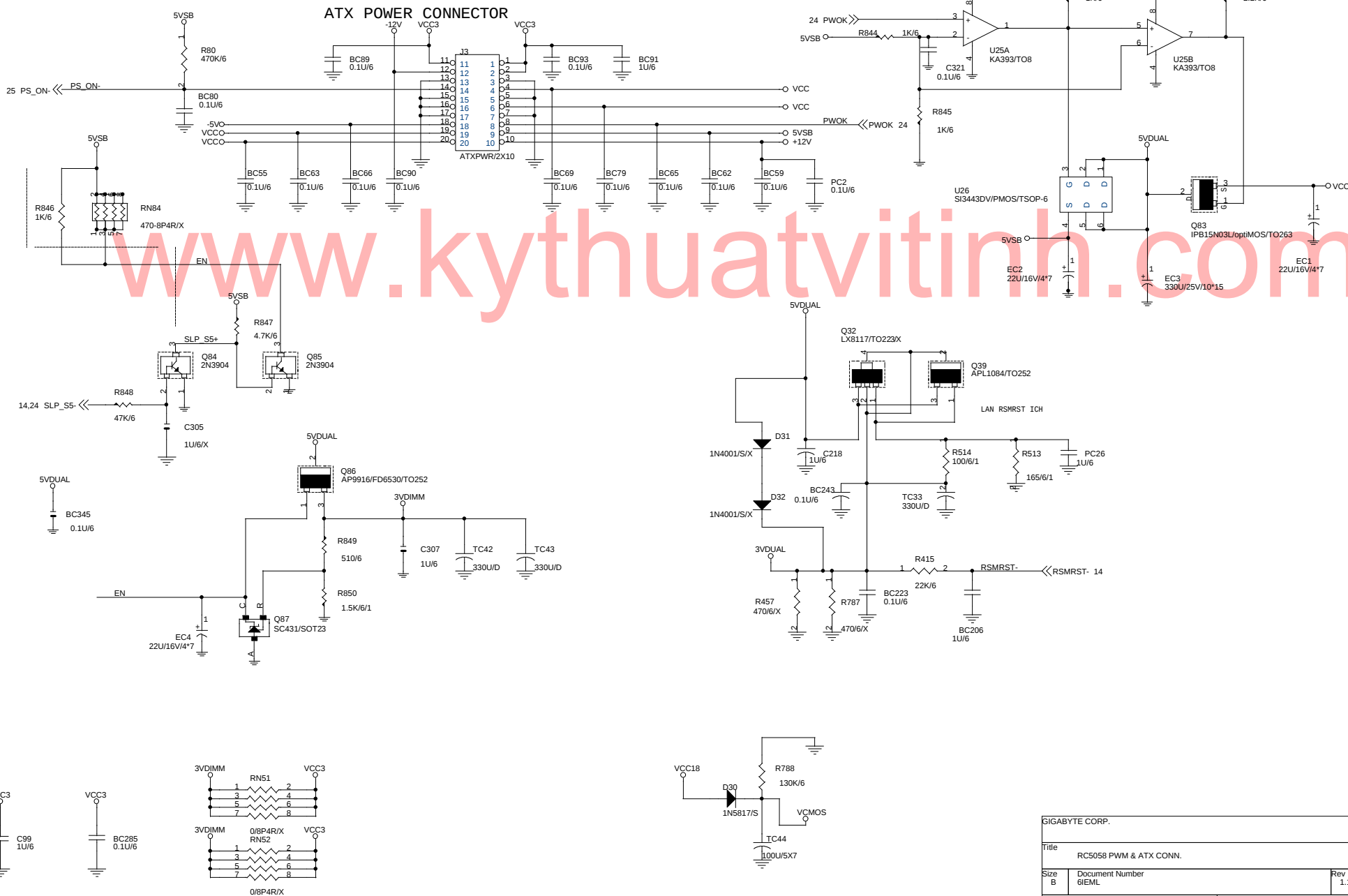
1
2
3

SYSTEM_FAN

C217
3300P/6/X

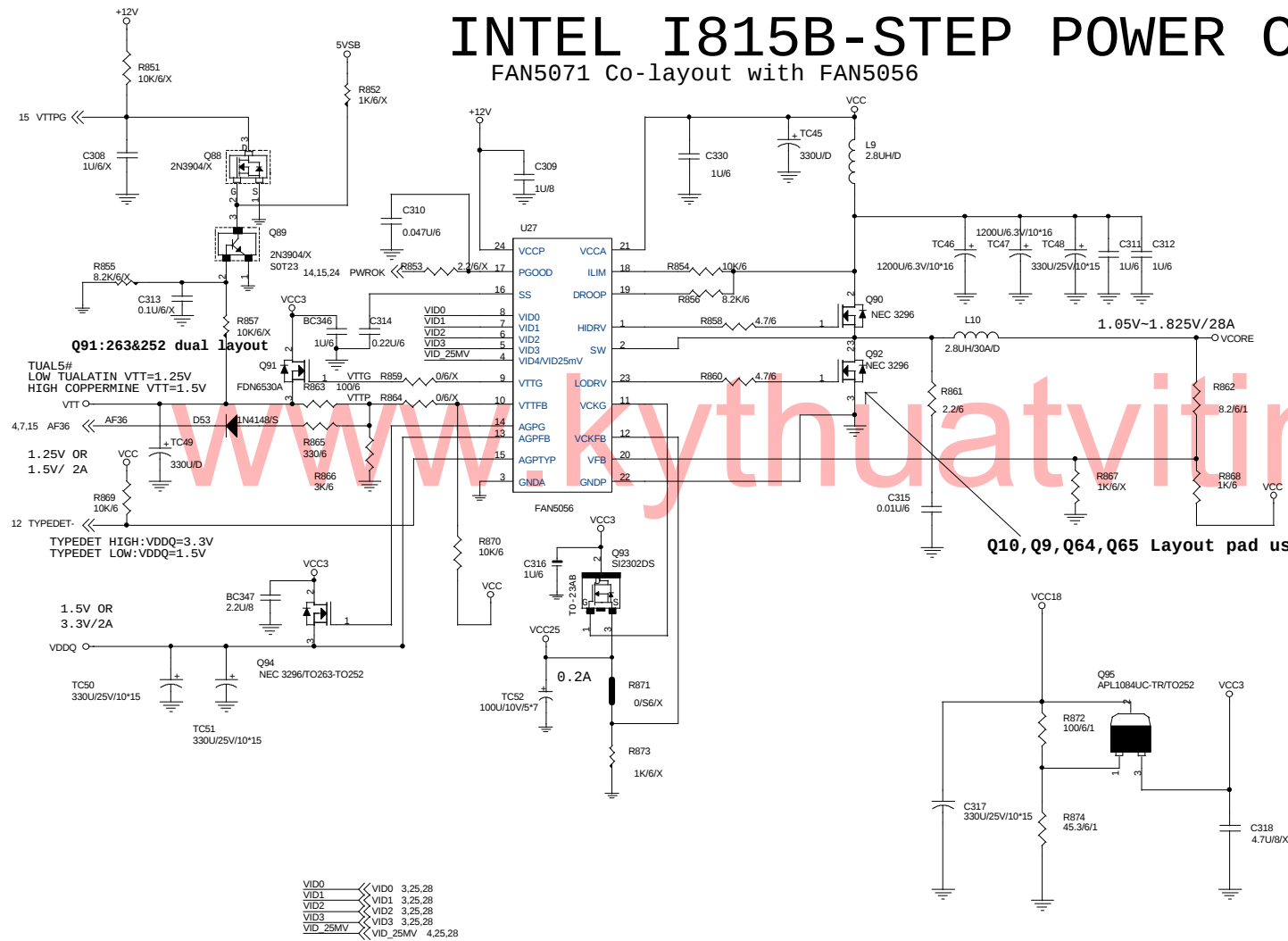


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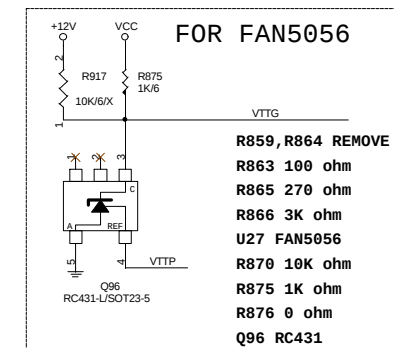
INTEL I815B-STEP POWER CKT

FAN5071 Co-layout with FAN5056

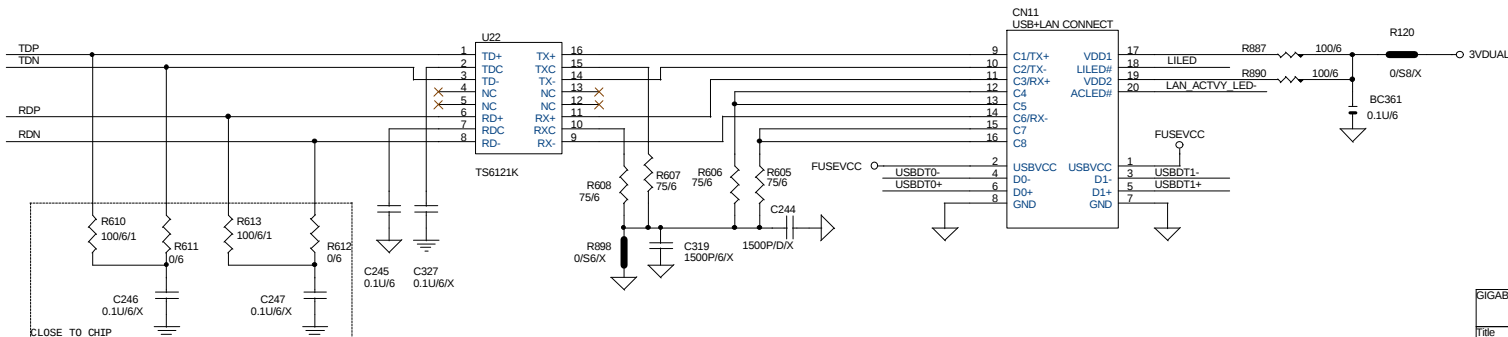
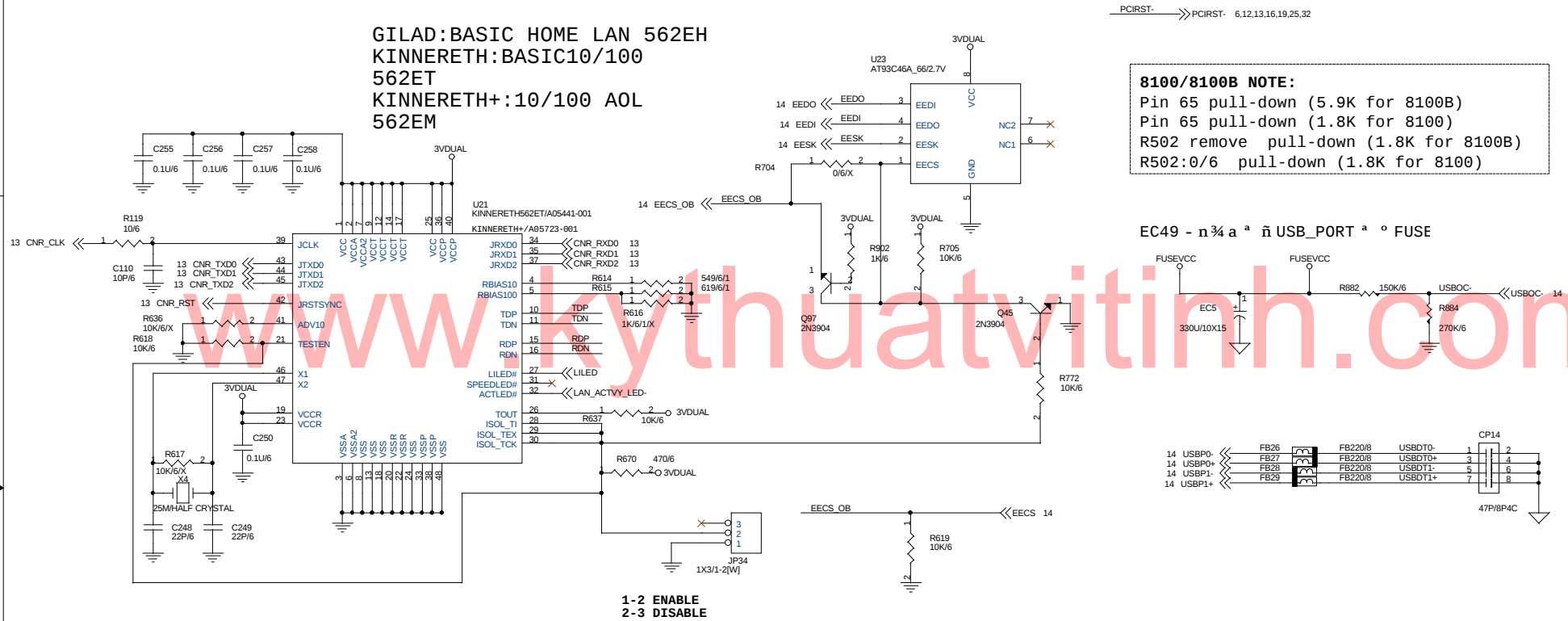


Q10,Q9,Q64,Q65 Layout pad use T0252+T0263 Dual Layout

LAYOUT CLOSE TO Q91

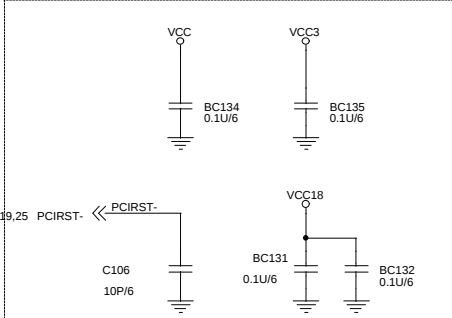


GILAD: BASIC HOME LAN 562EH
KINNERETH: BASIC10/100
562ET
KINNERETH+: 10/100 AOL
562EM



GIGABYTE CORP.

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