

GIGABYTE GA-8IPE1000-G REV4.0 Schematics Revision 4.0 2

Project Code:A93127-1

SHEET TITLE

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01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	P4_478A
05	P4_478B
06	P4_478C
07	SPRINGDALE HOST
08	SPRINGDALE DDR
09	SPRINGDALE AGP, HUB, CSA, VGA
10	SPRINGDALE PWR
11	DDR1,2 CHANNEL A
12	DDR3,4 CHANNEL B
13	DDR TERMINATION
14	AGP
15	ICH5 PCI, USB, HUB, LAN
16	ICH5 IDE, GPIO, SATA, CTRL
17	ICH5 VCC, GND
18	FWH
19	ICS952603 CLOCK GEN
20	PCI1_2
21	PCI3_4
22	PCI5

23	CODEC
24	AUDIO JACK, L_OUT, F_AUDIO
25	SMSC LPC47M997
26	COM_LPT
27	IDE
28	FAN
29	KB_PS2
30	FPANEL
31	USB CONN
32	ALL POWER
33	VCORE POWER
34	ATX, OTHERS POWER
35	REALTEK RTL8110SB LAN
36	DDR POWER
37	PCI ROUNT LIST
38	GPIO LIST
PCB	304.8*220.091mm/C/60+-15%
Vender	統盟,育富,慶生

		COMPONENT SIDE (1 oz. Copper) VCC SIDE (1 oz. Copper) GND SIDE (1 oz. Copper) SOLDER SIDE (1 oz. Copper)
GIGABYTE CORP.		
COVER SHEET		
Size Custom	Document Number GA-8IPE1000-G	Rev 4.02

Model Name: GA-8IPE1000-G
BOM: 9M8IPE1000G-00-40C
PCB: GA-8IPE1000-G REV4.0 4.0 2

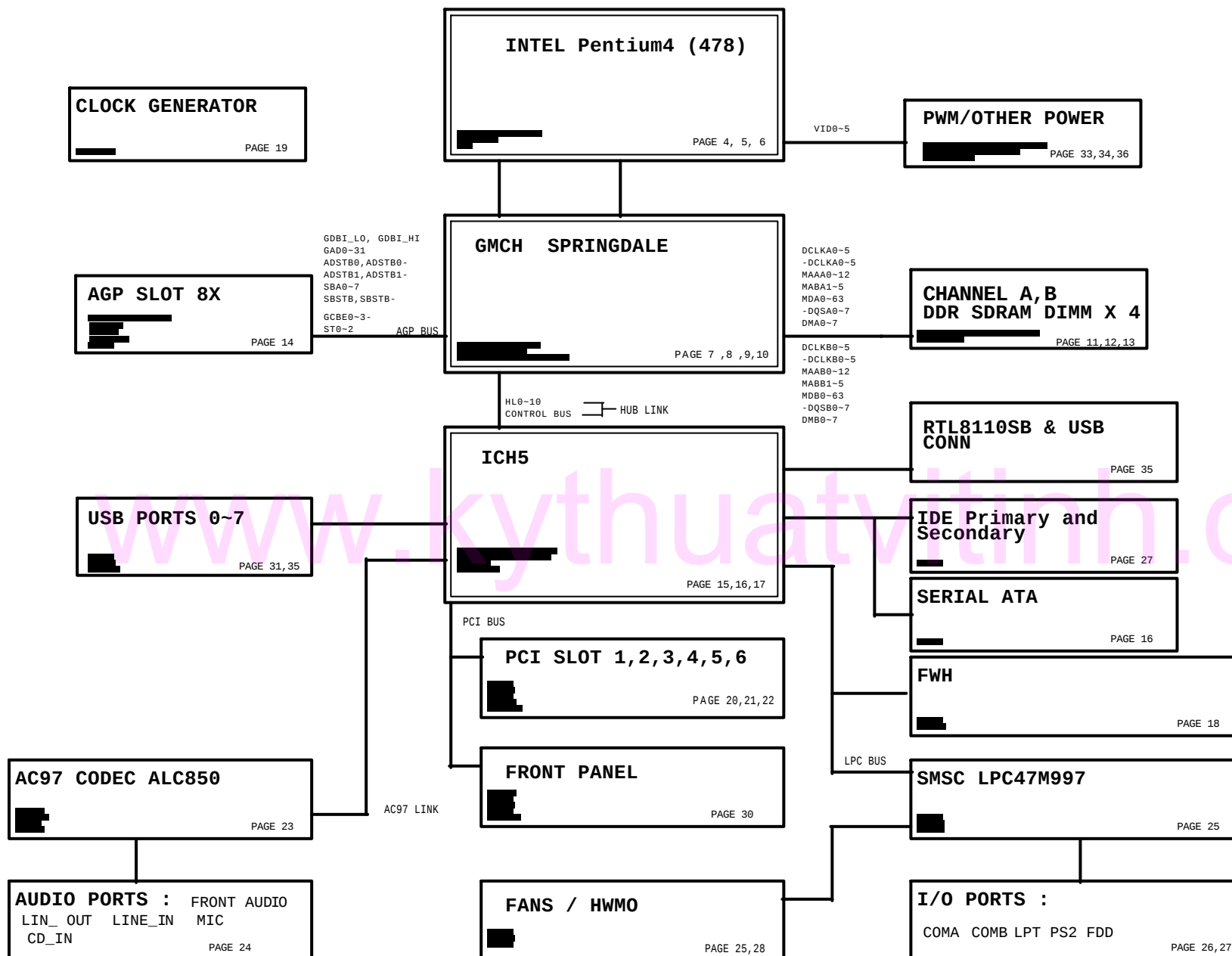
Component history

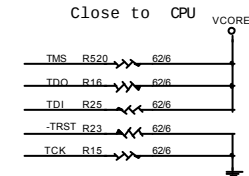
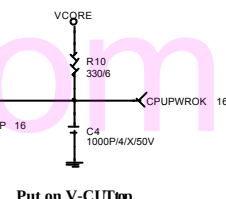
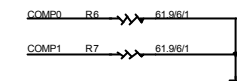
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Circuit or PCB layout history

[illegible]

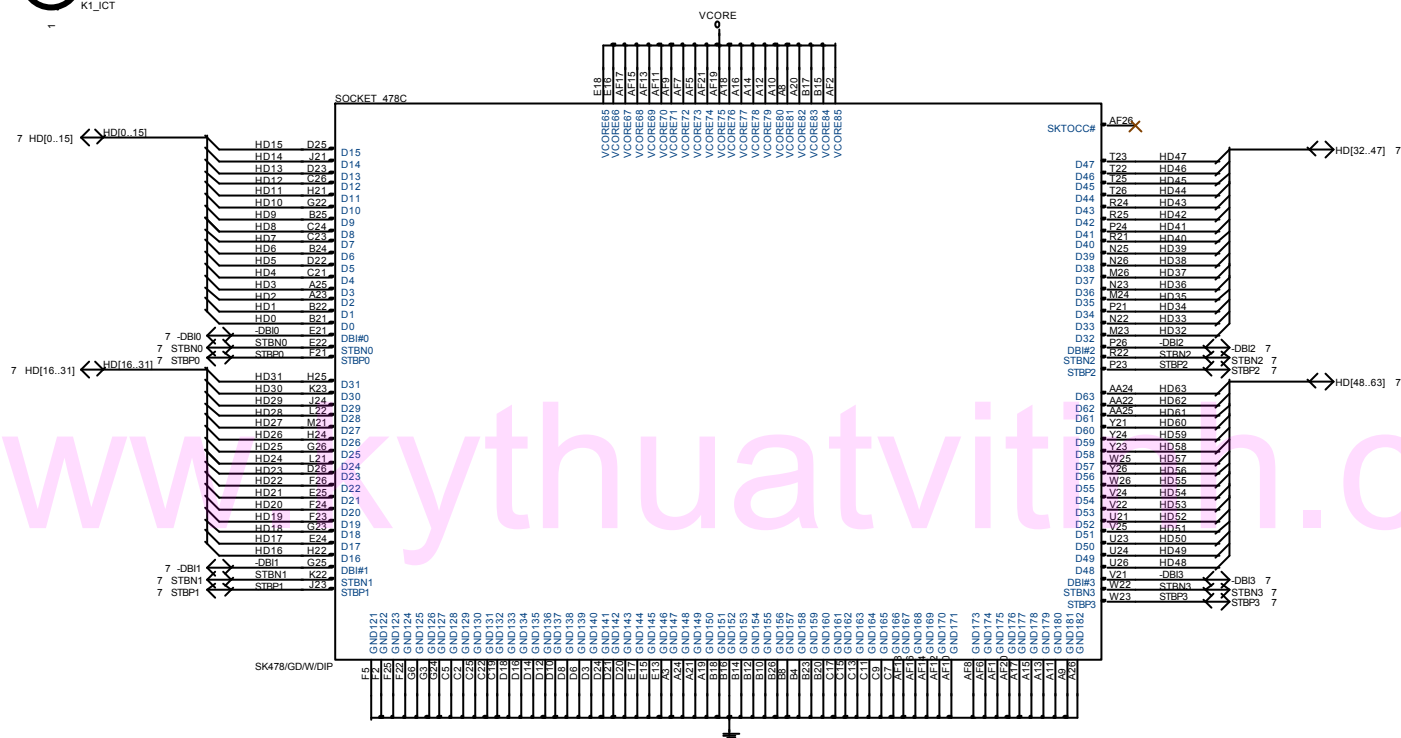
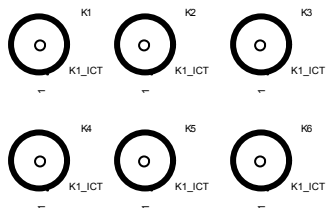
BLOCK DIAGRAM



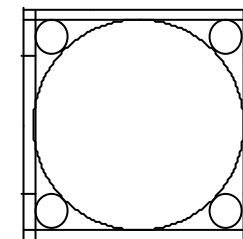


Pull up must place end
of route

Title			
P4 478B			
Size	Document Number	Rev	
Custom	GA-8IPE1000-G	4.0 2	
Date	Sheet 5 of 38		
1			



CR1
CPU_RM1BM_NEW



Title			P4 478C
Size	Document Number	Rev	
Custom	GA-8IPE1000-G	4.0 2	
Date	Sheet	6	of 38

11,13 MAA[0..12] ↔ MAA[0..12]
11,13 MAB[1..5] ↔ MAB[1..5]
11,13 DMA[0..7] ↔ DMA[0..7]
11,13 MDA[0..63] ↔ MDA[0..63]
11,13 DSA[0..7] ↔ DSA[0..7]

U1B
MAA0 AJ34 SMAA_A0
MAA1 AJ35 SMAA_A1
MAA2 AK29 SMAA_A2
MAA3 AN31 SMAA_A3
MAA4 AL30 SMAA_A4
MAA5 AL26 SMAA_A5
MAA6 AL28 SMAA_A6
MAA7 AN25 SMAA_A7
MAA8 AP28 SMAA_A8
MAA9 AP24 SMAA_A9
MAA10 AJ33 SMAA_A10
MAA11 AN23 SMAA_A11
MAA12 AN21 SMAA_A12
MAB1 AL34 SMAB_A1
MAB2 AM34 SMAB_A2
MAB3 AP29 SMAB_A3
MAB4 AP31 SMAB_A4
MAB5 AM26 SMAB_A5
SWEA AR34 SWE_A#
SCASA Y31 SCAS_A#
SRASA AC33 SRAS_A#
SBA0 AE33 SBA_A0
SBA1 AH34 SBA_A1
CSA0 AE34 SCS_A0#
CSA1 Y31 SCS_A1#
CSA2 Y32 SCS_A2#
CSA3 W34 SCS_A3#
CKEA0 AL20 SCKE_A0
CKEA1 AN19 SCKE_A1
CKEA2 AM20 SCKE_A2
CKEA3 AP20 SCKE_A3
DCLKA0 AK32 SCMDCLK_A0
DCLKA1 AP17 SCMDCLK_A1#
DCLKA1 AN17 SCMDCLK_A1#
DCLKA2 N34 SCMDCLK_A2#
DCLKA2 AK33 SCMDCLK_A2#
DCLKA3 AK34 SCMDCLK_A3#
DCLKA4 AM18 SCMDCLK_A3#
DCLKA4 AL10 SCMDCLK_A4#
DCLKA5 P31 SCMDCLK_A5#
DCLKA5 P32 SCMDCLK_A5#

DDR Channel A
SDQS_A0 AN11 DQSA0
SDM_A0 AP10 MDA0
SDQ_A0 AP11 MDA1
SDQ_A1 AM12 MDA2
SDQ_A2 AN13 MDA3
SDQ_A3 AM10 MDA4
SDQ_A4 AL10 MDA5
SDQ_A5 AL12 MDA6
SDQ_A6 AP13 MDA7
SDQS_A7 AP15 DQSA1
SDM_A7 AP16 DMA1
SDQ_A8 AP14 MDA8
SDQ_A9 AL15 MDA10
SDQ_A10 AP19 MDA11
SDQ_A11 AL14 MDA12
SDQ_A12 AN15 MDA13
SDQ_A13 AP18 MDA14
SDQ_A14 AM18 MDA15
SDQ_A15 AP23 DQSA2
SDM_A2 AM24 DMA2
SDQ_A16 AP22 MDA16
SDQ_A17 AM22 MDA17
SDQ_A18 AL24 MDA18
SDQ_A19 AN27 MDA19
SDQ_A20 AP21 MDA20
SDQ_A21 AP22 MDA21
SDQ_A22 AP25 MDA22
SDQ_A23 AP27 MDA23
SDQS_A3 AM30 DQSA3
SDM_A3 AP30 DMA3
SDQ_A24 AP28 MDA24
SDQ_A25 AP29 MDA25
SDQ_A26 AP33 MDA26
SDQ_A27 AM33 MDA27
SDQ_A28 AM28 MDA28
SDQ_A29 AN29 MDA29
SDQ_A30 AM31 MDA30
SDQ_A31 AN34 MDA31
SDQS_A4 AF34 DQSA4
SDM_A4 AF31 DMA4
SDQ_A32 AH32 MDA32
SDQ_A33 AG34 MDA33
SDQ_A34 AF32 MDA34
SDQ_A35 AD32 MDA35
SDQ_A36 AH31 MDA36
SDQ_A37 AG33 MDA37
SDQ_A38 AE34 MDA38
SDQ_A39 AD34 MDA39
SDQS_A5 V34 DQSA5
SDM_A5 W33 DMA5
SDQ_A40 AC34 MDA40
SDQ_A41 AB31 MDA41
SDQ_A42 V32 MDA42
SDQ_A43 AD31 MDA44
SDQ_A44 AB32 MDA45
SDQ_A45 U34 MDA46
SDQ_A46 U33 MDA47
SDQ_A47 M32 DQSA6
SDQS_A6 M34 DMA6
SDM_A6 T34 MDA48
SDQ_A48 T32 MDA49
SDQ_A49 K34 MDA50
SDQ_A50 K32 MDA51
SDQ_A51 T31 MDA52
SDQ_A52 P34 MDA53
SDQ_A53 L34 MDA54
SDQ_A54 L33 MDA55
SDQS_A7 H31 DQSA7
SDM_A7 H32 DMA7
SDQ_A56 J33 MDA56
SDQ_A57 H34 MDA57
SDQ_A58 E33 MDA58
SDQ_A59 F33 MDA59
SDQ_A60 K31 MDA60
SDQ_A61 J31 MDA61
SDQ_A62 G34 MDA62
SDQ_A63 F34 MDA63

DDRREF_A E34
SMVREF_A
SMXRCOMP A8
SMXRCOMPVOH A9
SMXRCOMPVOL AL9

BC46 1U/6Y/10V
Closed to MCH

DDRREF_A
BC50 1U/6Y/10V/X

DDR25V
BC58 1U/6Y/10V
R47 42.2/6/1
R49 42.2/6/1
BC55 33P4/X/50V/X
SC20 0.1U/6Y/25V/B/X
Closed to R47

MCH SPDPE I865PE SL722/A2
DDR INTERFACE

12,13 MAAB[0..12] ↔ MAAB[0..12]
12,13 MABB[1..5] ↔ MABB[1..5]
12,13 DMB[0..7] ↔ DMB[0..7]
12,13 MDB[0..63] ↔ MDB[0..63]
12,13 DQSB[0..7] ↔ DQSB[0..7]

U1C
MAAB0 AG31 SMAA_B0
MAAB1 AJ31 SMAA_B1
MAAB2 AD27 SMAA_B2
MAAB3 AE24 SMAA_B3
MAAB4 AK27 SMAA_B4
MAAB5 AG25 SMAA_B5
MAAB6 AE24 SMAA_B6
MAAB7 AF21 SMAA_B7
MAAB8 AL23 SMAA_B8
MAAB9 AJ22 SMAA_B9
MAAB10 AF28 SMAA_B10
MAAB11 AL21 SMAA_B11
MAAB12 AJ20 SMAA_B12
MABB1 AE27 SMAB_B1
MABB2 AD26 SMAB_B2
MABB3 AJ29 SMAB_B3
MABB4 AL27 SMAB_B4
MABB5 AE23 SMAB_B5
SWEB W27 SWE_B#
SCASB W31 SCAS_B#
SRASB W26 SRAS_B#
SBA0 Y26 SBA_B0
SBA1 AA26 SBA_B1
CSB0 U26 SCS_B0#
CSB1 T29 SCS_B1#
CSB2 V26 SCS_B2#
CSB3 W25 SCS_B3#
CKEB0 AK19 SCKE_B0
CKEB1 AE18 SCKE_B1
CKEB2 AG18 SCKE_B2
CKEB3 AE18 SCKE_B3
DCLKB0 AG26 SCMDCLK_B0
DCLKB0 AF17 SCMDCLK_B0#
DCLKB1 AF17 SCMDCLK_B1#
DCLKB2 N27 SCMDCLK_B1#
DCLKB2 N26 SCMDCLK_B2#
DCLKB3 A10 SCMDCLK_B2#
DCLKB3 AL29 SCMDCLK_B3#
DCLKB4 AK15 SCMDCLK_B4#
DCLKB4 N11 SCMDCLK_B4#
DCLKB5 N30 SCMDCLK_B5#

DDRREF_B
BC47 1U/6Y/10V
Closed to MCH

DDR25V
BC65 1U/6Y/10V
R42 150/6/1
R45 150/6/1
BC52 1U/6Y/10V/X
DDRREFB → DDRREFB

DDR25V
BC278 1U/6Y/10V
R48 42.2/6/1
R50 42.2/6/1
BC56 33P4/N/50V/X
BC513 0.1U/6Y/25V/X
SMYRCOMP
SMYRCOMPVOH
SMYRCOMPVOL

U1C
SDQS_B0 AF15 DQSB0
SDM_B0 AJ10 MDB0
SDQ_B0 AE15 MDB1
SDQ_B1 AL11 MDB2
SDQ_B2 AE16 MDB3
SDQ_B3 AL18 MDB4
SDQ_B4 AF12 MDB5
SDQ_B5 AK11 MDB6
SDQ_B6 AG12 MDB7
SDQ_B7
SDQS_B8 AG13 DQSB1
SDM_B8 AG15 DMB1
SDQ_B8 AE17 MDB8
SDQ_B9 AL13 MDB9
SDQ_B10 AK17 MDB10
SDQ_B11 AL17 MDB11
SDQ_B12 AK13 MDB12
SDQ_B13 AL14 MDB13
SDQ_B14 AL16 MDB14
SDQ_B15 A118 MDB15
AG21 DQSB2
SDM_B2 AE21 DMB2
SDQ_B16 AE19 MDB16
SDQ_B17 AE20 MDB17
SDQ_B18 AG23 MDB18
SDQ_B19 AG23 MDB19
SDQ_B20 AL19 MDB20
SDQ_B21 AK21 MDB21
SDQ_B22 AE22 MDB22
SDQ_B23 AE22 MDB23
SDQS_B3 AK25 DQSB3
SDM_B3 AK25 DMB3
SDQ_B24 AK26 MDB24
SDQ_B25 AG27 MDB25
SDQ_B26 AG27 MDB26
SDQ_B27 AE27 MDB27
SDQ_B28 AE26 MDB28
SDQ_B29 AJ27 MDB29
SDQ_B30 AD26 MDB30
SDQ_B31 AF28 MDB31
SDQS_B4 AD29 DQSB4
SDM_B4 AC31 DMB4
SDQ_B32 AE30 MDB32
SDQ_B33 AC30 MDB33
SDQ_B34 V29 MDB34
SDQ_B35 AE31 MDB35
SDQ_B36 AE29 MDB37
SDQ_B37 AE26 MDB38
SDQ_B38 AE27 MDB39
SDQ_B39
SDQS_B5 U30 DQSB5
SDM_B5 U31 DMB5
SDQ_B40 V30 MDB40
SDQ_B41 U27 MDB41
SDQ_B42 U26 MDB42
SDQ_B43 AA31 MDB44
SDQ_B44 V26 MDB45
SDQ_B45 U25 MDB46
SDQ_B46 R27 MDB47
SDQ_B47
SDQS_B6 M29 DQSB6
SDM_B6 M29 DMB6
SDQ_B48 P29 MDB48
SDQ_B49 K28 MDB49
SDQ_B50 L30 MDB51
SDQ_B51 R31 MDB52
SDQ_B52 R26 MDB53
SDQ_B53 P25 MDB54
SDQ_B54 L32 MDB55
SDQ_B55
SDQS_B7 J30 DQSB7
SDM_B7 J31 DMB7
SDQ_B56 K30 MDB56
SDQ_B57 H29 MDB57
SDQ_B58 F32 MDB58
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SDQ_B60 V25 MDB60
SDQ_B61 J29 MDB61
SDQ_B62 G32 MDB62
SDQ_B63

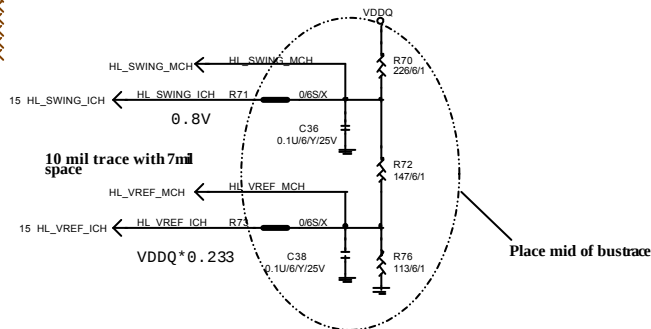
DDR Channel B
SDQS_B2 SDM_B2
SDQ_B16
SDQ_B17
SDQ_B18
SDQ_B19
SDQ_B20
SDQ_B21
SDQ_B22
SDQ_B23
SDQS_B3
SDM_B3
SDQ_B24
SDQ_B25
SDQ_B26
SDQ_B27
SDQ_B28
SDQ_B29
SDQ_B30
SDQ_B31
SDQS_B4
SDM_B4
SDQ_B32
SDQ_B33
SDQ_B34
SDQ_B35
SDQ_B36
SDQ_B37
SDQ_B38
SDQ_B39
SDQS_B5
SDM_B5
SDQ_B40
SDQ_B41
SDQ_B42
SDQ_B43
SDQ_B44
SDQ_B45
SDQ_B46
SDQ_B47
SDQS_B6
SDM_B6
SDQ_B48
SDQ_B49
SDQ_B50
SDQ_B51
SDQ_B52
SDQ_B53
SDQ_B54
SDQ_B55
SDQS_B7
SDM_B7
SDQ_B56
SDQ_B57
SDQ_B58
SDQ_B59
SDQ_B60
SDQ_B61
SDQ_B62
SDQ_B63

DDR25V
BC42 1U/6Y/10V
R35 10K/6/1
BC527 0.01U/6/X/50V
SMXRCOMPVOH
BC43 1U/6Y/10V
Closed to R35

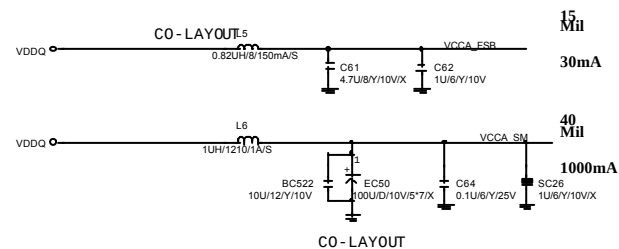
DDR25V
BC54 1U/6Y/10V
R38 30.9K/6/1
R37 10K/6/1
BC45 1U/6Y/10V
SMXRCOMPVOH
Closed to R38

DDR25V
BC71 0.01U/6/X/50V
BC277 1U/6Y/10V
R39 10K/6/1
BC528 0.01U/6/X/50V
SMYRCOMPVOH
BC49 1U/6Y/10V
Closed to R39

DDR25V
BC51 1U/6Y/10V/X
R43 30.9K/6/1
R46 10K/6/1
BC53 1U/6Y/10V
SMYRCOMPVOH
Closed to R43



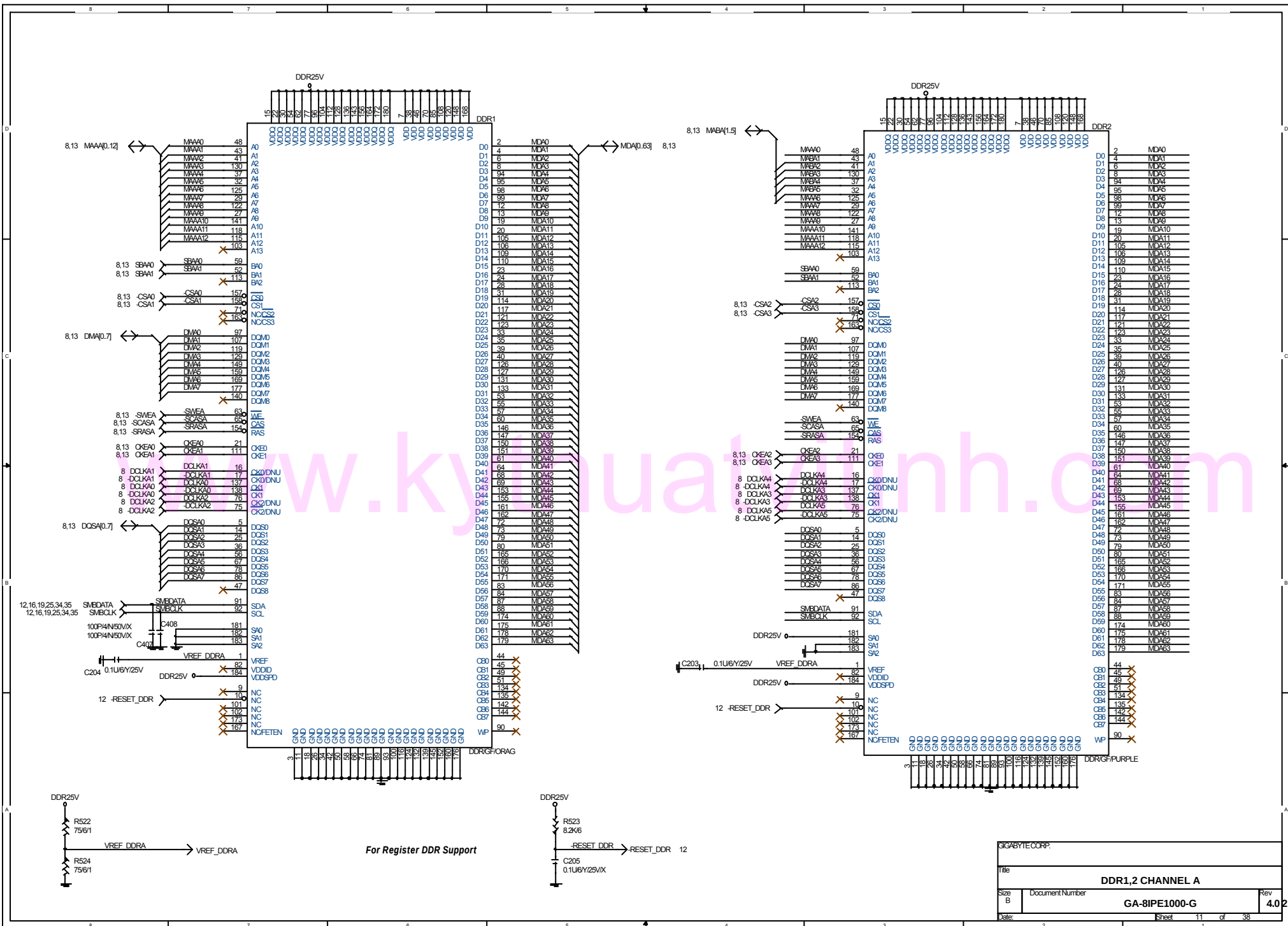
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Size	Document Number	GA-BIPE1000-G			Rev
Custom					4.0
Date	Sheet		9		of 38

[illegible]

9 CLSWING_SPG ← CLSWING_SPG
VDDQ*0.533 C43
0.1U/6/Y/25
10 mil trace with 7mil space

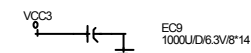
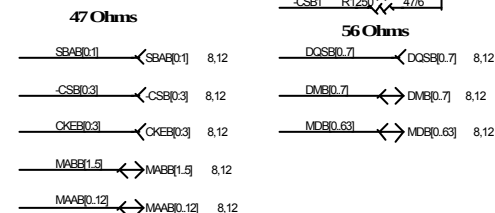
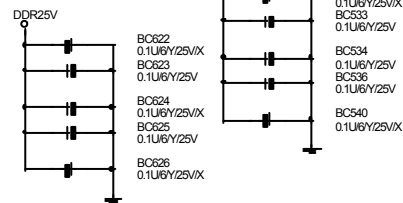
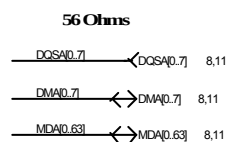
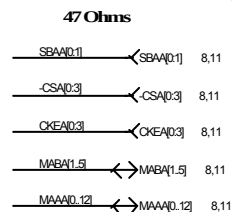
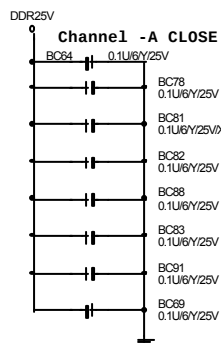
9 CLVREF_SPG ← CLVREF SPG

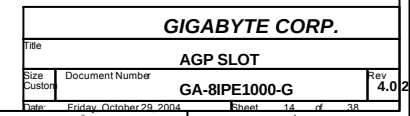
Close to N/B

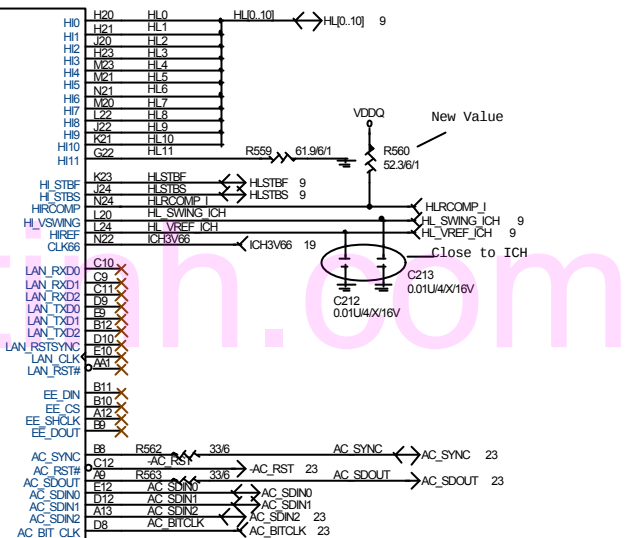
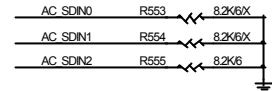


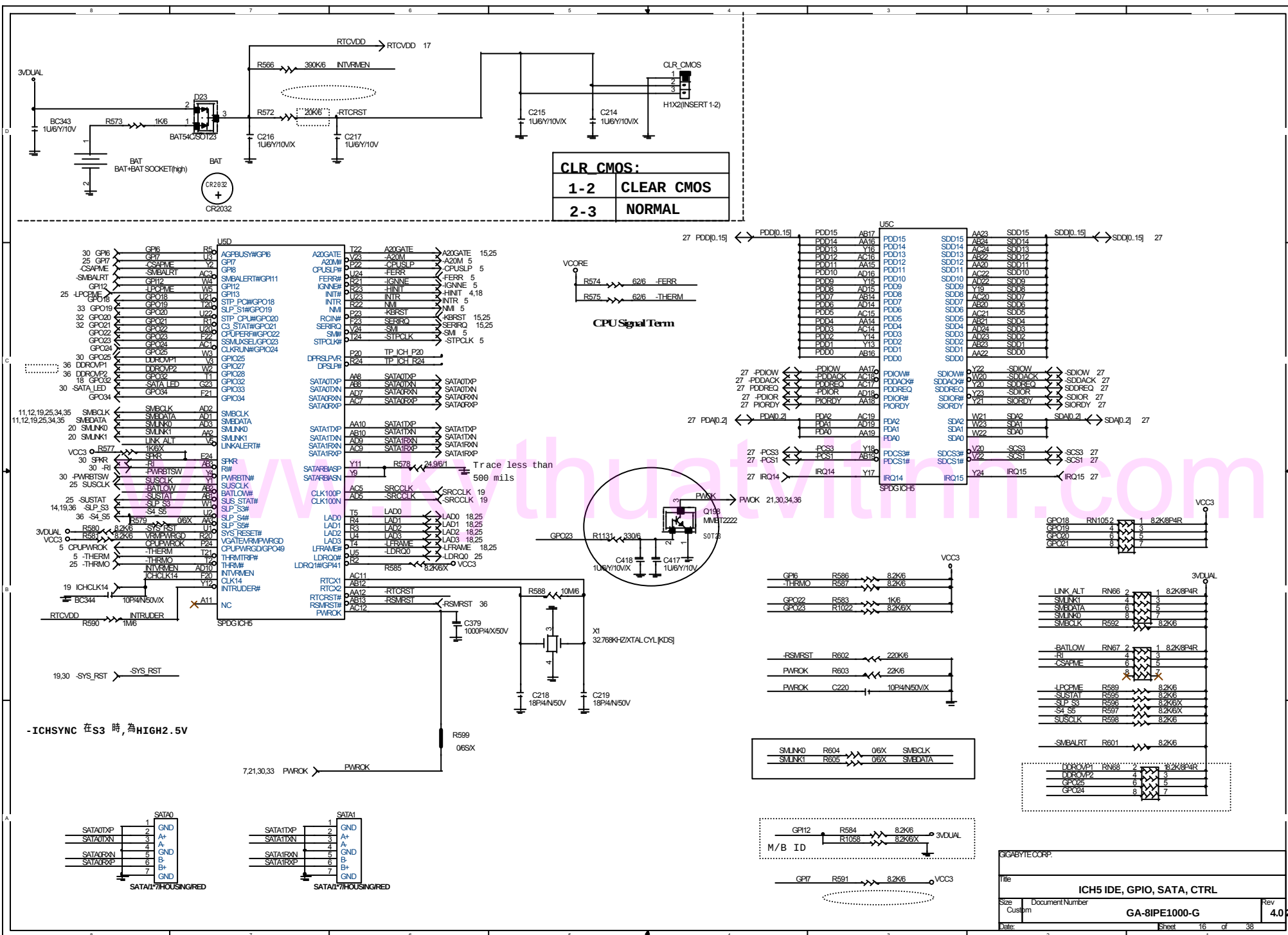
DDR TERMINATION CHANNEL A

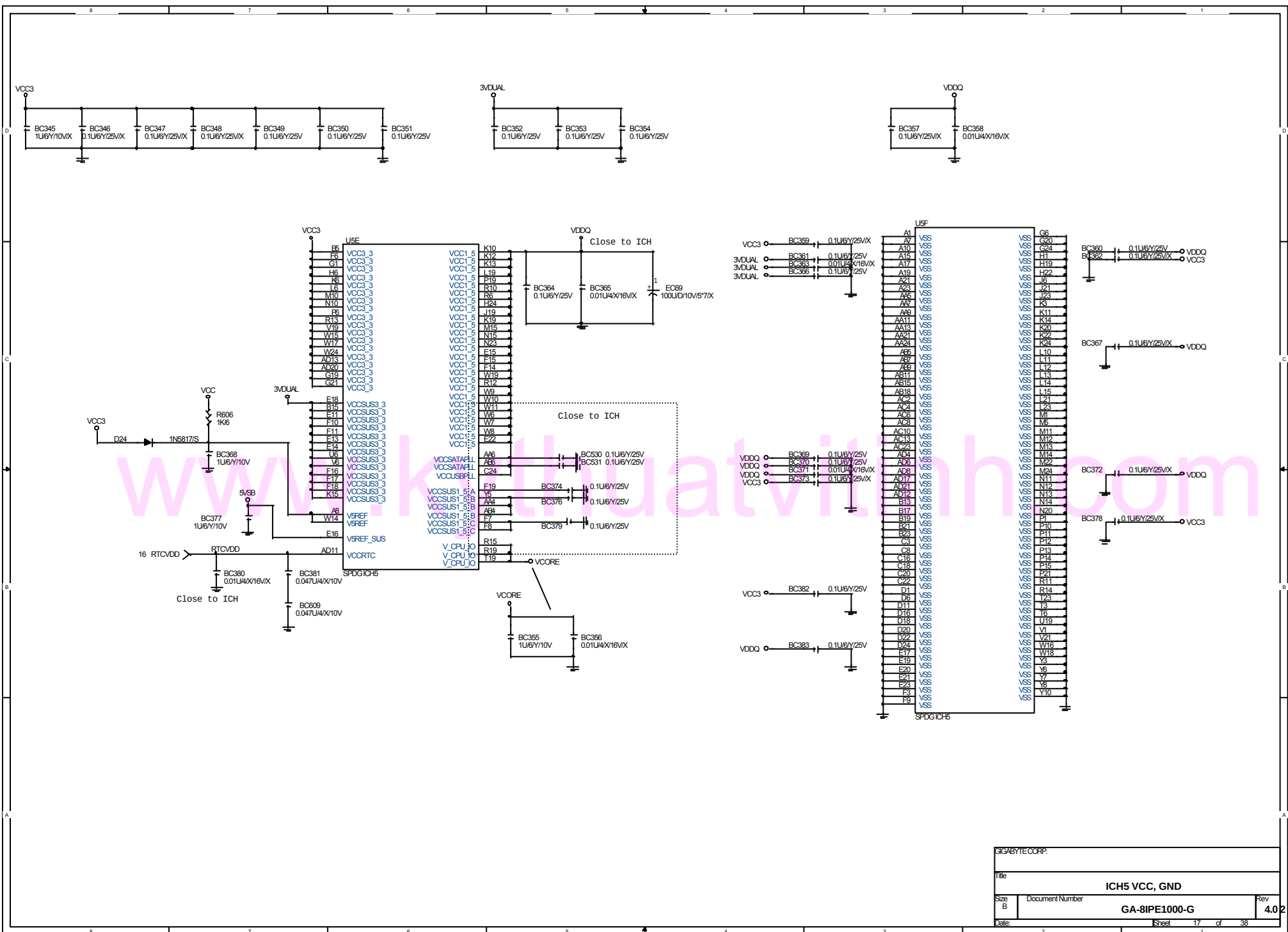
CHANNEL B

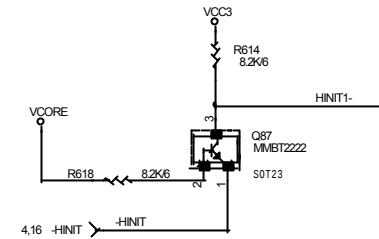
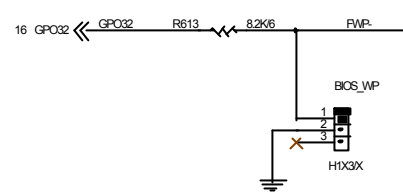






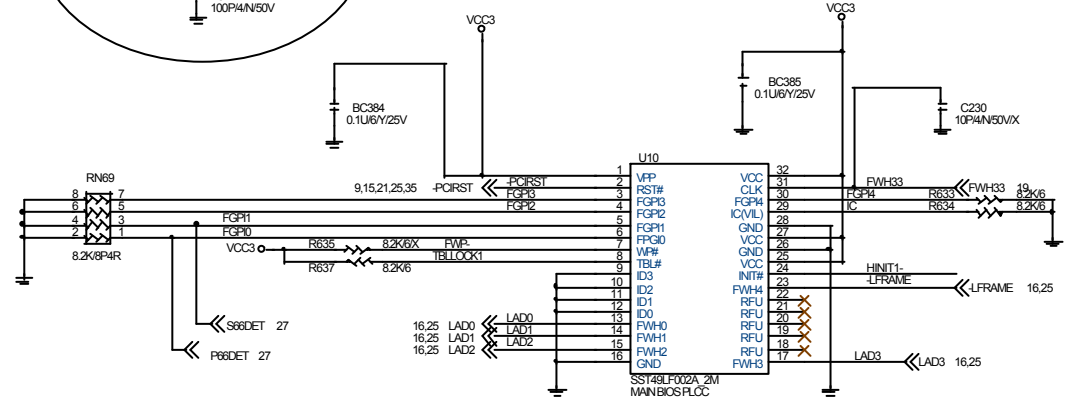
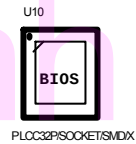
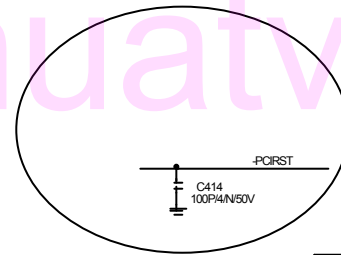






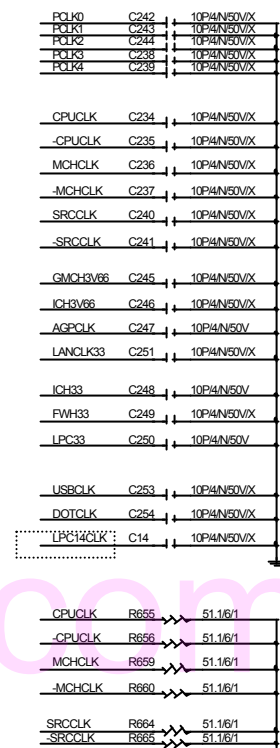
BIOS_WP:	
1-2	WRITE PROTECT
2-3	DISABLE

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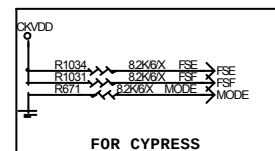
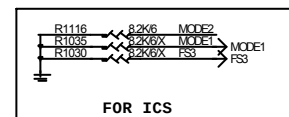
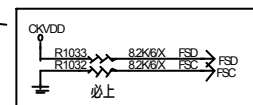
ADD WINBOUD FWH SEC. SOURCE

GIGABYTE CORP.		
Title		FWH
Size	Document Number	Rev
B	GA-8IPE1000-G	4.02
Date:	Friday, October 29, 2004	Sheet 18 of 38

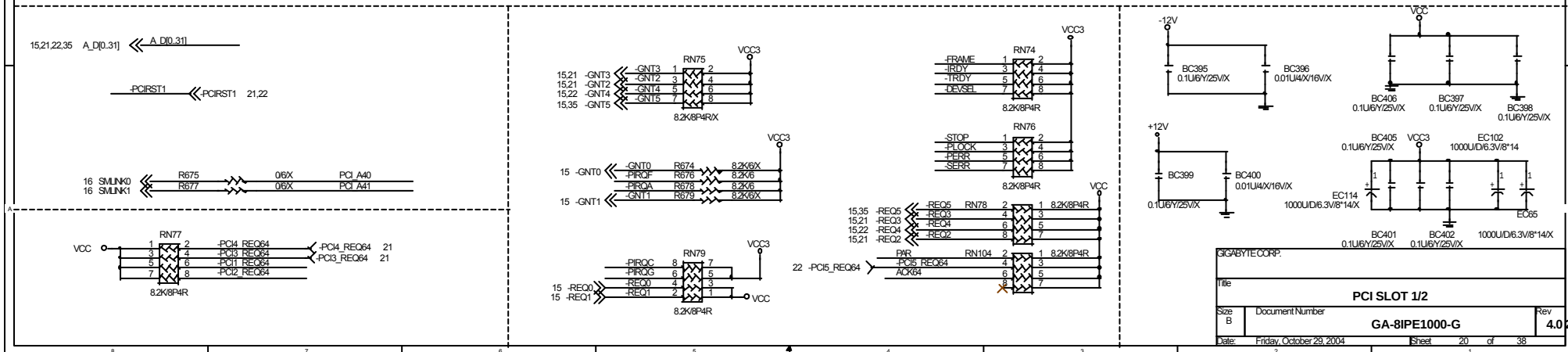


FS_E	FS_D	FS_C	FS_A	FS_B	Clock
1	1	0	0	0	100MHz
1	1	0	1	0	133MHz
1	1	0	1	1	166MHz
1	1	0	0	1	200MHz

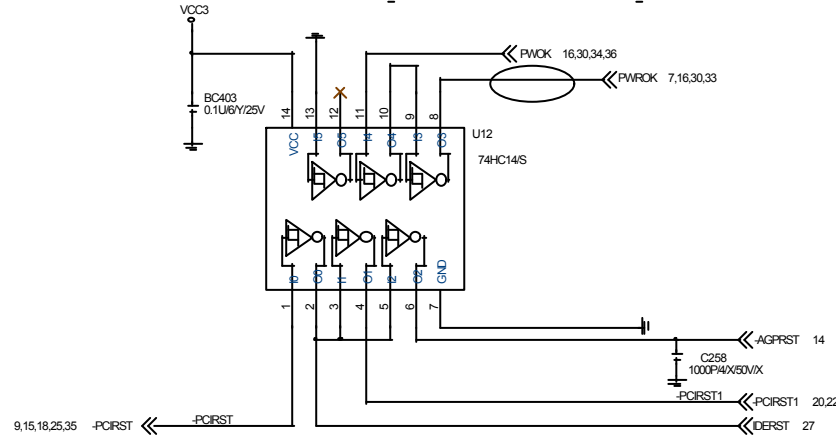
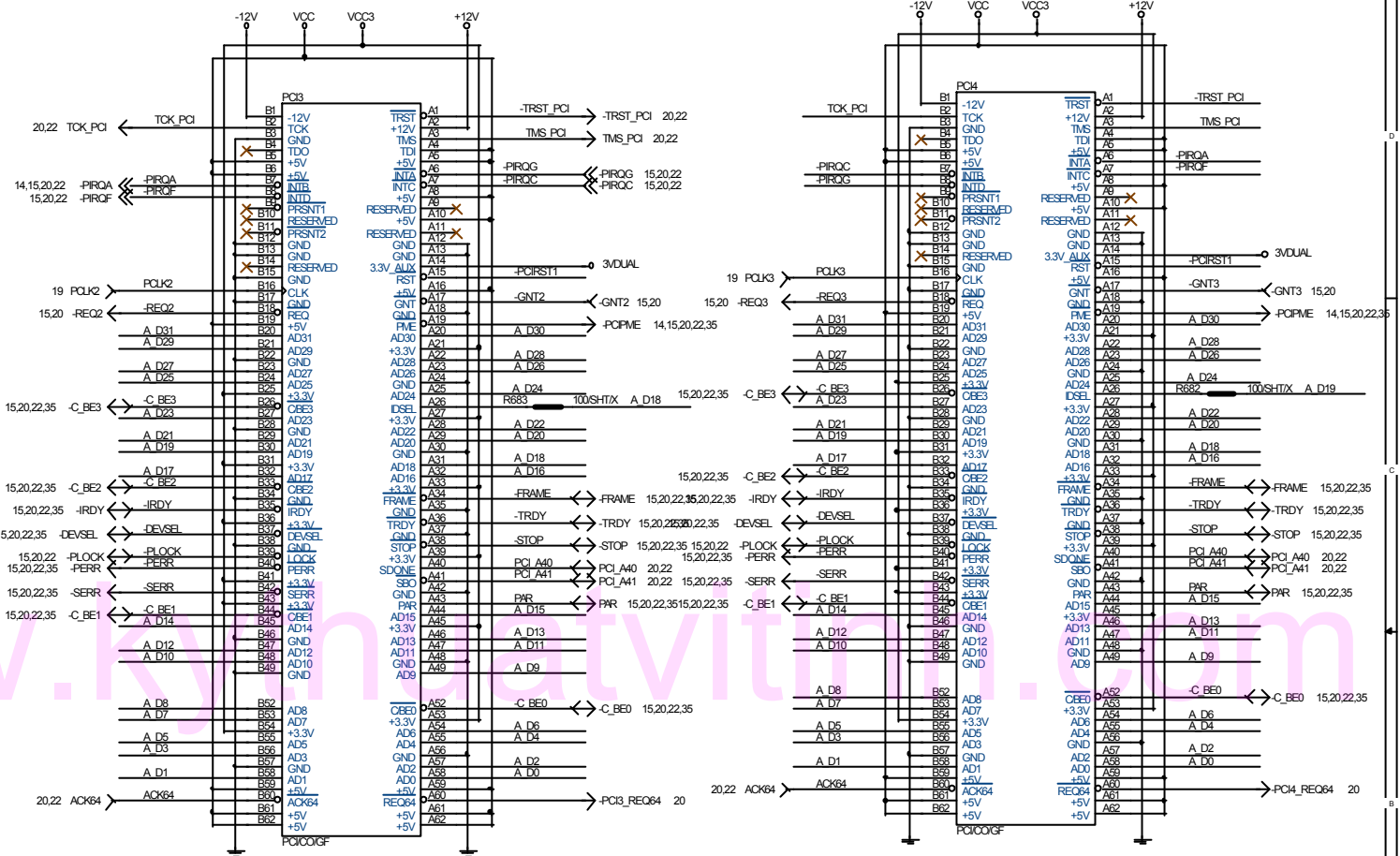
FS_D	FS_3	FS_C	FS_A	FS_B	Clock
1	0	0	0	0	100MHz
1	0	0	1	0	133MHz
1	0	0	1	1	166MHz
1	0	0	0	1	200MHz



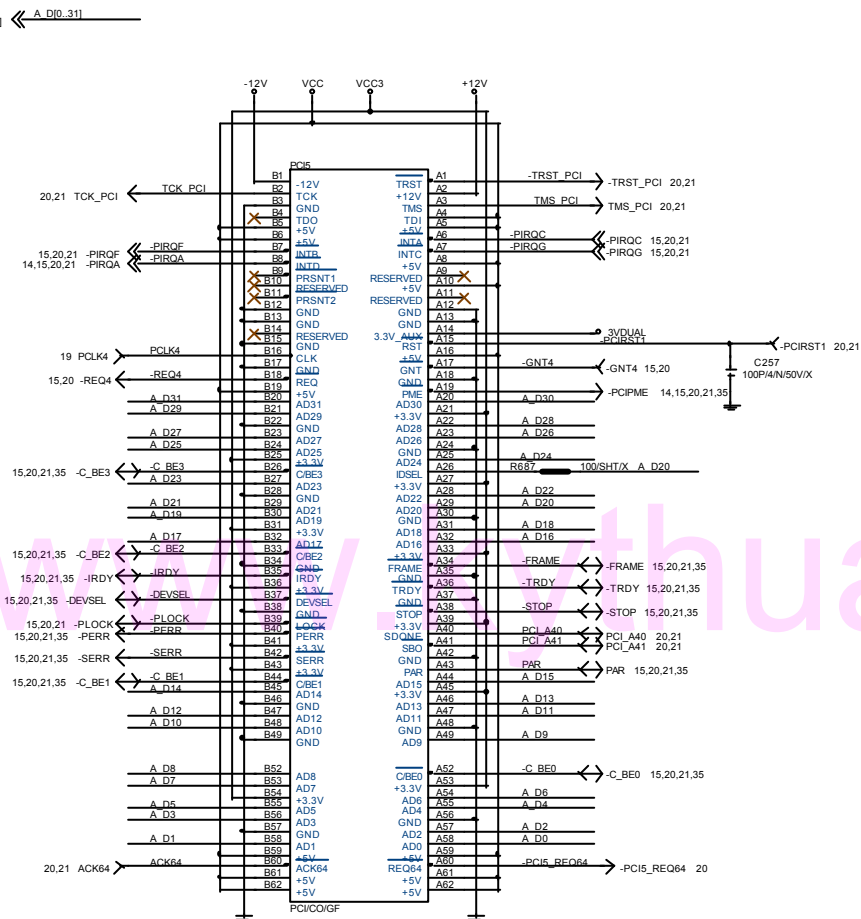
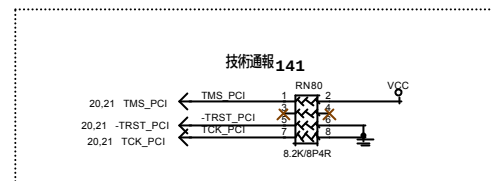
CY28405 上 R1031,R1034,R671
不上R1030,R1035
ICS952616上R1030,R1035
不上 R1031,R1034,R671



15,20,22,35 A_D[0..31] << A_D[0..31]



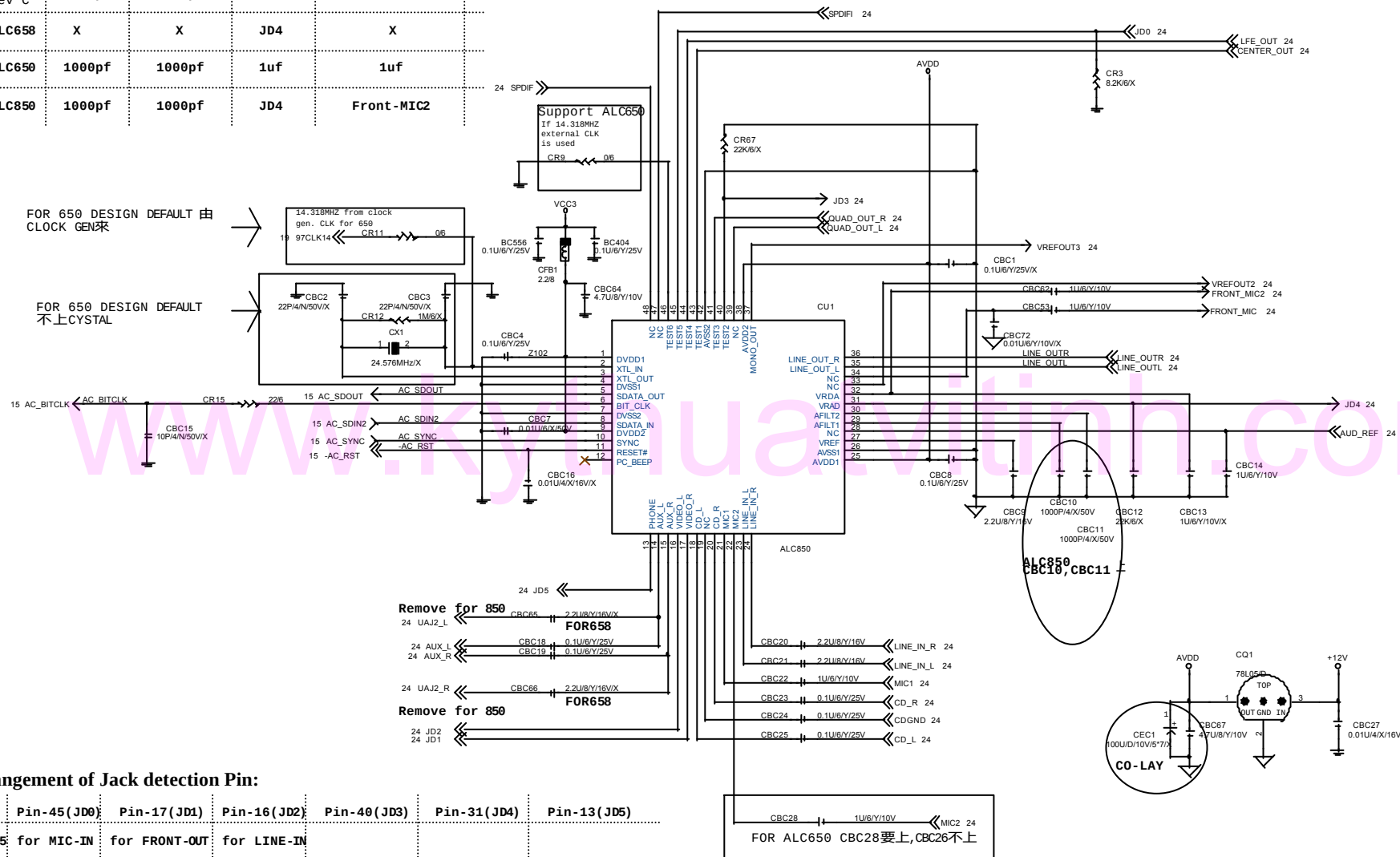
GIGABYTE CORP.			
Title			
PCI SLOT 3/4			
Size	Document Number	Rev	
B	GA-8IPE1000-G	4.0.2	
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GIGABYTE CORP.			
Title		PCI SLOT 5/6	
Size	Document Number	GA-8IPE1000-G	
Custom			Rev 4.0 2
Date	Friday, October 29, 2004	Sheet 22	of 38

Filter Cap design:

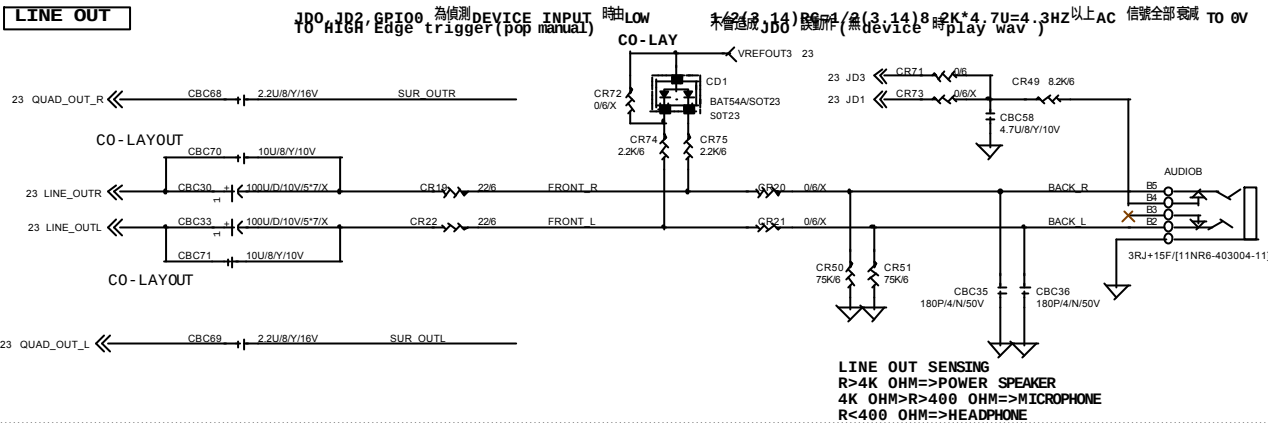
	Pin-29	Pin-30	Pin-31	Pin-32
ALC655 Rev D	1000pf	1000pf	1uf	Front-MIC2
ALC655 Rev C	1000pf	1000pf	1uf	X
ALC658	X	X	JD4	X
ALC650	1000pf	1000pf	1uf	1uf
ALC850	1000pf	1000pf	JD4	Front-MIC2



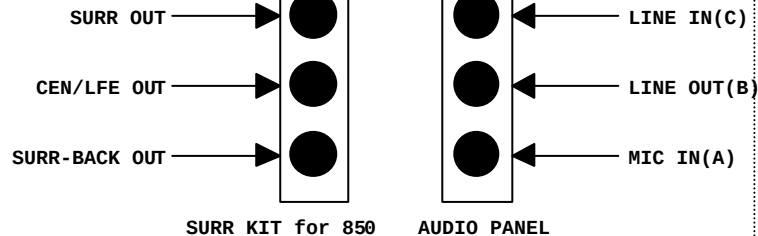
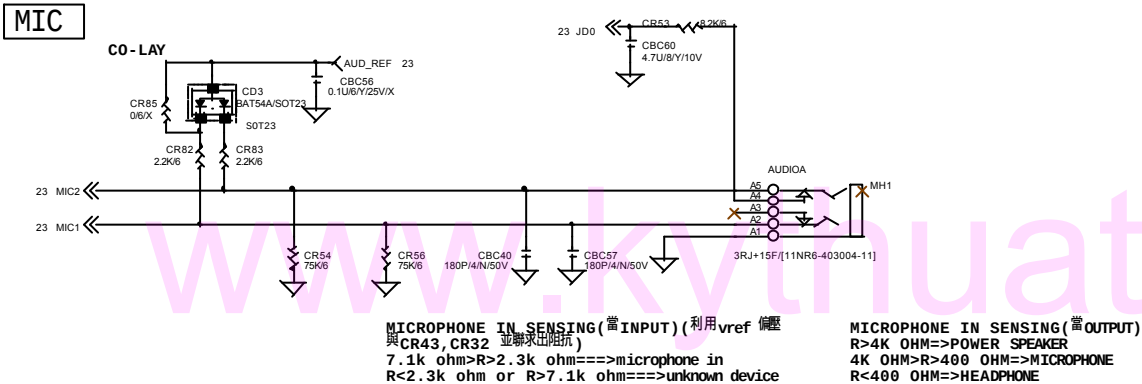
Arrangement of Jack detection Pin:

	Pin-45(JD0)	Pin-17(JD1)	Pin-16(JD2)	Pin-40(JD3)	Pin-31(JD4)	Pin-13(JD5)
ALC655	for MIC-IN	for FRONT-OUT	for LINE-IN			
ALC658	for MIC-IN	for UAJ1	for UAJ2	for FRONT-OUT	for LINE-IN	
				External pull high is needed	External pull high is needed	
ALC850	for MIC-IN	for Front Pannel OUT	for Front Pannel IN	for FRONT-OUT	for LINE-IN	for SurrBack Out

LINE OUT



MIC



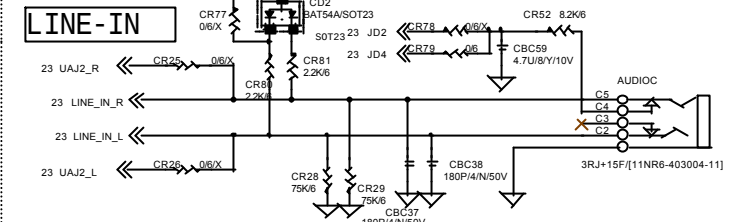
2x5 header for 850
For 850 if JD5 = low, AUX-In is configured as input
For 850 if JD5 = high AUX-In is configured as output, Surr-Back out

For 850 AUX-In is shared to Surr-Back out

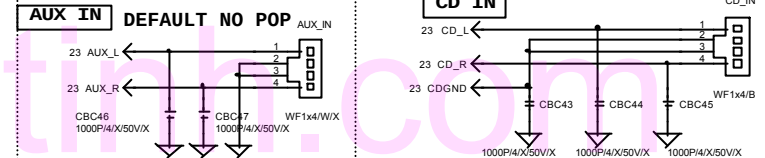
LINE IN SENSING (當 OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

FOR 850 LINE IN SENSING (當 INPUT)
swing of input signal>-40dbv(10mv)>line in device active
swing of input signal<-40dbv(10mv)>unknown line in device

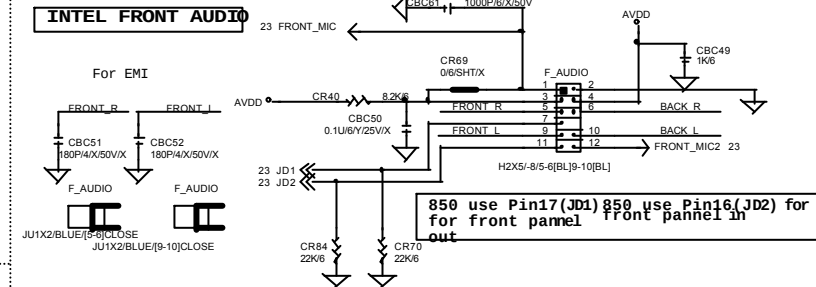
LINE-IN



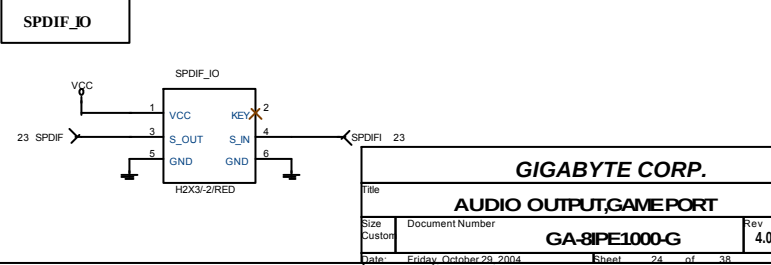
AUX IN

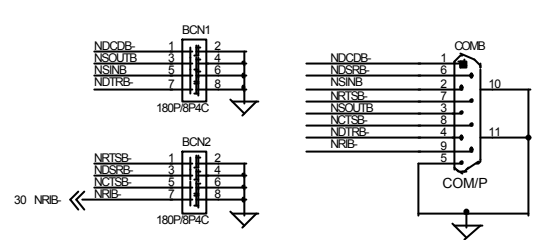
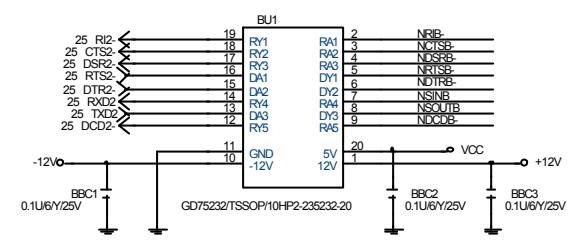
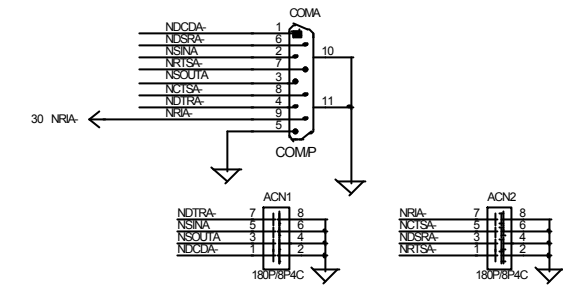
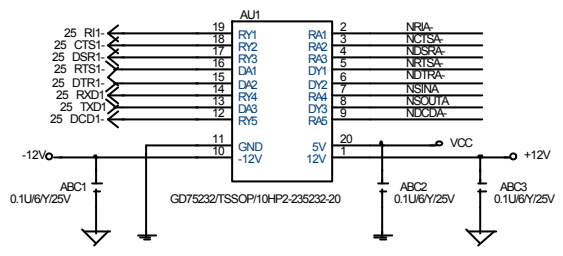


INTEL FRONT AUDIO



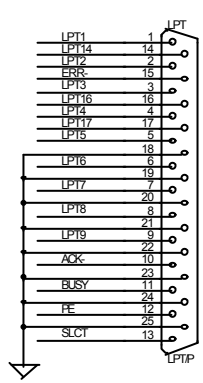
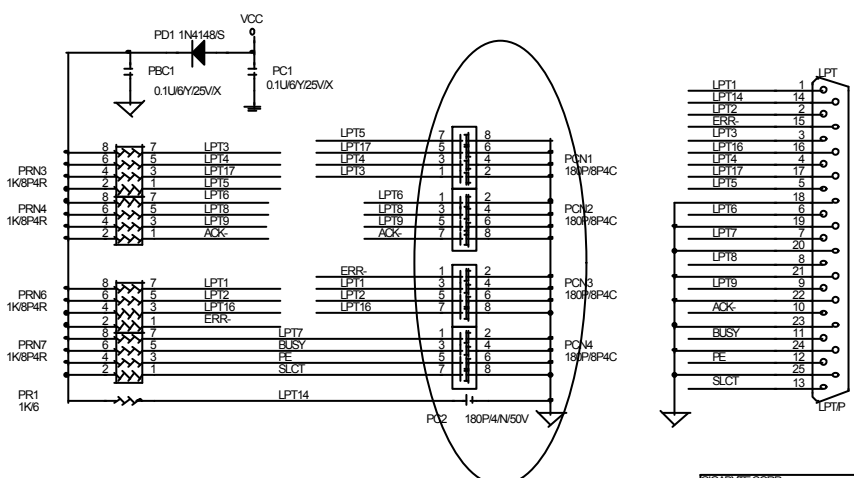
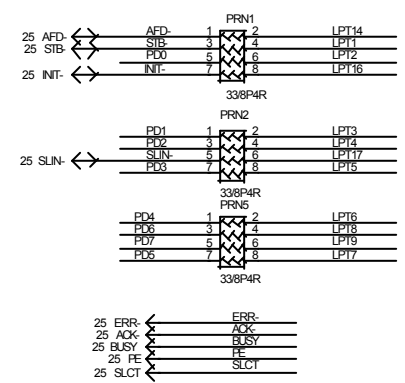
SPDIF_IO

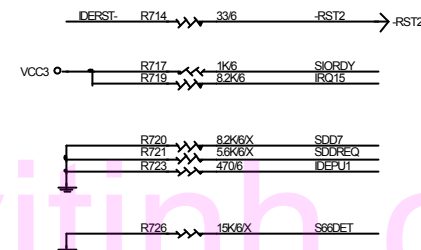
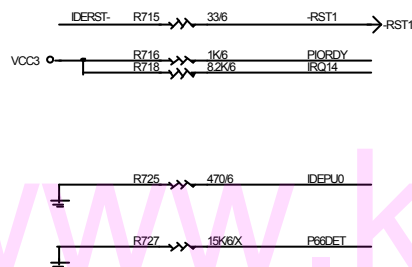
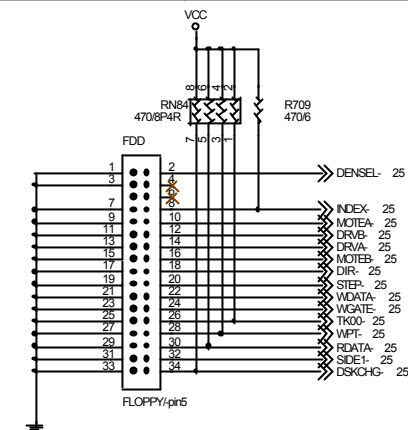
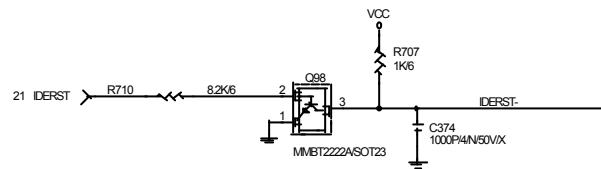




PLACE NEAR VGA_COM CONNECTOR

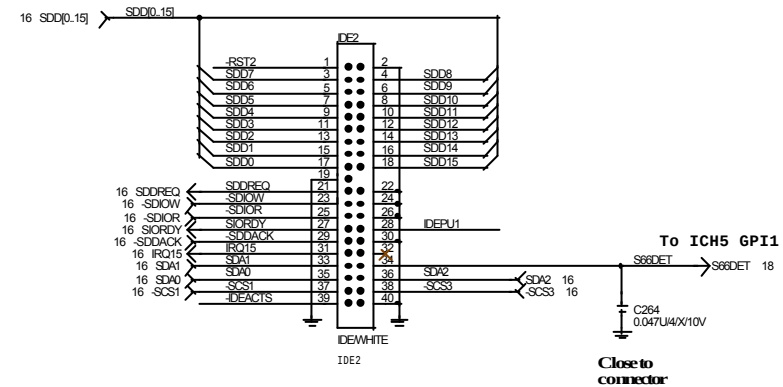
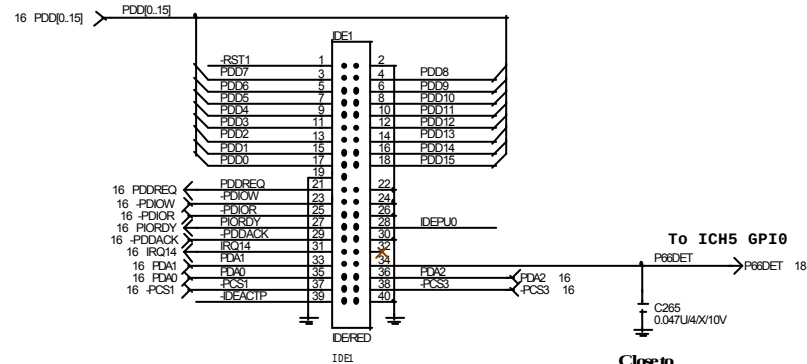
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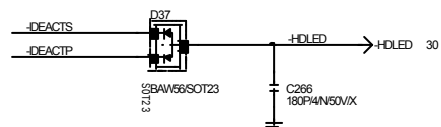
PRIMARY IDE CONNECTOR

SECONDARY IDE CONNECTOR

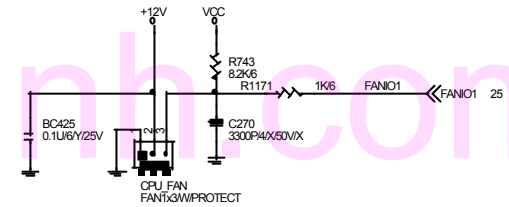
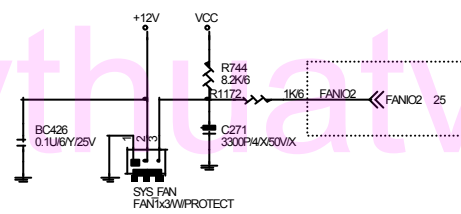


Close to connector

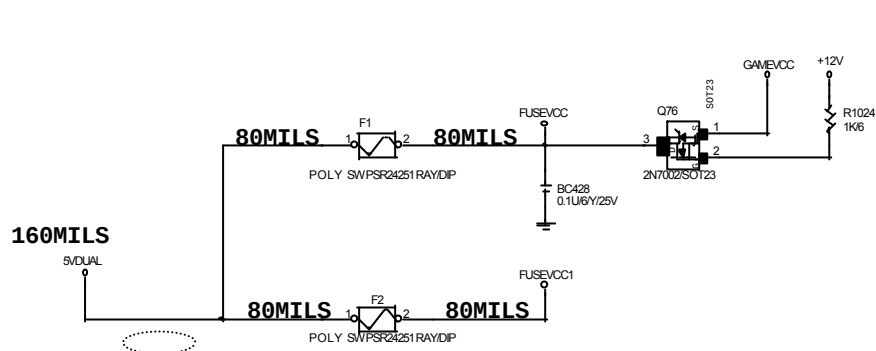
Close to connector



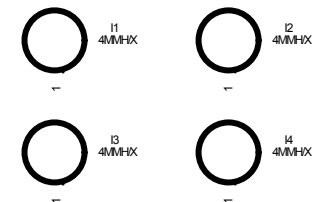
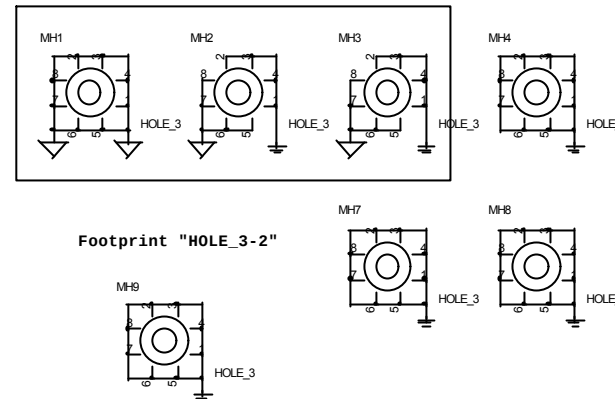
GIGABYTE CORP.		
Title		
IDE CONNECTOR		
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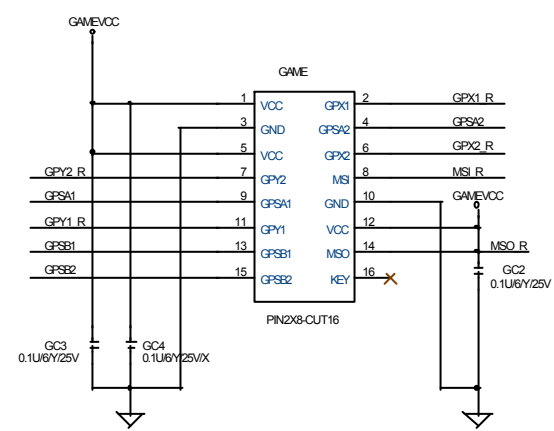
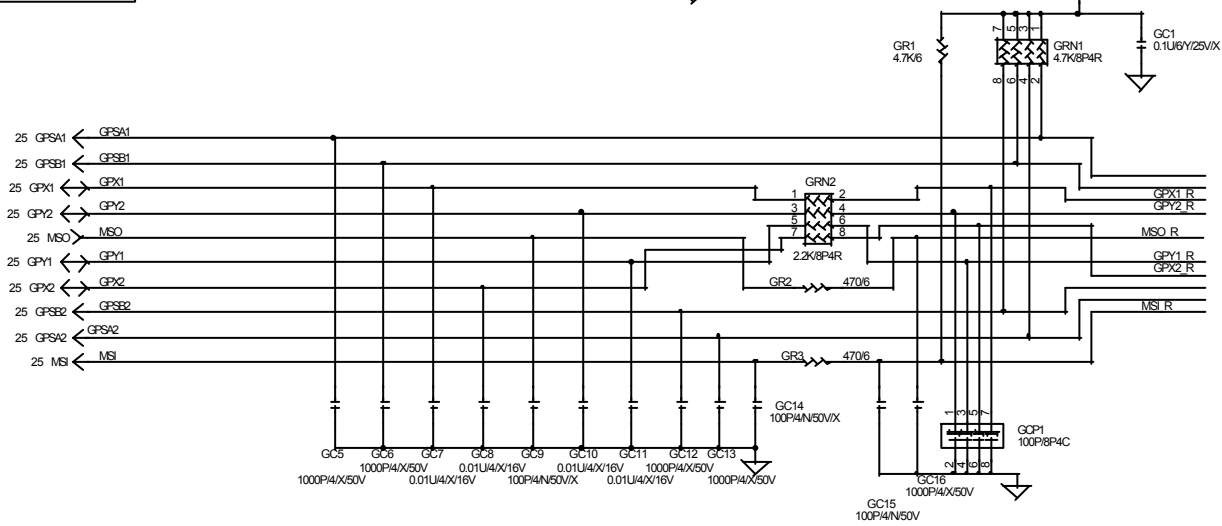
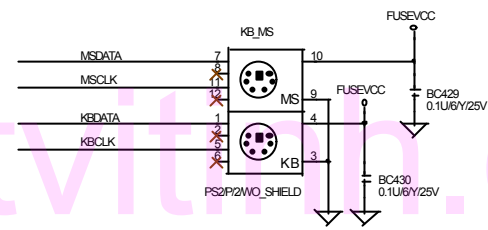
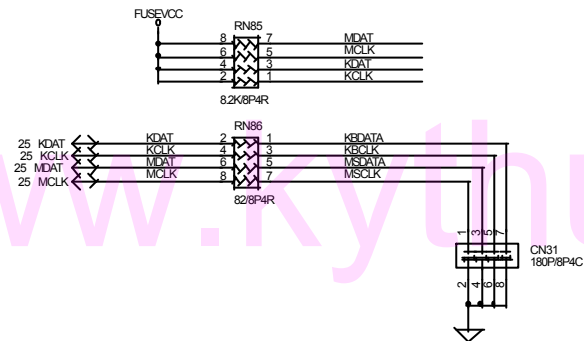
GIGABYTE CORP.			
Title			
FAN/HWMO			
Size B	Document Number	Rev 4.02	
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ATX AGND 與 GND 切割必須有三個

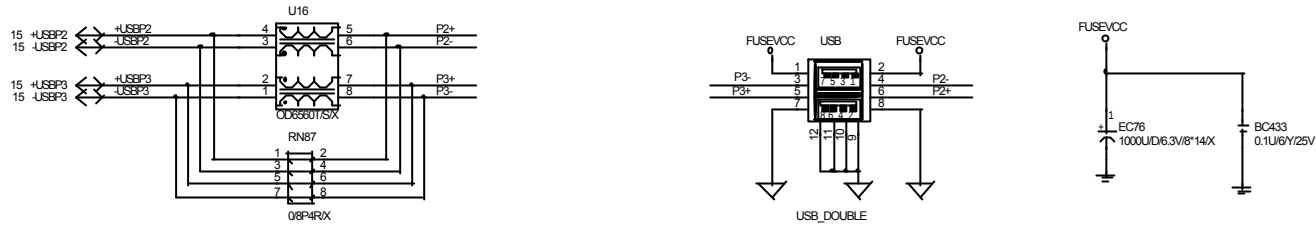


GAME_PORT

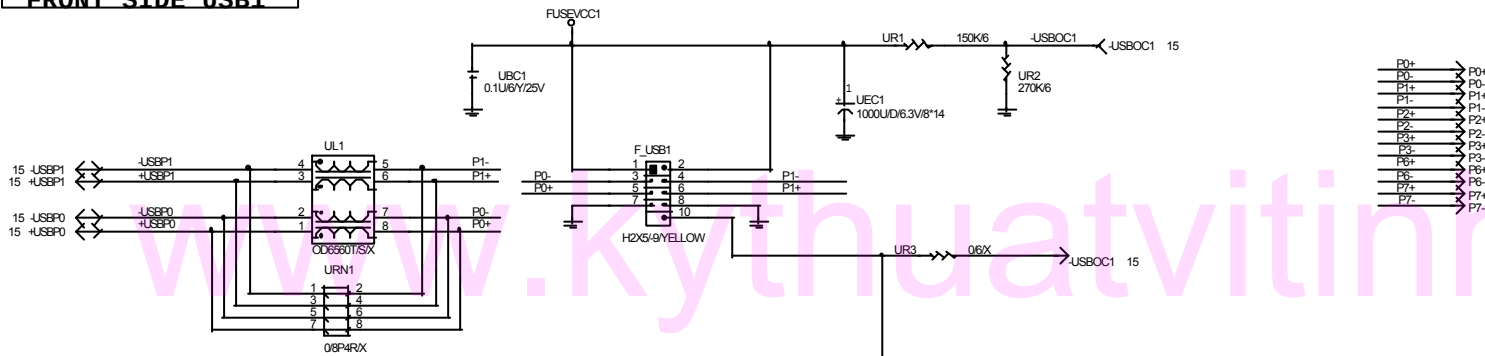


GIGABYTE CORP.			
Title			
KB & PS2 MOUSE & IR			
Size	Document Number		Rev
B	GA-8IPE1000-G		4.02
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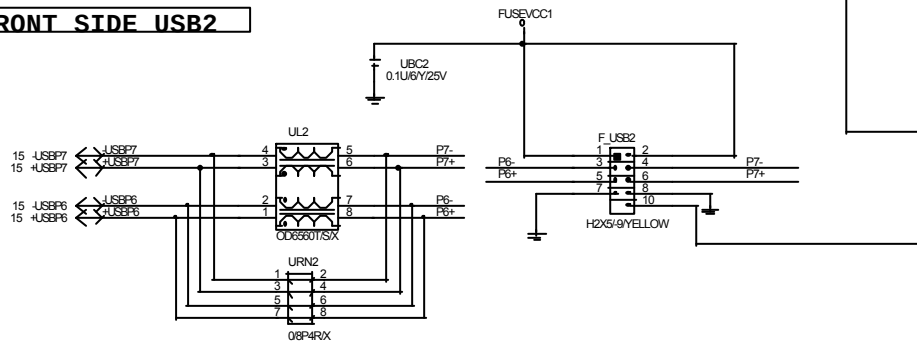
REAR USB

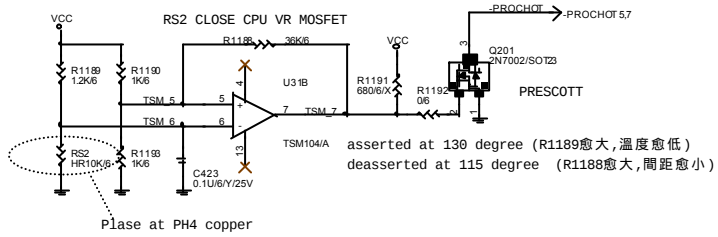


FRONT SIDE USB1

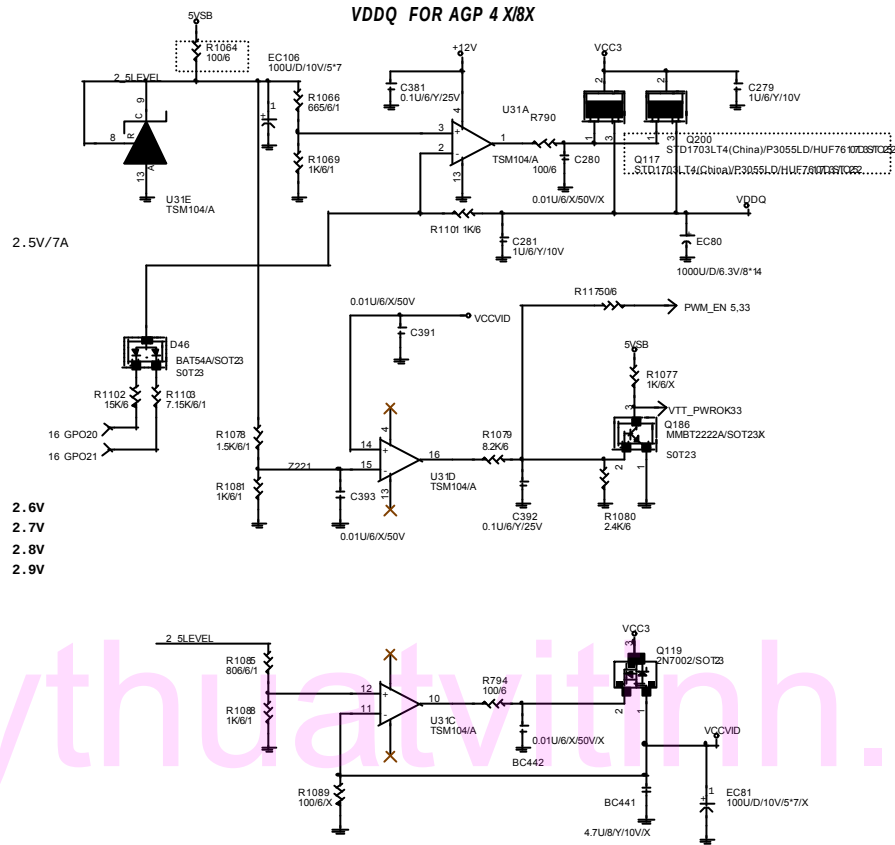


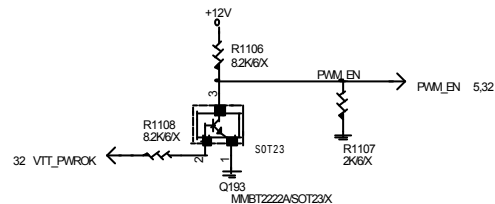
FRONT SIDE USB2



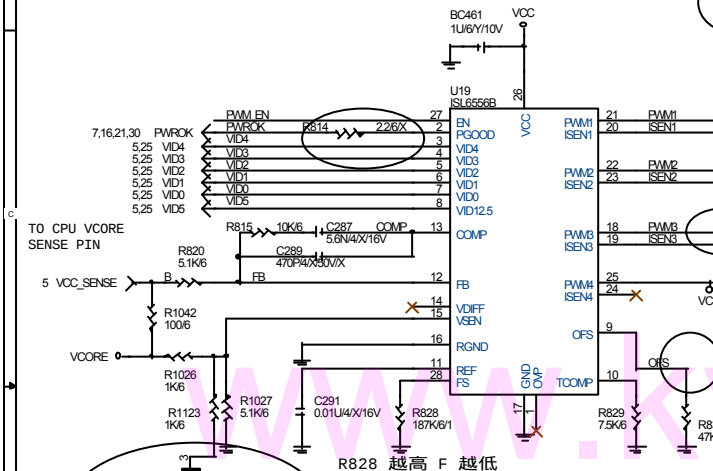
DD R25V FOR DDR DIMM & NB

DDROVP1	DDROVP2	
1	1	2.6V
0	1	2.7V
1	0	2.8V
0	0	2.9V





Near PWM



Differential Pair 10 mil

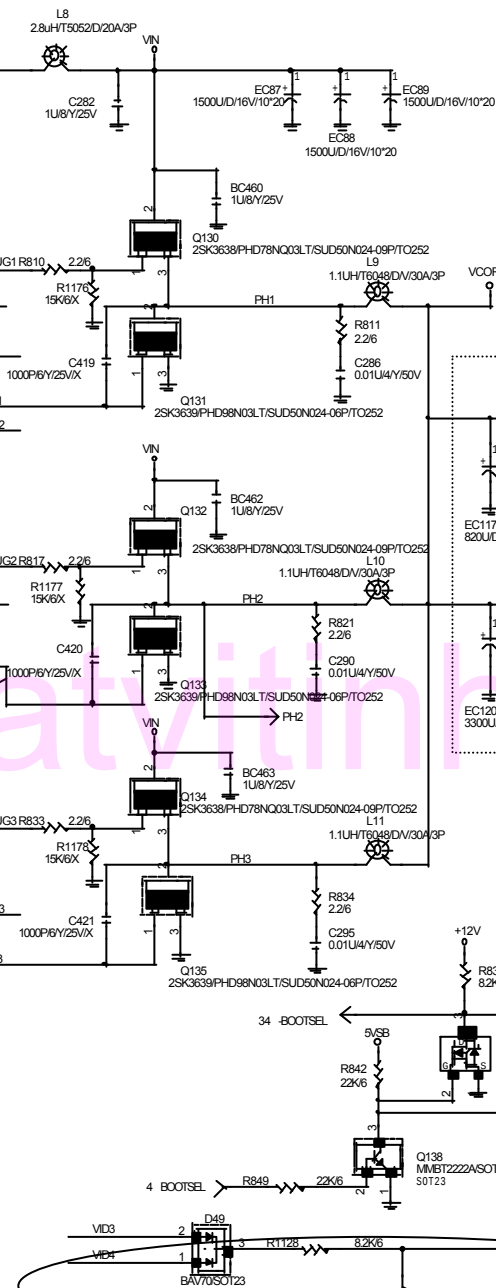
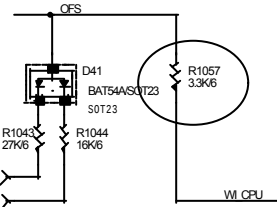
R829 越小 low side
rds_on越大 (temp越高70A), 斜率越大

R820 越小, 斜率越平

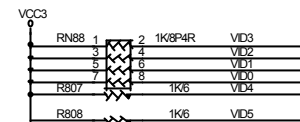
OFS spec = $0.5 \times R820 / R830$ = 调高? v
(特别注意: 若修改 R820 将会影响 OFS 电
, R1057, R1043, R1044 要跟着调整)

OVP (MAX VOLTAGE)
= $(VID + 200mV) / [R1027 / (R1026 + R1027)]$
目前 OVP SPEC 是 Vcore 的 35%

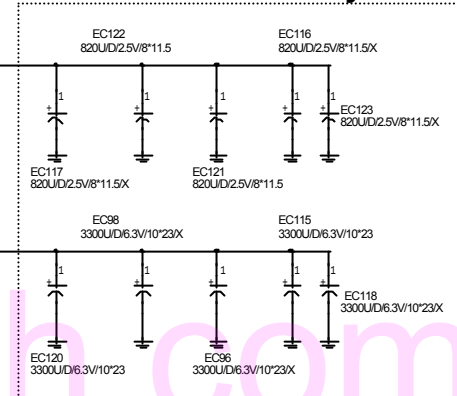
GP016	GP019	Vcore
1	1	normal (50mv)
0	1	5%
1	0	7.5%
0	0	10%



CPU Voltage ID output



Dual-Layout

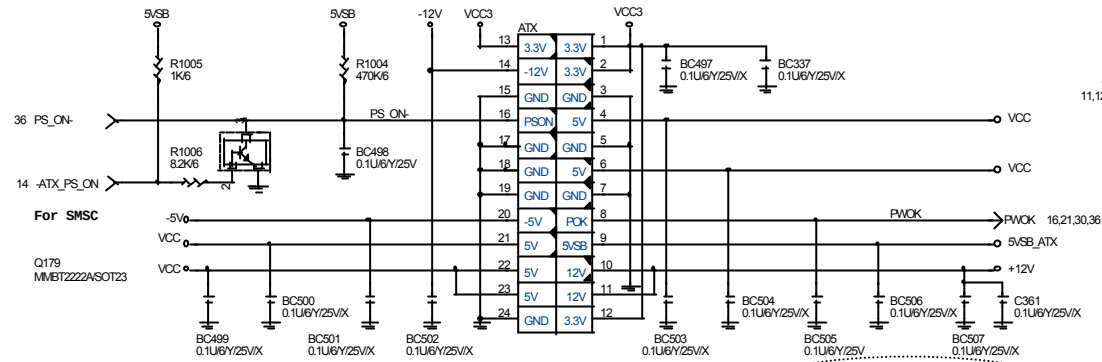


Vcore PWM HIP6302CB & DRIVER HIP6602CB

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ATX POWER CONNECTOR

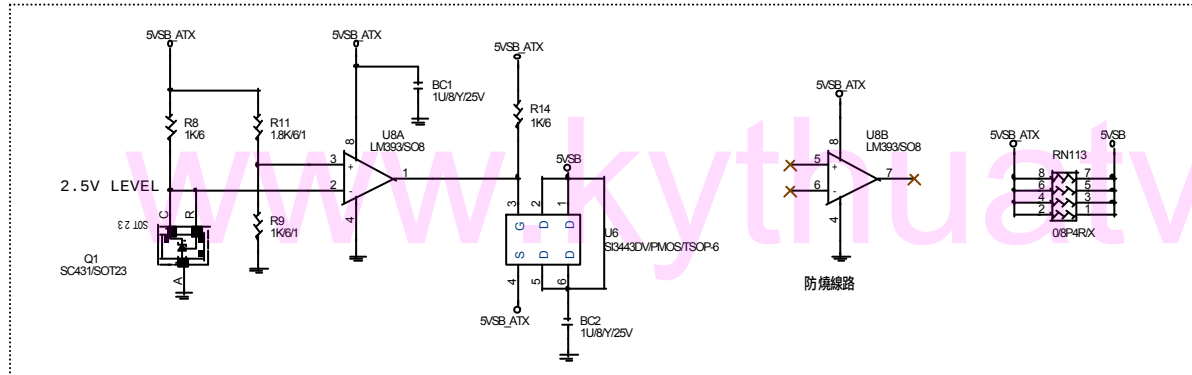


ATXPWR_ATX 20PIN(G/F+mlk)/11NH4-02002-61,62

Remove CAP. PLS test EMI

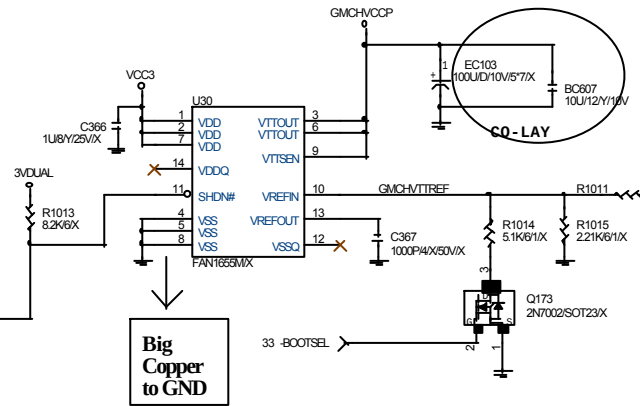
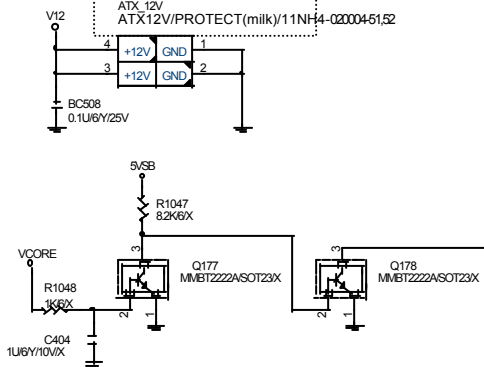
20PIN 乳白色, PIN不鍍金, ONLY左邊有防呆柱

Add BC505 0.1u/6 for PAT mode Enable

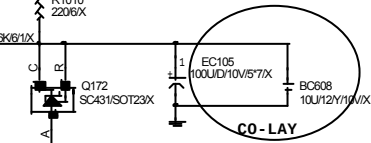


4PIN 乳白色, PIN不鍍金

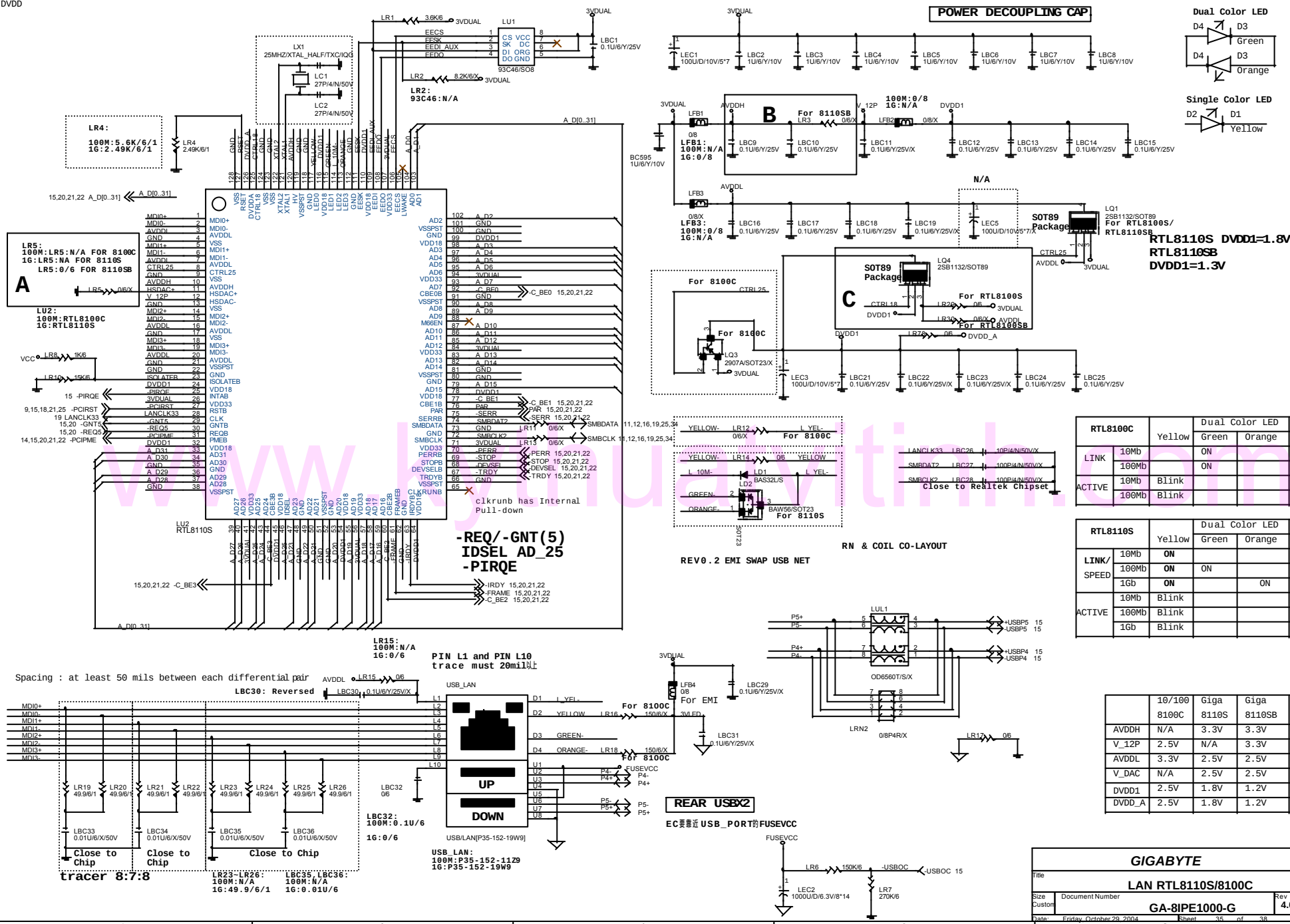
ATX 12V/ATX12V/PROTECT(mlk)/11NH4-02004-61,62



Northwood: +1.45V
Prescott: +1.225V



GIGABYTE CORP.			
File			
Misc. PWR & ATX CONN.			
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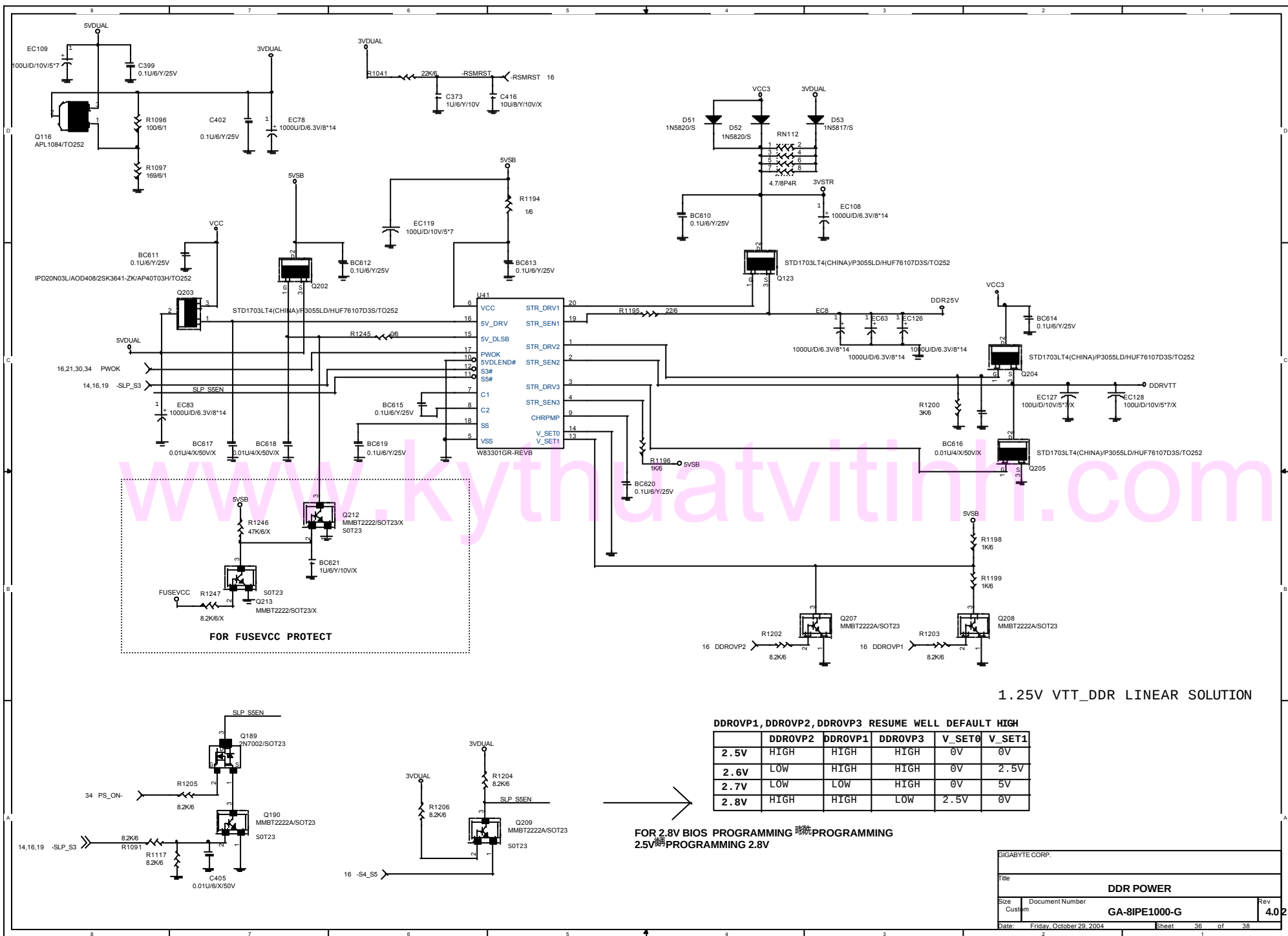


RTL8100C		Yellow	Dual Color LED	
			Green	Orange
LINK	10Mb		ON	
	100Mb		ON	
ACTIVE	10Mb	Blink		
	100Mb	Blink		

RTL8110S		Yellow	Dual Color LED	
			Green	Orange
LINK/ SPEED	10Mb	ON		
	100Mb	ON	ON	
	1Gb	ON		ON
ACTIVE	10Mb	Blink		
	100Mb	Blink		
	1Gb	Blink		

	10/100 8100C	Giga 8110S	Giga 8110SB
AVDDH	N/A	3.3V	3.3V
V_12P	2.5V	N/A	3.3V
AVDDL	3.3V	2.5V	2.5V
V_DAC	N/A	2.5V	2.5V
DVDD1	2.5V	1.8V	1.2V
DVDD_A	2.5V	1.8V	1.2V

GIGABYTE			
Title			
LAN RTL8110S/8100C			
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GIGABYTE GA-8IPE1000-G PCI ROUNTING LIST

PCI DEVICE	IDSEL	INT	CLOCK	REQ	GNT	
PCI SLOT1	16	C, F, G, A	PCLK0	REQ0-	GNT0-	
PCI SLOT2	17	F, G, A, C	PCLK1	REQ1-	GNT1-	
PCI SLOT3	18	G, A, C, F	PCLK2	REQ2-	GNT2-	
PCI SLOT4	19	A, C, F, G	PCLK3	REQ3-	GNT3-	
PCI SLOT5	20	C, F, G, A	PCLK4	REQ4-	GNT4-	
LAN (REALTEK)	25	E	LANCLK33	-REQ5 (REQB#)	-GNT5 (GNTB#)	

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GIGABYTE GA-8IPE1000-G GPIO LIST

SHEET

TITLE

GPIIP	I/O	FUNCTION
GPI0/REQA-	I	PULL HIGH 8.2K to VCC3, SMB connector.
GPI1/REQ5-		PULL HIGH 8.2K to VCC, REQ5-.
GPI2/PIRQE-		PULL HIGH 8.2K to VCC3, PIRQE-.
GPI3/PIRQF-		PULL HIGH 8.2K to VCC3, PIRQF-.
GPI4/PIRQG-		PULL HIGH 8.2K to VCC, PIRQG-.
GPI5/PIRQH-	NA	PULL HIGH 8.2K to VCC
GPI6/AGPBUSY-	I	PULL 8.2K TO VCC3, PANEL GREEN_BUTTON
GPI7	I	DUAL BIOS FIRST BOOT SELECT.
GPI8	I	PULL 8.2K TO 3VDUAL, -CASPME.
GPI9/OC4-	NA	USB OC4-.
GPI10/OC5-	NA	USB OC5-.
GPI11/-SMBALRT	NA	PULL 8.2K TO 3VDUAL, -SMBALERT.
GPI12	I	PULL 8.2K TO VCC3,M/B REVERSION ID.
GPI13	I	LPC PME.
GPI14/OC6-	NA	USB OC6-.
GPI15/OC7-	NA	USB OC7-.
GP016/GNTA-	NA	GP016.
GP017/GNT5-		GNT5-.
GP018/STP_PCI-	NA	GP018.
GP019/SLP_S1-	0	DUAL BIOS.
GP020/SLP_CPU-	0	DUAL BIOS.
GP021/C3_SATA-	0	BLOCK TOP TABLE.
GP022/CPUPERF-	0	PULL 8.2K TO VCC3,PANEL S3 POWER LED.

SHEET

TITLE

[illegible]

GIGABYTE CORP.

GPIO LIST

Size
Custom

Document Number

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