

GIGABYTE GA-8IP900-L

Schematics

Revision 1.01

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	P4_478A
05	P4_478B
06	P4_478C
07	SPRINGDALE HOST
08	SPRINGDALE DDR
09	SPRINGDALE AGP, HUB, CSA, VGA
10	SPRINGDALE PWR
11	DDR1,2 CHANNEL A
12	DDR3,4 CHANNEL B
13	DDR TERMINATION
14	AGP 8X SLOT
15	ICH5 PCI, USB, HUB, LAN
16	ICH5 IDE, GPIO, SATA, CTRL
17	ICH5 VCC, GND
18	DUAL FWH
19	CY28405 CLOCK GENERATOR
20	PCI SLOT1/SLOT2
21	PCI SLOT3/SLOT4/RESET BUFFER
22	PCI SLOT5/SLOT6

SHEET TITLE

23	AC '97 CODEC
24	AUDIO JACK, L_OUT, F_AUDIO
25	ITE 8712/IR_CIR/SCR/S_IRQ
26	COMA/VGA_COMB/LPT
27	IDE1/IDE2/FDD
28	FAN/HW MONITOR
29	KB_MS/GAME/FUSEVCC
30	FRONT PANEL
31	R_USB/F_USB1/F_USB2
32	DDR/VDDQ/5VDUAL/VCCVID POWER
33	VCORE POWER
34	ATX/ATX_12V/FAN1655M
35	KINNERETH-R LAN(CSA-1)
36	KINNERETH-R LAN(CSA-2)
37	KINNERETH-R LAN(CSA-3)
38	TI TSB43AB23(1394)
39	SIL3112/TQFP(S-ATA)
40	PCI ROUNTIONG
41	GPIO PIN LIST

PROCESS: C		COMPONENT SIDE (0.5 oz. Copper)	
		VCC SIDE (1 oz. Copper)	
		GND SIDE (1 oz. Copper)	
		SOLDER SIDE (0.5 oz. Copper)	
GIGABYTE CORP.			
Title COVER SHEET			
Size Custom	Document Number GA-8IP900-L		Rev 1.01
Date:	Sheet 1 of 41		

Version : 1.01

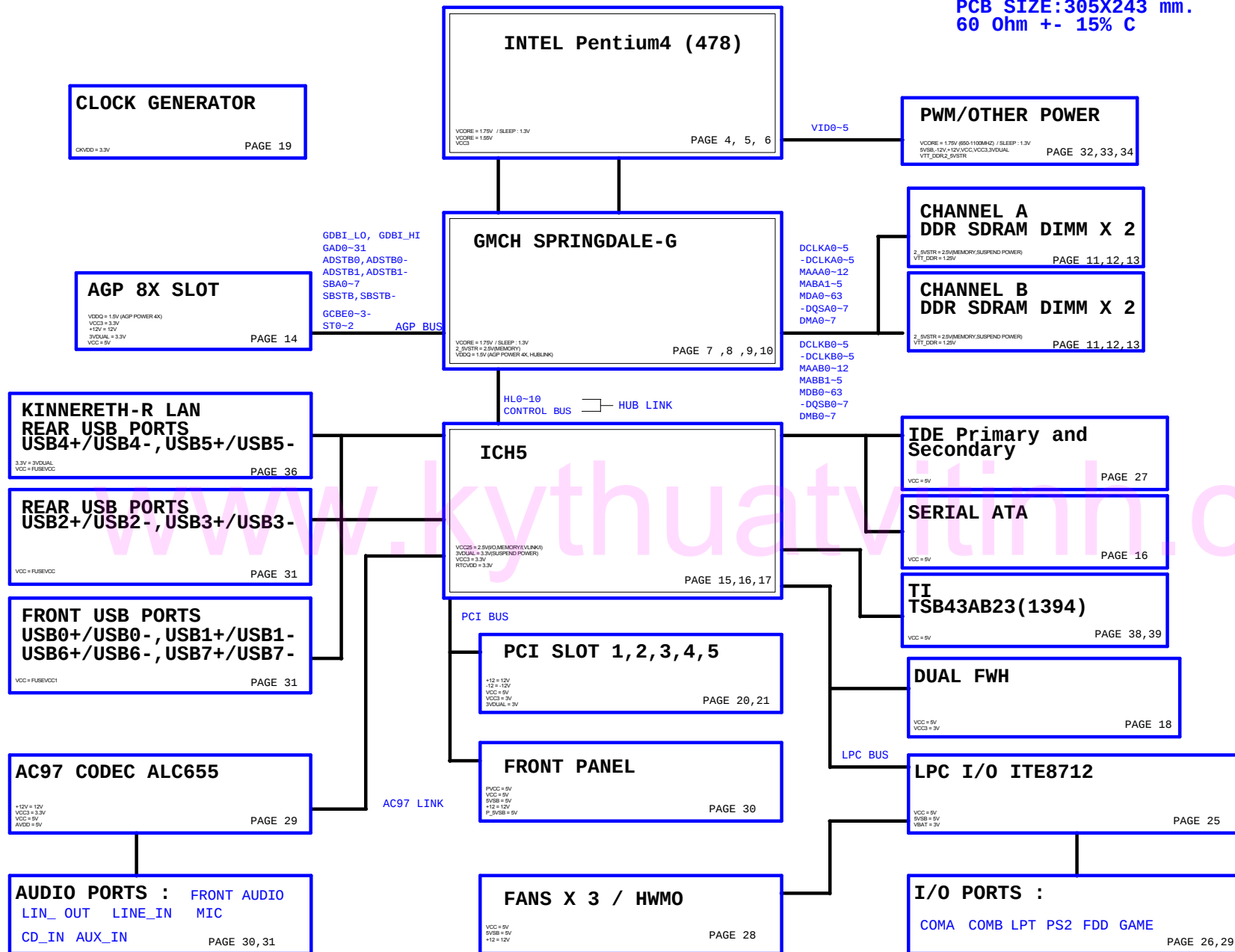
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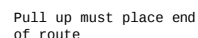
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Size Custom	Document Number GA-8IP900 -L	Rev 1.01
Date	Sheet 2 of 41	

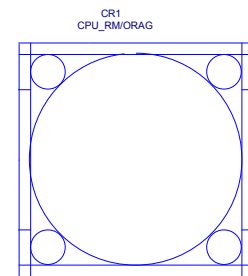
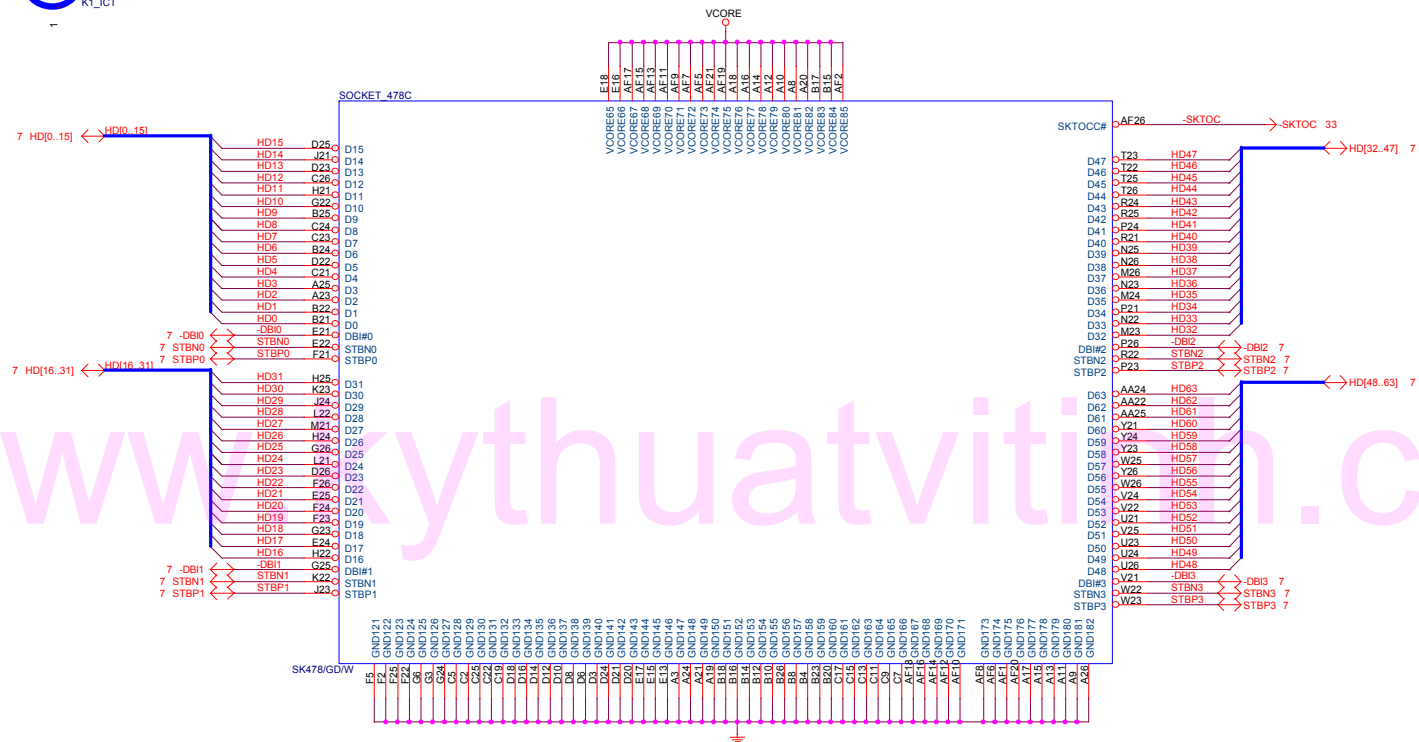
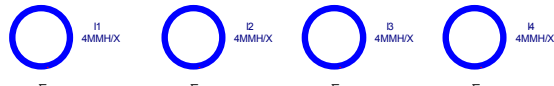
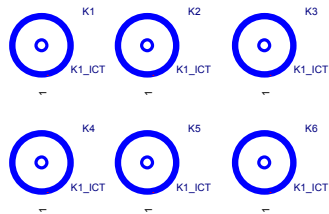
BLOCK DIAGRAM

PCB SIZE: 305X243 mm.
60 Ohm +- 15% C

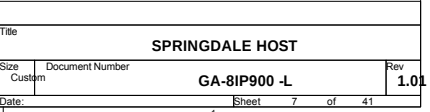




Title			
P4 478B			
Size	Document Number		Rev
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Date:	Sheet	5	of 41



Title			P4 478C	
Size	Document Number	GA-8IP900 -L		Rev
Custom				1.01
Date		1	Sheet 6 of 41	



DDR 400 1-2-3-4 CPC DISABLE
DDR 333 SIGNAL 1 CPC ENABLE
2 CPC DISABLE
DUAL 2 CPC ENABLE
DUAL 4 CPC DISABLE

11.13 MAA[0..12] ↔ MAA[0..12]
11.13 MAB[1..5] ↔ MAB[1..5]
11.13 DMA[0..7] ↔ DMA[0..7]
11.13 MDA[0..63] ↔ MDA[0..63]
11.13 DQSA[0..7] ↔ DQSA[0..7]

U1B
MAAA0 AL34 SMAA_A0
MAAA1 AL33 SMAA_A1
MAAA2 AK29 SMAA_A2
MAAA3 AN31 SMAA_A3
MAAA4 AL30 SMAA_A4
MAAA5 AL28 SMAA_A5
MAAA6 AL28 SMAA_A6
MAAA7 AN25 SMAA_A7
MAAA8 AP26 SMAA_A8
MAAA9 AP24 SMAA_A9
MAAA10 AJ33 SMAA_A10
MAAA11 AN23 SMAA_A11
MAAA12 AN21 SMAA_A12
MABA1 AL34 SMAB_A1
MABA2 AP32 SMAB_A2
MABA3 AP31 SMAB_A3
MABA4 AP31 SMAB_A4
MABA5 AM26 SMAB_A5
SWE_A# AB34
SCASA_Y34
SRASA_AC33
SBAA0 AE33 SBA_A0
SBAA1 AH34 SBA_A1
CSA0 AA34 SCS_A0#
CSA1 Y21 SCS_A1#
CSA2 Y32 SCS_A2#
CSA3 W34 SCS_A3#
CKEA0 AL20 SCKE_A0
CKEA1 AN19 SCKE_A1
CKEA2 AM20 SCKE_A2
CKEA3 AP20 SCKE_A3
DCLKA0 AK32 SCMDCLK_A0
DCLKA0 AK31 SCMDCLK_A0#
DCLKA1 AP17 SCMDCLK_A1
DCLKA1 AN17 SCMDCLK_A1#
DCLKA2 N33 SCMDCLK_A2
DCLKA2 N34 SCMDCLK_A2#
DCLKA3 AK33 SCMDCLK_A3
DCLKA3 AK34 SCMDCLK_A3#
DCLKA4 AM16 SCMDCLK_A4
DCLKA4 AL16 SCMDCLK_A4#
DCLKA5 P31 SCMDCLK_A5
DCLKA5 P32 SCMDCLK_A5#
DDRVRFA E34
SMVREF_A
SMXRCOMP AK9
SMXRCOMPVOH AN9
SMXRCOMPVOL AL9
SMXRCOMPVOL

SDQS_A0 AN11 DQSA0
SDM_A0 AP10 MDA0
SDQ_A0 AP11 MDA1
SDQ_A1 AN12 MDA2
SDQ_A2 AN13 MDA3
SDQ_A3 AM10 MDA4
SDQ_A4 AL10 MDA5
SDQ_A5 AL12 MDA6
SDQ_A6 AP13 MDA7
SDQ_A7
SDQS_A1 AP15 DQSA1
SDM_A1 AP16 MDA1
SDQ_A8 AP14 MDA8
SDQ_A9 AM14 MDA9
SDQ_A10 AL18 MDA10
SDQ_A11 AP19 MDA11
SDQ_A12 AL14 MDA12
SDQ_A13 AN15 MDA13
SDQ_A14 AP18 MDA14
SDQ_A15 AM18 MDA15
SDQS_A2 AP23 DQSA2
SDM_A2 AM24 MDA2
SDQ_A16 AP22 MDA16
SDQ_A17 AM22 MDA17
SDQ_A18 AL24 MDA18
SDQ_A19 AN27 MDA19
SDQ_A20 AP21 MDA20
SDQ_A21 AL22 MDA21
SDQ_A22 AP25 MDA22
SDQ_A23 AP27 MDA23
SDQS_A3 AP30 DQSA3
SDM_A3 AM33 MDA3
SDQ_A24 AP28 MDA24
SDQ_A25 AP25 MDA25
SDQ_A26 AP33 MDA26
SDQ_A27 AM33 MDA27
SDQ_A28 AM28 MDA28
SDQ_A29 AN29 MDA29
SDQ_A30 AM31 MDA30
SDQ_A31 AN34 MDA31
SDQS_A4 AP34 DQSA4
SDM_A4 AF31 MDA4
SDQ_A32 AH32 MDA32
SDQ_A33 AG34 MDA33
SDQ_A34 AF32 MDA34
SDQ_A35 AD32 MDA35
SDQ_A36 AH31 MDA36
SDQ_A37 AG33 MDA37
SDQ_A38 AE34 MDA38
SDQ_A39 AD34 MDA39
SDQS_A5 V34 DQSA5
SDM_A5 W33 MDA5
SDQ_A40 AC34 MDA40
SDQ_A41 AB31 MDA41
SDQ_A42 V32 MDA42
SDQ_A43 V31 MDA43
SDQ_A44 AD31 MDA44
SDQ_A45 AB32 MDA45
SDQ_A46 U34 MDA46
SDQ_A47 U33 MDA47
SDQS_A6 M32 DQSA6
SDM_A6 M34 MDA6
SDQ_A48 T34 MDA48
SDQ_A49 T32 MDA49
SDQ_A50 K34 MDA50
SDQ_A51 K32 MDA51
SDQ_A52 T31 MDA52
SDQ_A53 P34 MDA53
SDQ_A54 L34 MDA54
SDQ_A55 L33 MDA55
SDQS_A7 H31 DQSA7
SDM_A7 H32 MDA7
SDQ_A56 J33 MDA56
SDQ_A57 H34 MDA57
SDQ_A58 E33 MDA58
SDQ_A59 F32 MDA59
SDQ_A60 K31 MDA60
SDQ_A61 J34 MDA61
SDQ_A62 G34 MDA62
SDQ_A63 F34 MDA63

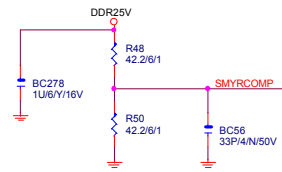
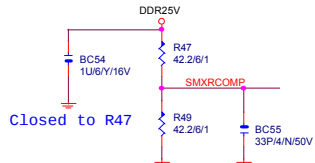
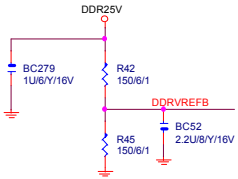
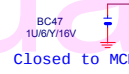
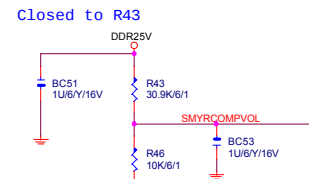
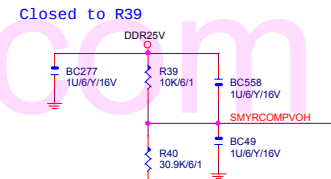
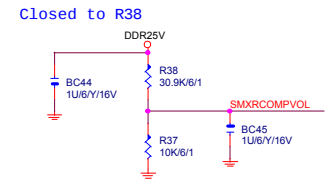
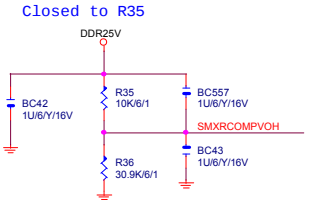
DDR Channel A

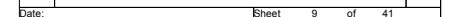
12.13 MAB[0..12] ↔ MAB[0..12]
12.13 MAB[1..5] ↔ MAB[1..5]
12.13 DMB[0..7] ↔ DMB[0..7]
12.13 MDB[0..63] ↔ MDB[0..63]
12.13 DQSB[0..7] ↔ DQSB[0..7]

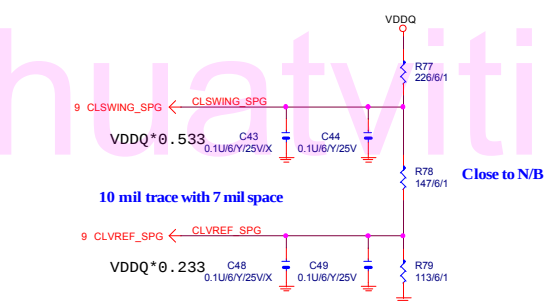
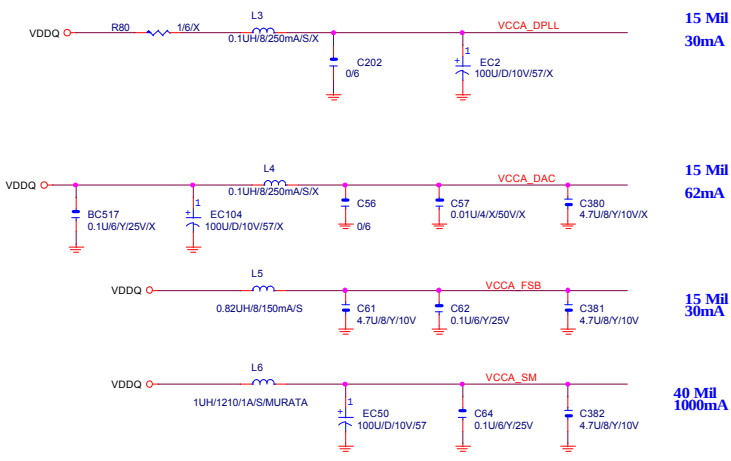
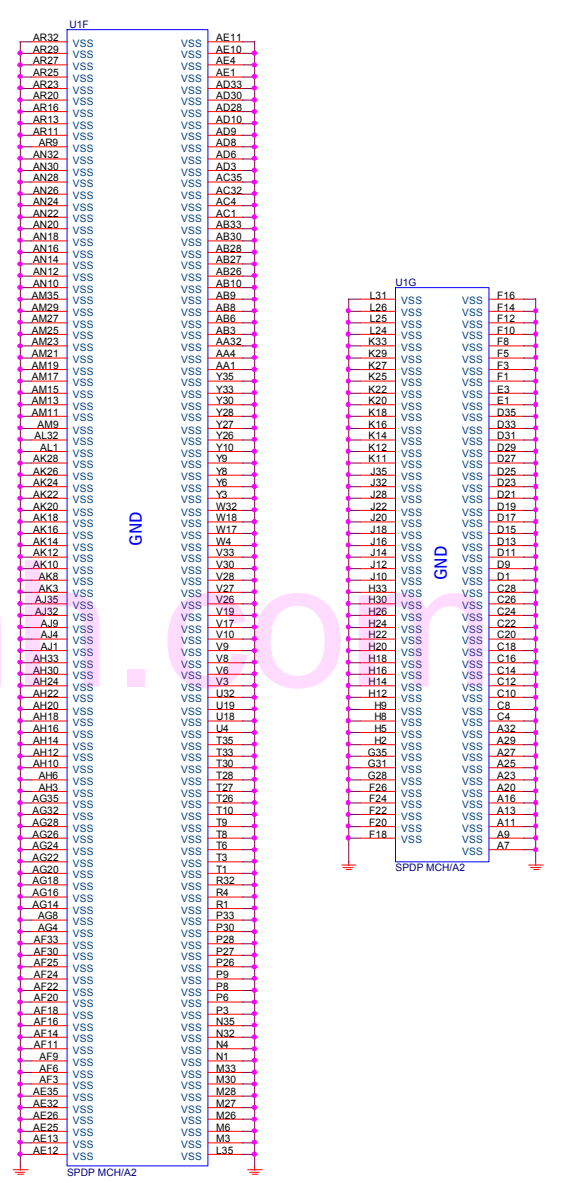
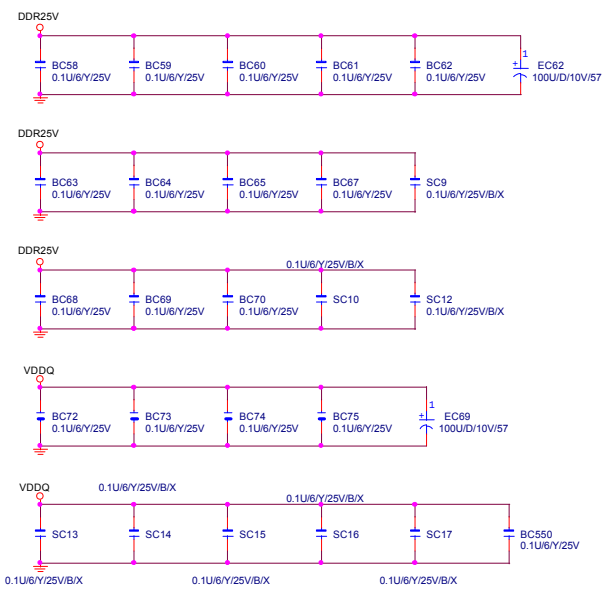
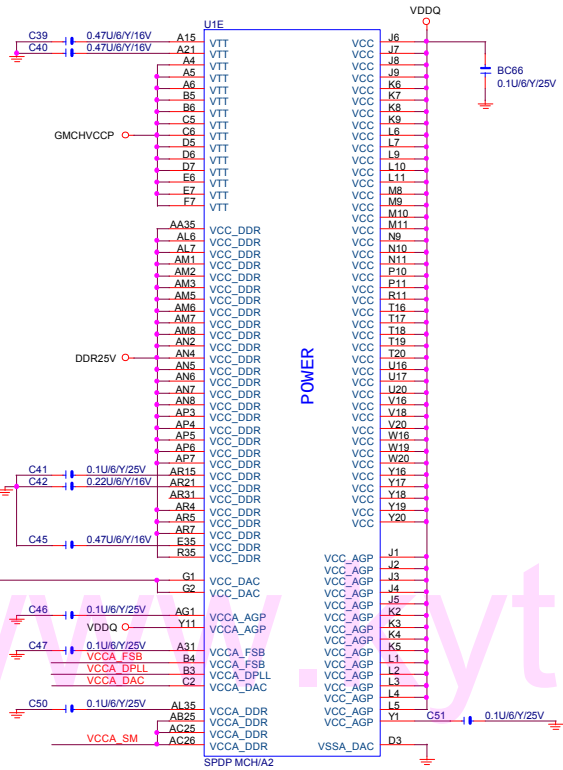
U1C
MAAB0 AG31 SMAA_B0
MAAB1 AJ31 SMAA_B1
MAAB2 AD27 SMAA_B2
MAAB3 AE24 SMAA_B3
MAAB4 AK27 SMAA_B4
MAAB5 AE25 SMAA_B5
MAAB6 AL25 SMAA_B6
MAAB7 AE25 SMAA_B7
MAAB8 AL23 SMAA_B8
MAAB9 AE29 SMAA_B9
MAAB10 AE29 SMAA_B10
MAAB11 AL21 SMAA_B11
MAAB12 AJ20 SMAA_B12
SMAB_B1
SMAB_B2
SMAB_B3
SMAB_B4
SMAB_B5
SWE_B# W27
SCASB_W31
SRASB_W26
SBAB0 Y25 SBA_B0
SBAB1 AA25 SBA_B1
CSB0 U26 SCS_B0#
CSB1 T29 SCS_B1#
CSB2 V25 SCS_B2#
CSB3 W26 SCS_B3#
CKEB0 AK19 SCKE_B0
CKEB1 AF19 SCKE_B1
CKEB2 AG19 SCKE_B2
CKEB3 AE19 SCKE_B3
DCLKB0 AG29 SCMDCLK_B0
DCLKB0 AG30 SCMDCLK_B0#
DCLKB1 AF17 SCMDCLK_B1
DCLKB1 AG17 SCMDCLK_B1#
DCLKB2 N27 SCMDCLK_B2
DCLKB2 N28 SCMDCLK_B2#
DCLKB3 AJ29 SCMDCLK_B3
DCLKB3 AH29 SCMDCLK_B3#
DCLKB4 AK15 SCMDCLK_B4
DCLKB4 AL15 SCMDCLK_B4#
DCLKB5 N31 SCMDCLK_B5
DCLKB5 N30 SCMDCLK_B5#
DDRVRFB AP9
SMYRCOMP AN3
SMYRCOMPVOH R34
SMYRCOMPVOL R33

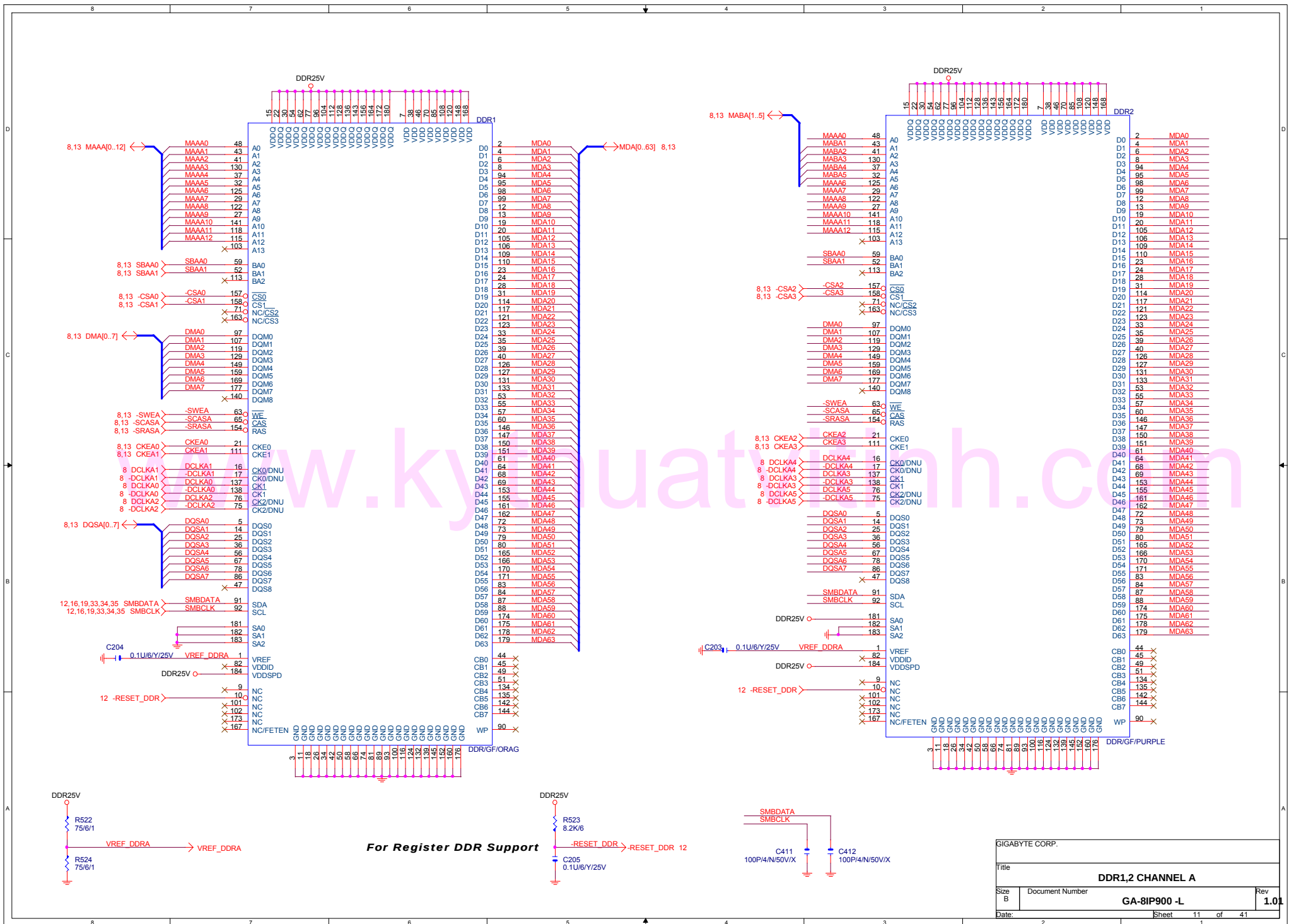
DDR Channel B

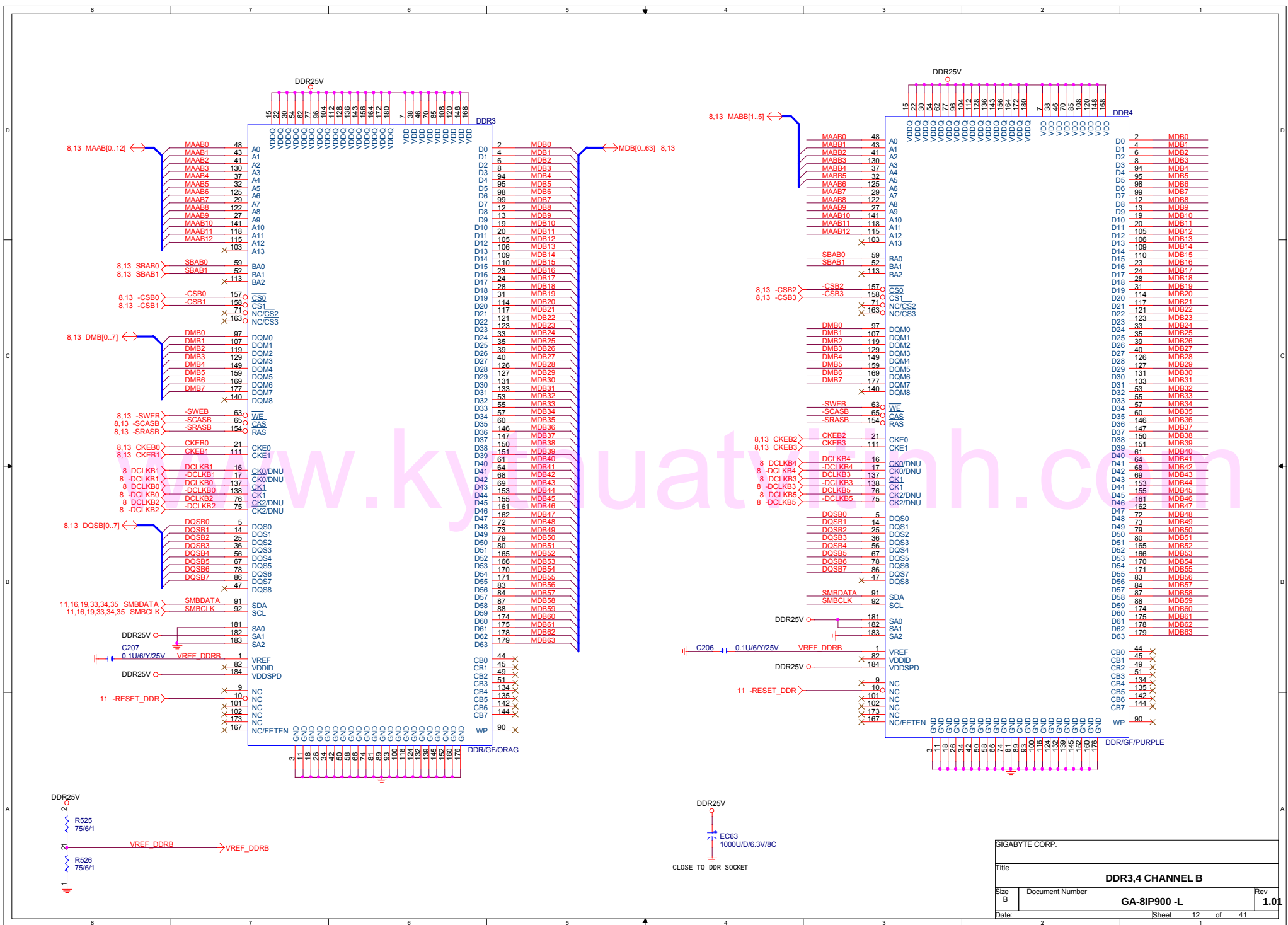
SDQS_B0 AE16 DQSB0
SDM_B0 AG11 DMB0
SDQ_B0 AJ10 MDB0
SDQ_B1 AE15 MDB1
SDQ_B1 AL11 MDB2
SDQ_B2 AE16 MDB3
SDQ_B3 AL8 MDB4
SDQ_B4 AE12 MDB5
SDQ_B5 SMAA_B7
SDQ_B6 AK11 MDB6
SDQ_B7 AG12 MDB7
SDQS_B1 AG13 DQSB1
SDM_B1 AG15 DMB1
SDQ_B8 AE17 MDB8
SDQ_B9 AL13 MDB9
SDQ_B10 AK17 MDB10
SDQ_B11 AL17 MDB11
SDQ_B12 AK13 MDB12
SDQ_B13 AJ14 MDB13
SDQ_B14 AJ16 MDB14
SDQ_B15 AT18 MDB15
SDQS_B2 AG21 DQSB2
SDM_B2 AE21 DMB2
SDQ_B16 AE19 MDB16
SDQ_B17 AE20 MDB17
SDQ_B18 AG23 MDB18
SDQ_B19 AK23 MDB19
SDQ_B20 AL19 MDB20
SDQ_B21 AJ24 MDB21
SDQ_B22 AE22 MDB23
SDQS_B3 AH27 DQSB3
SDM_B3 AJ28 DMB3
SDQ_B24 AK25 MDB24
SDQ_B25 AH26 MDB25
SDQ_B26 AG27 MDB26
SDQ_B27 AF27 MDB27
SDQ_B28 AJ26 MDB28
SDQ_B29 AD25 MDB29
SDQ_B30 AF28 MDB31
SDQS_B4 AD29 DQSB4
SDM_B4 AC31 DMB4
SDQ_B32 AE30 MDB32
SDQ_B33 AC27 MDB33
SDQ_B34 AC30 MDB34
SDQ_B35 Y29 MDB35
SDQ_B36 AE31 MDB36
SDQ_B37 AB29 MDB37
SDQ_B38 AA26 MDB38
SDQ_B39 AA27 MDB39
SDQS_B5 U30 DQSB5
SDM_B5 U31 DMB5
SDQ_B40 AA30 MDB40
SDQ_B41 W30 MDB41
SDQ_B42 U27 MDB42
SDQ_B43 T26 MDB43
SDQ_B44 AA31 MDB44
SDQ_B45 V29 MDB45
SDQ_B46 U25 MDB46
SDQ_B47 R27 MDB47
SDQS_B6 L27 DQSB6
SDM_B6 M29 DMB6
SDQ_B48 P29 MDB48
SDQ_B49 R30 MDB49
SDQ_B50 K28 MDB50
SDQ_B51 L30 MDB51
SDQ_B52 R31 MDB52
SDQ_B53 R28 MDB53
SDQ_B54 P25 MDB54
SDQ_B55 L32 MDB55
SDQS_B7 J30 DQSB7
SDM_B7 J31 DMB7
SDQ_B56 K30 MDB56
SDQ_B57 H29 MDB57
SDQ_B58 F32 MDB58
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SDQ_B61 M25 MDB61
SDQ_B62 J29 MDB62
SDQ_B63 G32 MDB63









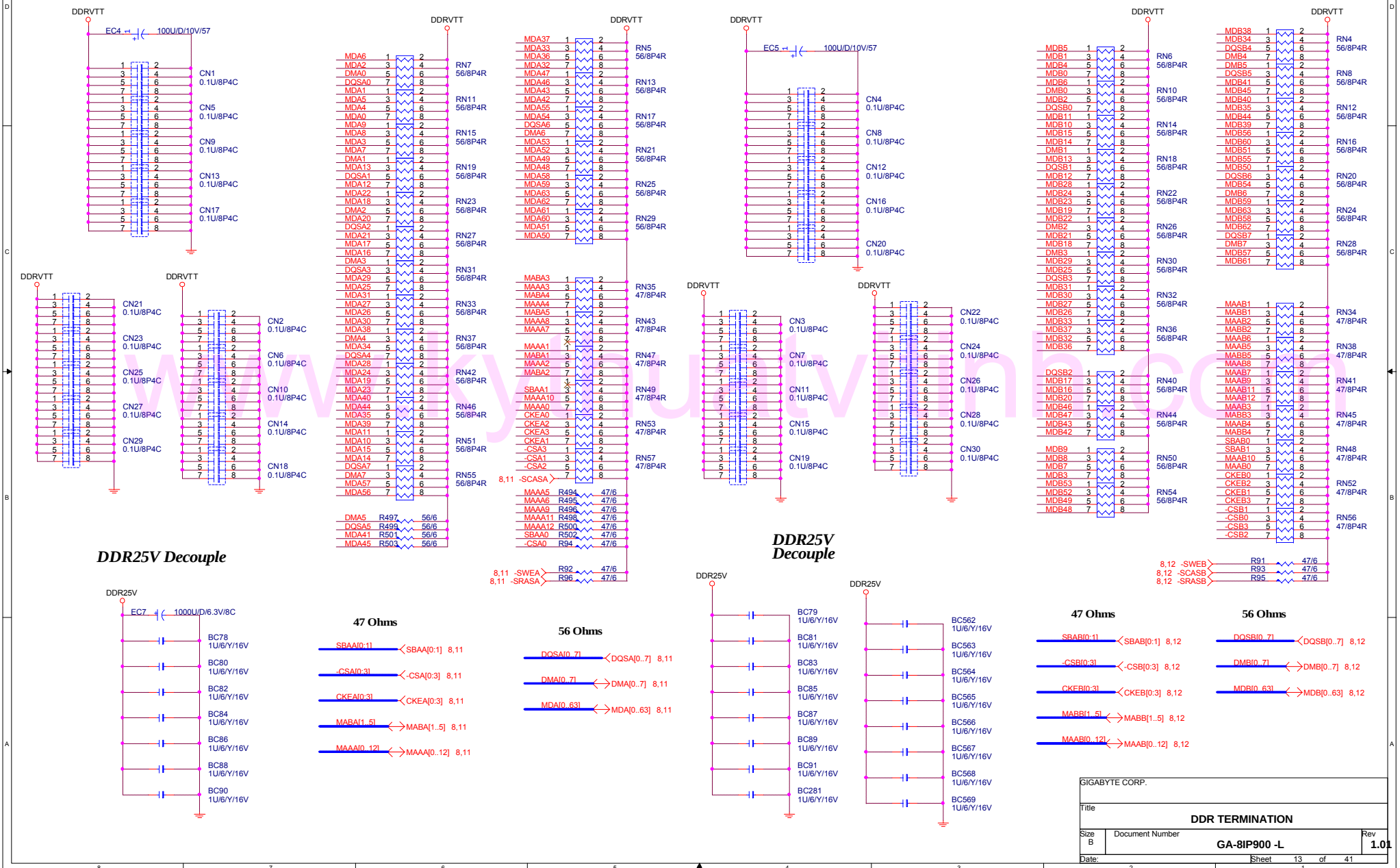


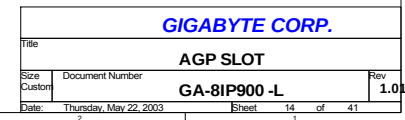
DDRVTT Decouple

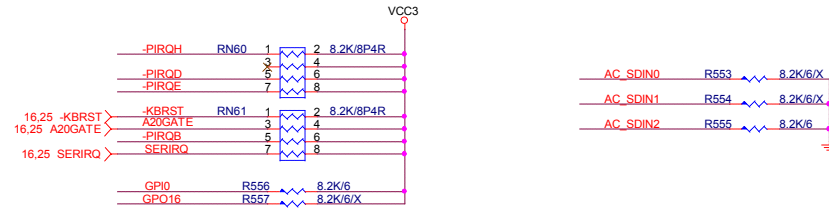
DDR TERMINATION CHANNEL A

DDRVTT Decouple

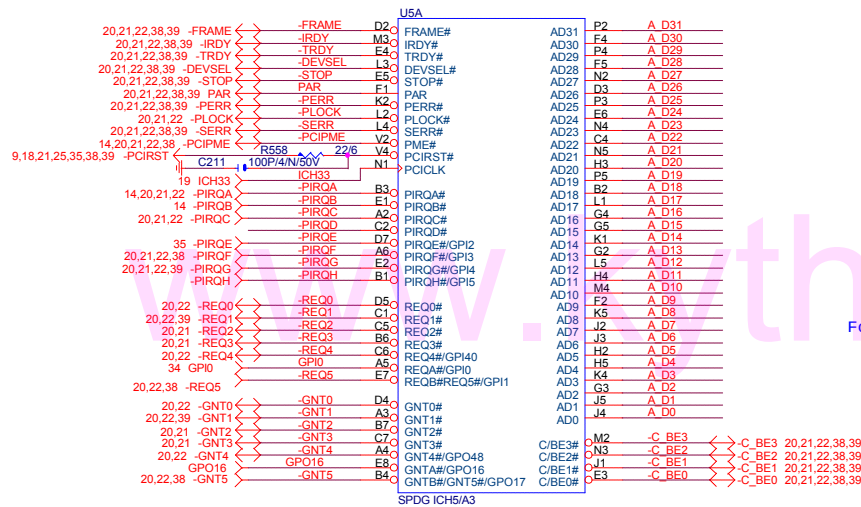
CHANNEL B





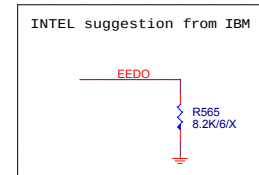
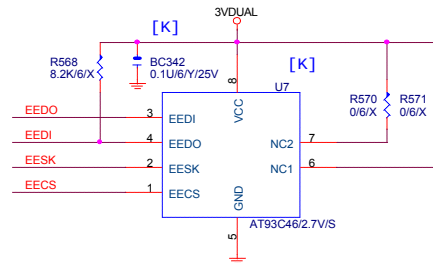
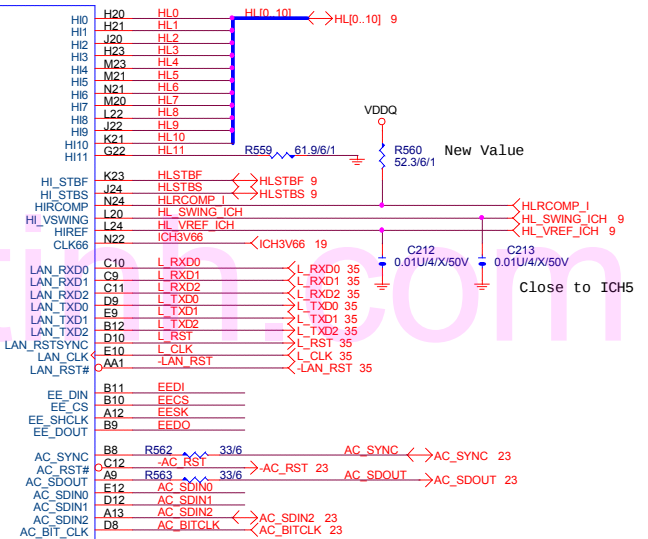
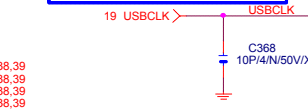


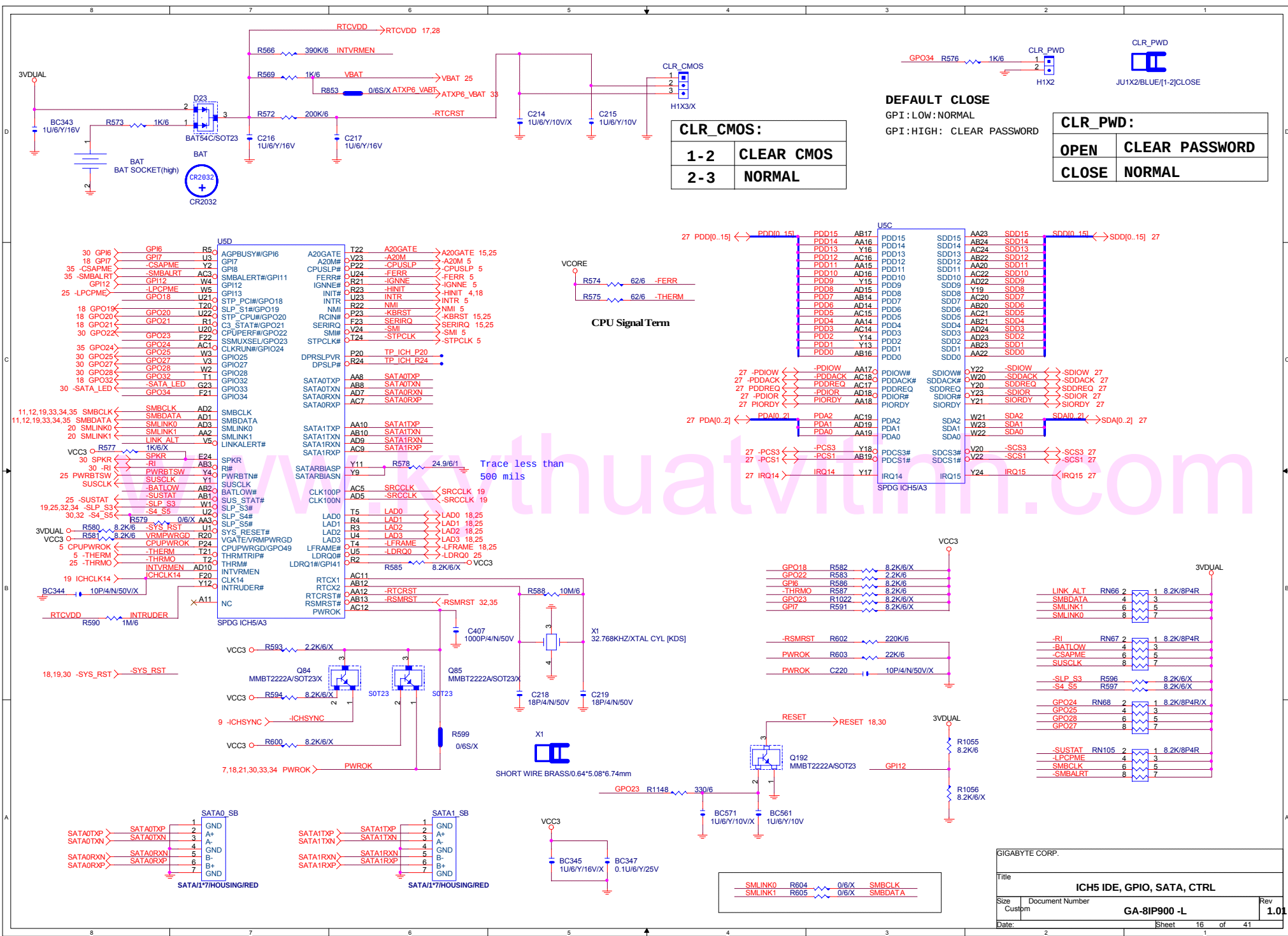
A_DIO_311 <-> A_DIO[0..31] 20,21,22,38,39



Follow Intel Layout

Less than 500mils from ICH5

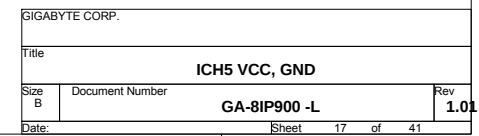


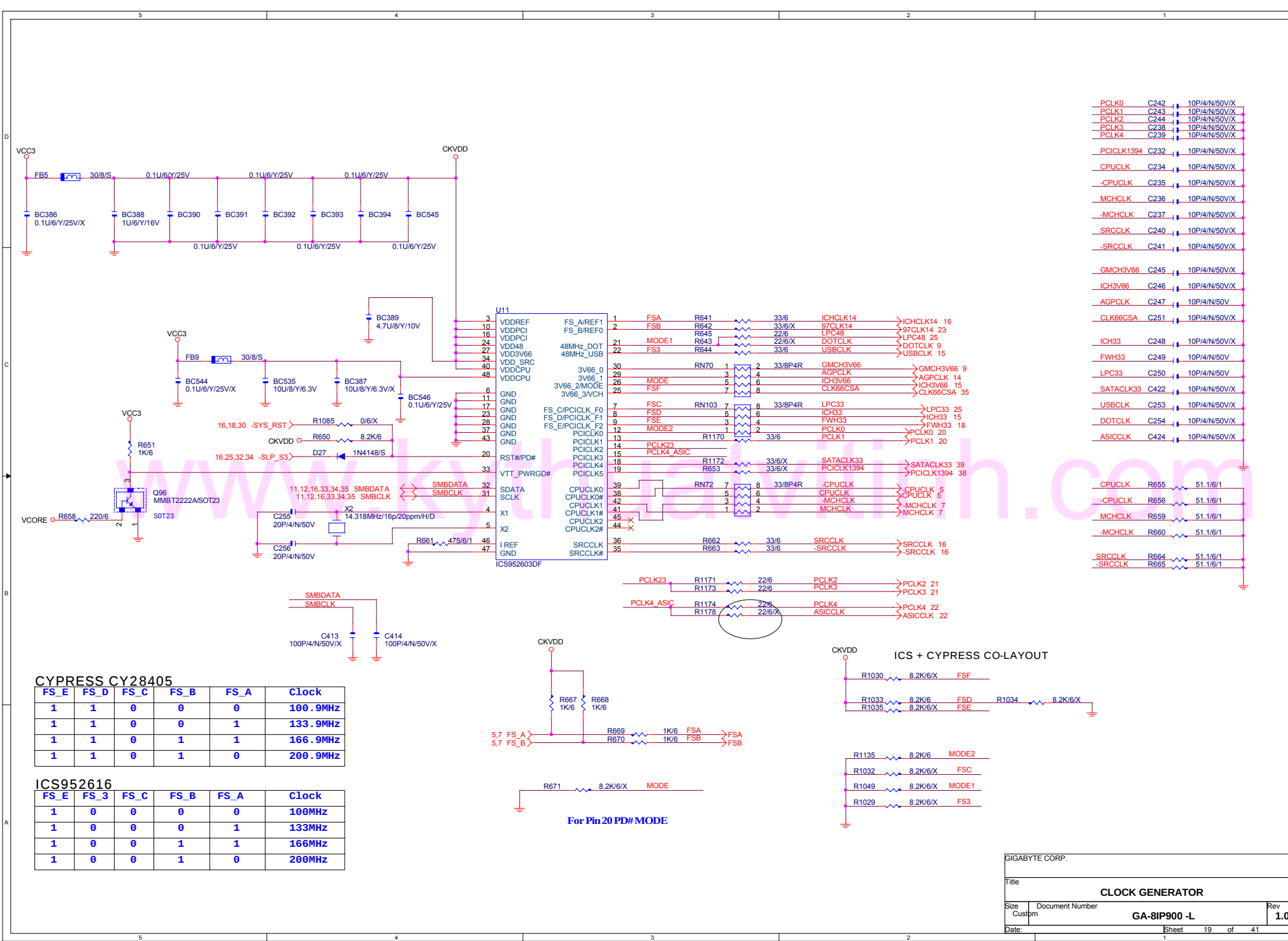


CLR_CMOS:	
1-2	CLEAR CMOS
2-3	NORMAL

DEFAULT CLOSE
GPI: LOW: NORMAL
GPI: HIGH: CLEAR PASSWORD

CLR_PWD:	
OPEN	CLEAR PASSWORD
CLOSE	NORMAL





- PCLK0 C242 10P/4/N/50V/X
- PCLK1 C243 10P/4/N/50V/X
- PCLK2 C244 10P/4/N/50V/X
- PCLK3 C238 10P/4/N/50V/X
- PCLK4 C239 10P/4/N/50V/X
- PCICLK1394 C232 10P/4/N/50V/X
- CPUCCLK C234 10P/4/N/50V/X
- CPUCCLK C235 10P/4/N/50V/X
- MCHCLK C236 10P/4/N/50V/X
- MCHCLK C237 10P/4/N/50V/X
- SRCCCLK C240 10P/4/N/50V/X
- SRCCCLK C241 10P/4/N/50V/X
- GMCH3V66 C245 10P/4/N/50V/X
- ICH3V66 C246 10P/4/N/50V/X
- AGPCLK C247 10P/4/N/50V
- CLK66CSA C251 10P/4/N/50V/X
- ICH33 C248 10P/4/N/50V/X
- FWH33 C249 10P/4/N/50V
- LPC33 C250 10P/4/N/50V
- SATACLK33 C422 10P/4/N/50V/X
- USBCLK C253 10P/4/N/50V/X
- DOTCLK C254 10P/4/N/50V/X
- ASICCLK C424 10P/4/N/50V/X
- CPUCCLK R655 51.1/6/1
- CPUCCLK R656 51.1/6/1
- MCHCLK R659 51.1/6/1
- MCHCLK R660 51.1/6/1
- SRCCCLK R664 51.1/6/1
- SRCCCLK R665 51.1/6/1

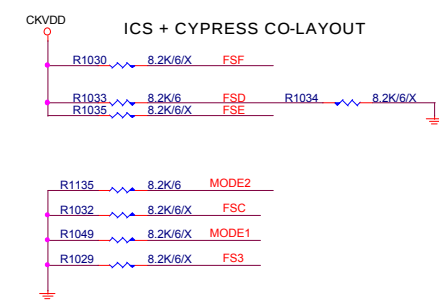
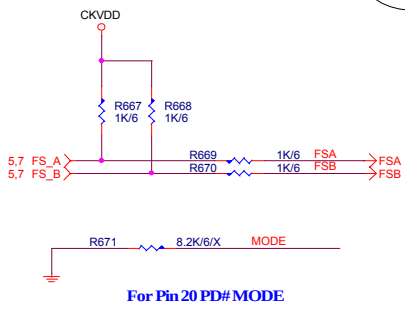
- PCLK23 R1171 22/6
- PCLK2 R1173 22/6
- PCLK3 R1174 22/6
- PCLK4 R1178 22/6
- PCLK4 ASIC R1178 22/6
- PCLK4 22 R1178 22/6
- ASICCLK 22 R1178 22/6

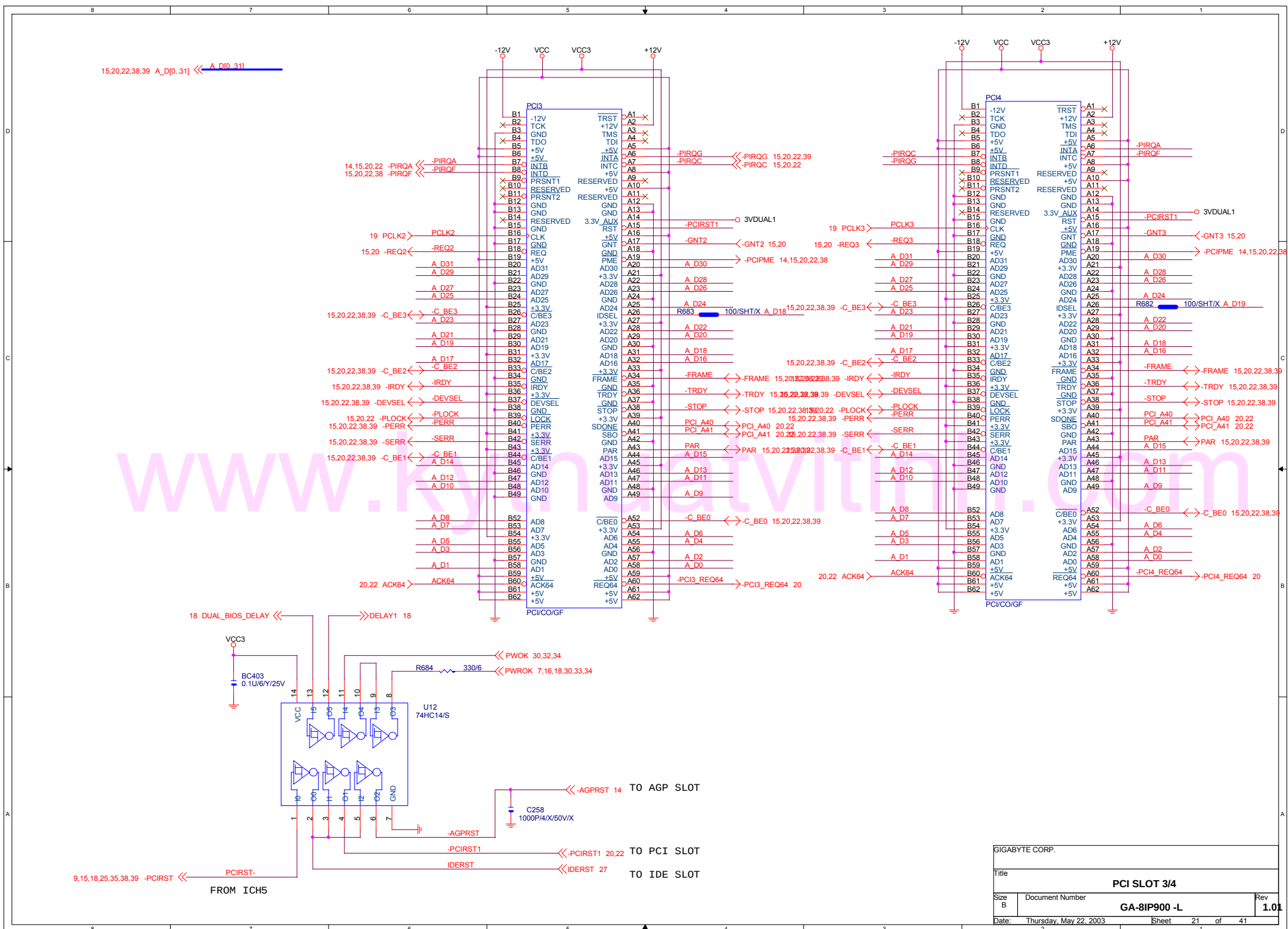
CYPRESS CY28405

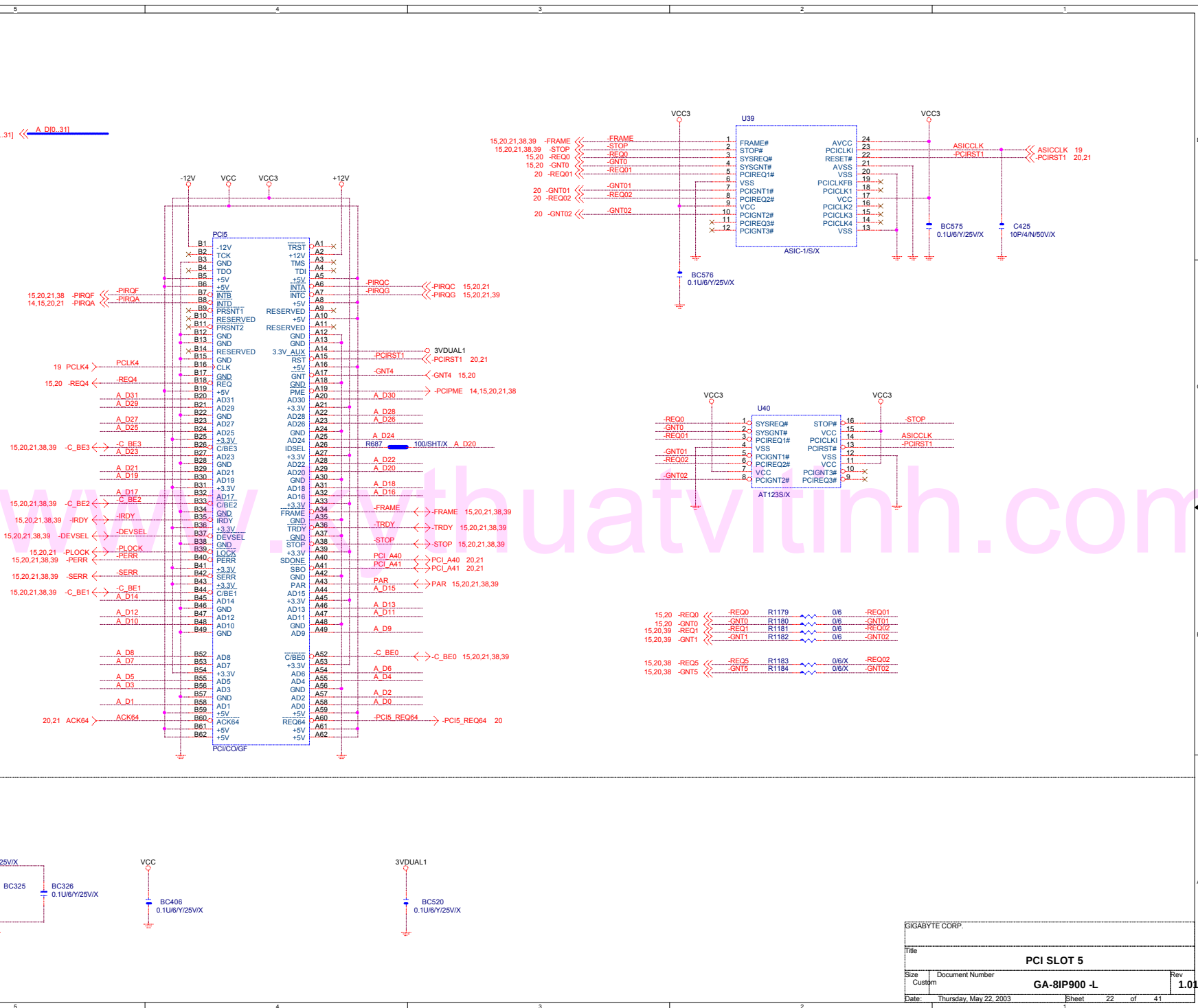
FS_E	FS_D	FS_C	FS_B	FS_A	Clock
1	1	0	0	0	100.9MHz
1	1	0	0	1	133.9MHz
1	1	0	1	1	166.9MHz
1	1	0	1	0	200.9MHz

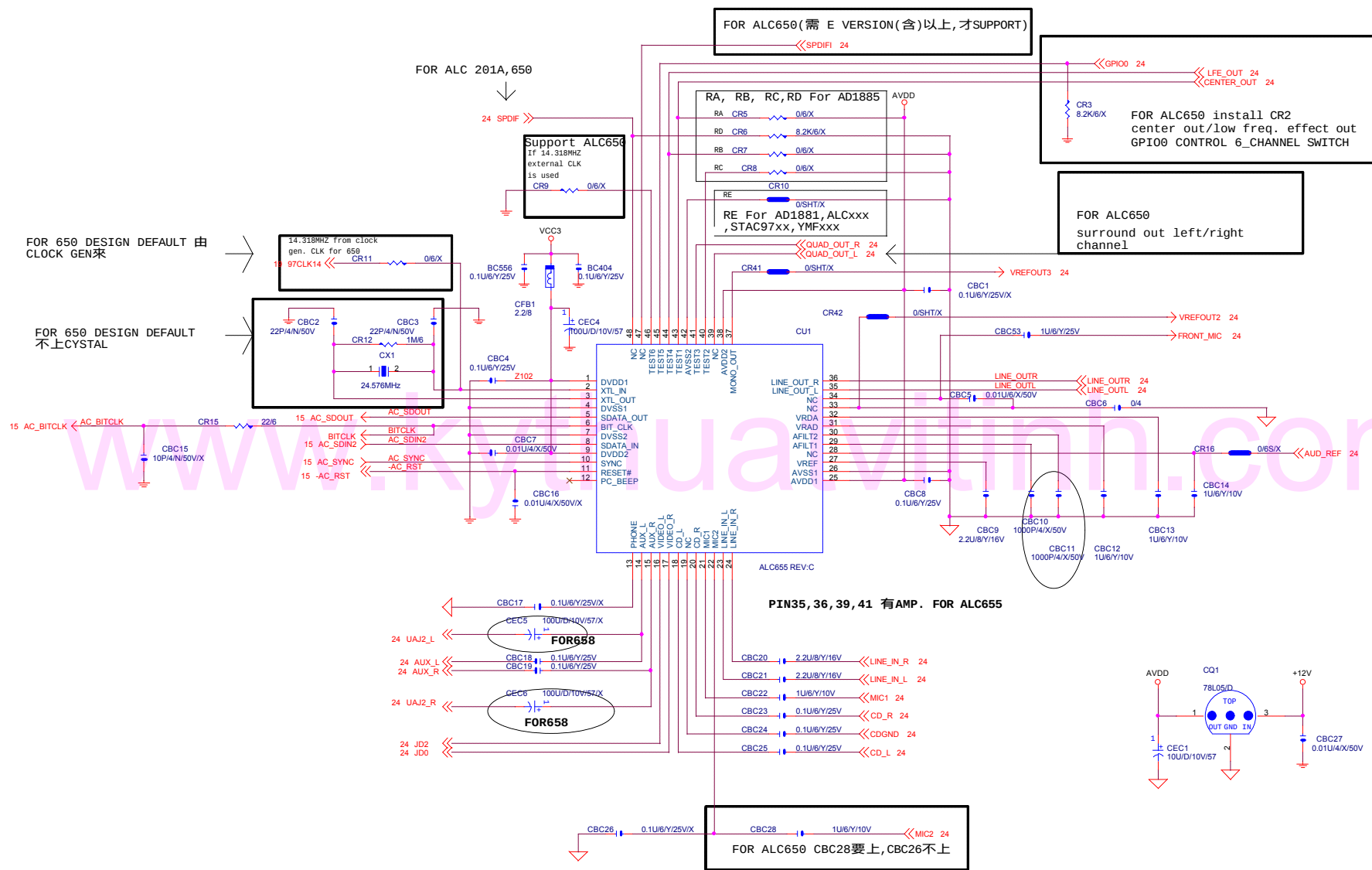
ICS952616

FS_E	FS_3	FS_C	FS_B	FS_A	Clock
1	0	0	0	0	100MHz
1	0	0	0	1	133MHz
1	0	0	1	1	166MHz
1	0	0	1	0	200MHz

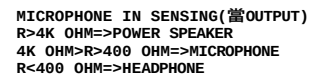
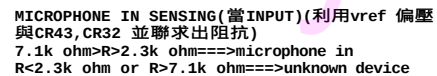
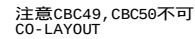




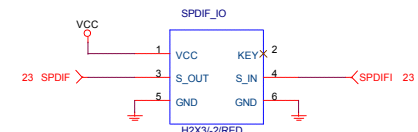
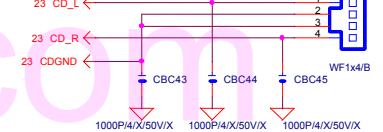
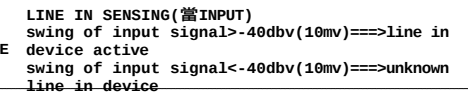




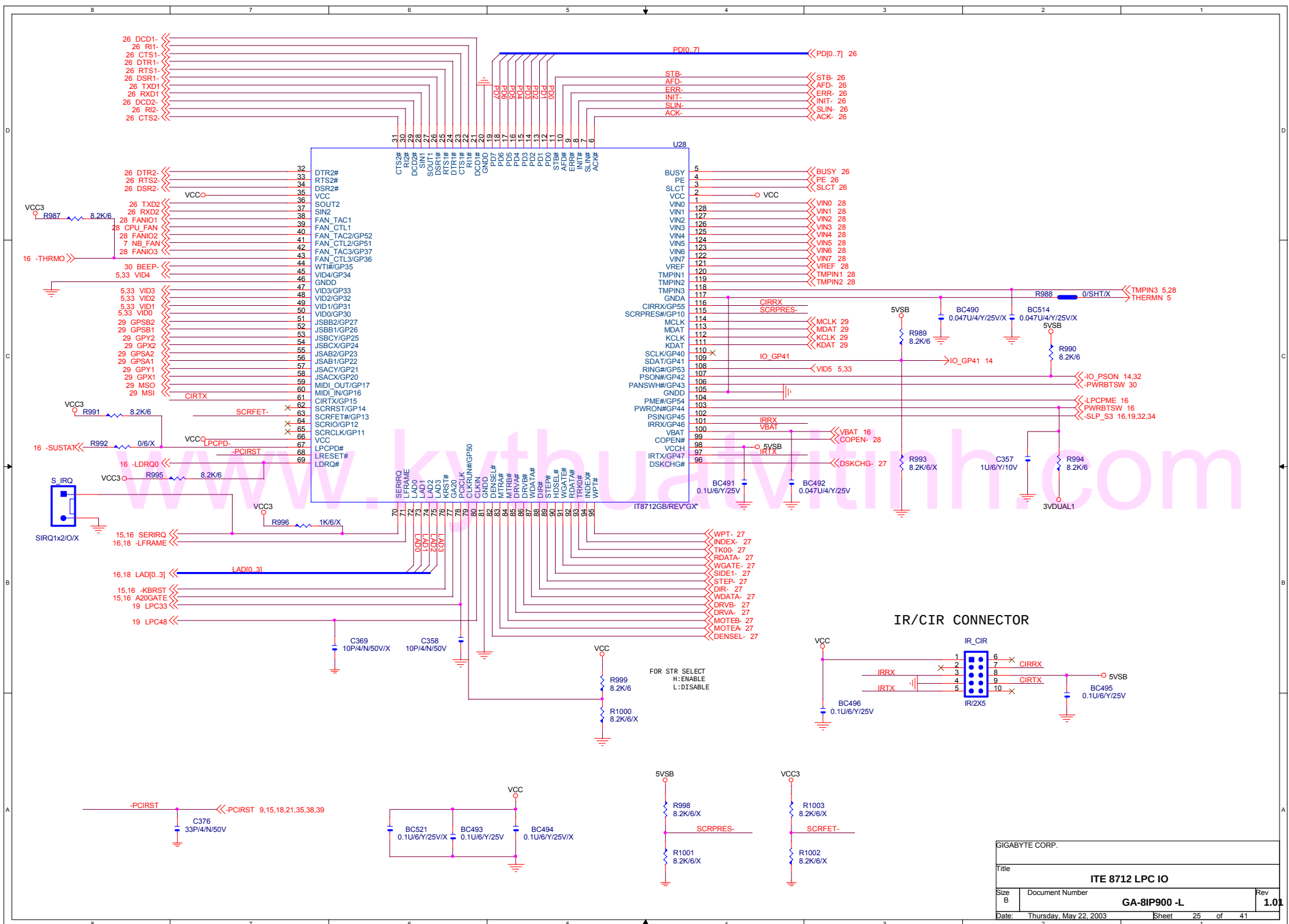
1/2(3.14)RC=1/2(3.14)8.2K*4.7U=4.3HZ以上AC 信號全部衰減 TO 0V
不會造成JDO 誤動作(無device 時play wav)

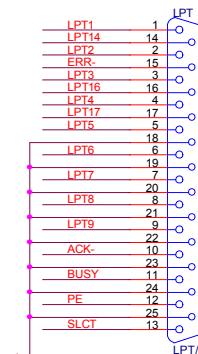
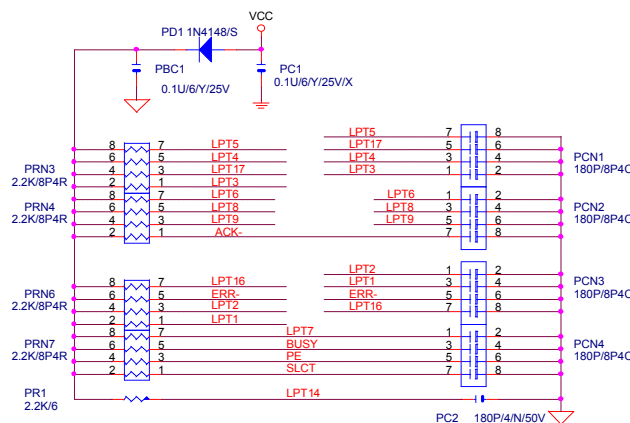
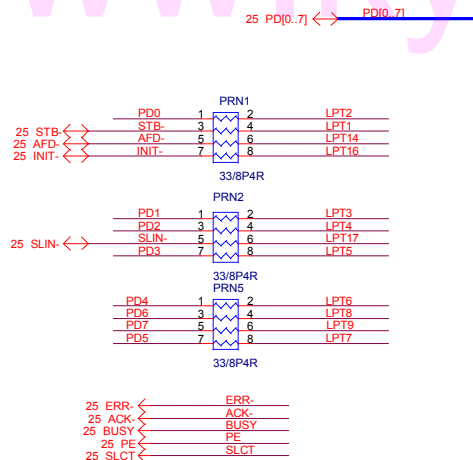
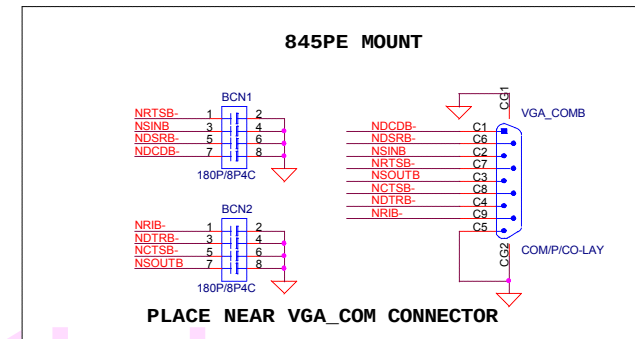
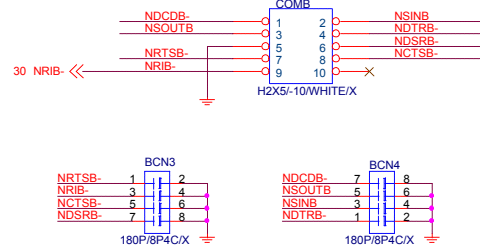
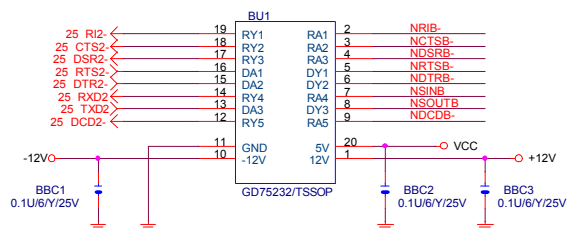
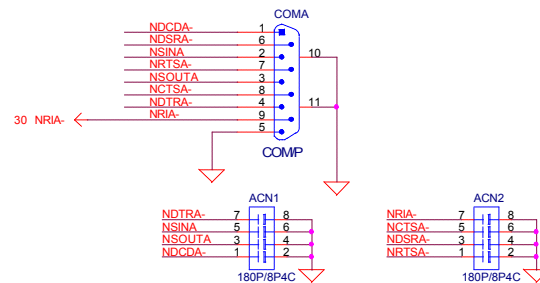
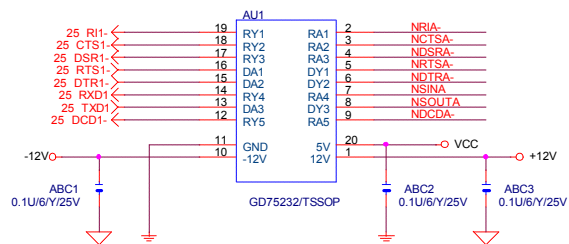


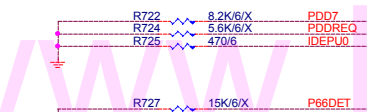
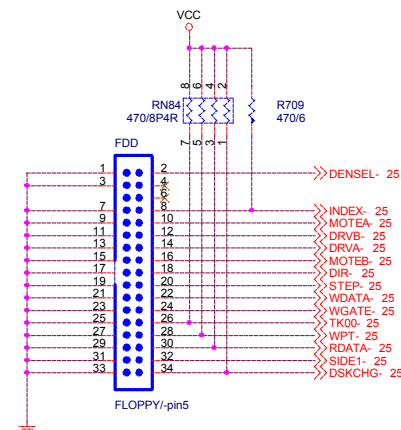
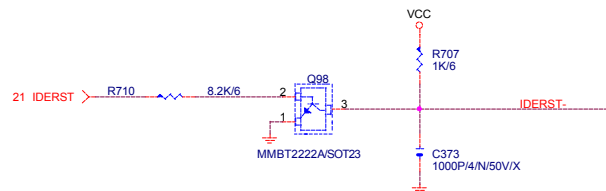
CENTER OUT, LOW
FREQUENCY EFFECT OUT



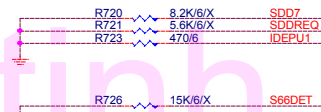
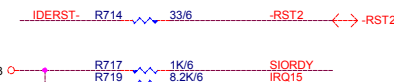
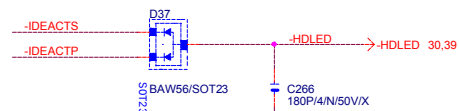
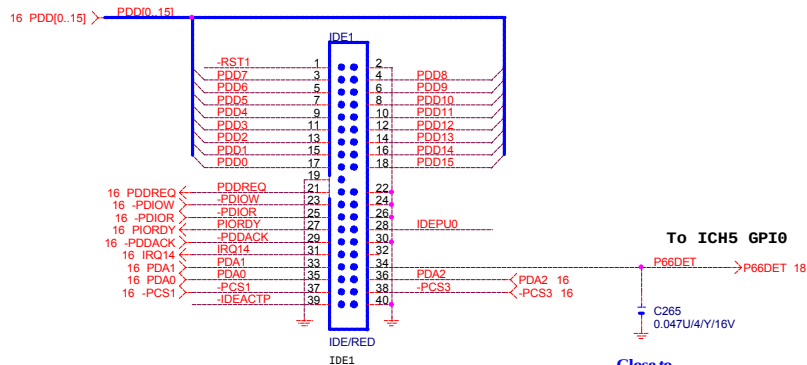
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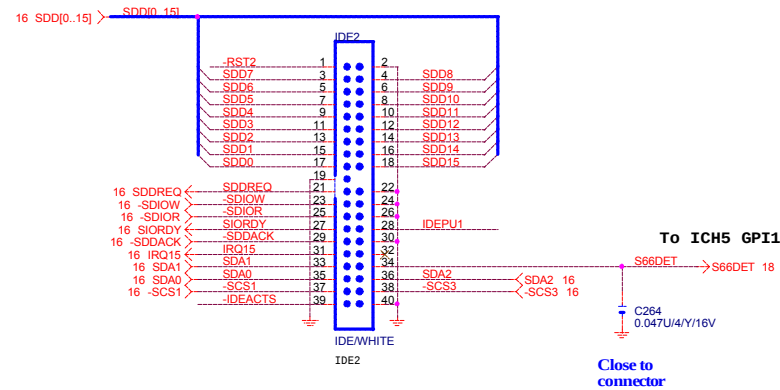




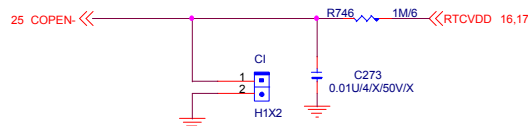
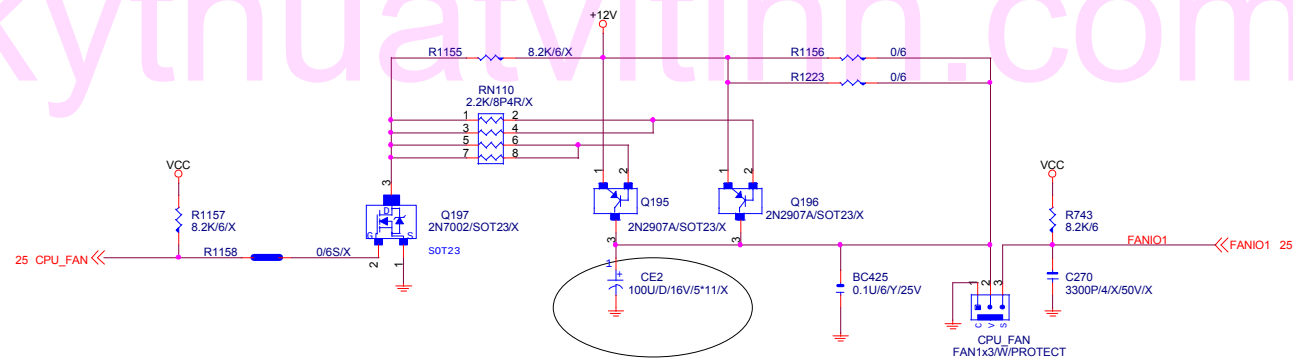
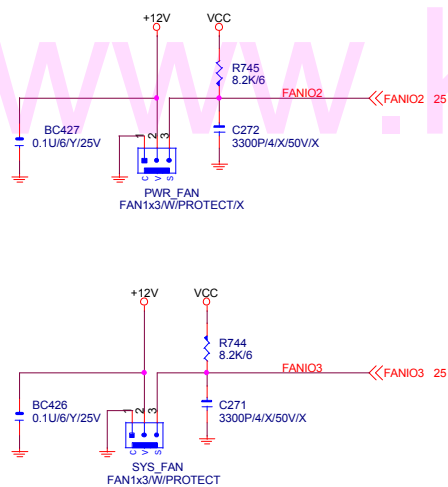
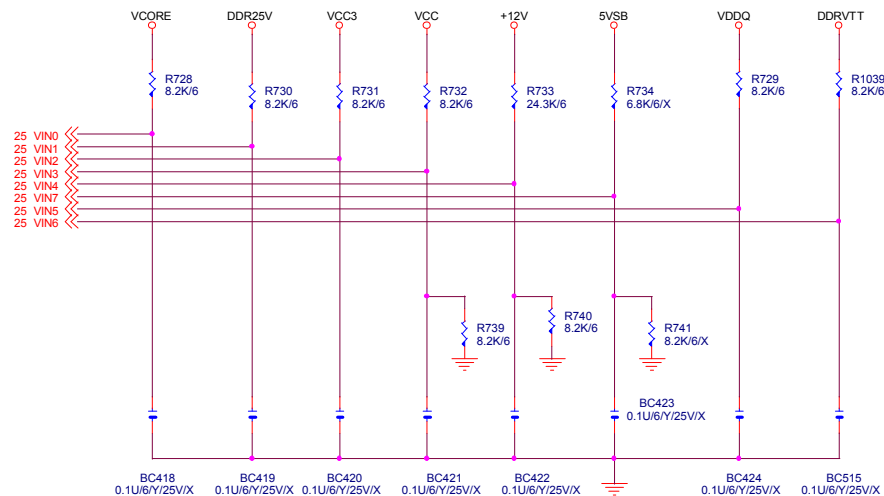
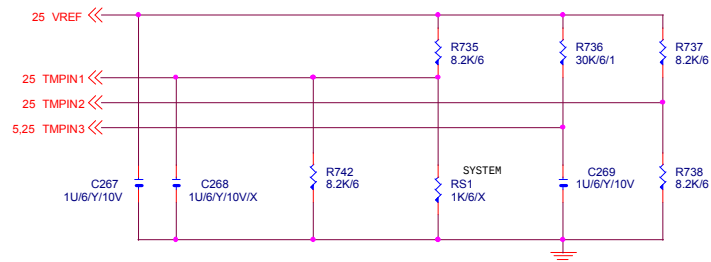
PRIMARY IDE CONNECTOR

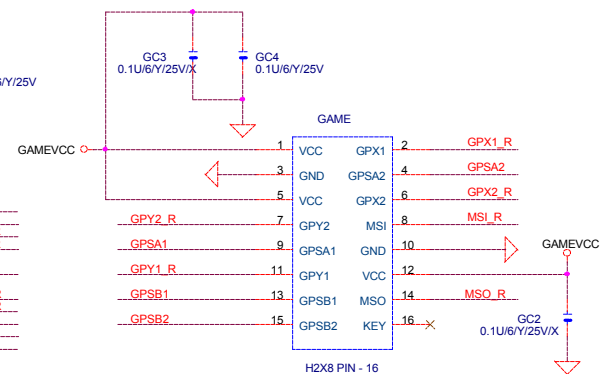
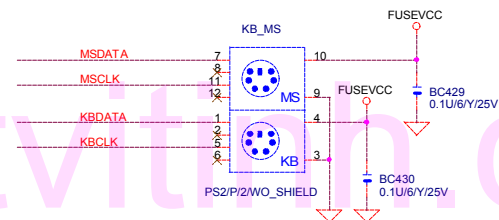
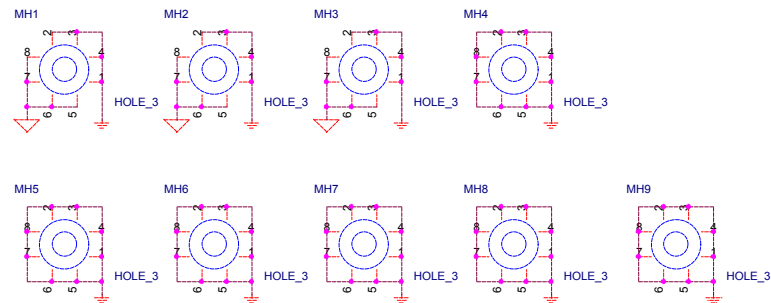


SECONDARY IDE CONNECTOR



Hardware Monitor circuits





INTEL FRONT PANEL

Pad trace same as pad width

3 PIN POWER LED

PWR_LED
PWR_LED+ 1 +MPD
PWR_LED- 2 -MPD
PWR_LED- 3 -MPD
P_LED_1X3

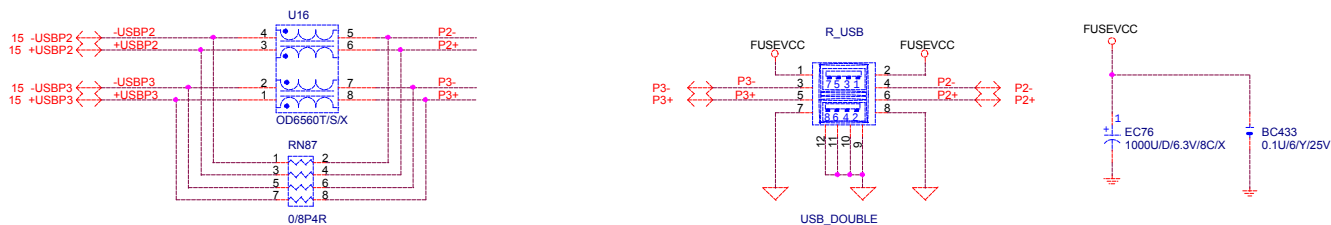
GIGABYTE CORP.

Title
PANEL & STR LED & RI

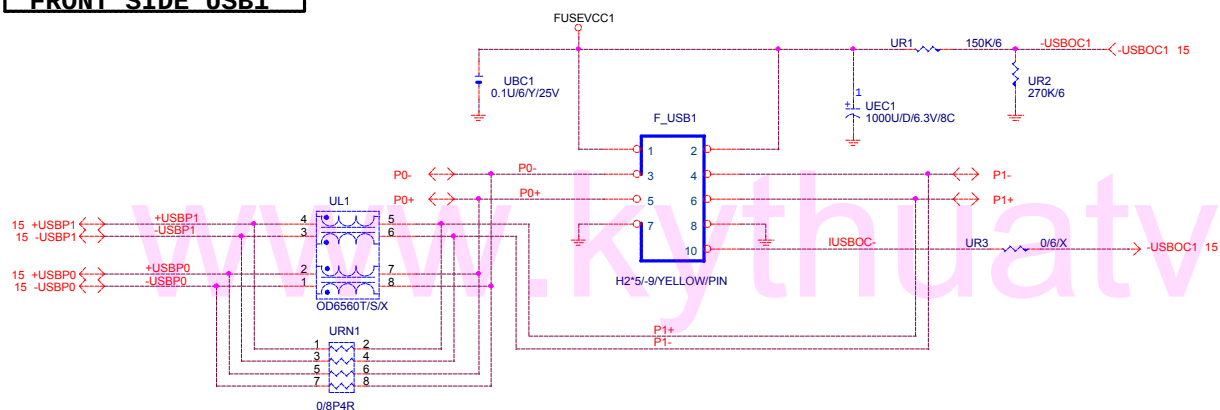
Size B Document Number
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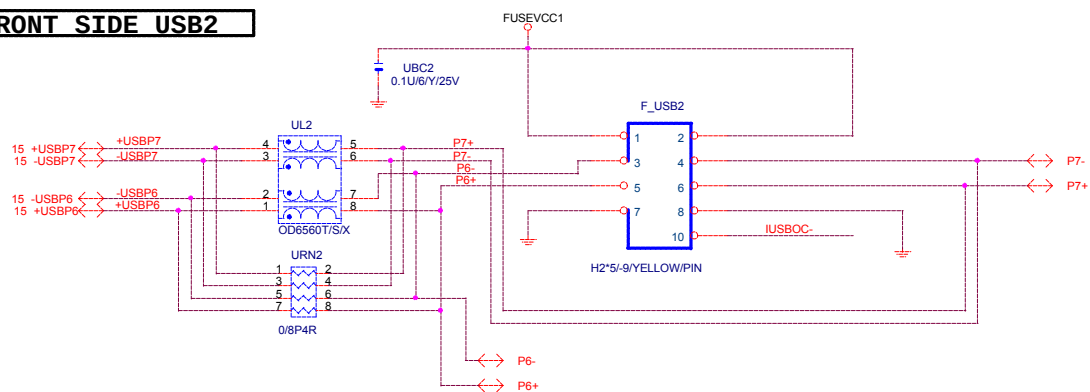
REAR USB



FRONT SIDE USB1

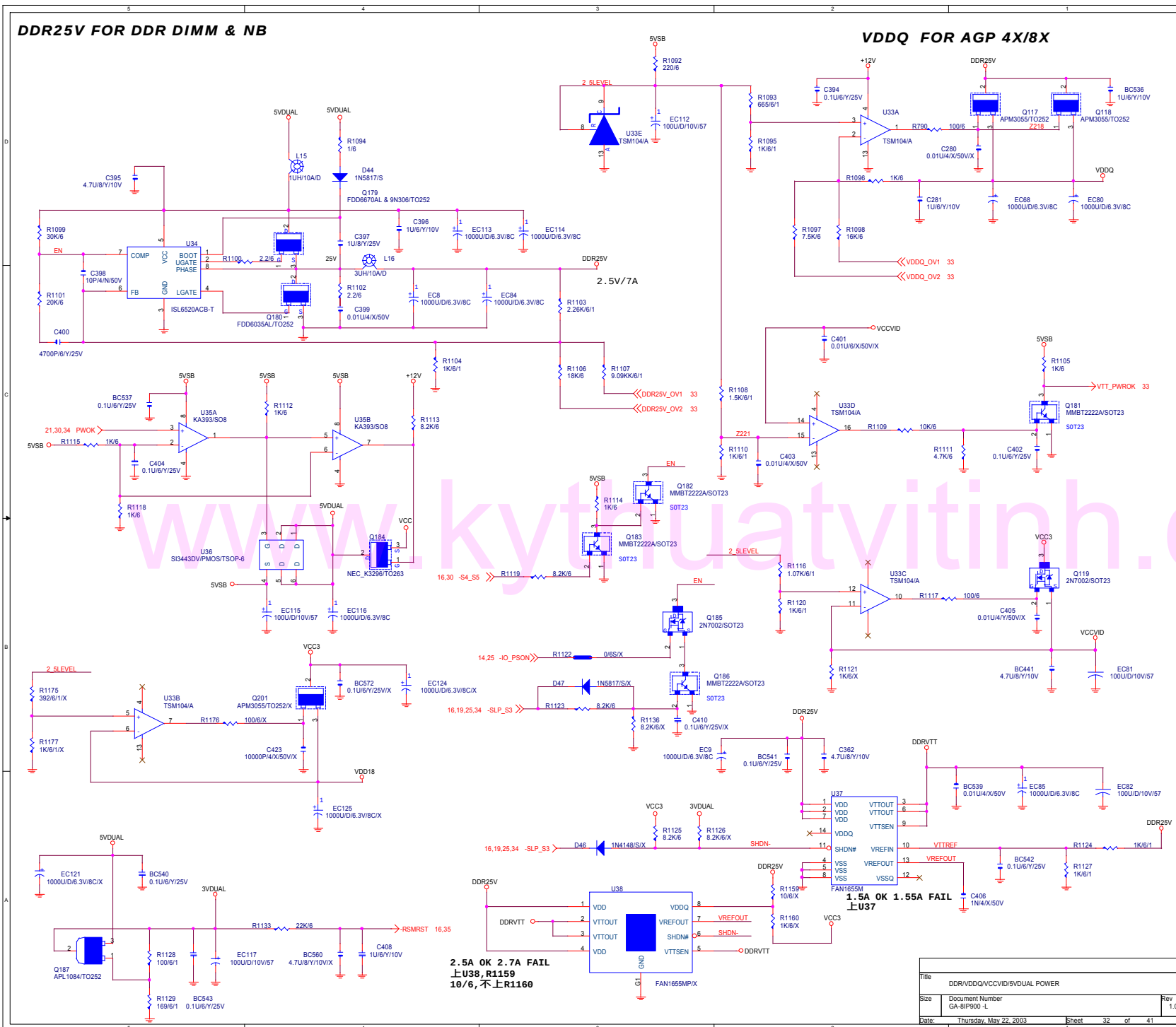


FRONT SIDE USB2

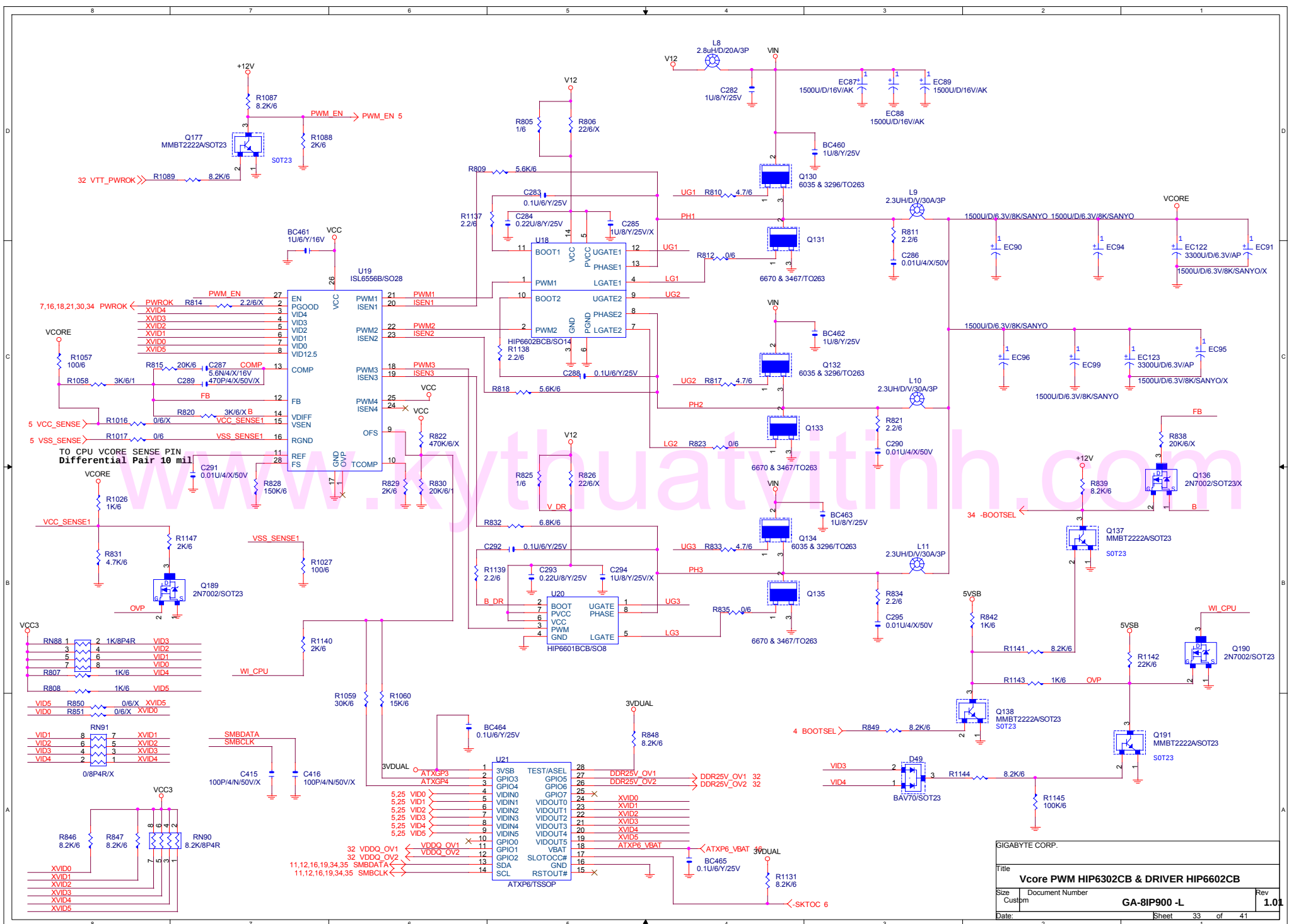


DDR25V FOR DDR DIMM & NB

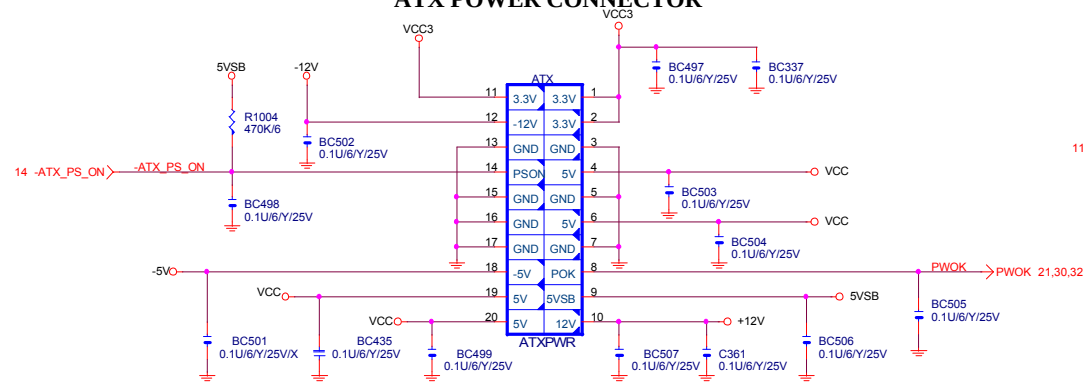
VDDQ FOR AGP 4X/8X



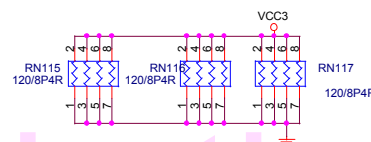
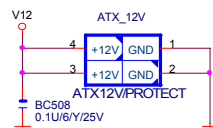
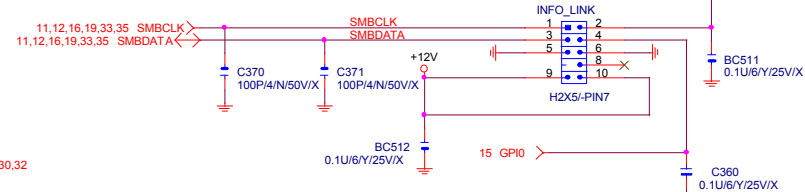
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DDR/VDDQ/VCCVID/5VDUAL POWER				
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ATX POWER CONNECTOR

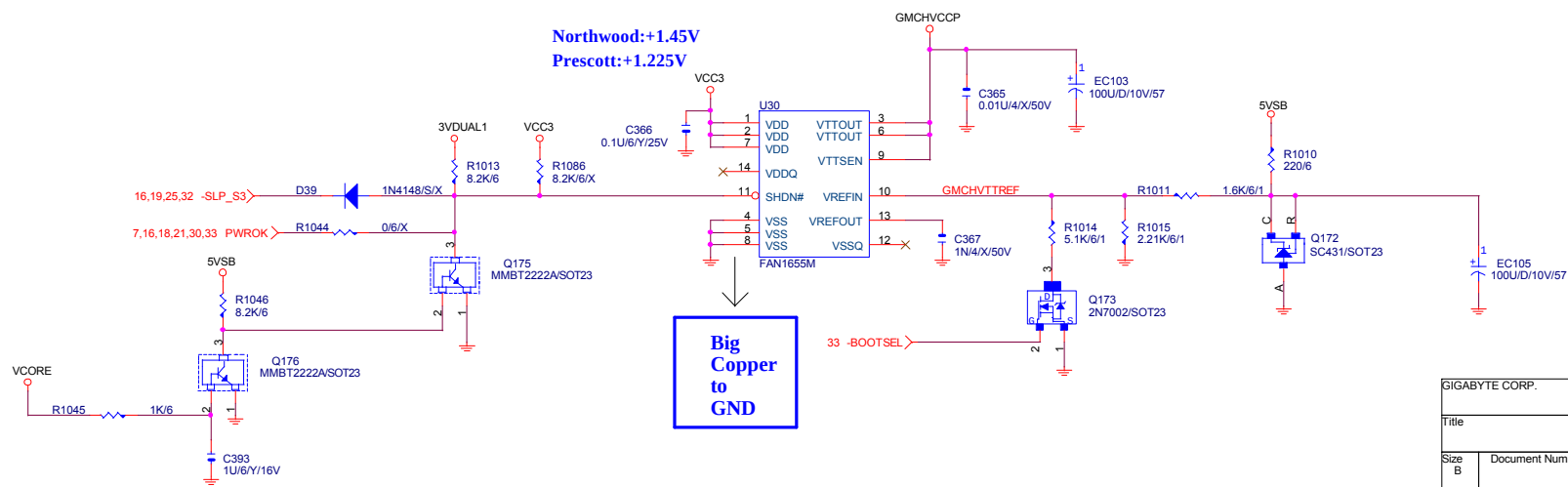


SMBUS CONN.



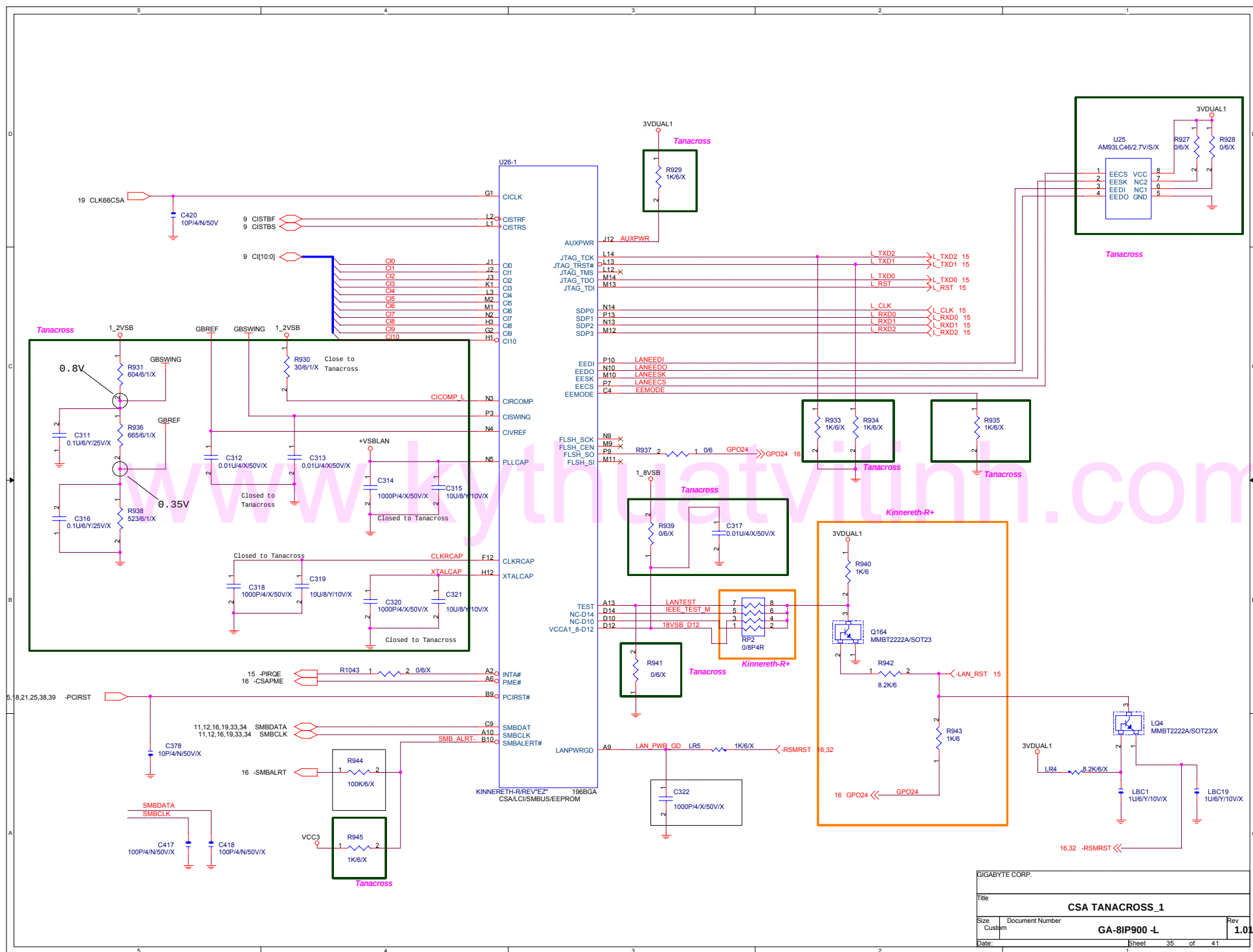
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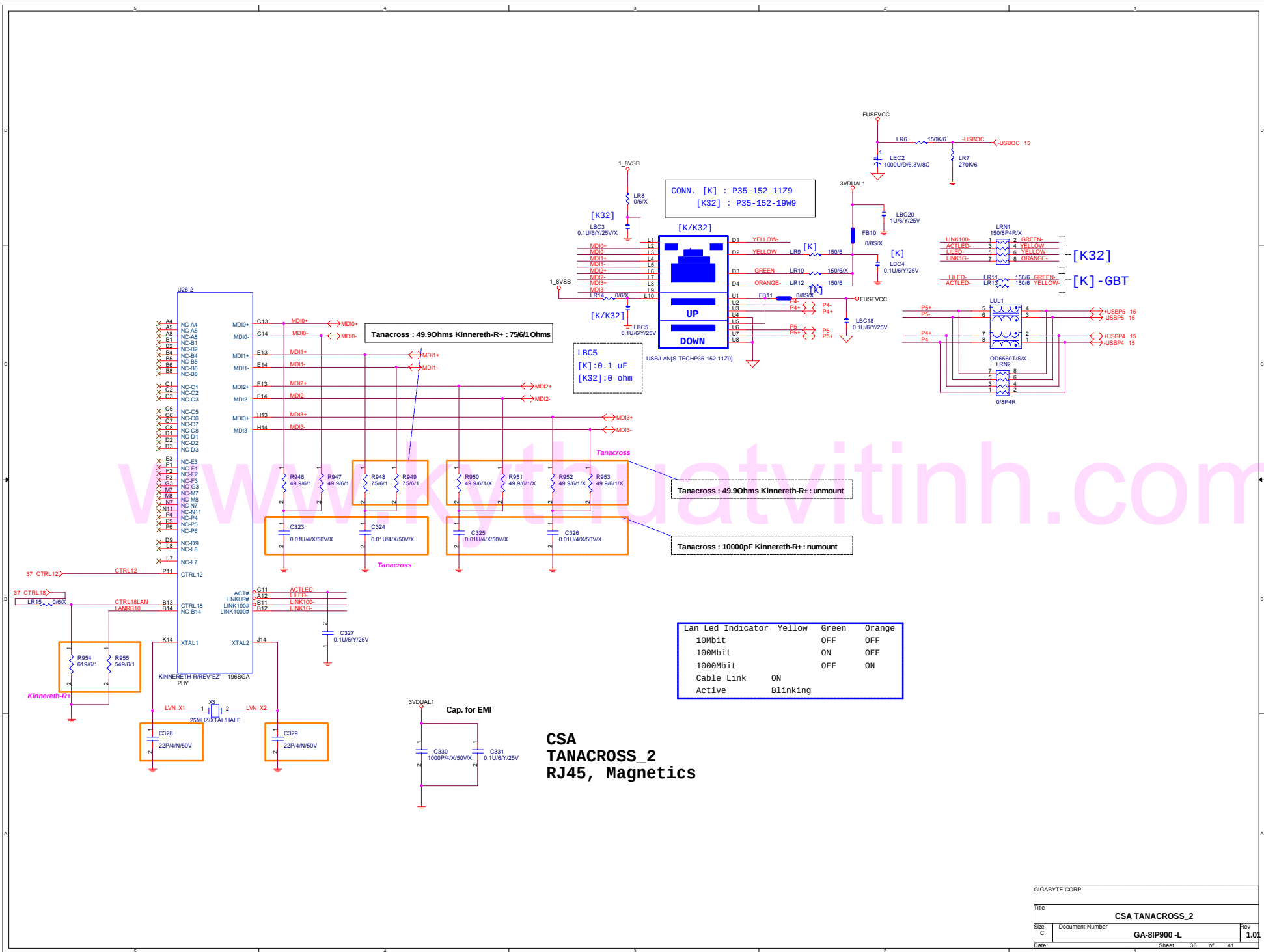
Northwood:+1.45V
Prescott:+1.225V



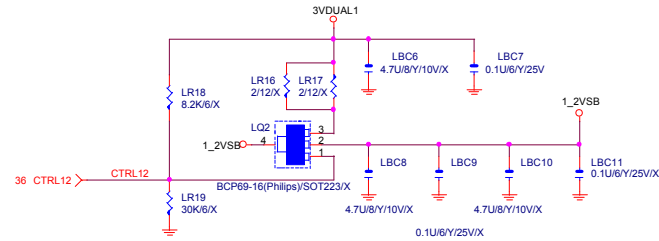
Big Copper to GND

GIGABYTE CORP.			
Title			
Misc. PWR & ATX CONN.			
Size	Document Number	Rev	
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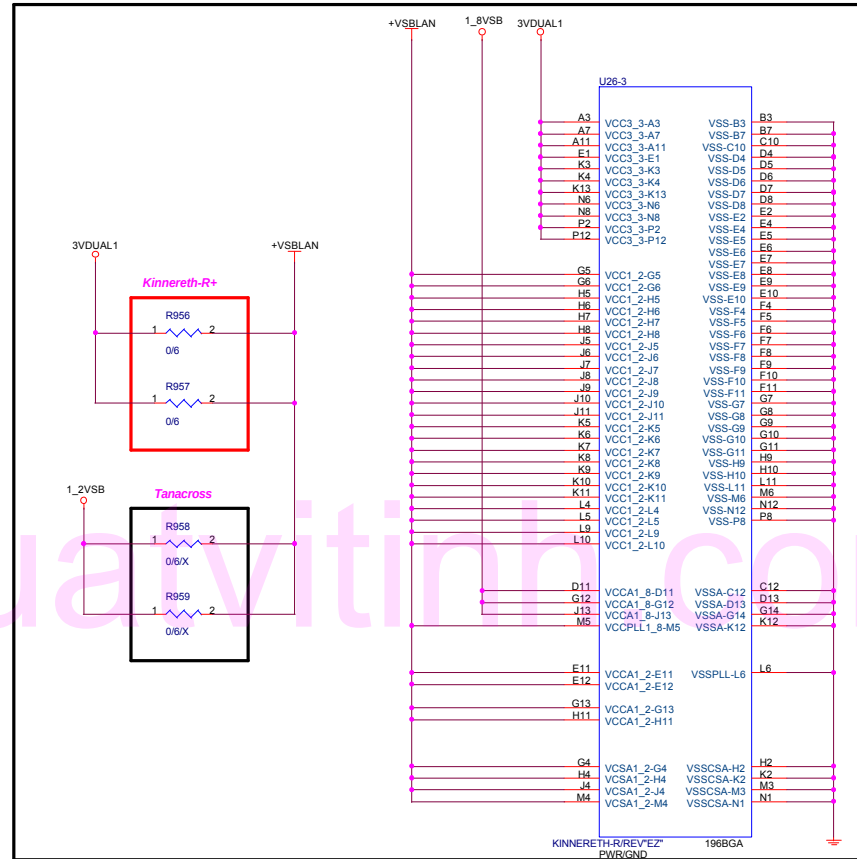
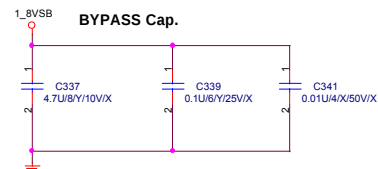
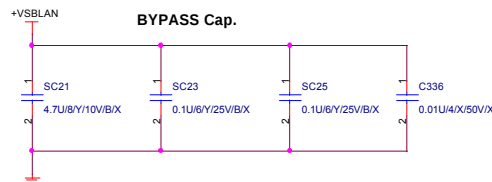
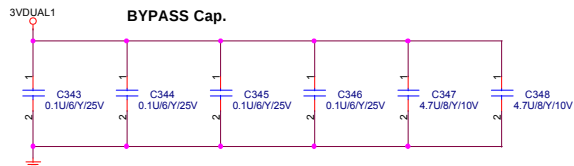
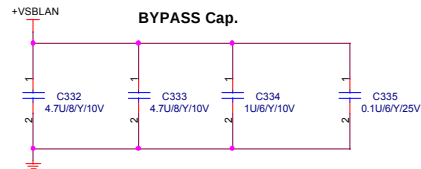
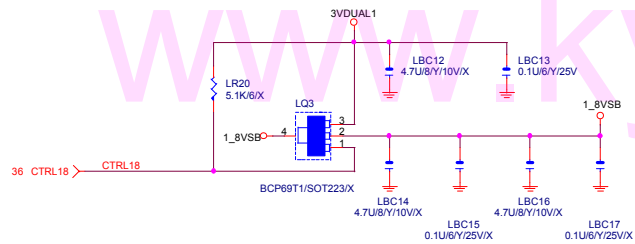




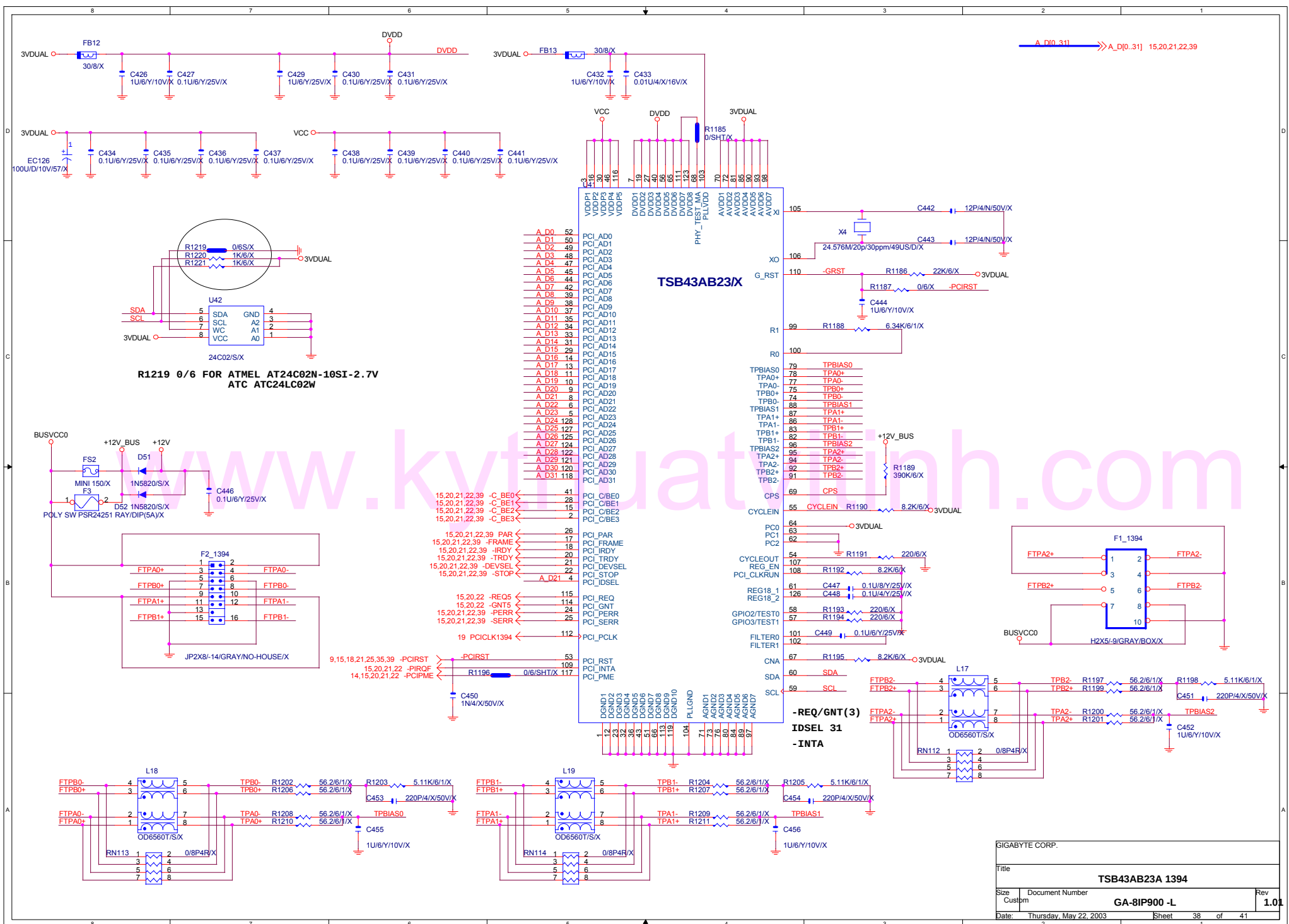
+1.2VSB Max800mA Typ500mA

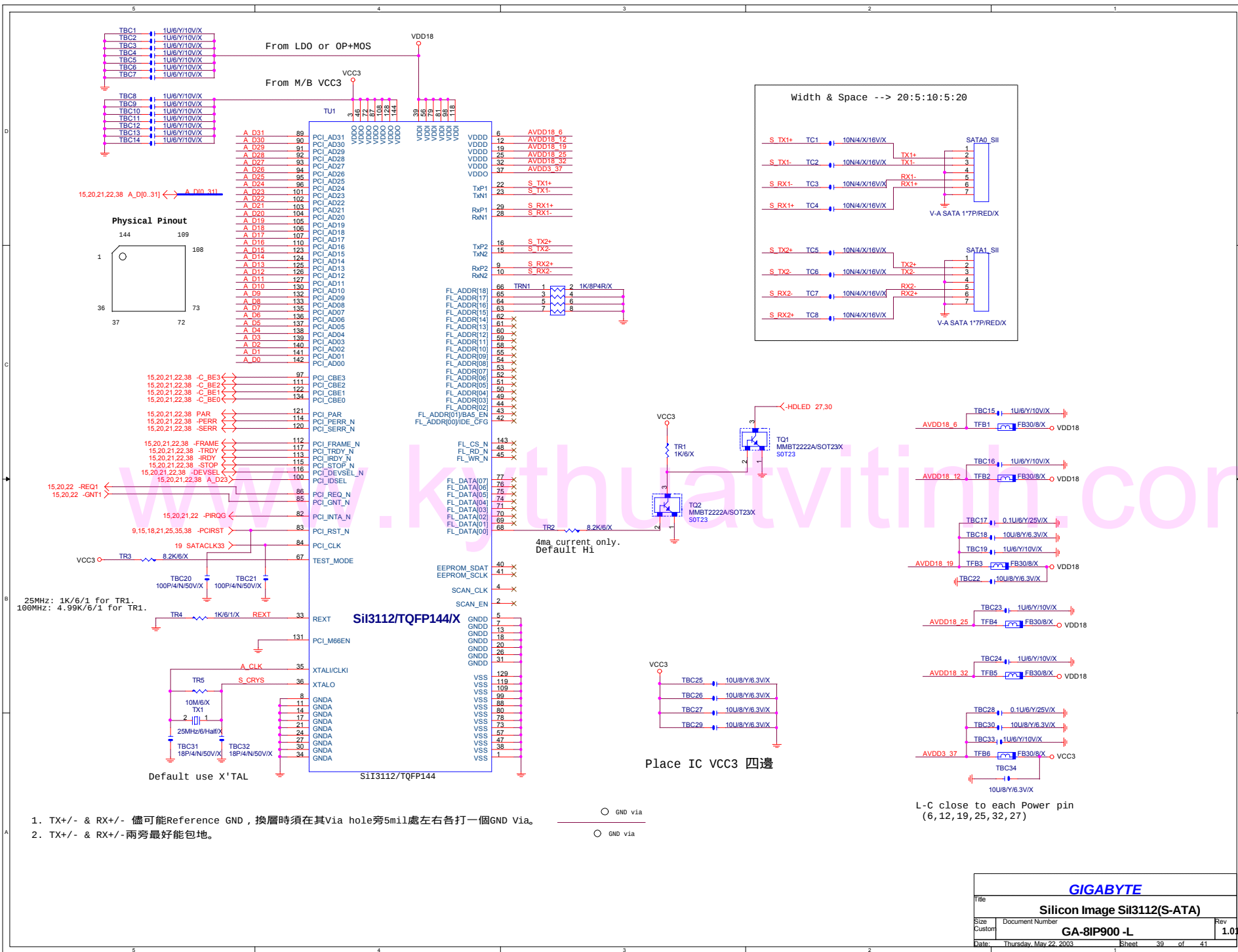


+1.8VSB Max500mA Typ250mA



SAGABYTE CORP.			
Title			
CSA TANACROSS_3			
Size	Document Number	Rev	
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GIGABYTE GA-8IP900-L PCI ROUNTING LIST

PCI DEVICE	IDSEL	INT	CLOCK	REQ	GNT	
PCI SLOT1	16	C, F, G, A	PCLK0	REQ01 -	GNT01 -	
PCI SLOT2	17	F, G, A, C	PCLK1	REQ02 -	GNT02 -	
PCI SLOT3	18	G, A, C, F	PCLK2	REQ2 -	GNT2 -	
PCI SLOT4	19	A, C, F, G	PCLK3	REQ3 -	GNT3 -	
PCI SLOT5	20	C, F, G, A	PCLK4	REQ4 -	GNT4 -	
TI 1394	21	F	PCICLK1394	REQ5 - (REQB#)	GNT5 - (GNTB#)	
SATA SI3112A	23	G	SATACLK33	GP00 (REQA#)	GP016 (GNTA#)	

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GIGABYTE GA-8IP900-L GPIO LIST

SHEET

TITLE

GPIP	I/O	FUNCTION
GPI0/REQA-	I	PULL HIGH 8.2K to VCC3, SMB connector.
GPI1/REQ5-		PULL HIGH 8.2K to VCC, REQ5-.
GPI2/PIRQE-		PULL HIGH 8.2K to VCC3, PIRQE-.
GPI3/PIRQF-		PULL HIGH 8.2K to VCC3, PIRQF-.
GPI4/PIRQG-		PULL HIGH 8.2K to VCC, PIRQG-.
GPI5/PIRQH-	NA	PULL HIGH 8.2K to VCC
GPI6/AGPBUSY-	I	PULL 8.2K TO VCC3, PANEL GREEN_BUTTON
GPI7	I	DUAL BIOS FIRST BOOT SELECT.
GPI8	I	PULL 8.2K TO 3VDUAL, -CASPME.
GPI9/OC4-	NA	USB OC4-.
GPI10/OC5-	NA	USB OC5-.
GPI11/-SMBALRT	NA	PULL 8.2K TO 3VDUAL, -SMBALERT.
GPI12	I	PULL 8.2K TO VCC3,M/B REVERSION ID.
GPI13	I	LPC PME.
GPI14/OC6-	NA	USB OC6-.
GPI15/OC7-	NA	USB OC7-.
GP016/GNTA-	NA	GP016.
GP017/GNT5-		GNT5-.
GP018/STP_PCI-	NA	GP018.
GP019/SLP_S1-	0	DUAL BIOS.
GP020/SLP_CPU-	0	DUAL BIOS.
GP021/C3_SATA-	0	BLOCK TOP TABLE.
GP022/CPUPERF-	0	PULL 8.2K TO VCC3,PANEL S3 POWER LED.

SHEET

TITLE

GPIP	I/O	FUNCTION
GP023	NA	PULL 8.2K TO VCC3
GP024	0	INTEL LAN ENABLE/DISABLE.
GP025	0	FRONT PANEL -MPD.
GP027	0	FRONT PANEL +MPD.
GP028	0	GREEN LED
GP032	0	BIOS WRITE PROTECT.
GP033	0	SATA LED.
GP034	I	CLEAR PASSWORD.

GIGABYTE

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