

GIGABYTE GA-8IP775-G Schematics

Revision 1.01

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	P4 LGA775A
05	P4_478B
06	P4_478C
07	SPRINGDALE HOST
08	SPRINGDALE DDR
09	SPRINGDALE AGP, HUB, CSA, VGA
10	SPRINGDALE PWR
11	DDR1,2 CHANNEL A
12	DDR3,4 CHANNEL B
13	DDR TERMINATION
14	AGP 8X SLOT
15	ICH5 PCI, USB, HUB, LAN
16	ICH5 IDE, GPIO, SATA, CTRL
17	ICH5 VCC, GND
18	DUAL FWH
19	ICS952635 CLOCK GENERATOR
20	PCI SLOT1/SLOT2
21	PCI SLOT3/SLOT4/RESET BUFFER
22	PCI SLOT5/SLOT6

SHEET TITLE

23	AC '97 CODEC
24	AUDIO JACK, L_OUT, F_AUDIO
25	ITE 8712/IR_CIR/SCR/S_IRQ
26	COMA/VGA_COMB/LPT
27	IDE1/IDE2/FDD
28	FAN/HW MONITOR
29	KB_MS/GAME/FUSEVCC
30	FRONT PANEL
31	R_USB/F_USB1/F_USB2
32	DDR/VDDQ/5VDUAL/VCCVID POWER
33	VCORE POWER
34	ATX/ATX_12V/FAN1655M
35	MARVELL 88E8001
36	TI TSB43AB23(1394)
37	PCI ROUNTIONG
38	GPIO PIN LIST

PROCESS: C		COMPONENT SIDE (0.5 oz. Copper)	
		VCC SIDE (1 oz. Copper)	
		GND SIDE (1 oz. Copper)	
		SOLDER SIDE (0.5 oz. Copper)	
GIGABYTE			
Title COVER SHEET			
Size Custom	Document Number GA-8IP775-G		Rev 1.01
Date:	Sheet 1 of 40		

Model Name: GA-8IP775-G

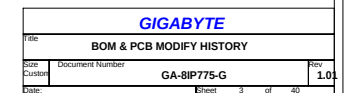
Component history

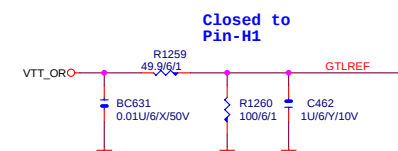
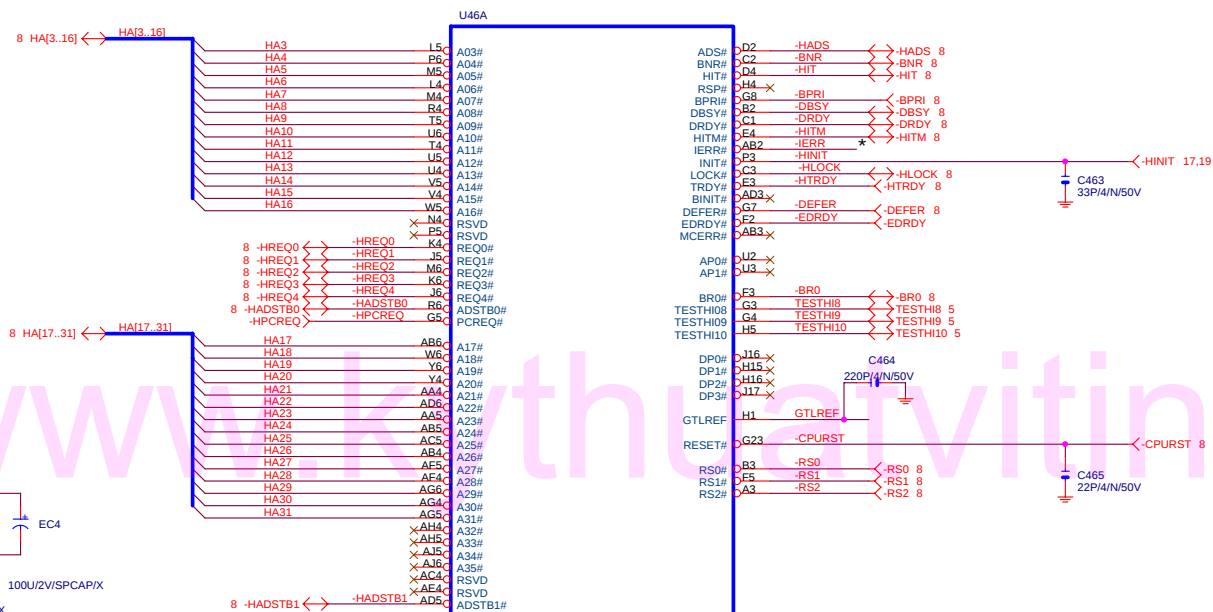
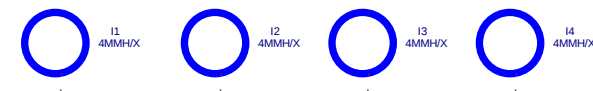
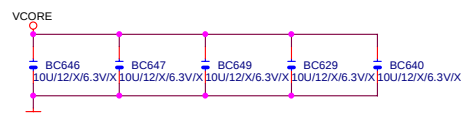
Date	Change Item	Reason
0.1	由8IPE1000 PRO G REV3.1 修改成8IPE775 PRO REV0.1	
1.0	ATX 改成 11NH4-020004-71/72 R1297 0/6 -->178/6/1 R1299 1K/6 U11 ICS952643 --> ICS952635BF DL2-DL5 0.6uH/D/40A/3P --> 0.5uH/D/30A/3PM (擺法同8I915G-0.4) ADD TESTHI0 & GTL_DET CIRCUIT F1_1394 LIB 修改(改抽pin7) PCI TEST PIN修改 FANPWM3 & THRM0- 對調	
1.0B	CPU_FAN FOOTPRINT H1X4P3-FAN --> H1X4P-FAN DC11 4700P --> 8200P/4/X/25V DR33 1.3K/6/1 --> 1K/6/1 EC1,EC2 SP-CAP 100U REMOVE ADD BC621,BC622,BC623,BC624,BC626,BC627 10U/12/X/6.3V DL2-DL5 CHOKE05U-30A_3PM --> CHOCK05U-30A_3PM-TAI	
1.0C	DEC13 --> DEC15 3300UF/D/6.3V DR34 16K/6/1 --> 33K/6/1	
8IPE775		
1.0C	1. REMOVE LAN FUNCTION	
1.0D	R1240 1K--> 10K/6/1,RS2 HR10K-->100K/6/1,R1239 3.74K-->9.53K/6/1,R1241 2.15K-->3.24K/6/1	
1.0E	DDR25V Q123 改成 包材修改 DRIVER 865/848 REV1.0 --> 875/865/848 R5W1L0A775 INSTALLATION & STICKER 8-CH AUDIO	
8IP775		
1.0E	9M8IPE775-00-10E --> 9M8IP775-00-10E N/B 865PE --> 865P	
1.0E	9M8IP775-00-10E --> 9M8IP775-00-10E FOR PVT	
1.0F	FDD:11NH3-010217-B6 , IDE1:11NH3-010220-R6 , IDE2:11NH3-010220-W6 ADD STICKER :12LB6-T00093-01	
8IP775-G		
1.0F	ADD LAN FUNCTION	
1.0F-0819	ADD FLASH ROM 2M:SST 49LF002A REV.B & PMC 2M REV.E ADD R1155 8.2K/6	
9MD-NB-1.0F	FOR CKD BOM	
1.0G	R1093 665/6/1 --> 499/6/1	FOR FSB800 CPU
9MD-NB-1.0G	不穩	

Circuit or PCB layout history

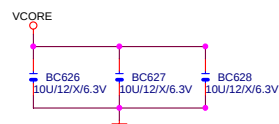
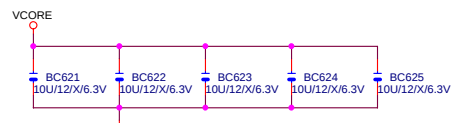
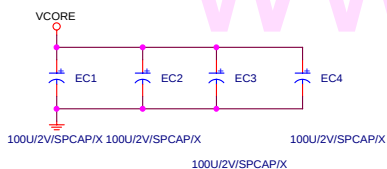
[illegible]

PCB SIZE: 305X230 mm.
60 Ohm +- 15% C

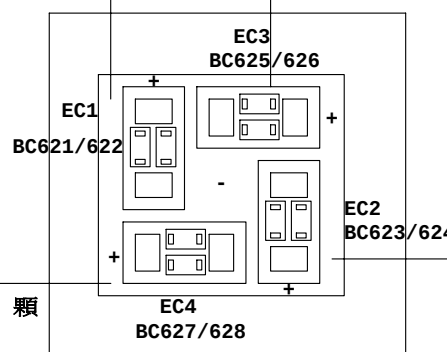




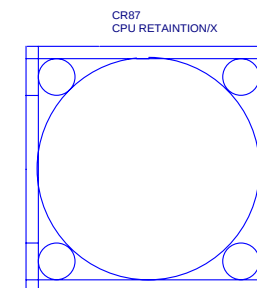
SP-CAP X 4PCS



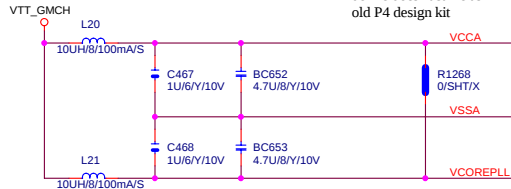
上100U/2V/SPCAP 上10U/12/X/6.3V X2 顆



- 上100U/2V/SPCAP

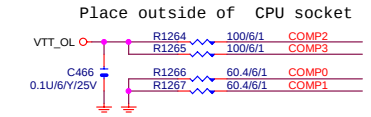
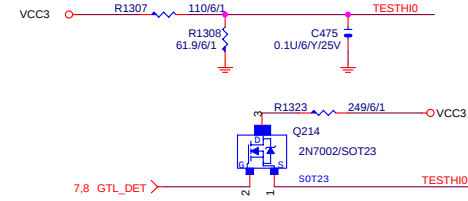


Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit

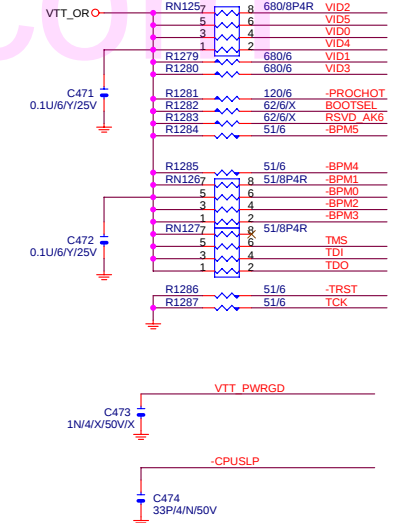
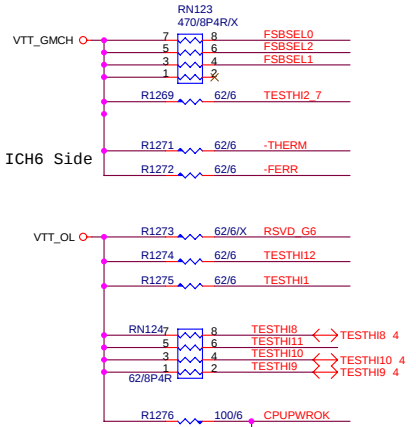


As close as possible to
CPU socket

Trace width doesn't
less than 12 Mil



Locate at ICH6 Side



CPU

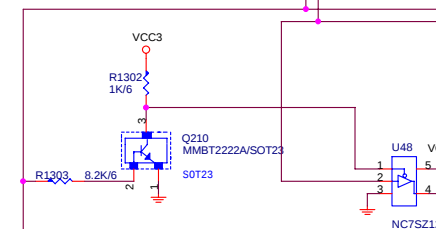
FSA	FSB	NA	Clock
FSBSEL0	FSBSEL1	FSBSEL3	Clock
1	0	1	100MHZ
1	0	0	133MHZ
1	1	0	166MHZ
0	1	0	200MHZ
0	0	0	266MHZ

ICS952635

FSD	FS3	FSC	FSA	FSB	Clock
FS4	FS3	FS2	FS1	FS0	Clock
0	1	0	0	0	100MHZ
0	1	0	1	0	133MHZ
0	1	0	1	1	166MHZ
0	1	0	0	1	200MHZ

ICS952643

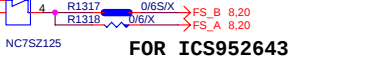
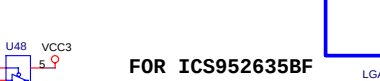
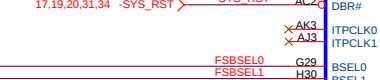
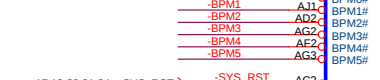
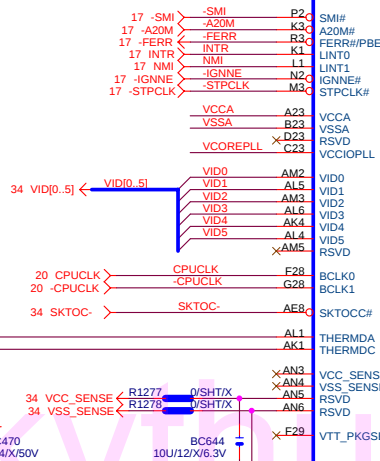
FSD	FS3	FSC	FSA	FSB	Clock
FS4	FS3	FS2	FS1	FS0	Clock
0	1	0	0	0	100MHZ
0	1	0	1	0	133MHZ
0	1	0	1	1	166MHZ
0	1	0	0	1	200MHZ



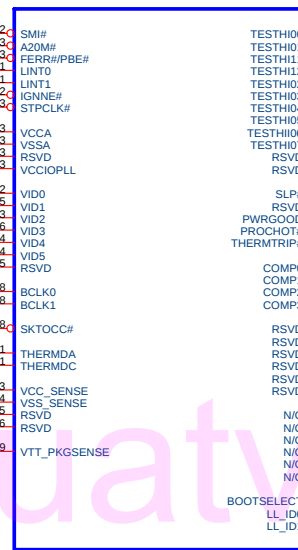
FOR ICS952635BF

FOR ICS952643

FOR ICS952643

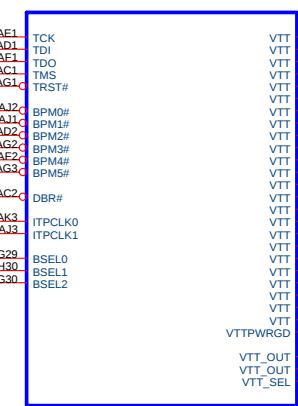


U46C

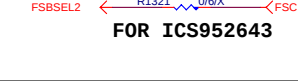
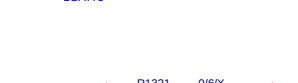


LGA775

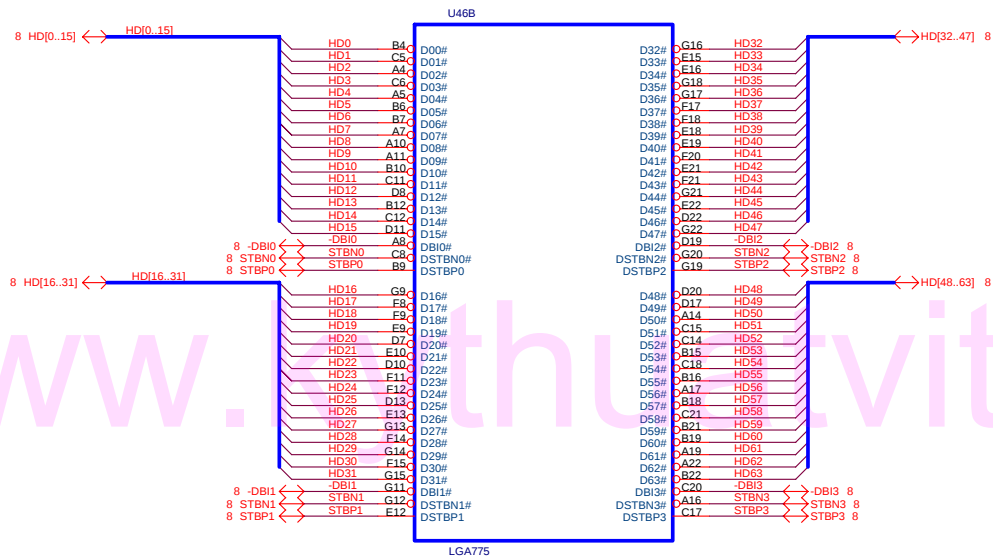
U46D

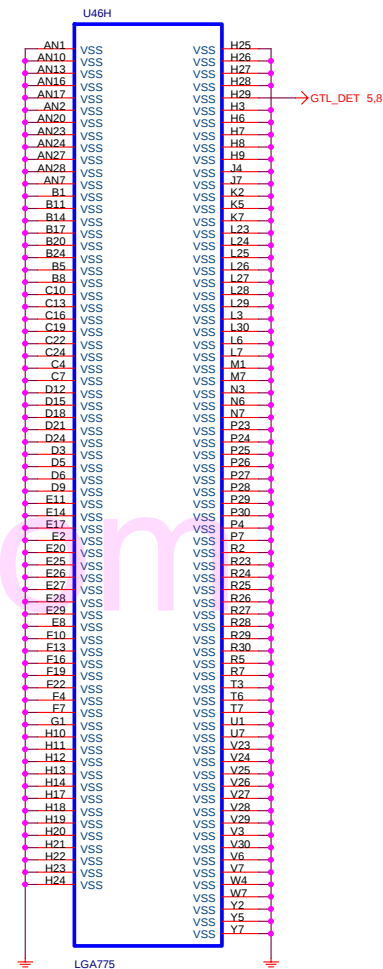
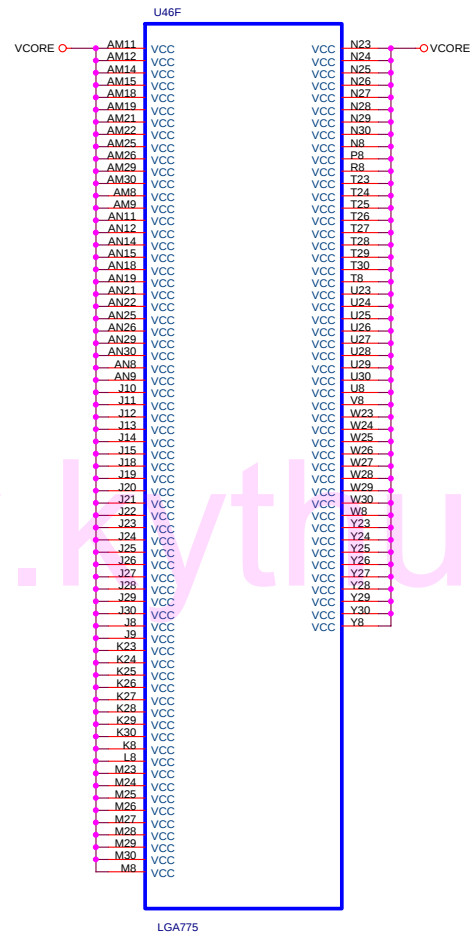
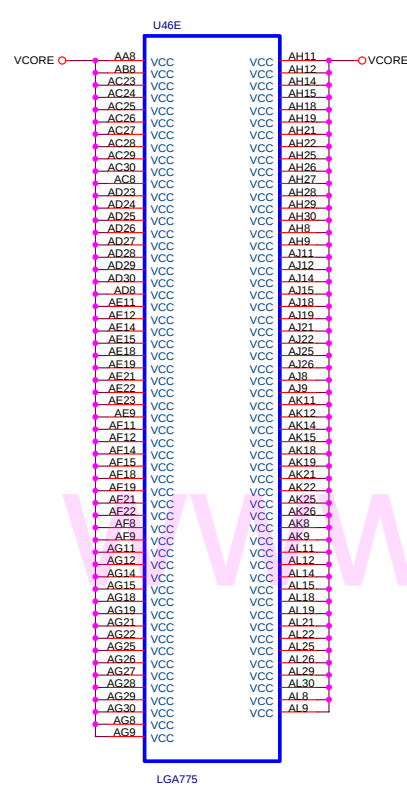


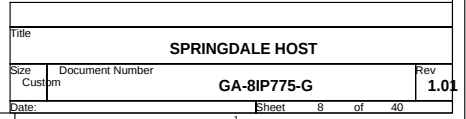
LGA775

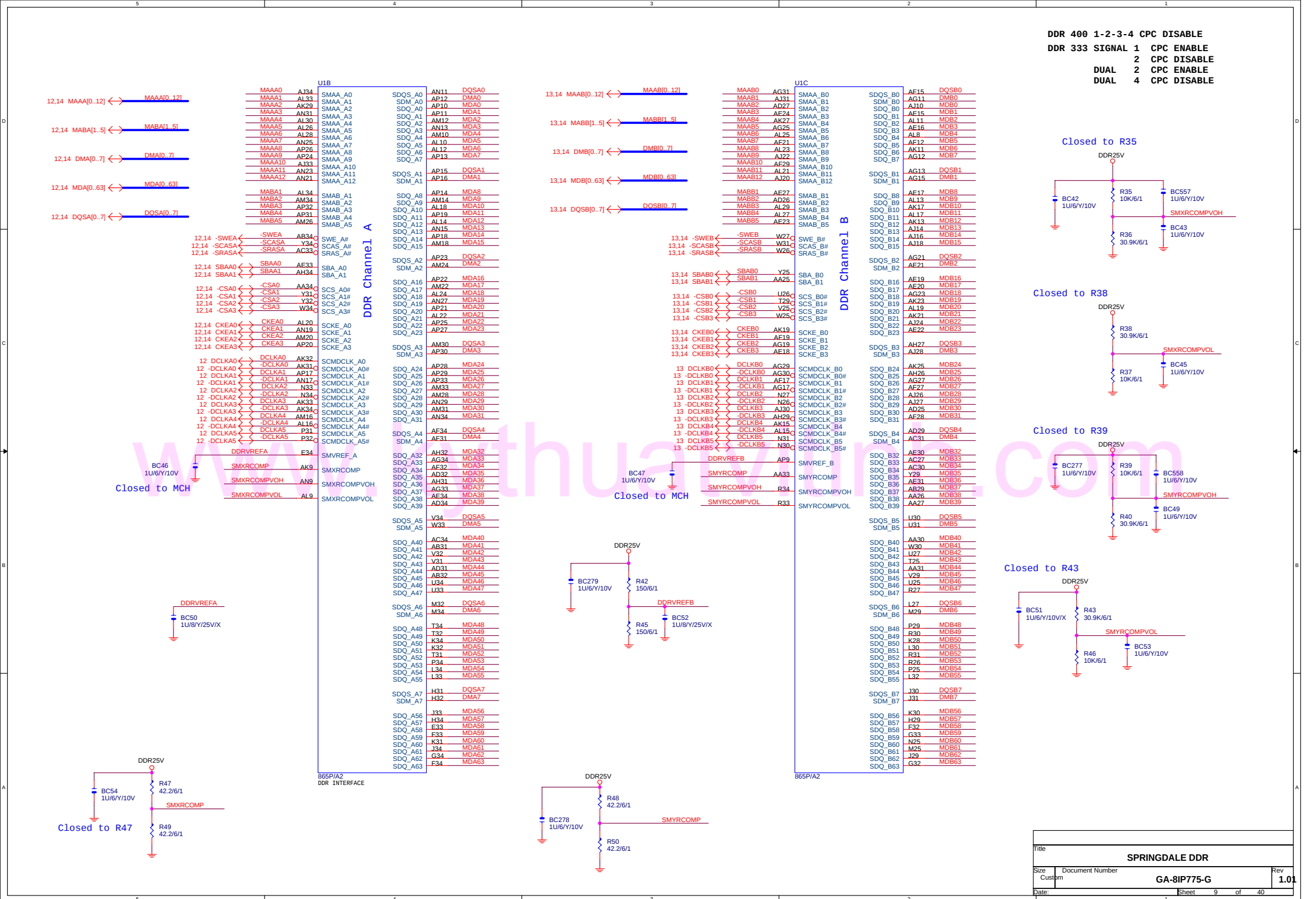


GIGABYTE			
P4_LGA775-B			
Title	Document Number	GA-8IP775-G	
Size	Custom	Date	Rev
Tuesday, September 14, 2004	1	Sheet	5 of 40









15 GAD[0..31] <-> GAD[0..31]
15 SBA[0..7] <-> SBA[0..7]
16 HL[0..10] <-> HL[0..10]

15 -GCBE0 <-> -GCBE0 Y7
15 -GCBE1 <-> -GCBE1 W5
15 -GCBE2 <-> -GCBE2 AA3
15 -GCBE3 <-> -GCBE3 U2
15 -GFRAME <-> -GFRAME U6
20 GMCH3V66 <-> GMCH3V66 H4
15 -GDEVSEL <-> -GDEVSEL AB4
15 -GIRDY <-> -GIRDY V11
15 -GTRDY <-> -GTRDY AB5
15 -GSTOP <-> -GSTOP W11
15 -GPAR <-> -GPAR AB2
15 -GREQ <-> -GREQ N6
15 -GGNT <-> -GGNT M7

15 GSWING <-> GRCOMP AC2
15 MCH_AGPREF <-> GSWING AC3
15 -RBF <-> -RBF R10
15 -WBF <-> -WBF R9
15 -PIPE <-> -PIPE M4
15 -GDBI_LO <-> -GDBI_LO M5

15 ST0 <-> ST0 N3
15 ST1 <-> ST1 N5
15 ST2 <-> ST2 N2

HL0 <-> AF5
HL1 <-> AG3
HL2 <-> AK2
HL3 <-> AC5
HL4 <-> AK5
HL5 <-> AL3
HL6 <-> AL2
HL7 <-> AL4
HL8 <-> AJ2
HL9 <-> AH2
HL10 <-> AJ3
HLSTBF <-> HLSTBF AH5
HLSTBS <-> HLSTBS AH4

Close to MCH
C27 0.01U/4X/16V
C28 0.01U/4X/16V

AK7 <-> C10
AH7 <-> C11
AD11 <-> C12
C13 <-> C13
AD7 <-> C14
AC10 <-> C15
AF8 <-> C16
AG7 <-> C17
AE9 <-> C18
AH9 <-> C19
AG6 <-> C20
AJ6 <-> C21
AJ5 <-> C22

11 CLSWING_SPG <-> CLRCOMP AG2
11 CLVREF_SPG <-> CLVREF_SPG AF2
20 DOTCLK <-> DOTCLK G4
17 -ICHSYNC <-> -ICHSYNC AP8
16,19,22,26,37,38 -PCIRST <-> -PCIRST AJ8
TP GMCH AG10 <-> TP GMCH AG10
TP GMCH AG9 <-> TP GMCH AG9

VDDQ
R69 52.3/6/1
CLRCOMP

GSWING C34 0.1U/6/Y/25V
MCH_AGPREF C35 0.01U/4/X/16V

CLOSED TO MCH

VDDQ
R74 52.3/6/1
HLRCOMP_MCH
VDDQ
R75 43.2/6/1
GRCOMP

U1D
GADSTBF0
GADSTBS0
GAD0
GAD1
GAD2
GAD3
GAD4
GAD5
GAD6
GAD7
GAD8
GAD9
GAD10
GAD11
GAD12
GAD13
GAD14
GAD15

AGP

GRCOMP AC2
GSWING AC3
GVSWING AC4
GVREF AC5

-RBF R10
-WBF R9
-PIPE M4
-GDBI_LO M5

ST0 N3
ST1 N5
ST2 N2

HL0 AF5
HL1 AG3
HL2 AK2
HL3 AC5
HL4 AK5
HL5 AL3
HL6 AL2
HL7 AL4
HL8 AJ2
HL9 AH2
HL10 AJ3
HLSTBF AH5
HLSTBS AH4

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HLSTBS AH4

AC6 ADSTB0
AC5 -ADSTB0

ADSTB0 15
-ADSTB0 15

AE6 GAD0
AC11 GAD1
AD5 GAD2
AE5 GAD3
AA10 GAD4
AC9 GAD5
AB11 GAD6
AB7 GAD7
AA9 GAD8
AA6 GAD9
AA5 GAD10
W10 GAD11
AA11 GAD12
W6 GAD13
W9 GAD14
V7 GAD15

ADSTB1 15
-ADSTB1 15

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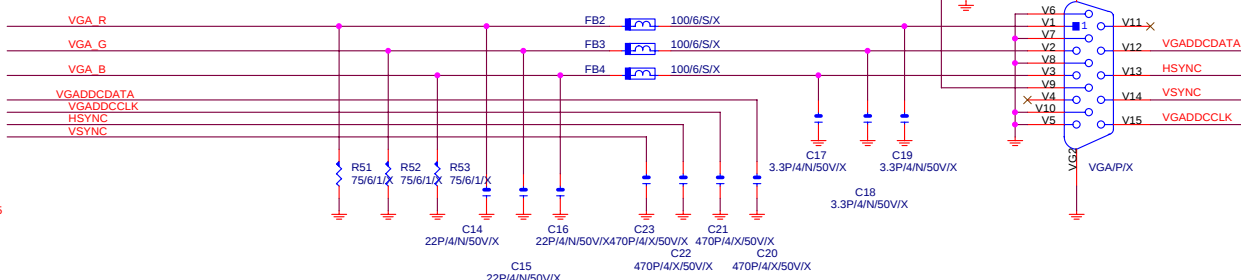
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-ADSTB1 15

ADSTB1 15
-ADSTB1 15

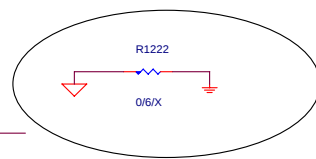
ADSTB1 15
-ADSTB1 15

ADSTB1 15
-ADSTB1 15

ADSTB1 15
-ADSTB1 15



VGA & COMB DUAL LAYOUT



VGA

DDCA_DATA
DDCA_CLK

RED#
GREEN#
BLUE#

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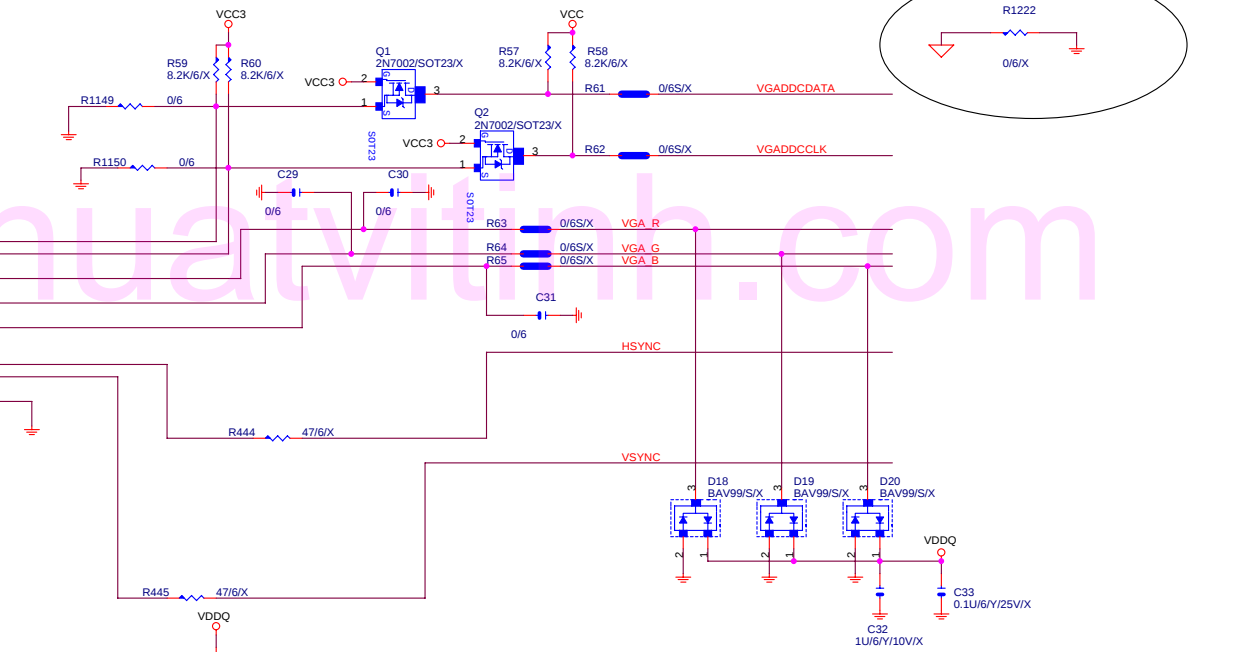
RED#
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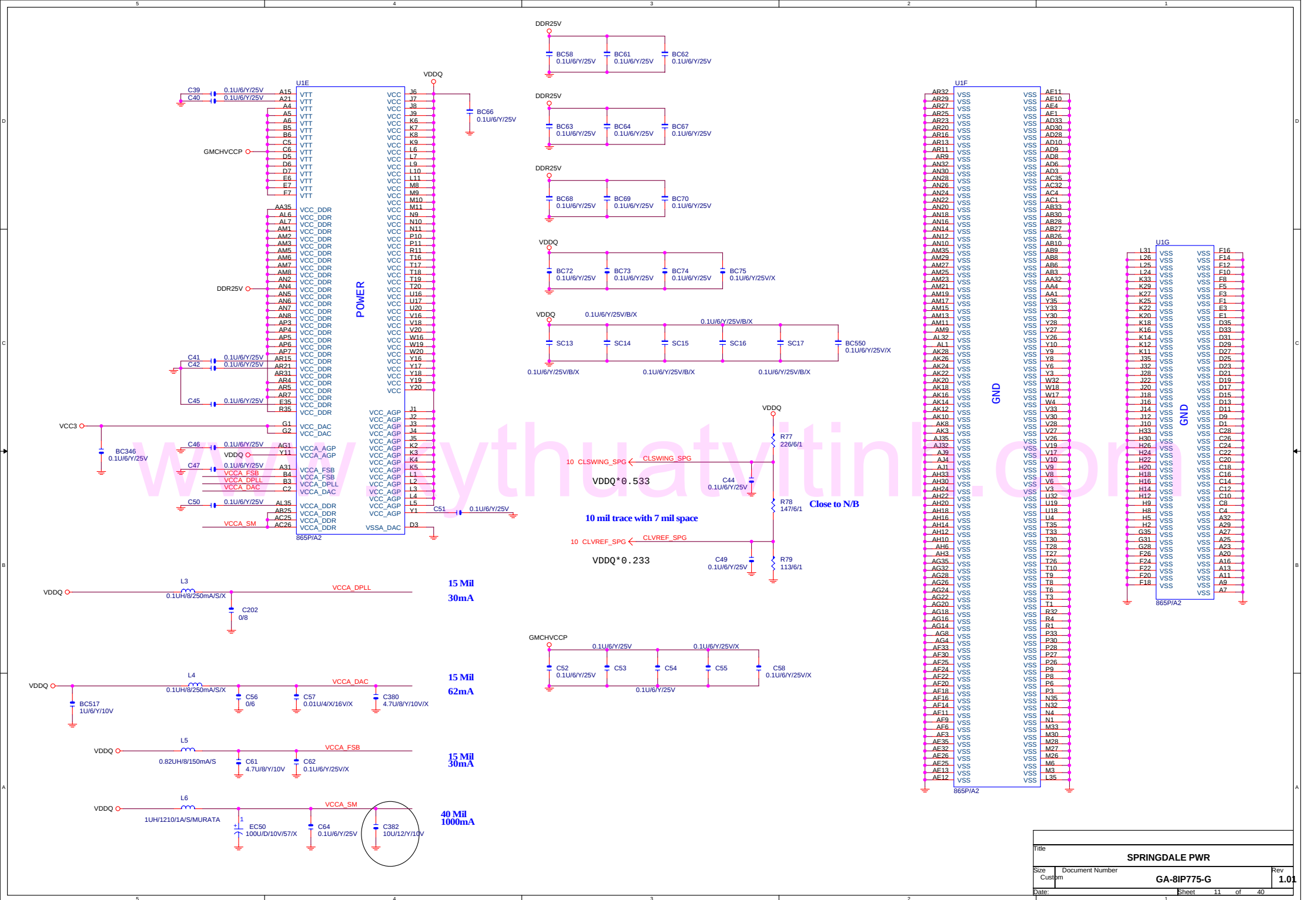
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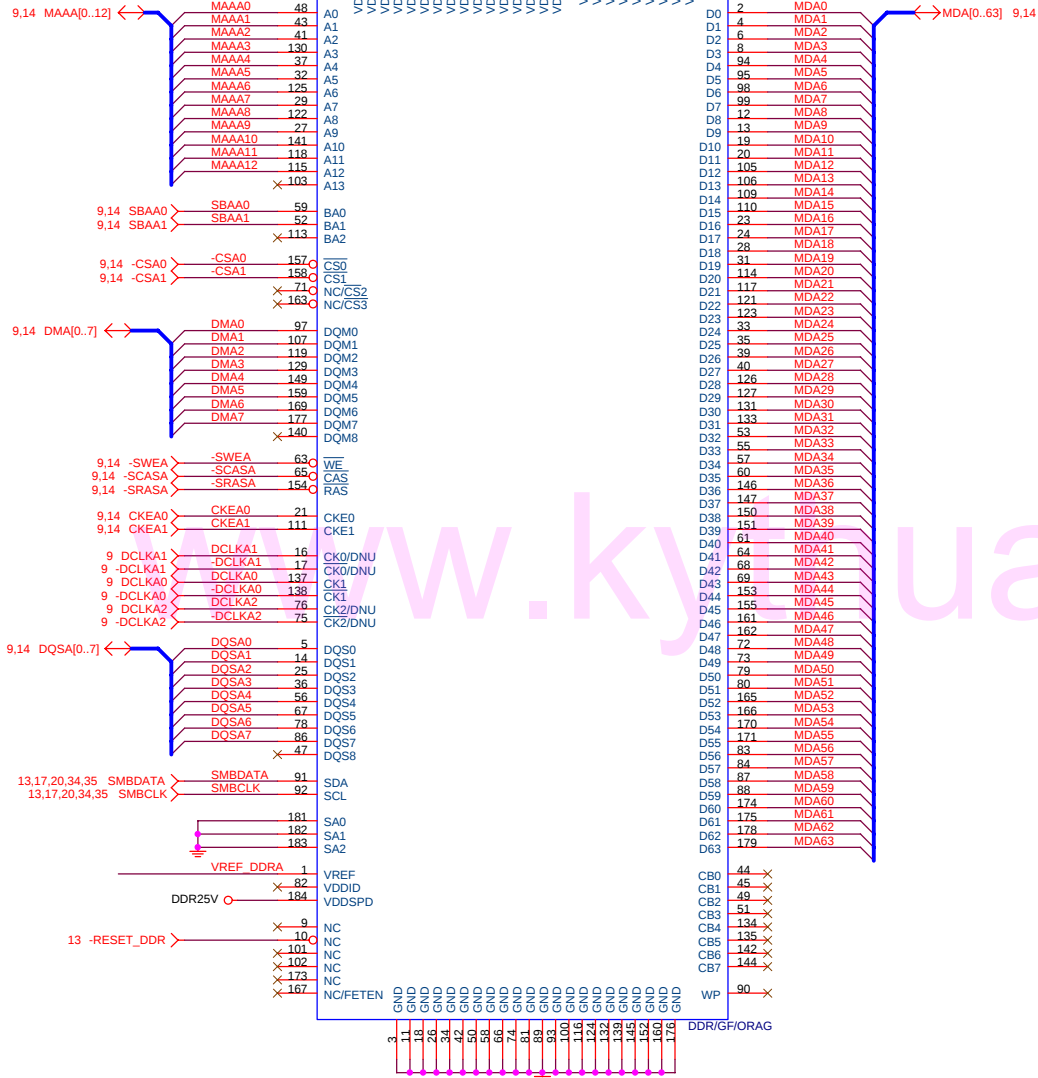
RED#
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BLUE#



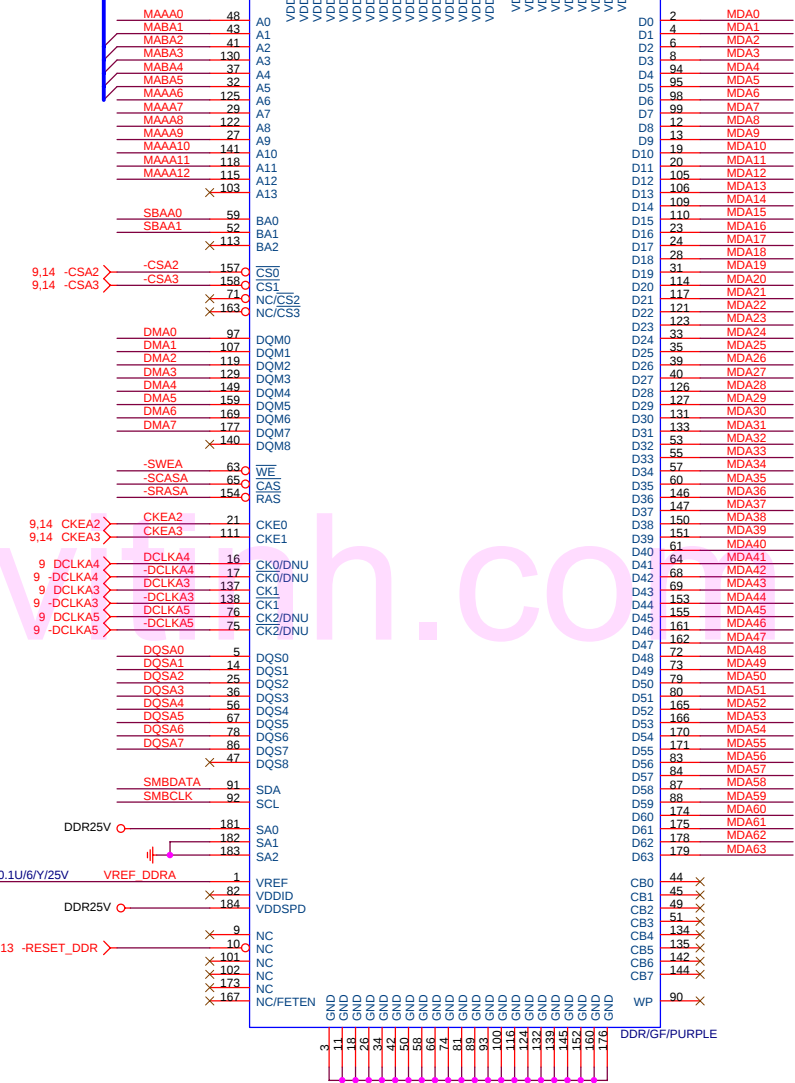
Place mid of bus trace

Title			SPRINGDALE AGP,HUB,CSA,VGA		
Size			Document Number		
Custom			GA-8IP775-G		
Date:			Rev 1.01		
Sheet			10 of 40		

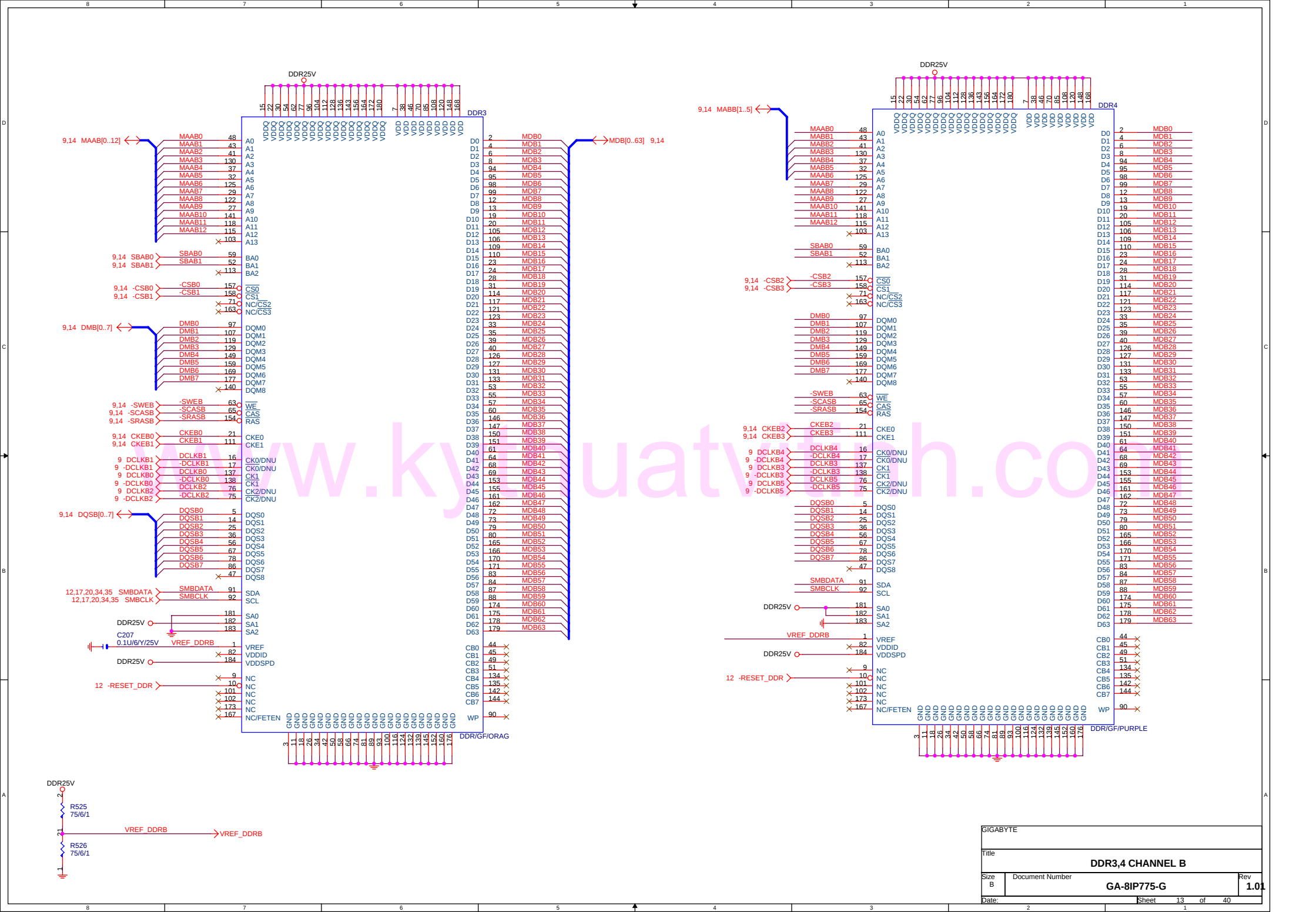




9.14 MABA[1..5] <--



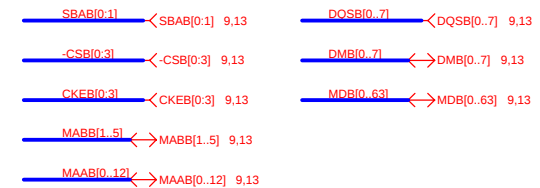
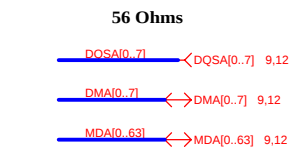
For Register DDR Support



DDR TERMINATION CHANNEL A



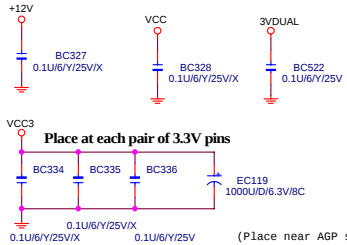
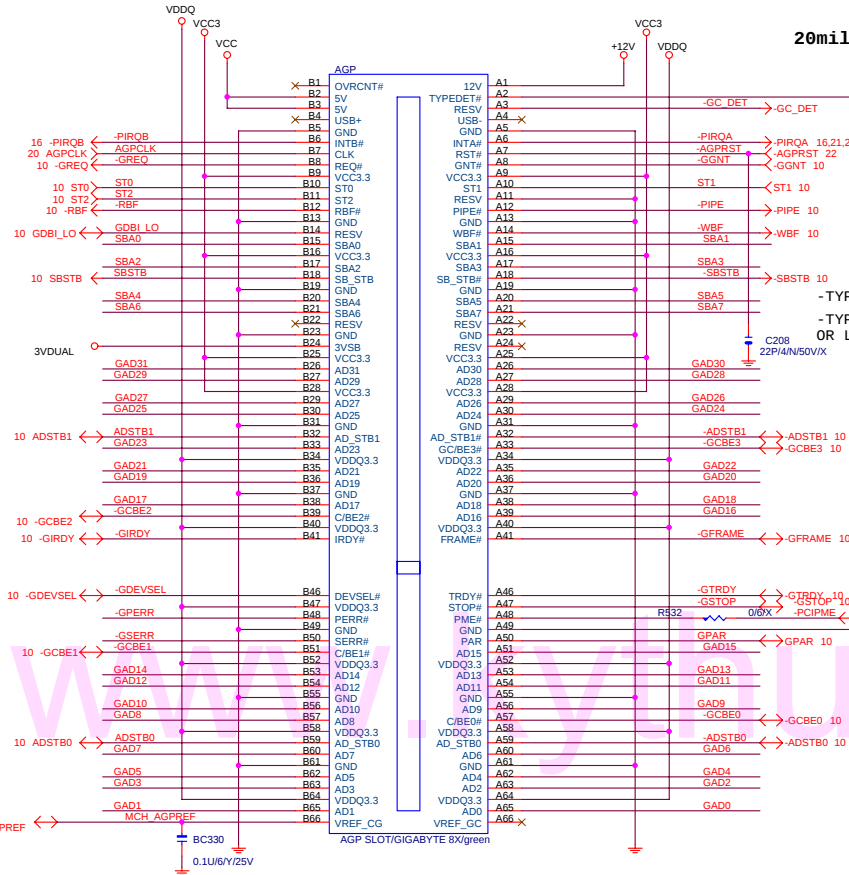
56 Ohms



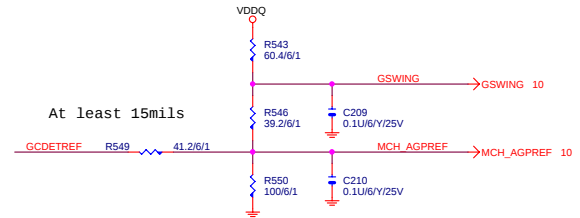
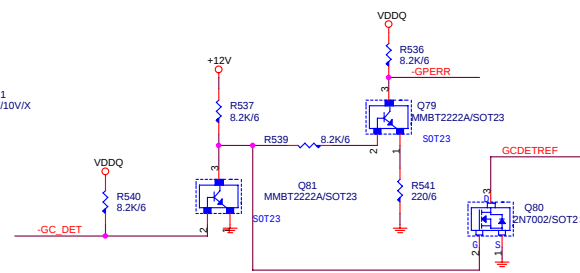
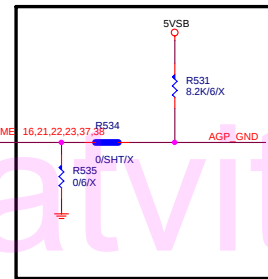
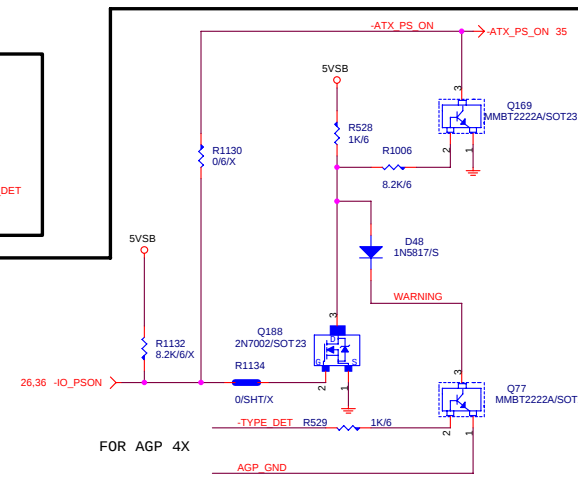
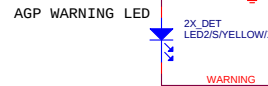
AGP 4X/8X

20mil

10 SBA[0:7] SBA[0:7]
10 GAD[0:31] GAD[0:31]

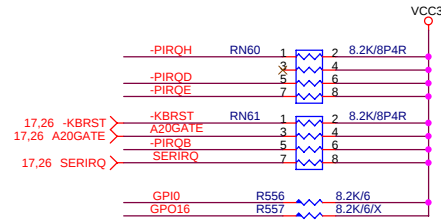


Place 1 at each pair of VDDQ pins
Place an additional for spread from A14 - A33

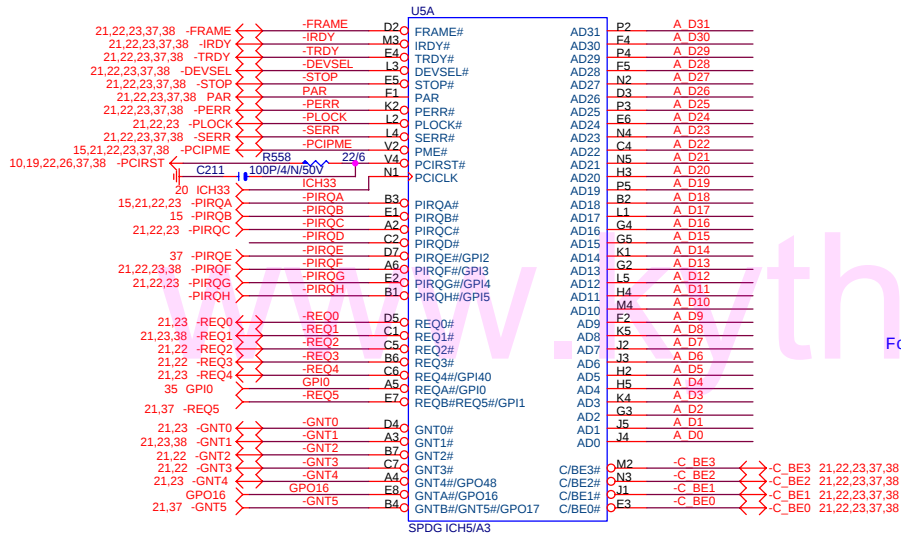


At least 15mils

GIGABYTE			
File			
AGP SLOT			
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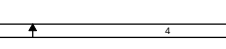
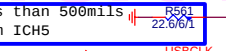


A_D[0..31] ↔ A_D[0..31] 21,22,23,37,38

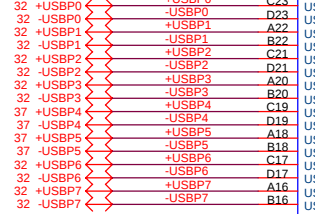


Follow Intel Layout

Less than 500mils
from ICH5



U5B



U5C



U5D



U5E

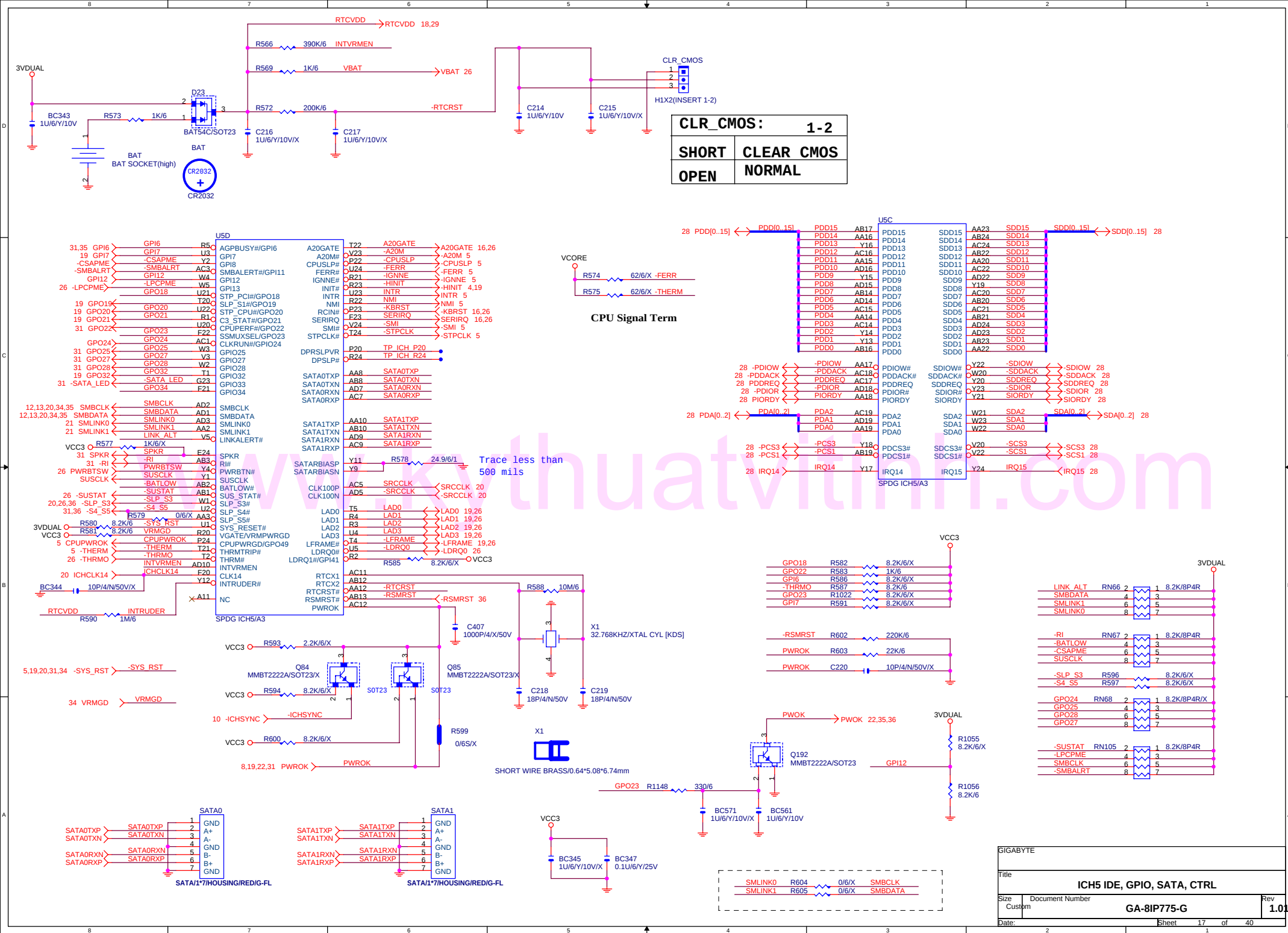


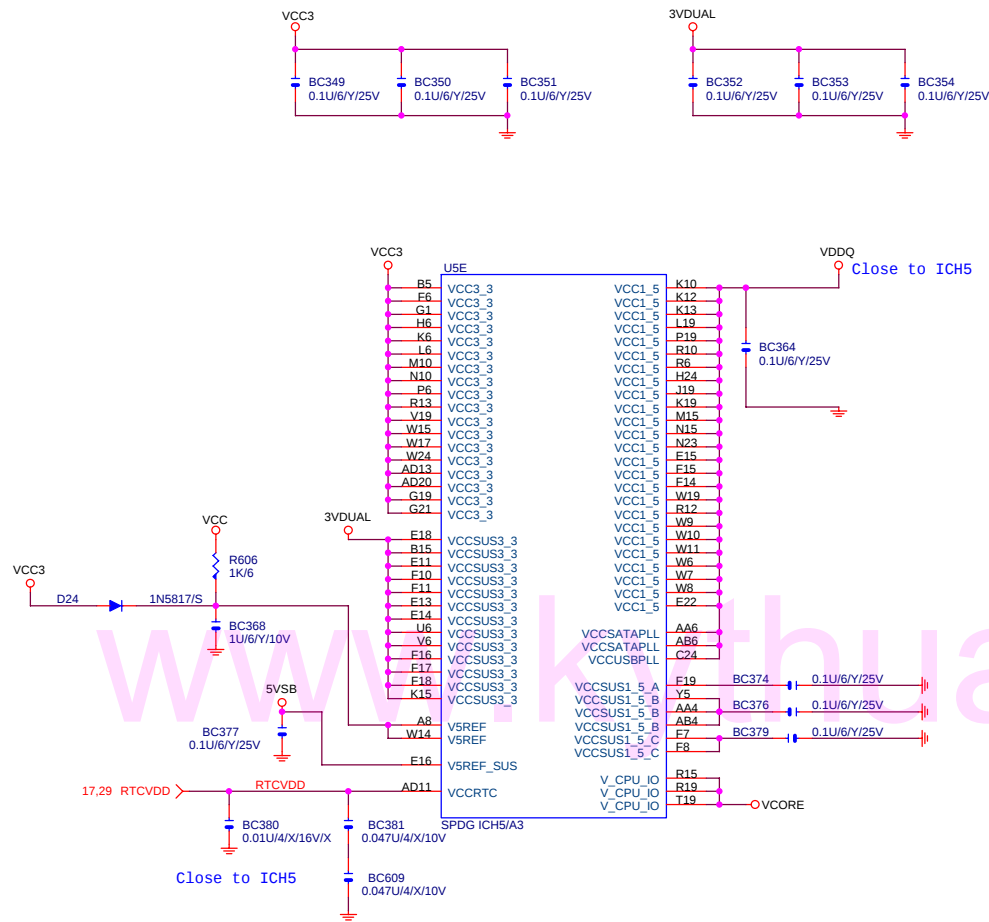
U5F

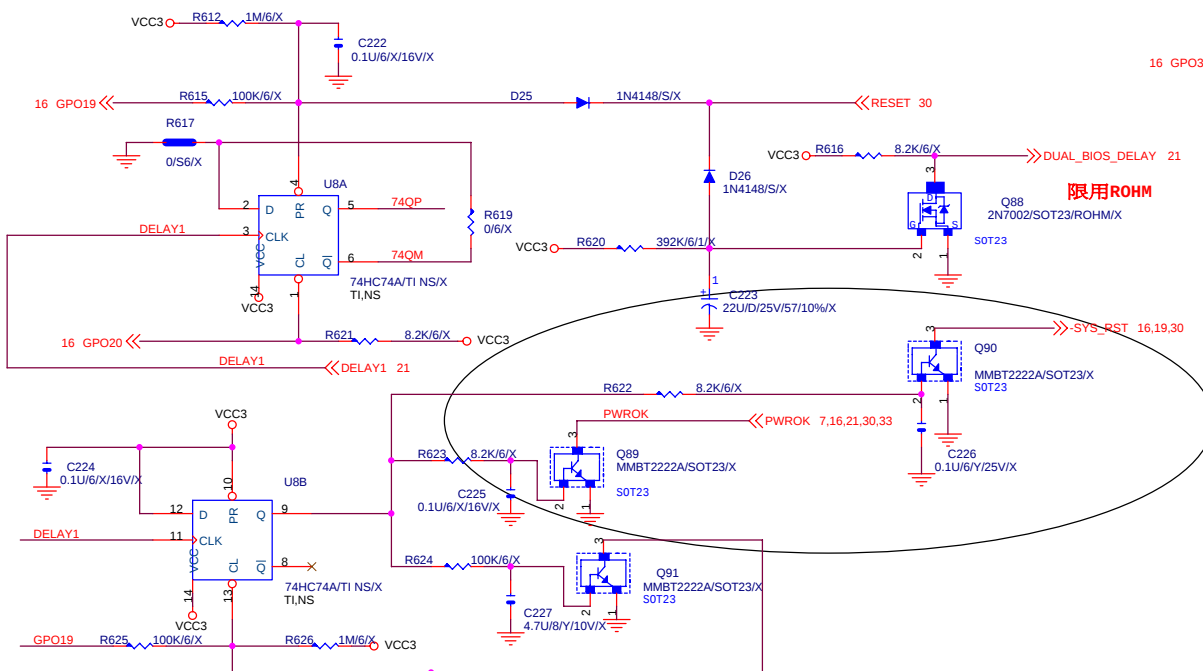


U5G

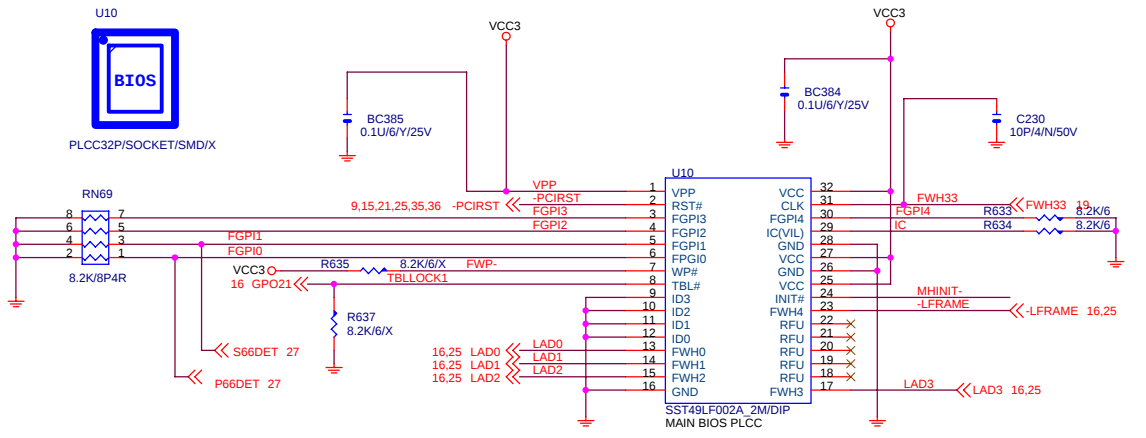
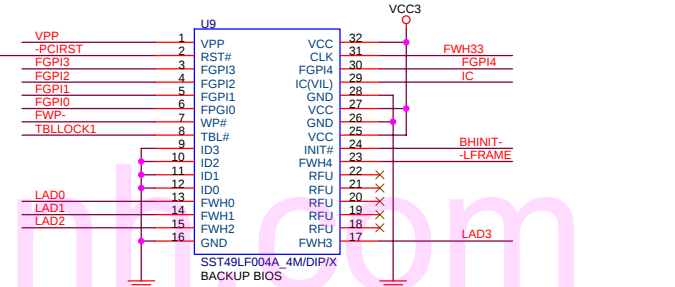
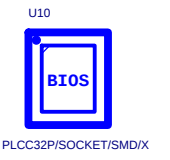
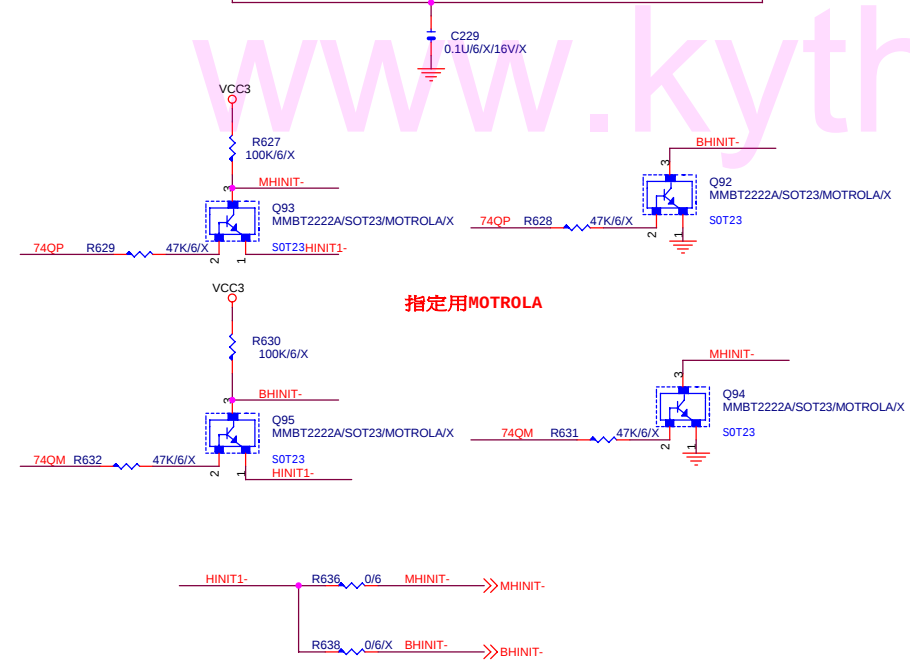
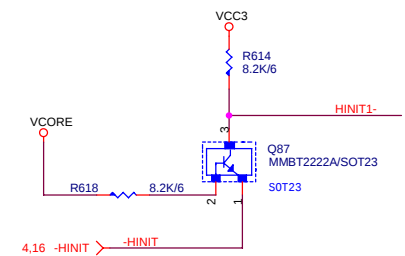






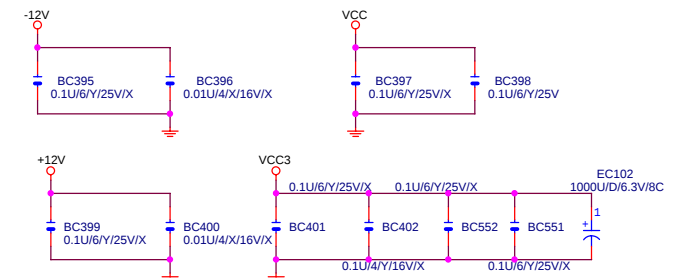
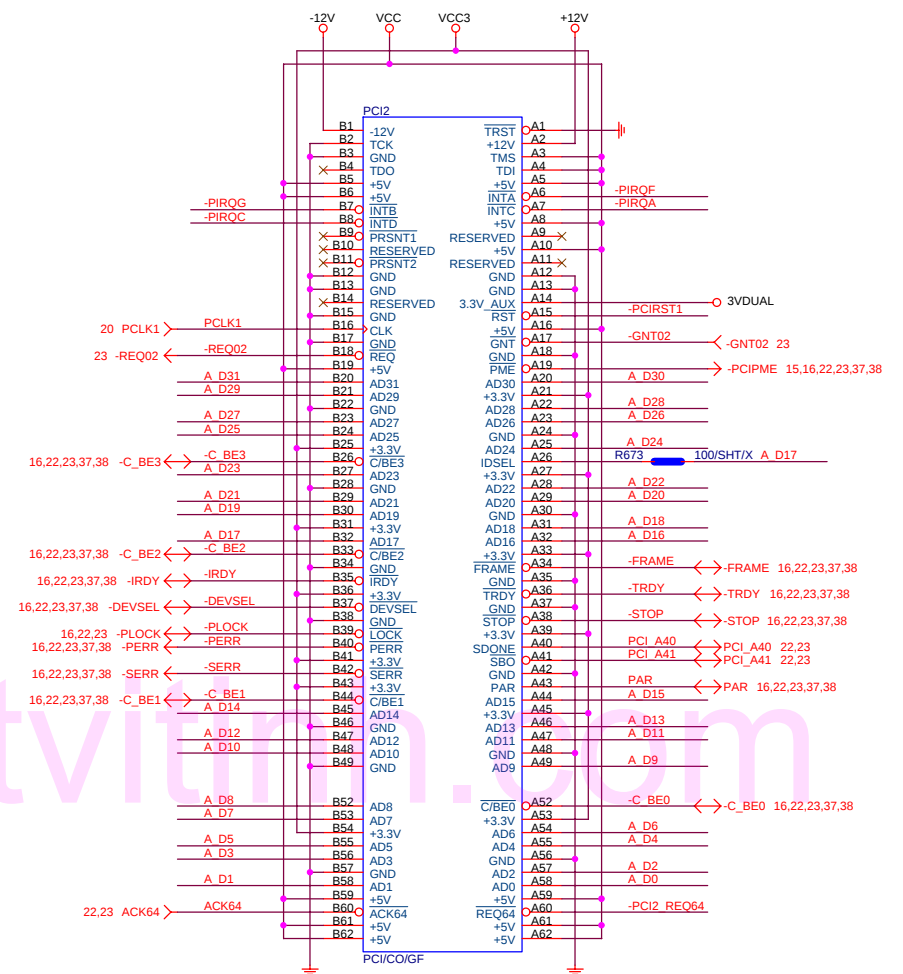


BIOS_WP:	
1-2	WRITE PROTECT
2-3	DISABLE

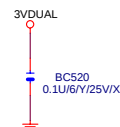
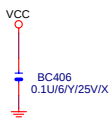
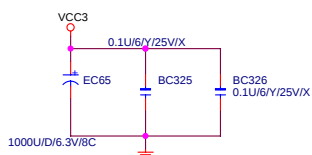
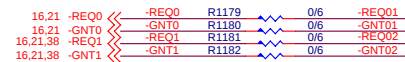
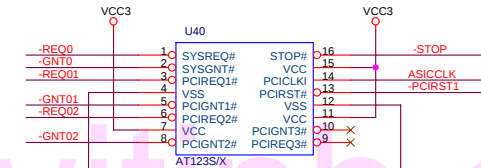
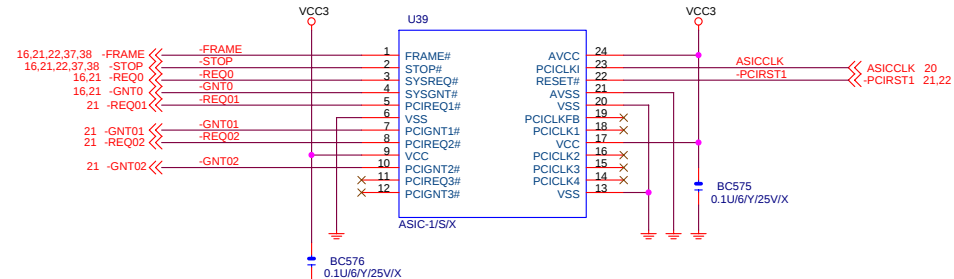
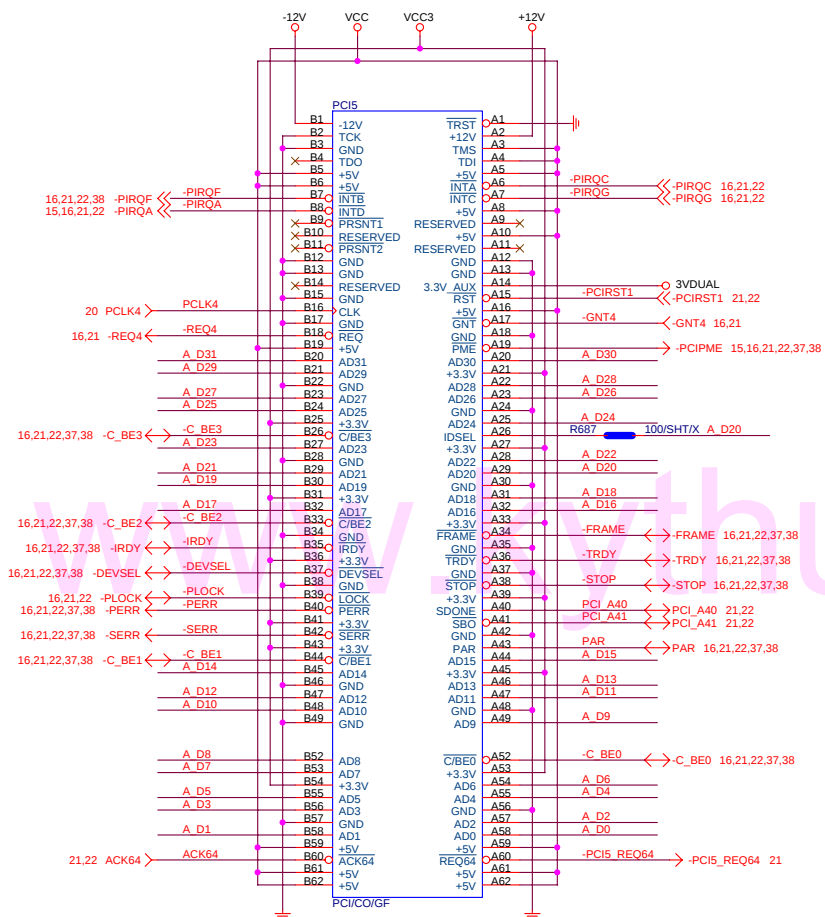


GIGABYTE	
Title	
FWH	
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ADD WINBOUD FWH SEC. SOURCE



16,21,22,37,38 A_D[0..31] << A D[0..31]

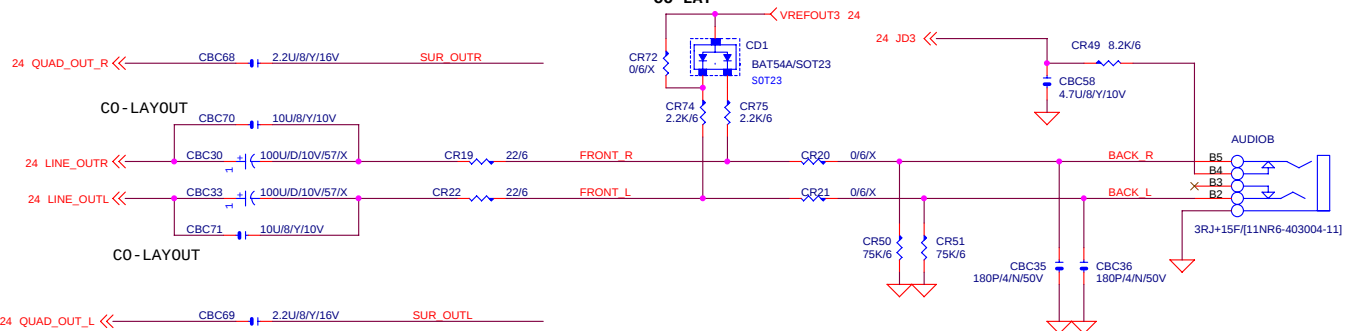


GIGABYTE			
Title			
PCI SLOT 5			
Size	Document Number	Rev	
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LINE OUT

JD0, JD2, GPIO0 爲偵測 DEVICE INPUT 時由 LOW TO HIGH Edge trigger (pop manual)

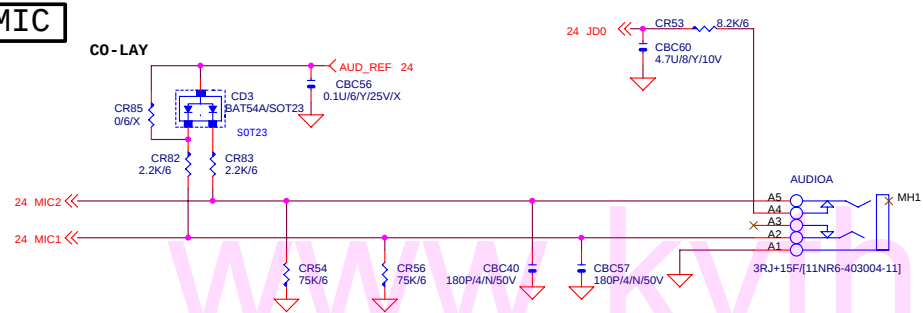
1/2(3.14)RC=1/2(3.14)8.2K*4.7U=4.3HZ 以上 AC 信號全部衰減 TO 0V 不會造成 JD0 誤動作 (無 device 時 play wav)



LINE OUT SENSING
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

MIC

CO-LAY



MICROPHONE IN SENSING (當INPUT) (利用 vref 偏壓 與 CR43, CR32 並聯求出阻抗)
7.1k ohm>R>2.3k ohm==>microphone in
R<2.3k ohm or R>7.1k ohm==>unknown device

MICROPHONE IN SENSING (當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

2x5 header for 850

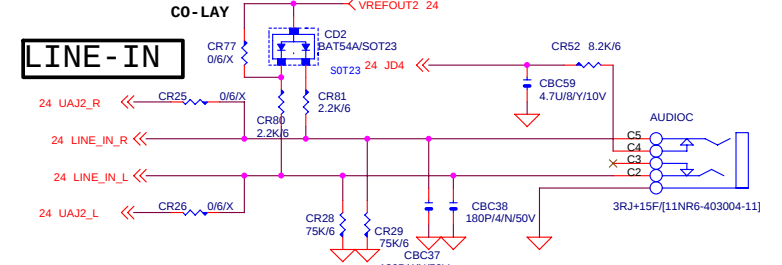
For 850 if JD5 = low AUX-In is configured as input
For 850 if JD5 = high AUX-In is configured as output, Surr-Back out

For 850 AUX-In is shared to Surr-Back out

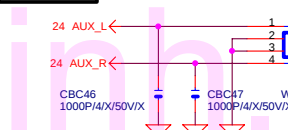
LINE IN SENSING (當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

LINE IN SENSING (當INPUT)
swing of input signal>-40dbv(10mv)==>line in device active
swing of input signal<-40dbv(10mv)==>unknown line in device

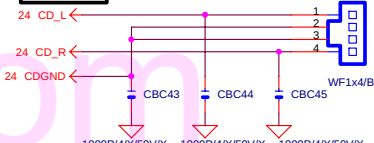
LINE-IN



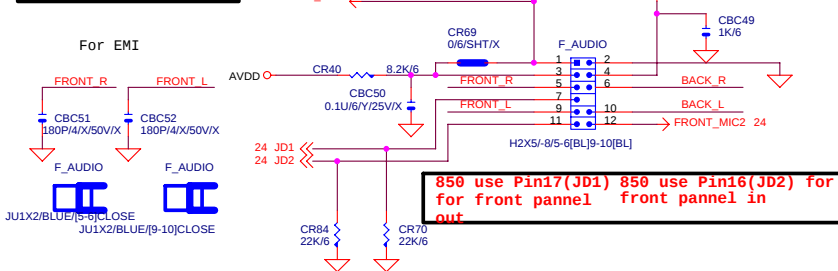
AUX IN DEFAULT NO POP



CD IN

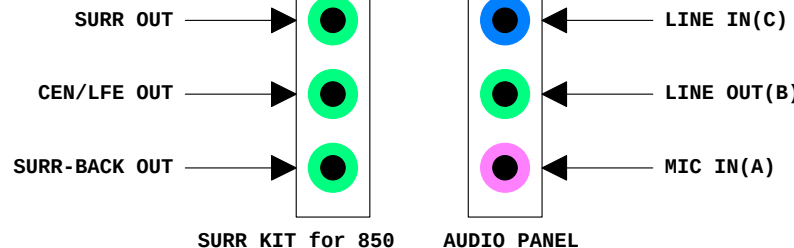
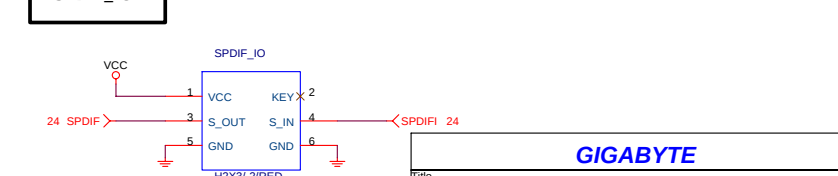


INTEL FRONT AUDIO



850 use Pin17(JD1) 850 use Pin16(JD2) for front panel front panel in out

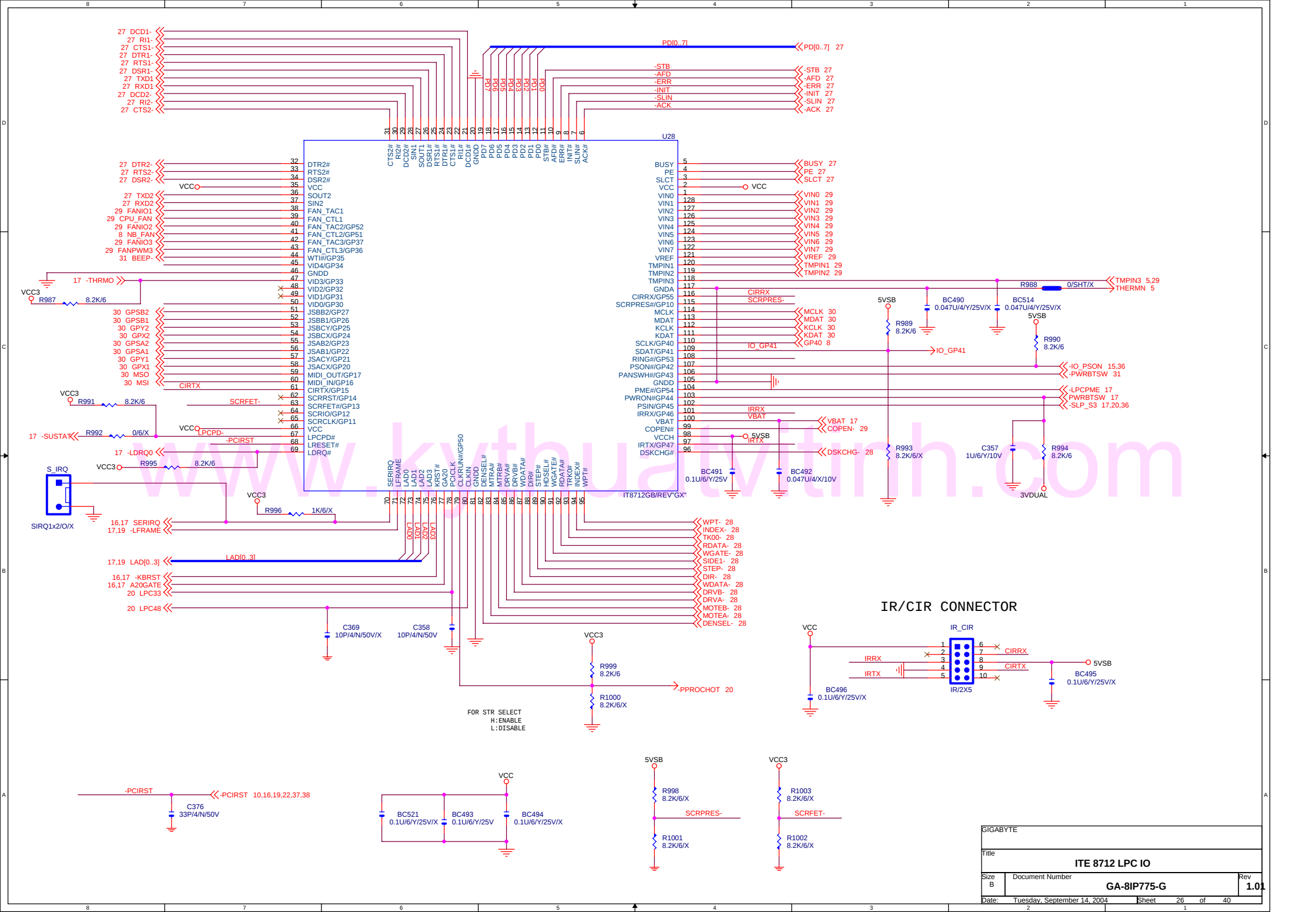
SPDIF_IO

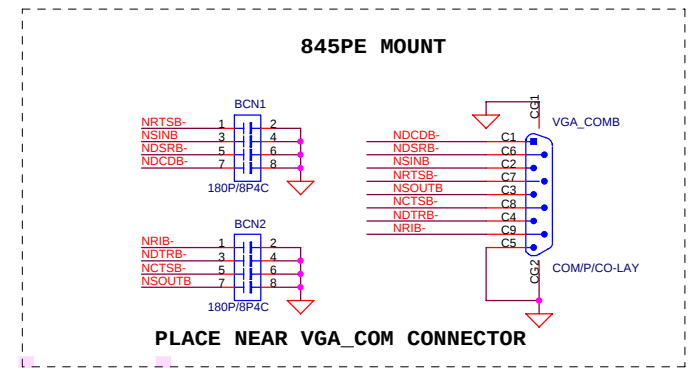
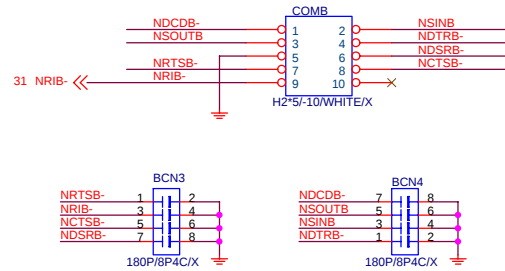
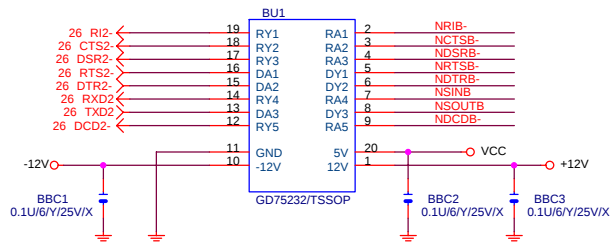
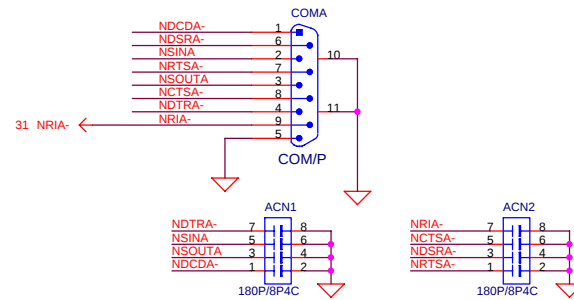
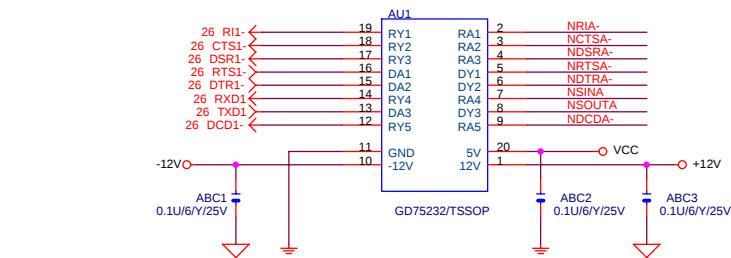


GIGABYTE

AUDIO OUTPUT, GAME PORT

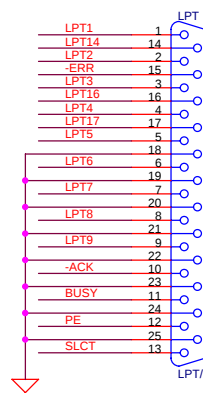
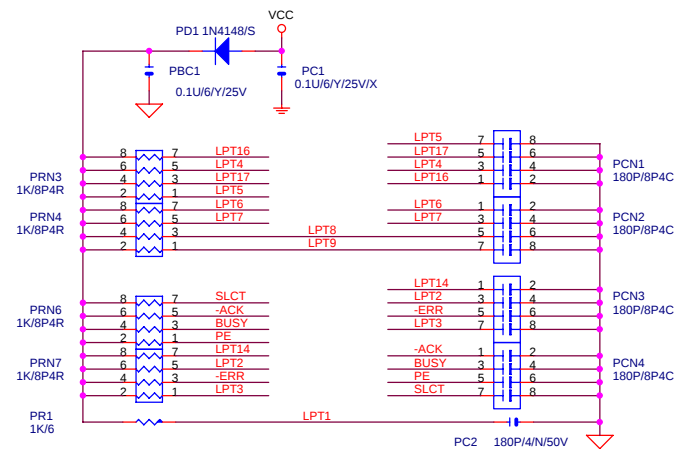
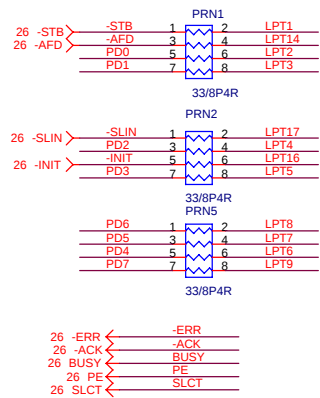
Size Custom GA-8IP775-G Rev 1.01
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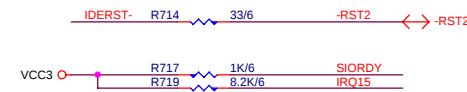
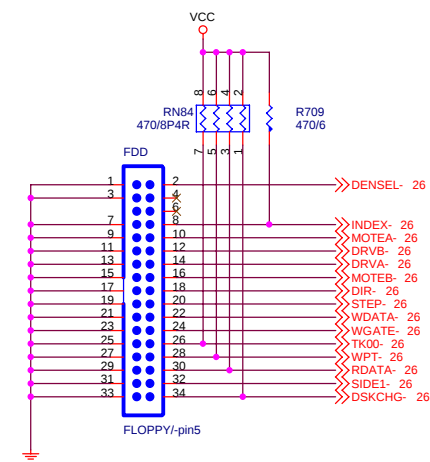




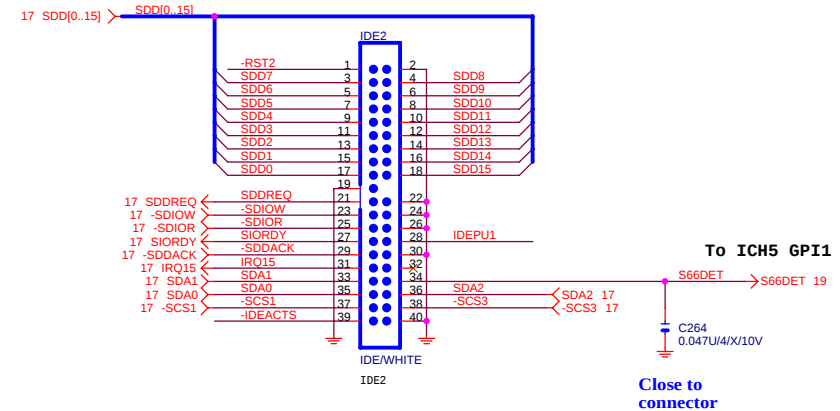
PLACE NEAR VGA_COM CONNECTOR

www.kythuatvithinh.com

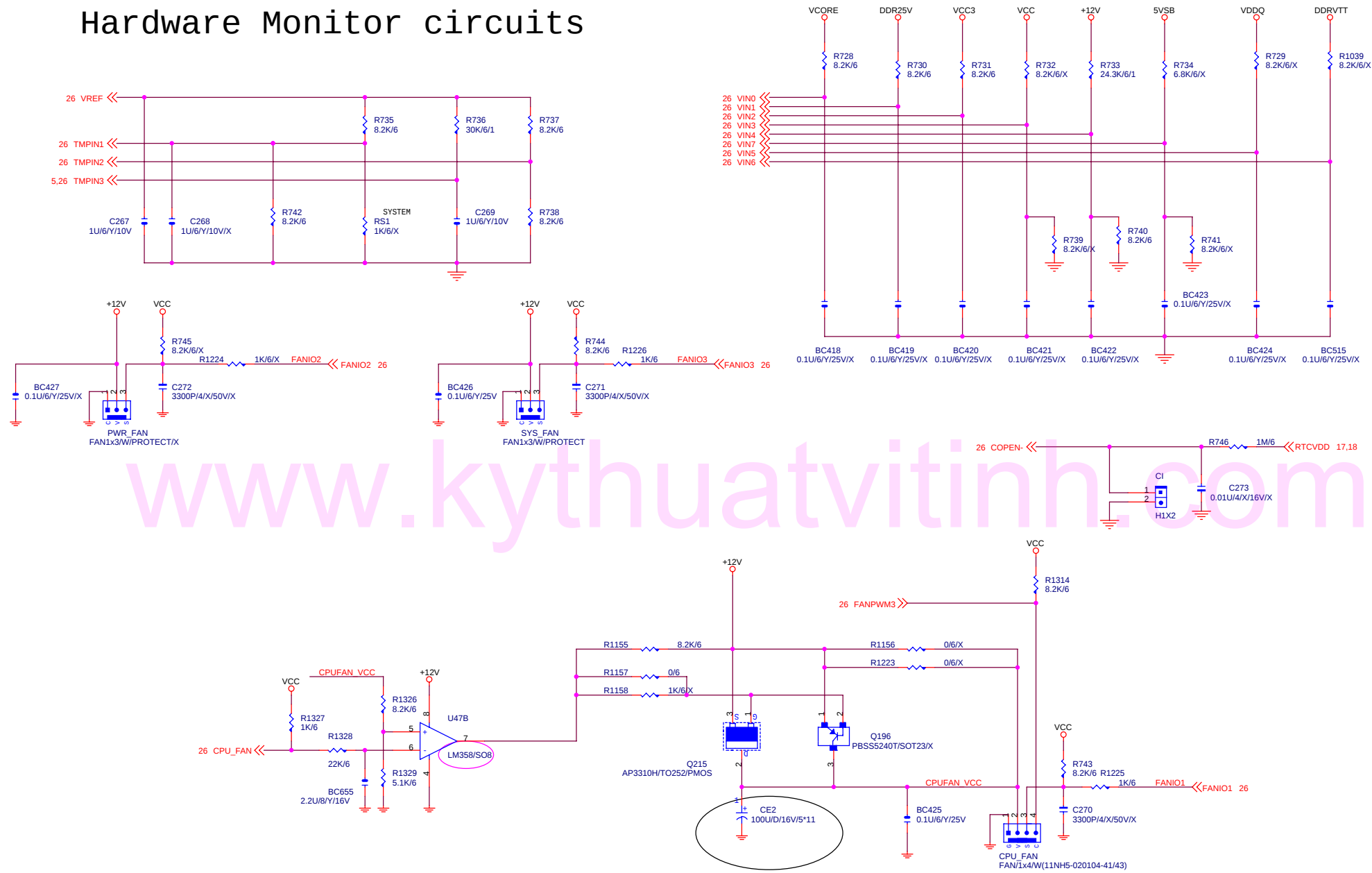




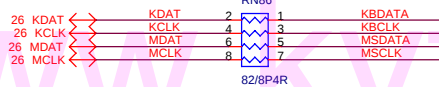
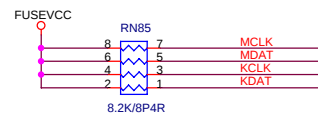
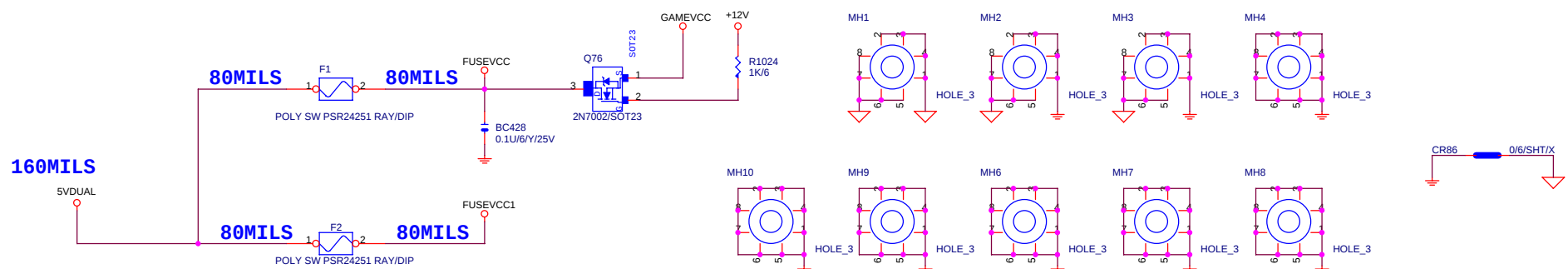
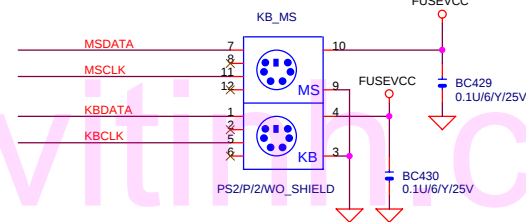
SECONDARY IDE CONNECTOR



Hardware Monitor circuits

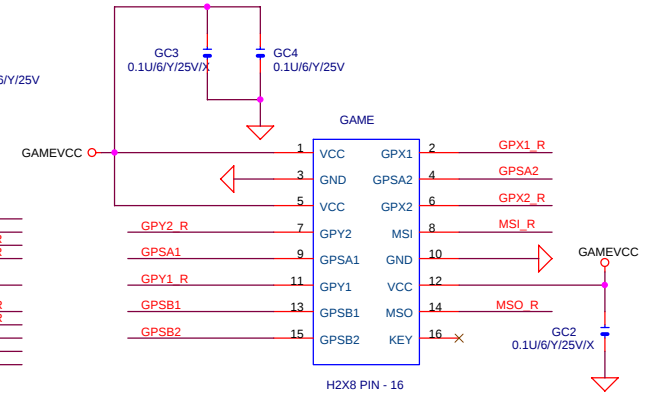
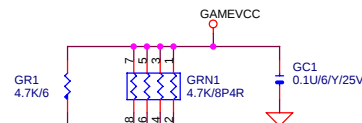
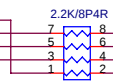
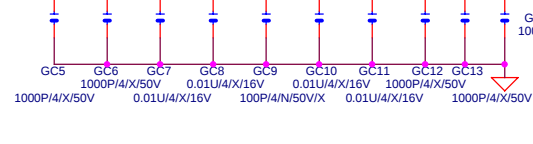


160MILS

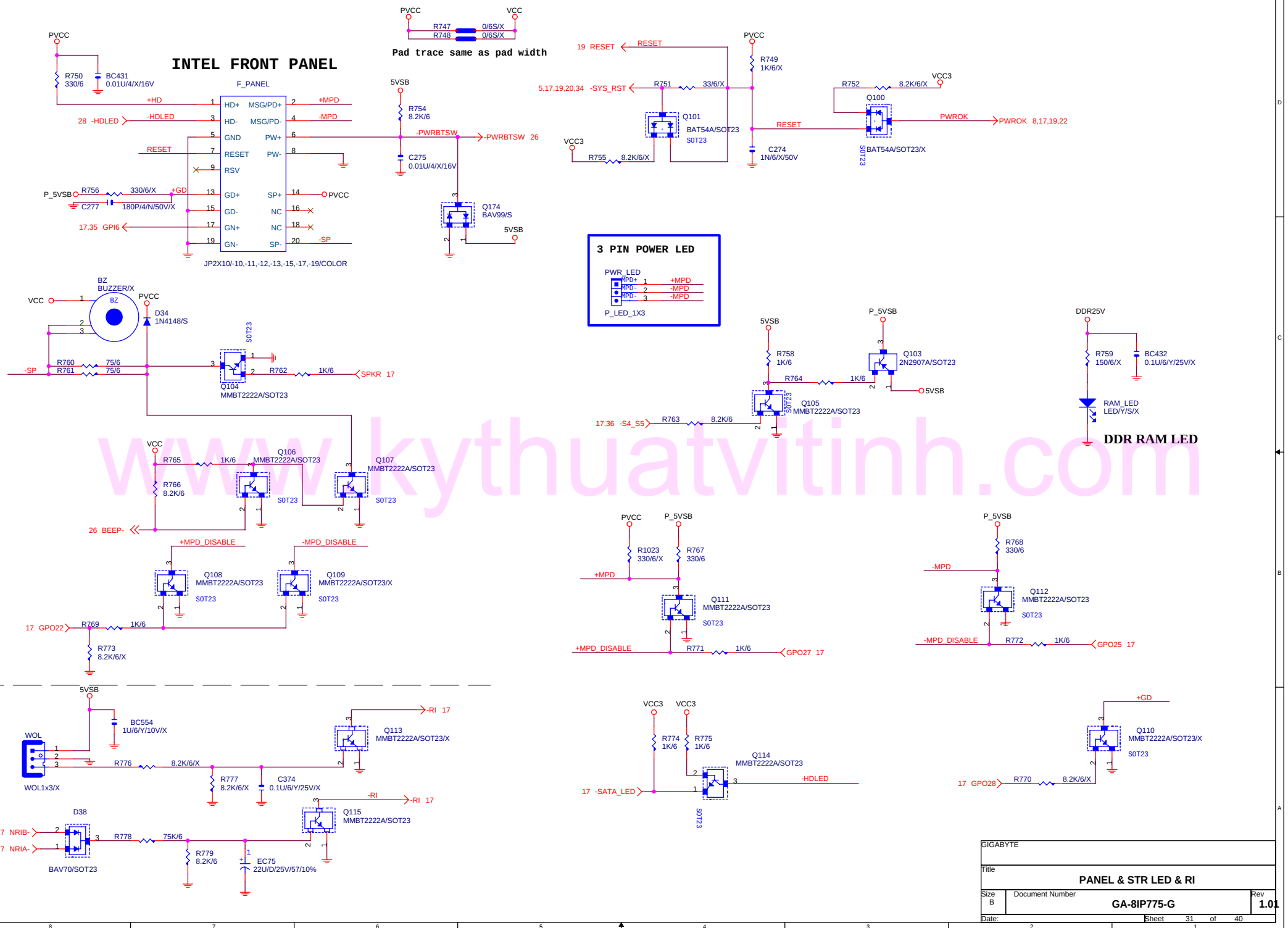
CN31
180P/8P4C

GAME_PORT

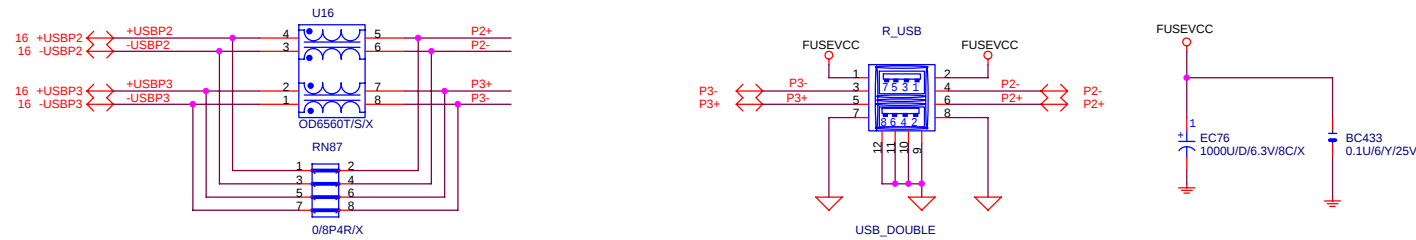
26 GPSA1 ← GPSA1
26 GPSB1 ← GPSB1
26 GPX1 ↔ GPX1
26 GPX2 ↔ GPX2
26 MSO ← MSO
26 GPY2 ↔ GPY2
26 GPY1 ↔ GPY1
26 GPSB2 ← GPSB2
26 GPSA2 ← GPSA2
26 MSI ← MSI



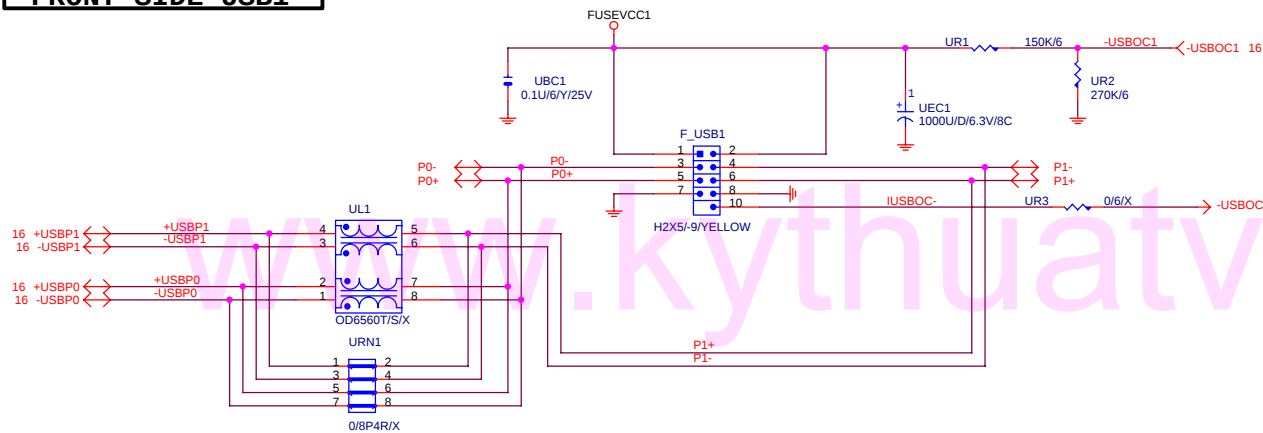
GIGABYTE			
Title			
KB & PS2 MOUSE & IR			
Size	Document Number		Rev
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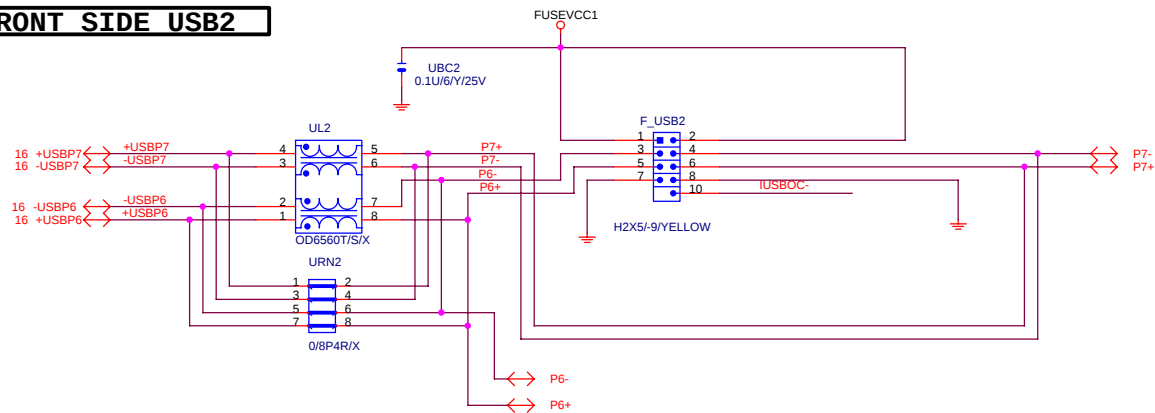
REAR USB



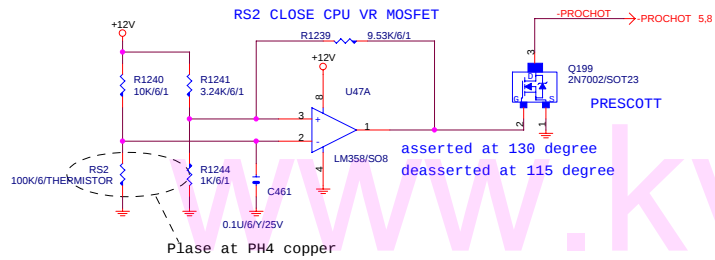
FRONT SIDE USB1



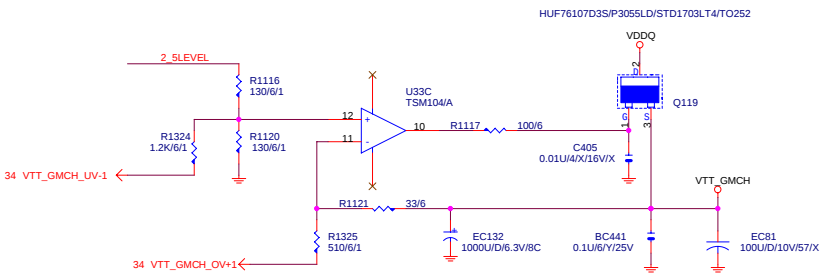
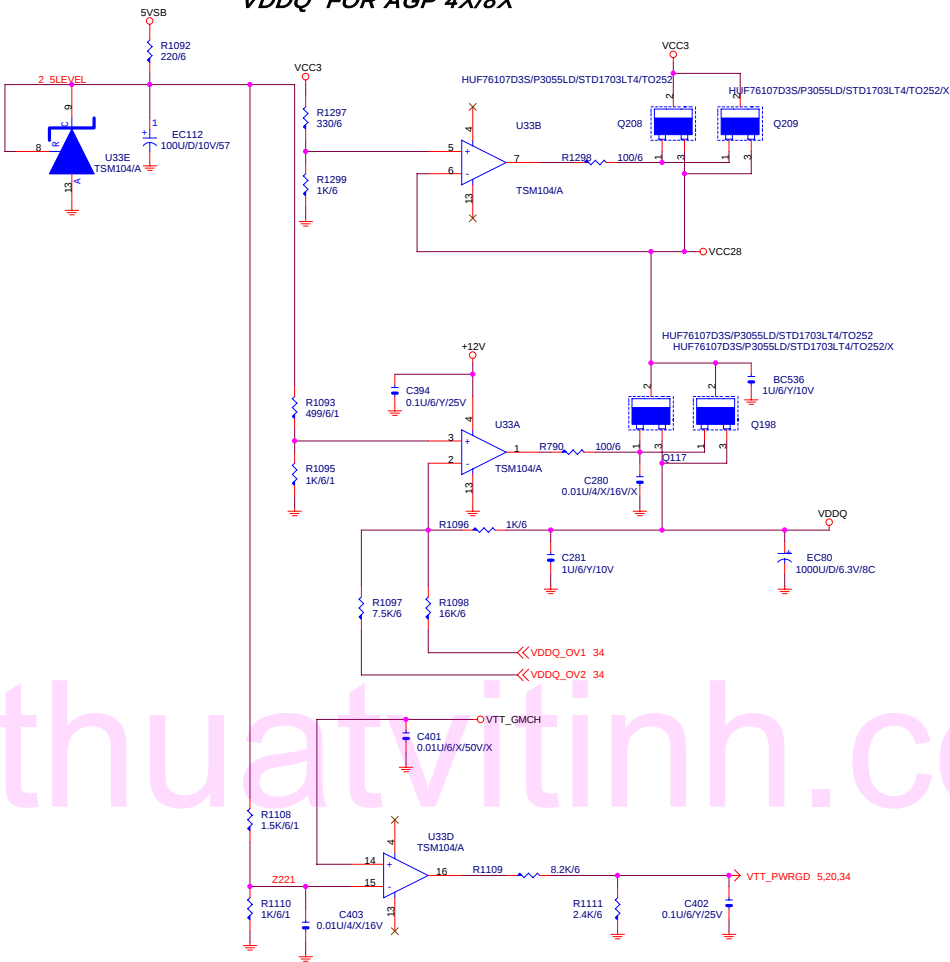
FRONT SIDE USB2



DDR25V FOR DDR DIMM & NB

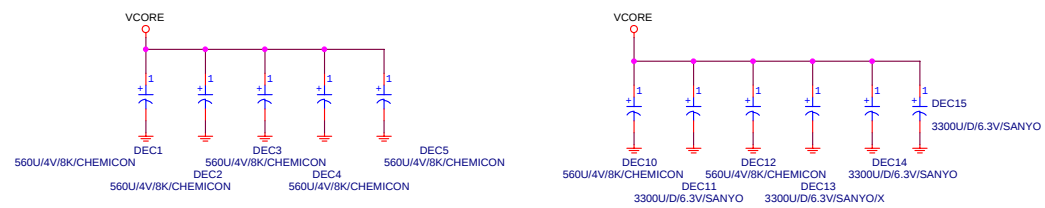


VDDQ FOR AGP 4X/8X



VTT_GMCH=1.4V~1.0V

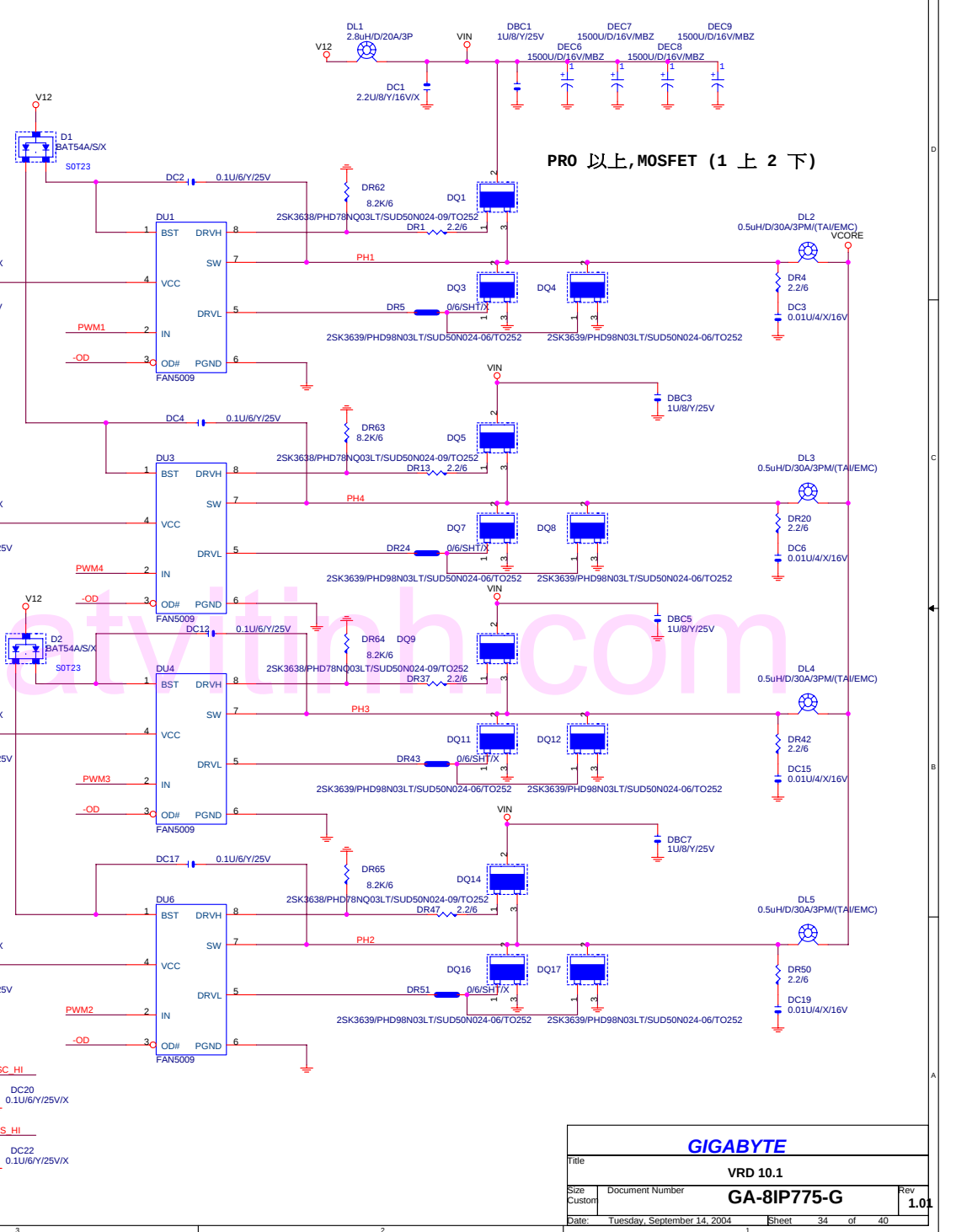
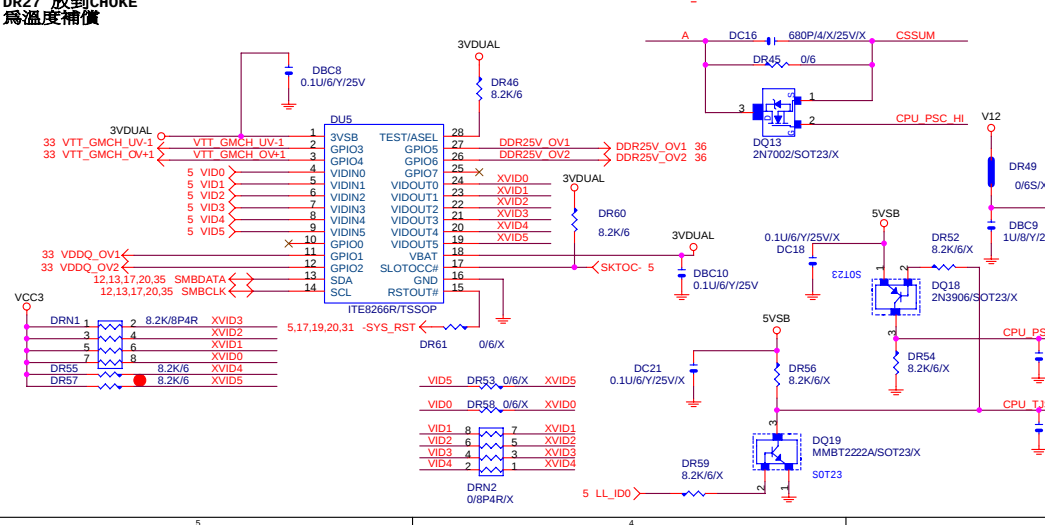
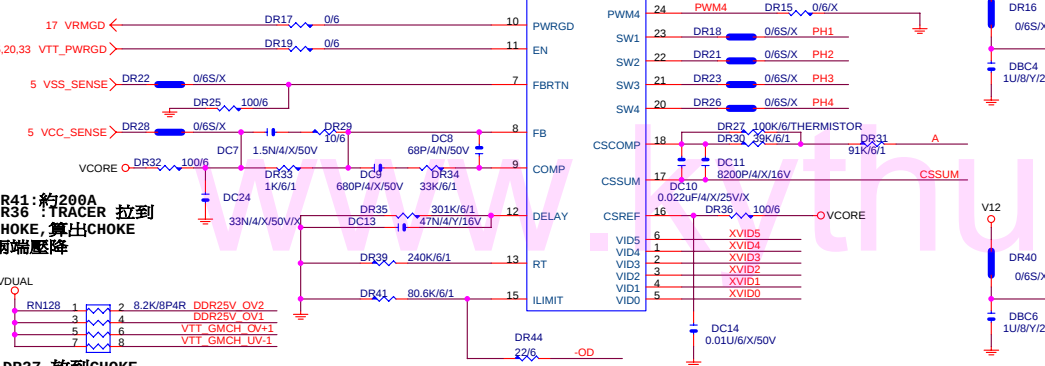
Title		
DDR/VDDQ/VCCVID/SVDUAL POWER		
Size	Document Number	Rev
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DD1 :在HIGH SIDE 發生S-D SHORT 時(0VP), LOW SIDE MOSFET TURN ON ,讓V12 降壓,同時V12 也會下降6-7V以下時,造成PWM 無法提供POWER TO LOW SIDE TURN ON,所以加DD1 延長PWM POWER 提供給LOW SIDE MOSFET TURN ON 保護CPU VOLTAGE 過高.

DR9,DC5 FOR V12 OVER 19V 以上,SPEC (18V)

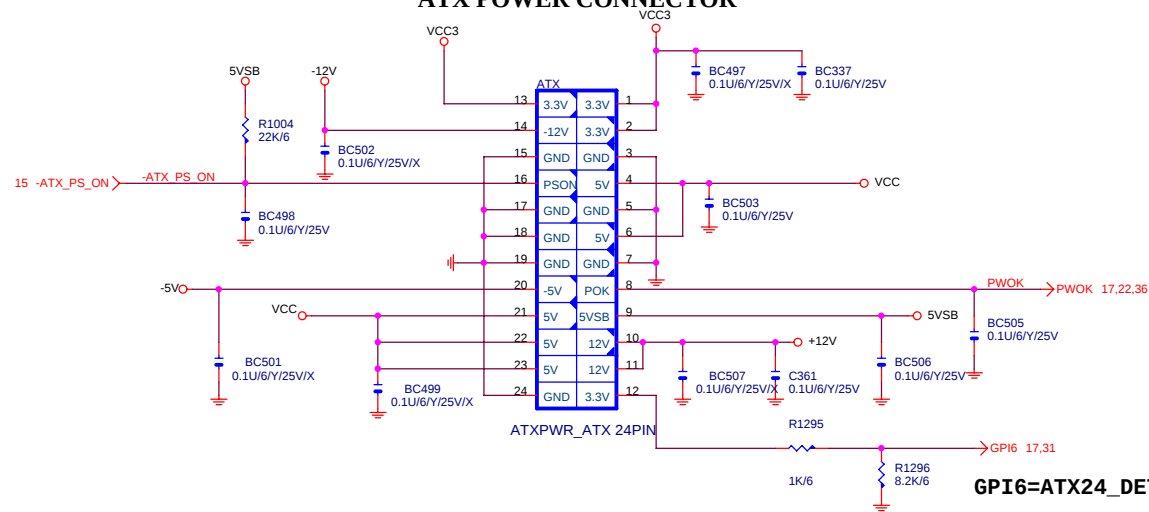
DR12 為一個 CYCLE的DUTY(50%)
DELAY :讓限流時間DELAY (DR35,DC13) 才啟動.
DR39:240K
工作頻率=720KHZ / 4PHASE=180K(1 PHASE)



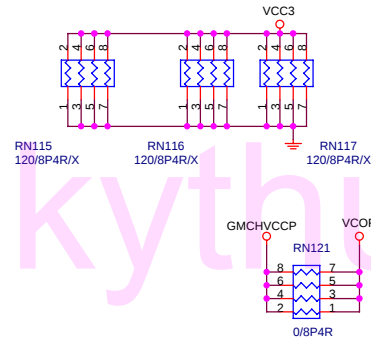
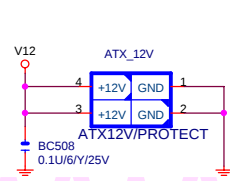
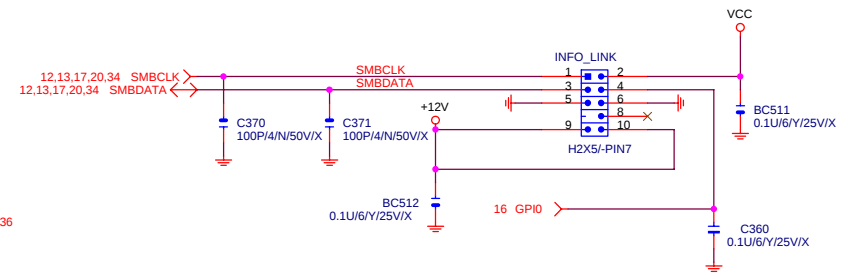
PRO 以上,MOSFET (1 上 2 下)

GIGABYTE			
VRD 10.1			
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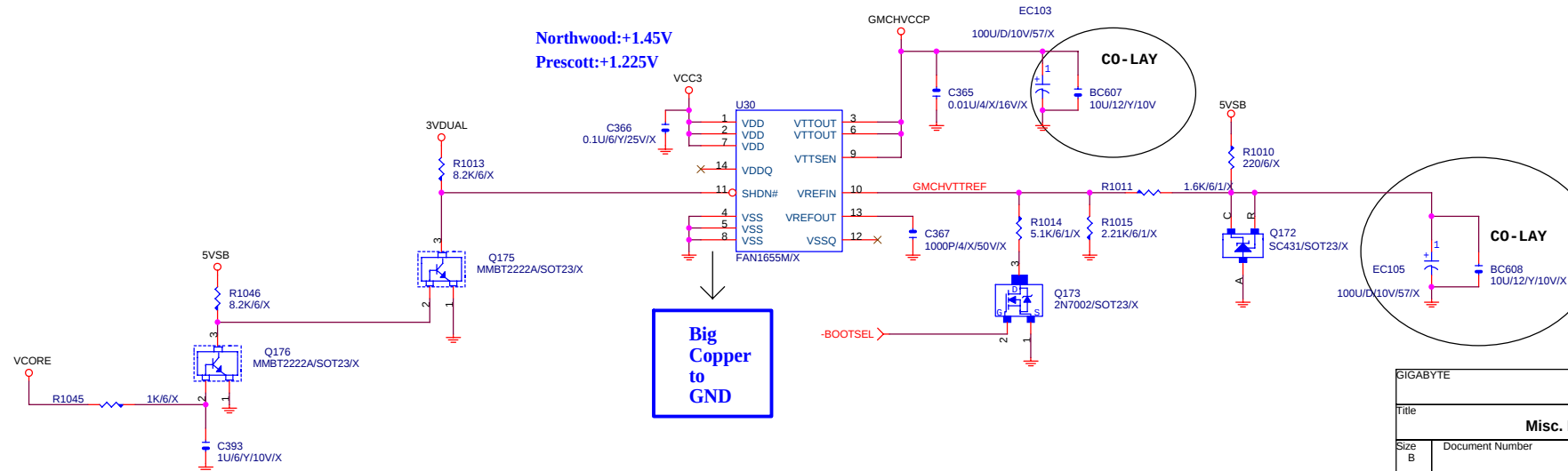
ATX POWER CONNECTOR



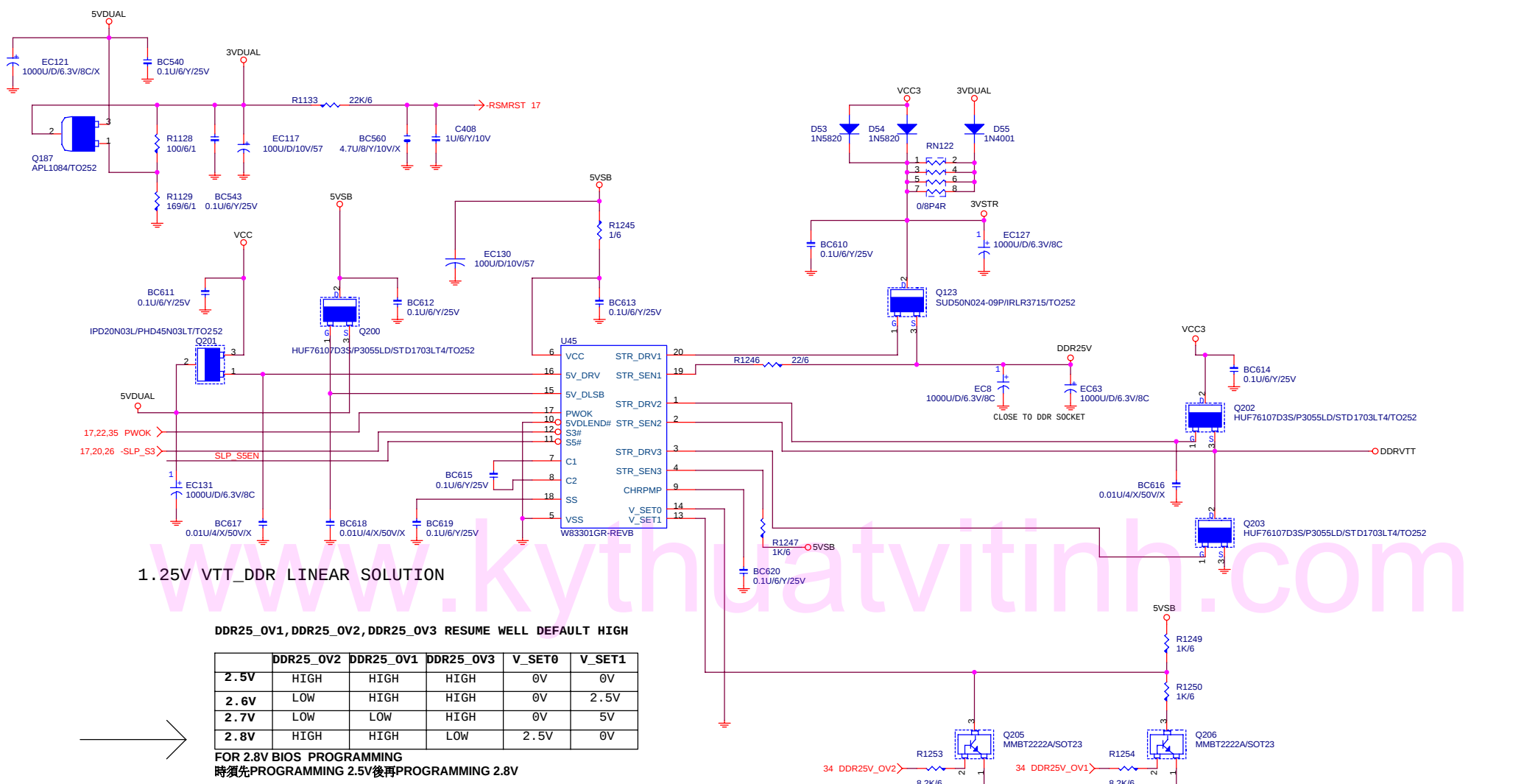
SMBUS CONN.



Northwood:+1.45V
Prescott:+1.225V



GIGABYTE			
Title			
Misc. PWR & ATX CONN.			
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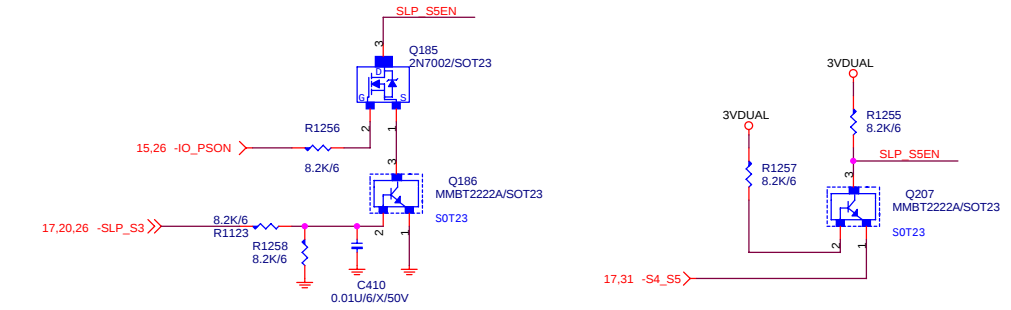


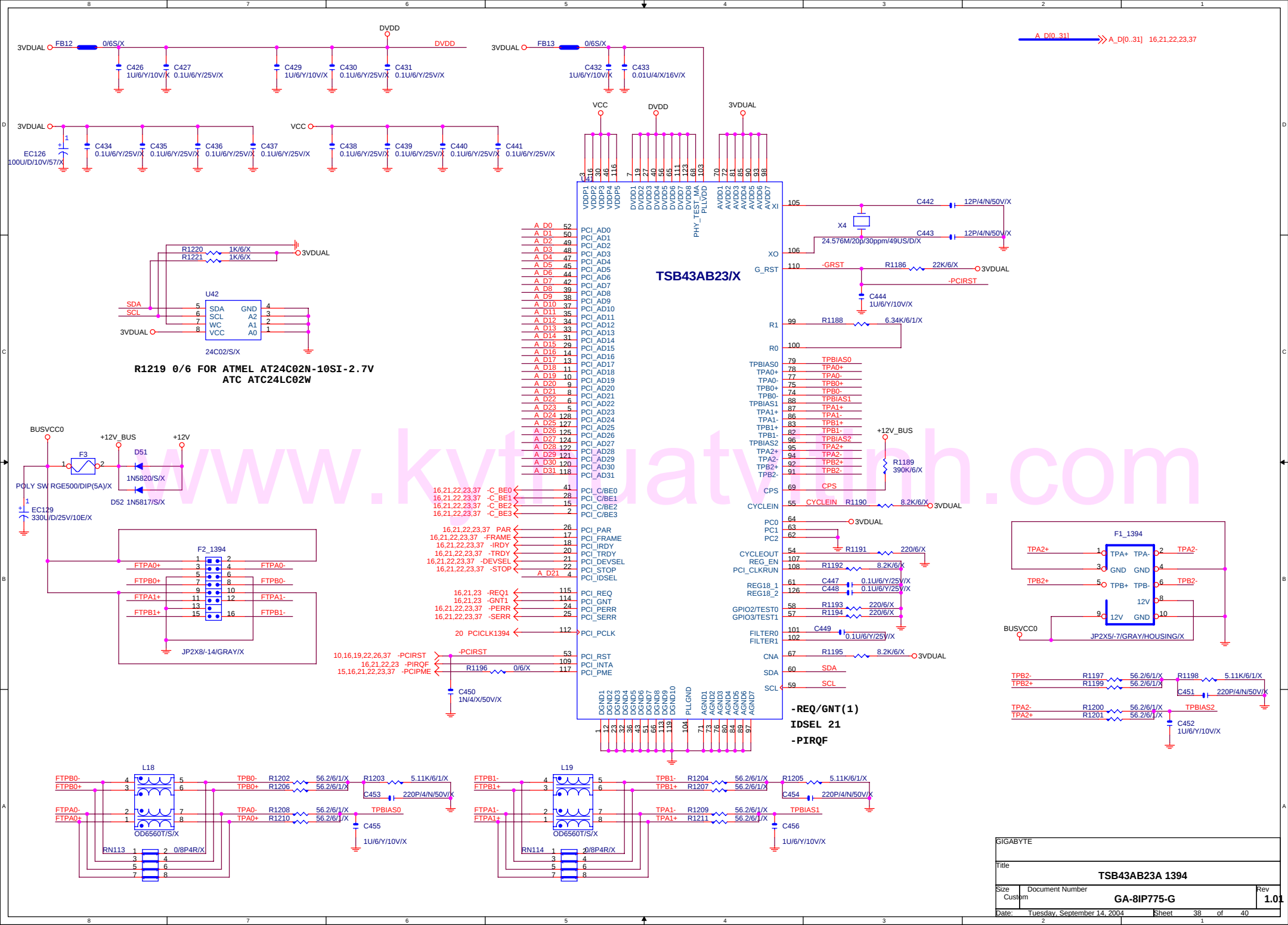
1.25V VTT_DDR LINEAR SOLUTION

DDR25_OV1,DDR25_OV2,DDR25_OV3 RESUME WELL DEFAULT HIGH

	DDR25_OV2	DDR25_OV1	DDR25_OV3	V_SET0	V_SET1
2.5V	HIGH	HIGH	HIGH	0V	0V
2.6V	LOW	HIGH	HIGH	0V	2.5V
2.7V	LOW	LOW	HIGH	0V	5V
2.8V	HIGH	HIGH	LOW	2.5V	0V

FOR 2.8V BIOS PROGRAMMING
時須先PROGRAMMING 2.5V後再PROGRAMMING 2.8V





GIGABYTE GA-8IP775-G PCI ROUNTING LIST

PCI DEVICE	IDSEL	INT	CLOCK	REQ	GNT	
PCI SLOT1	16	C, F, G, A	PCLK0	-REQ01	-GNT01	
PCI SLOT2	17	F, G, A, C	PCLK1	-REQ02	-GNT02	
PCI SLOT3	18	G, A, C, F	PCLK2	-REQ2	-GNT2	
PCI SLOT4	19	A, C, F, G	PCLK3	-REQ3	-GNT3	
PCI SLOT5	20	C, F, G, A	PCLK4	-REQ4	-GNT4	
TI 1394	21	F	PCICLK1394	-REQ1	-GNT1	
LAN (Marvell)	25	E	LANCLK33	-REQ5 (REQB#)	-GNT5 (GNTB#)	

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GIGABYTE GA-8IP775-G GPIO LIST

SHEET TITLE

GPIP	I/O	FUNCTION
GPI0/REQA-	I	PULL HIGH 8.2K to VCC3, SMB connector.
GPI1/REQ5-		PULL HIGH 8.2K to VCC, REQ5-.
GPI2/PIRQE-		PULL HIGH 8.2K to VCC3, PIRQE-.
GPI3/PIRQF-		PULL HIGH 8.2K to VCC3, PIRQF-.
GPI4/PIRQG-		PULL HIGH 8.2K to VCC, PIRQG-.
GPI5/PIRQH-	NA	PULL HIGH 8.2K to VCC
GPI6/AGPBUSY-	I	PULL 8.2K TO VCC3, PANEL GREEN_BUTTON
GPI7	I	DUAL BIOS FIRST BOOT SELECT.
GPI8	I	PULL 8.2K TO 3VDUAL, -CASPME.
GPI9/OC4-	NA	USB OC4-.
GPI10/OC5-	NA	USB OC5-.
GPI11/-SMBALRT	NA	PULL 8.2K TO 3VDUAL, -SMBALERT.
GPI12	I	PULL 8.2K TO VCC3, M/B REVERSION ID.
GPI13	I	LPC PME.
GPI14/OC6-	NA	USB OC6-.
GPI15/OC7-	NA	USB OC7-.
GP016/GNTA-	NA	GP016.
GP017/GNT5-		GNT5-.
GP018/STP_PCI-	NA	GP018.
GP019/SLP_S1-	0	DUAL BIOS.
GP020/SLP_CPU-	0	DUAL BIOS.
GP021/C3_SATA-	0	BLOCK TOP TABLE.
GP022/CPUPERF-	0	PULL 8.2K TO VCC3, PANEL S3 POWER LED.

SHEET TITLE

GPIP	I/O	FUNCTION
GP023	NA	PULL 8.2K TO VCC3
GP024	0	INTEL LAN ENABLE/DISABLE.
GP025	0	FRONT PANEL -MPD.
GP027	0	FRONT PANEL +MPD.
GP028	0	GREEN LED
GP032	0	BIOS WRITE PROTECT.
GP033	0	SATA LED.
GP034	I	CLEAR PASSWORD.