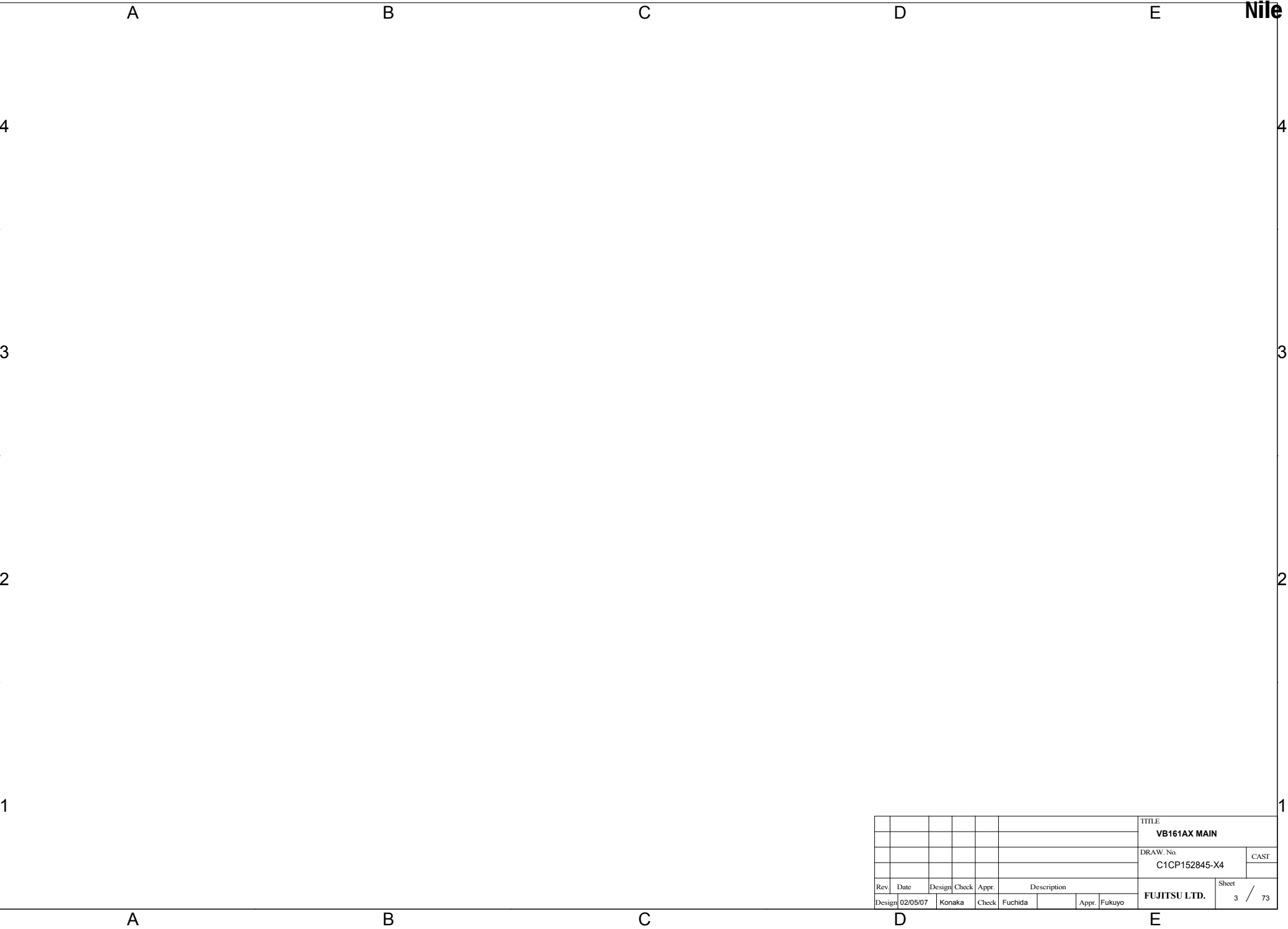


Nile

[illegible][illegible]

Block Diagram

										TITLE	
										VB161AX MAIN	
										DRAW. No.	
										C1CP152845-X4	
										CAST	
Rev.	Date	Design	Check	Appr.	Description						
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	FUJITSU LTD.			Sheet 2 / 73



							TITLE	
							VB161AX MAIN	
							DRAW. No.	
							C1CP152845-X4	CAST
Rev.	Date	Design	Check	Appr.	Description			Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	3 / 73

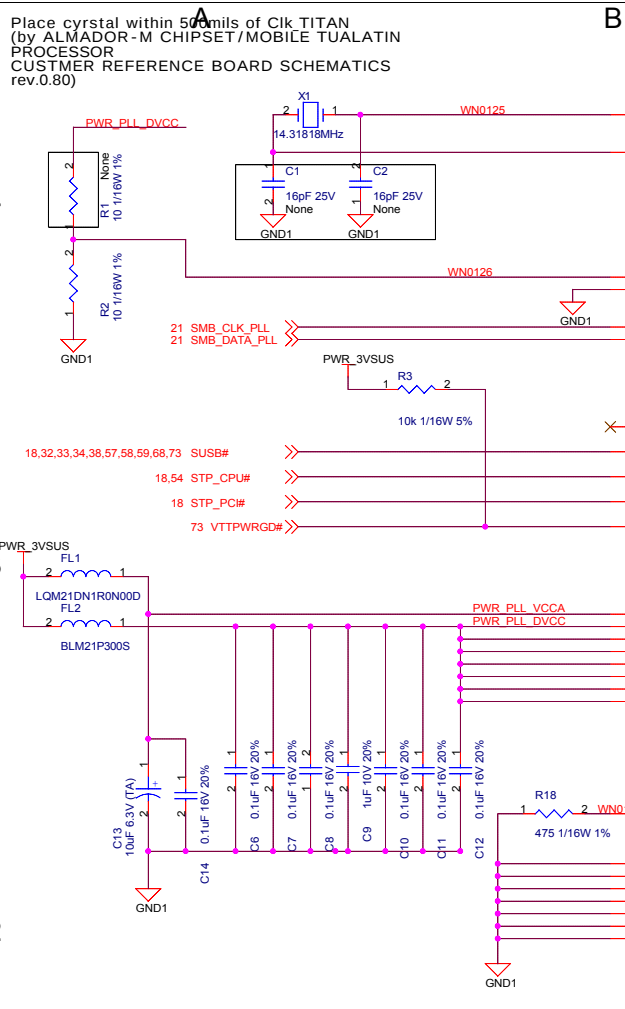
Place crystal within 50mm of CLK TITAN
(by ALMADOR-M CHIPSET/MOBILE TUALATIN
PROCESSOR
CUSTOMER REFERENCE BOARD SCHEMATICS
rev.0.80)

PLL

2

3

4



Place these capacitors near PLL.

A

B

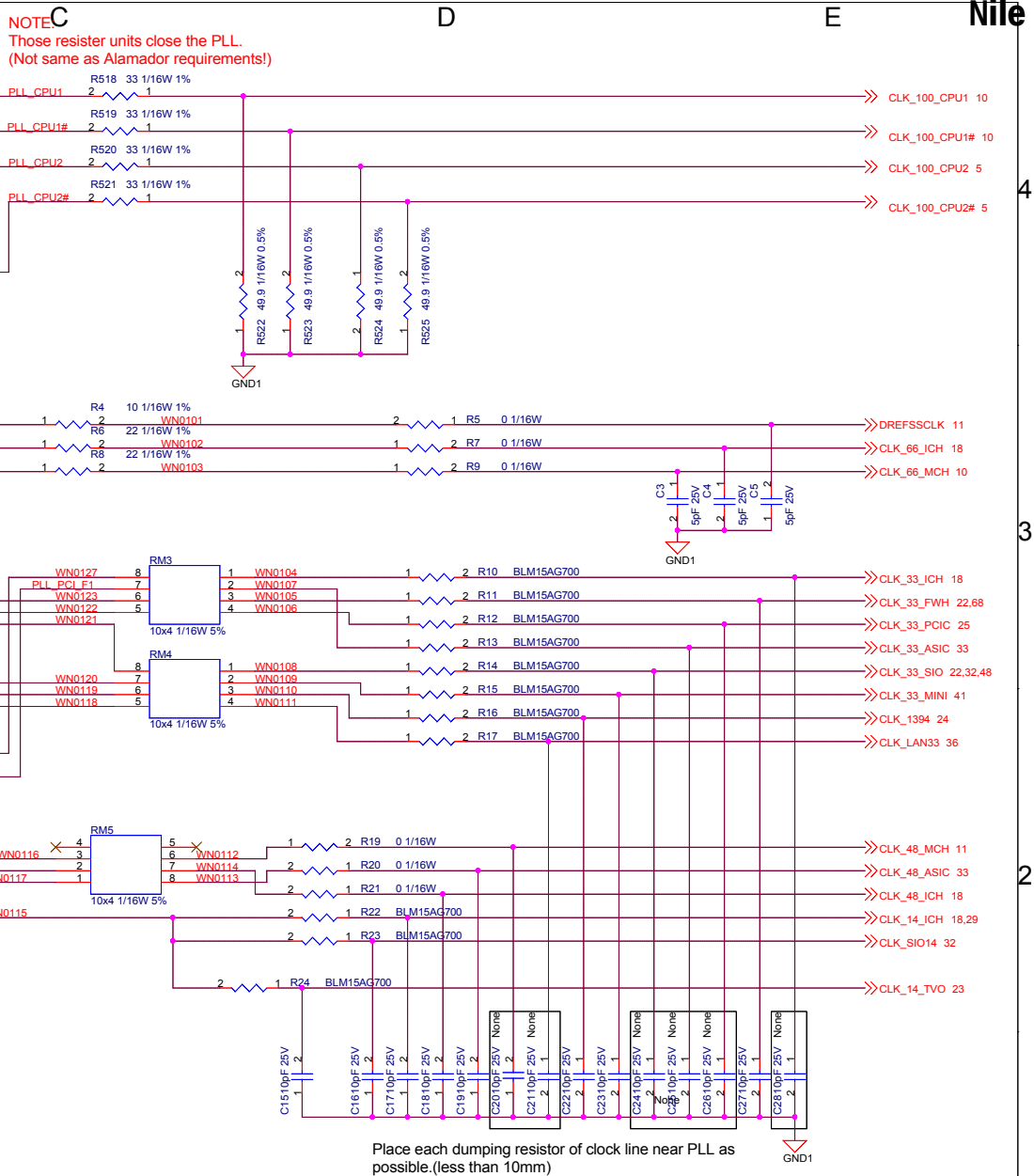
NOTE
Those resister units close the PLL.
(Not same as Alamador requirements!)

C

2

3

4



Place each dumping resistor of clock line near PLL as possible.(less than 10mm)

								TITLE	
								VB161AX MAIN	
								DRAW. No.	C1CP152845-X4
								CAST	
Rev.	Date	Design	Check	Appr.	Description			Sheet	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	4	73

D

E

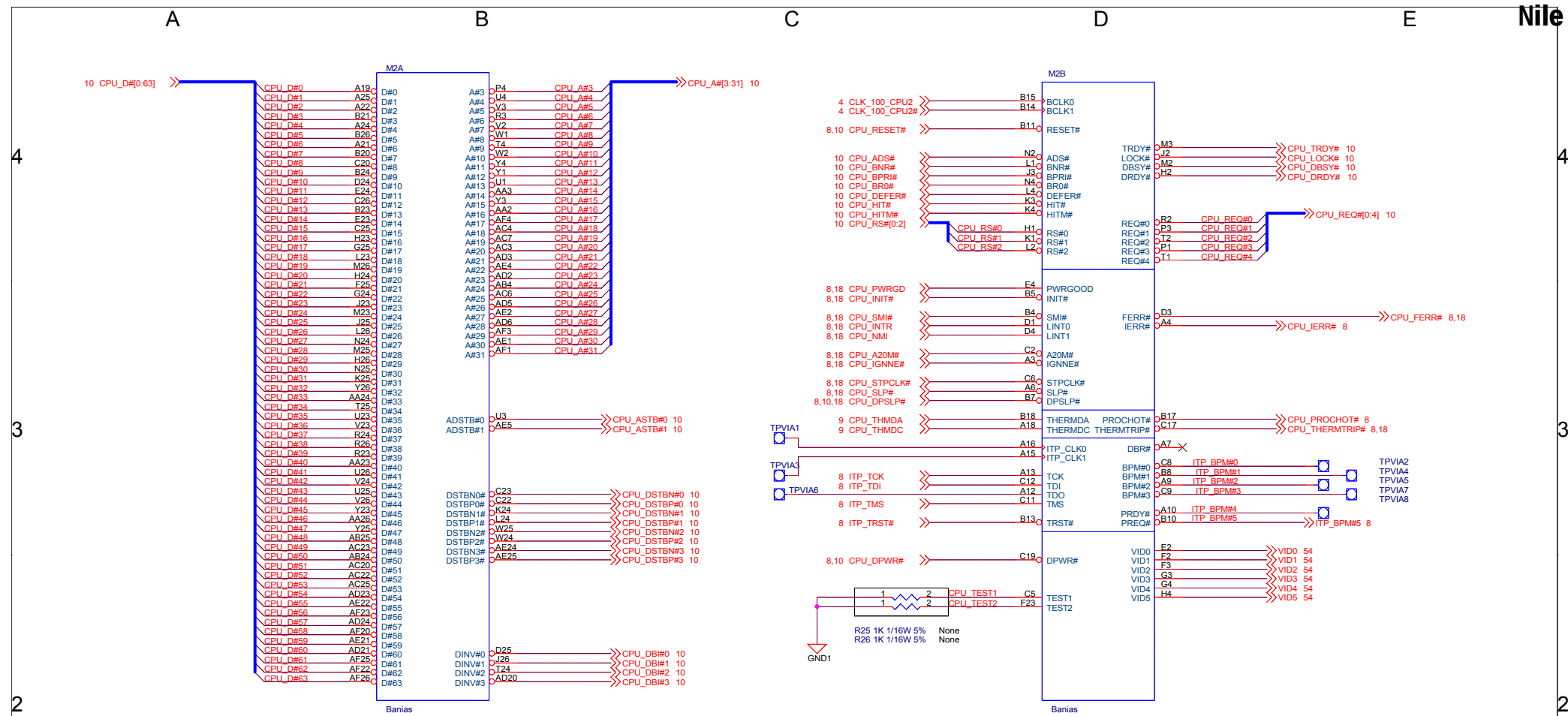
Nile

2

3

4

1



CPU_ASTB#0, CPU_ASTB#1はそれぞれの両側をGND1にて囲うこと
Guard CPU_ASTB#0, CPU_ASTB#1 with GND1 respectively.

CPU_DSTBx#[0:3]は各ペアごとに両側をGND1にて囲うこと
Each CPU_DSTBx#[0:3] strobe pair should be guarded with GND1 respectively.

印の付いた信号線(CPUTHMADA, CPUTHMDC)は、
GND1で両側をガードすること
(12ページ参照)

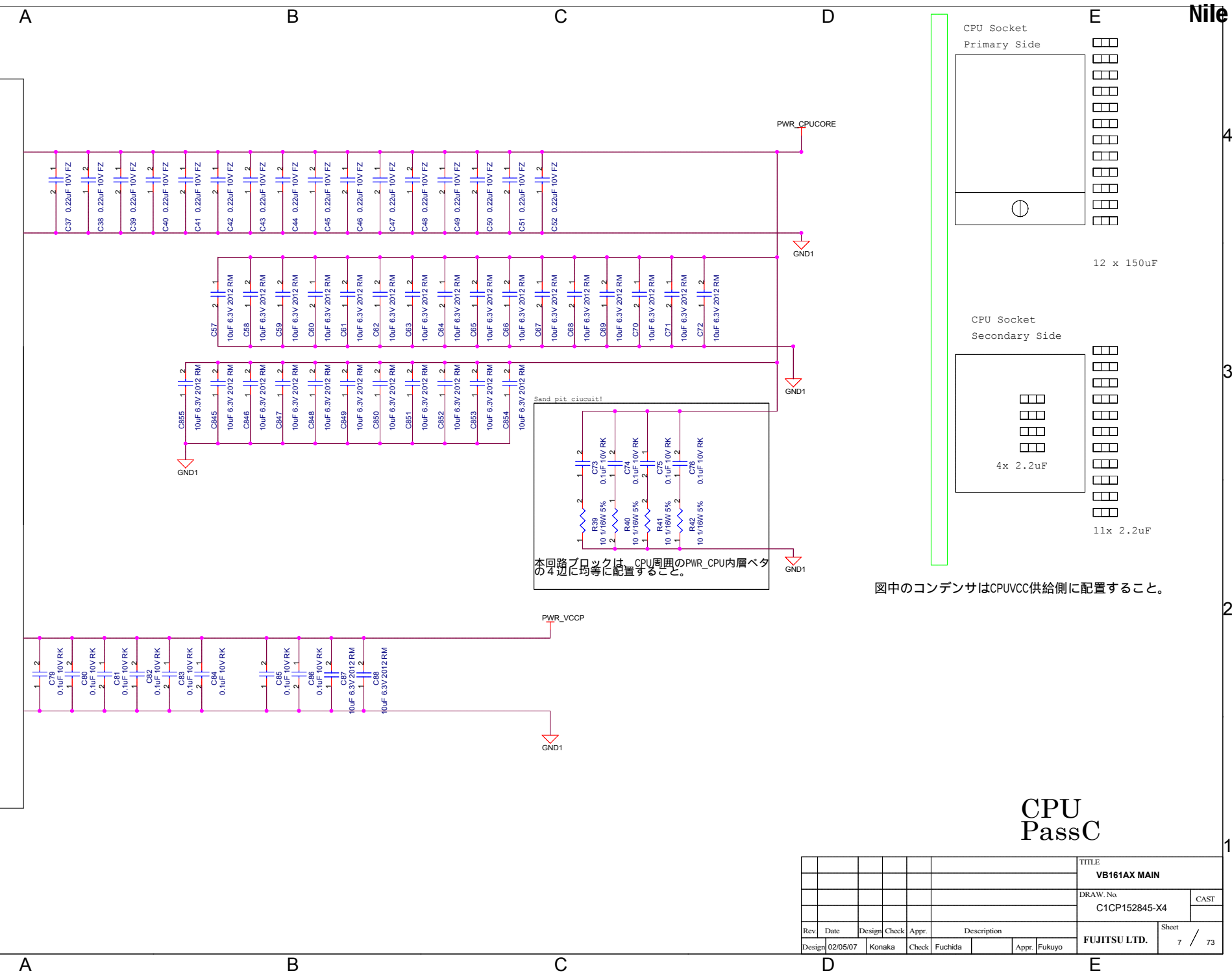
CPUTHMADA, CPUTHMDC with
is guarded with GND1

BANIAS_1

										TITLE	
										VB161AX MAIN	
										DRAW. No.	C1CP152845-X4
										CAST	
Rev.	Date	Design	Check	Appr.	Description					FUJITSU LTD.	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	Sheet		5	73



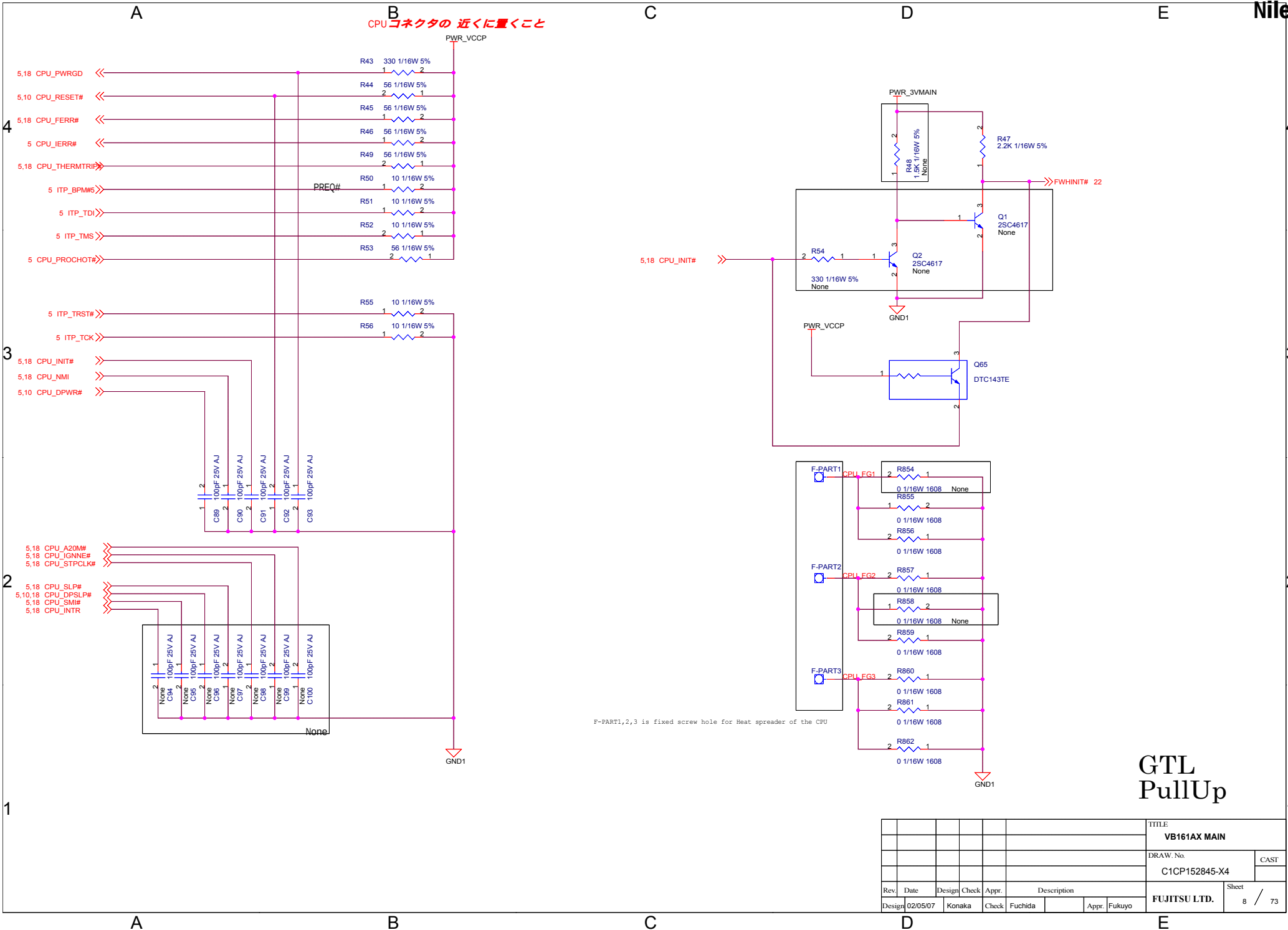
										TITLE	
										VB161AX MAIN	
										DRAW. No.	
										C1CP152845-X4	
										CAS	
Rev.	Date	Design	Check	Appr.	Description					Sheet 6 / 7	
Design	02/05/07	Konaka	Check	Fuchida			Appr.	Fukuyo	FUJITSU LTD.		



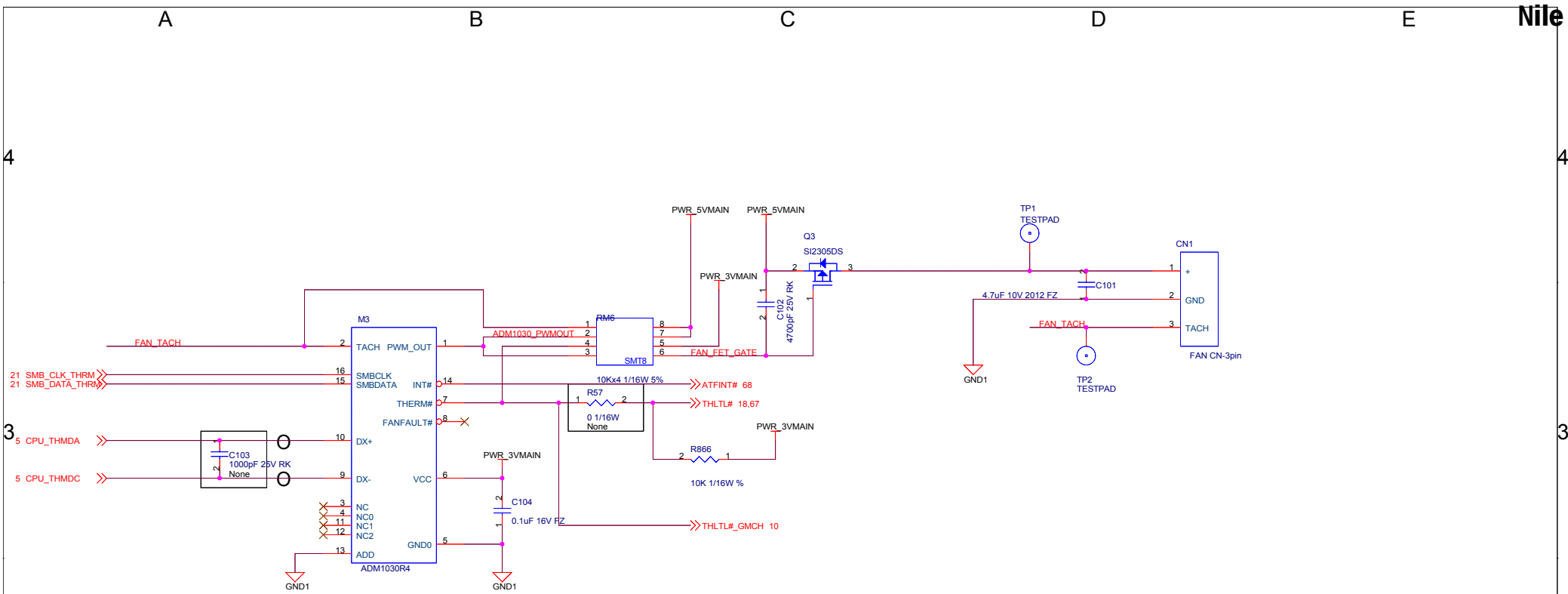
図中のコンデンサはCPUVCC供給側に配置すること。

CPU
PassC

							TITLE	
							VB161AX MAIN	
							DRAW. No.	CAST
							C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description		Sheet	
Design	02/05/07	Konaka	Check	Fuchida		Appr. Fukuyo	FUJITSU LTD.	7 / 73



GTL
PullUp



これらの部品はCPUの近くに配置すること。
Place each component near CPU.

CPU_THMDA, CPU_THMDCのパターンが最短になるよう、ADM1030(M5)は、CPUの近傍に配置すること。

Place ADM1030(M5) near CPU(M3) to control CPU_THMDA,CPU_THMDC patterns as short as possible.

CPU_THMDA, CPU_THMDC両端に入っているコンデンサC108(1000pF)は、ADM1030(M5)の直近に配置すること。
Place and C108(1000pF) nearest ADM1030(M5).

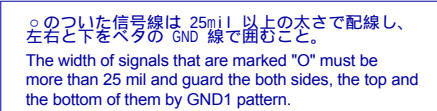
○印の付いた信号線(CPU_THMDA,CPU_THMDC)は、GND1で両側をガードすること
(下図参照)

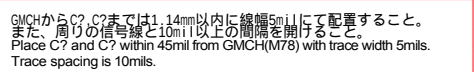
Guard these signals(CPU_THMDA,CPU_THMDC) that marked "O" at the both sides by GND1 pattern.
(Look at follow figure.)



								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAST
								C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description			FUJITSU LTD.	Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo		9 / 73

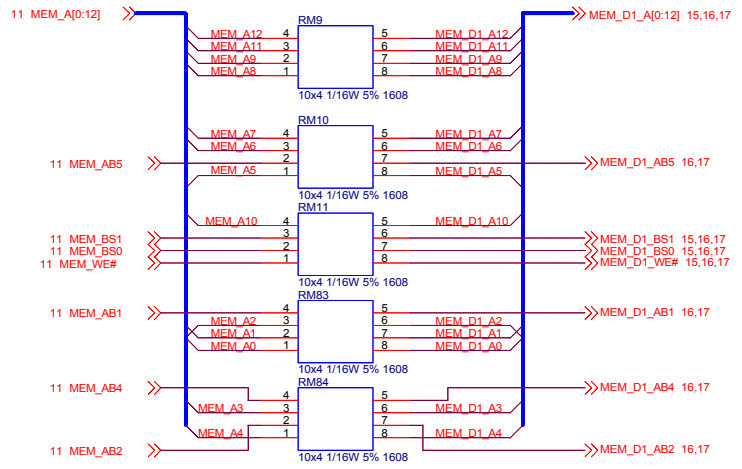




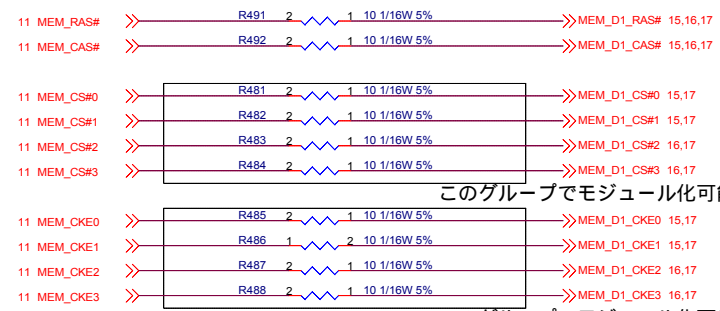




						TITLE	
						VB161AX MAIN	
						DRAW. No.	CAST
						C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description		Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr. Fukuyo	FUJITSU LTD. 13 / 73

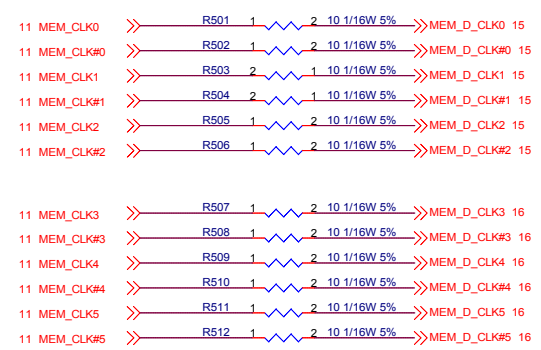


このグループで自由にピンスワップ可能



このグループでモジュール化可能

このグループでモジュール化可能



												TITLE VB161AX MAIN	
												DRAW. No. C1CP152845-X4	CAST
												Sheet 14 / 73	
Rev.	Date	Design	Check	Appr.	Description							FUJITSU LTD.	
Design	02/05/07	Konaka	Check	Fuchida						Appr.	Fukuyo		

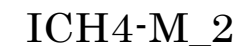


TITLE		VB161AX MAIN	
DRAW. No.		C1CP152845-X4	CAS
FUJITSU LTD.		Sheet	15 / 7





E



										TITLE	
										VB161AX MAIN	
										DRAW. No.	CAS
										C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description					Sheet	
Design	02/05/07	Konaka	Check	Fuchida			Appr.	Fukuyo	FUJITSU LTD.		19 / 73



E

A

B

C

D

E

Nile

4

4

3

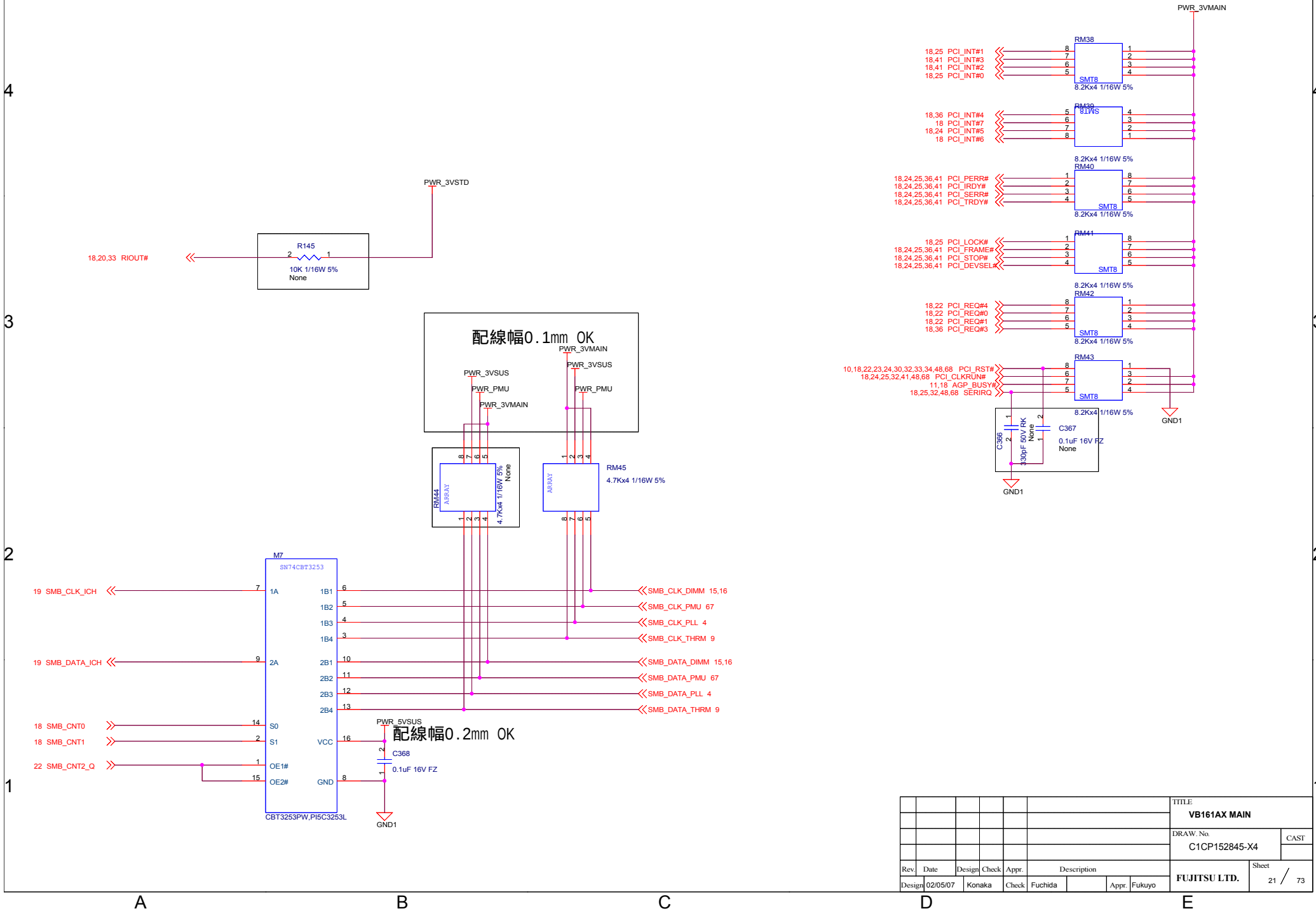
3

2

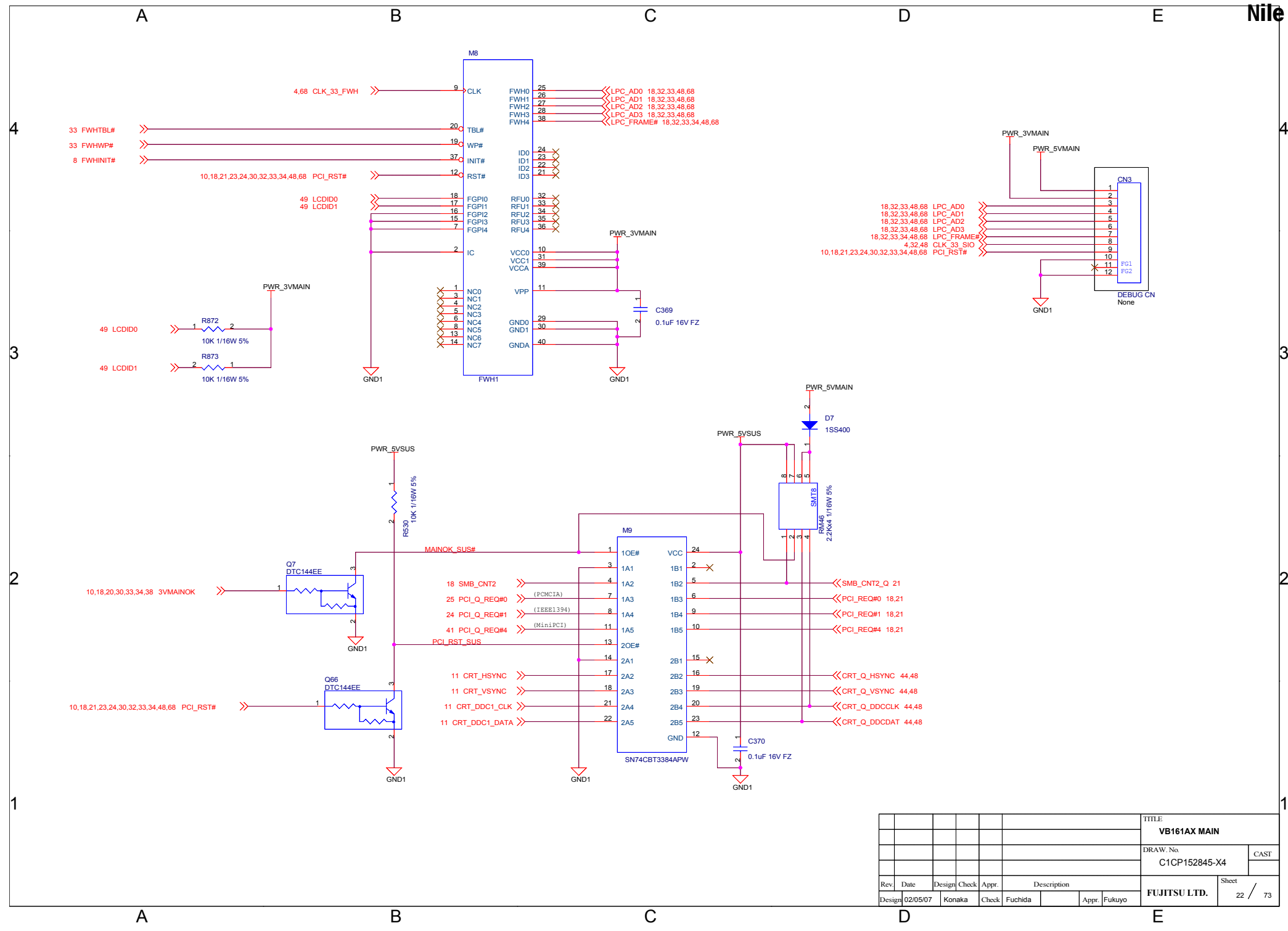
2

1

1



						TITLE	
						VB161AX MAIN	
						DRAW. No.	CAST
						C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description	Fujitsu Ltd.	
Design	02/05/07	Konaka	Check	Fuchida	Appr. Fukuyo	Sheet	21 / 73



										TITLE	
										VB161AX MAIN	
										DRAW. No.	CAST
										C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description					FUJITSU LTD.	Sheet
Design	02/05/07	Konaka	Check	Fuchida							22 / 73

A

B

C

D

E

A

B

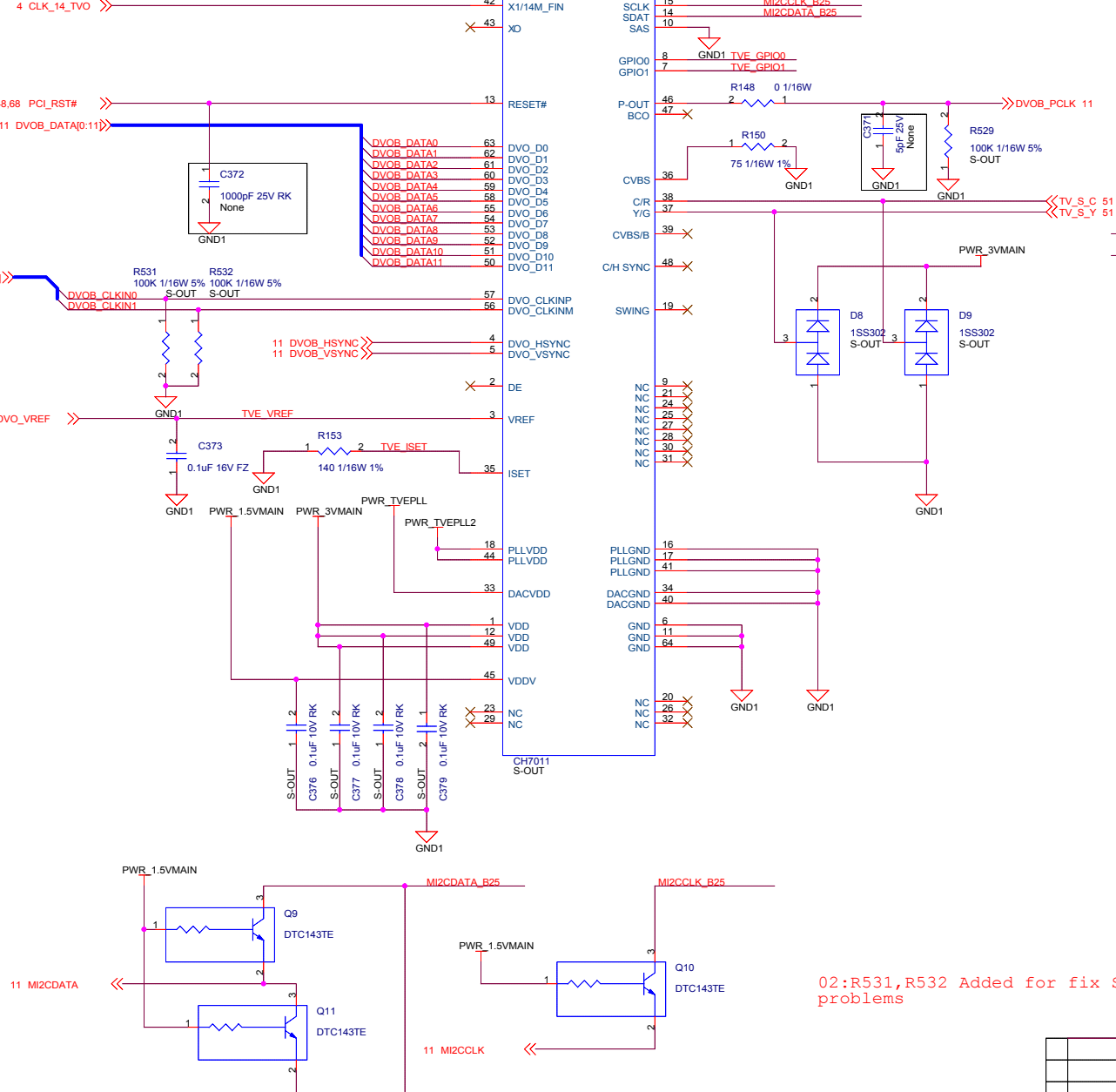
C

D

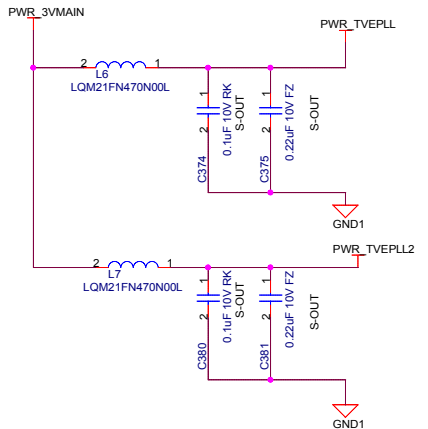
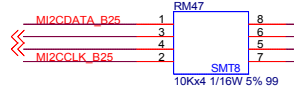
E

C?はTVEncoderの近くに配置すること
Place C? near TVEncoder

TV_MOBESEL	
TVE_GPIO0	
Pullup	PAL
Pulldown	NTSC



Pullup presents internally



このページの部品はTVEncoderの各電源ピンの近くに置くこと
Place all components in this page near TVEncoder.

02:R531,R532 Added for fix S-video startup problems

								TITLE	
								VB161AX MAIN	
								DRAW. No.	C1CP152845-X4
								CAST	
Rev.	Date	Design	Check	Appr.	Description			FUJITSU LTD.	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	Sheet	23 / 73

A

B

C

D

E

4

4

3

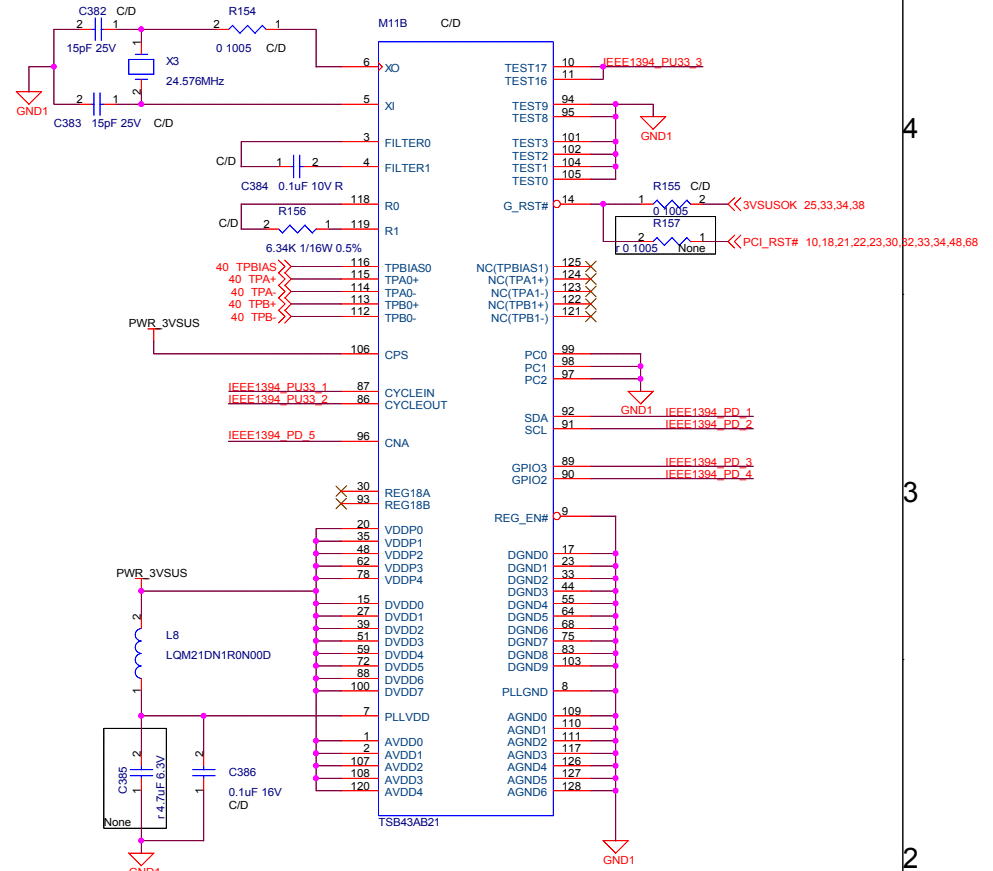
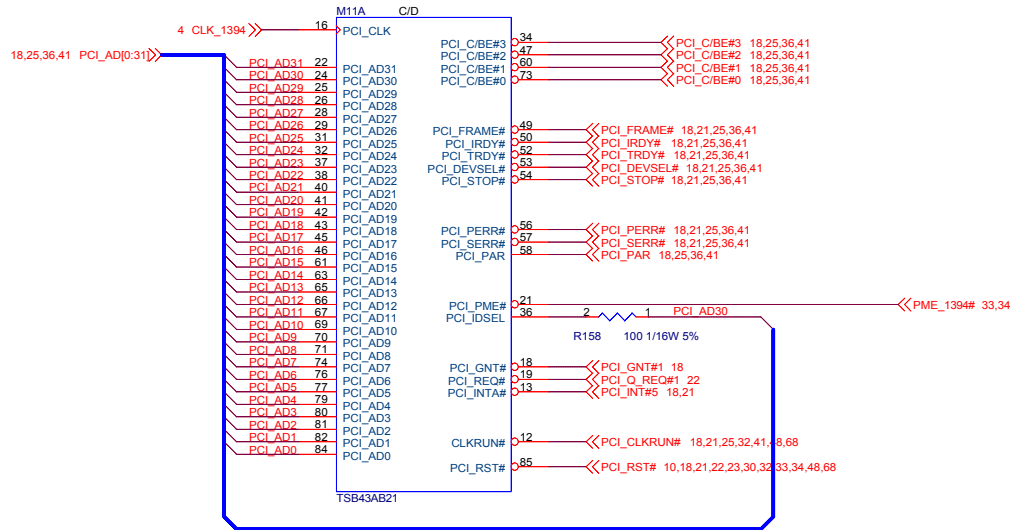
3

2

2

1

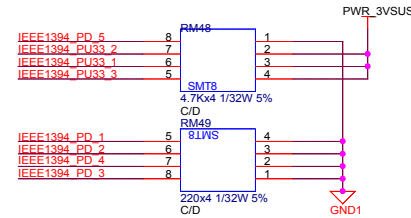
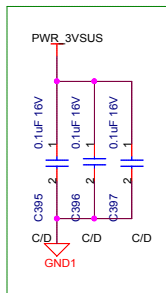
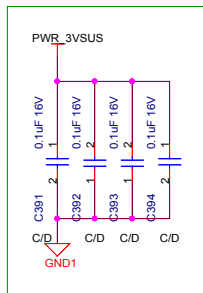
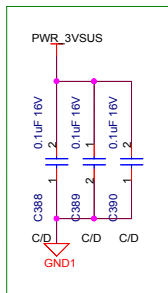
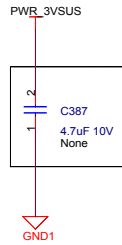
1



下記のコンデンサはTSB43AA21の
VDDPxピン近傍に配置

下記のコンデンサはTSB43AA21の
AVDDxピン近傍に配置

下記のコンデンサはTSB43AA21の
DVDDxピン近傍に配置



IEEE1394

				TITLE			
				VB161AX MAIN			
				DRAW. No.		CAST	
				C1CP152845-X4			
Rev.	Date	Design	Check	Appr.	Description		Sheet
Design	02/05/07	Konaka	Check	Fuchida	Appr.	Fukuyo	24 / 73

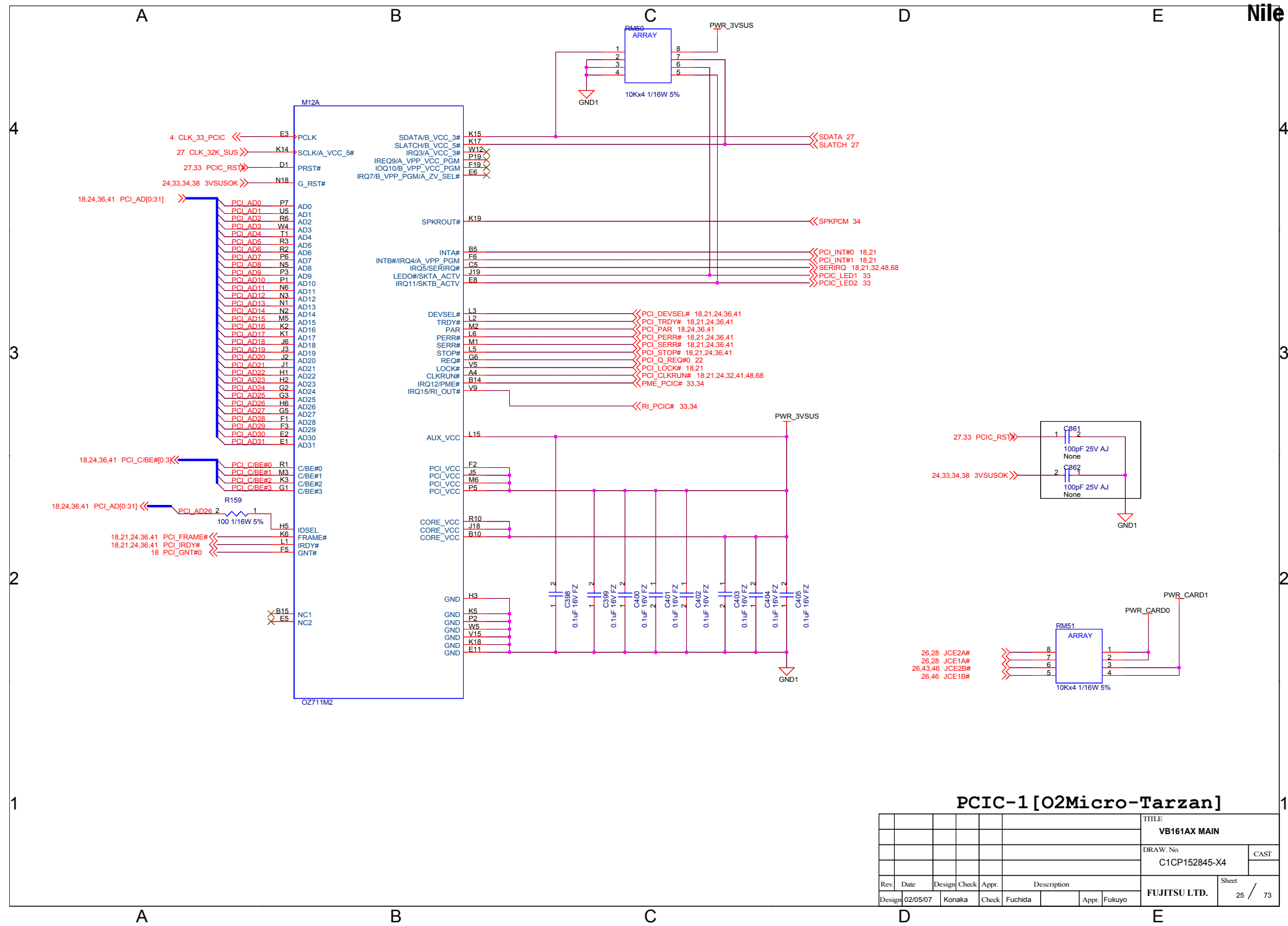
A

B

C

D

E



PCIC-1 [O2Micro-Tarzan]

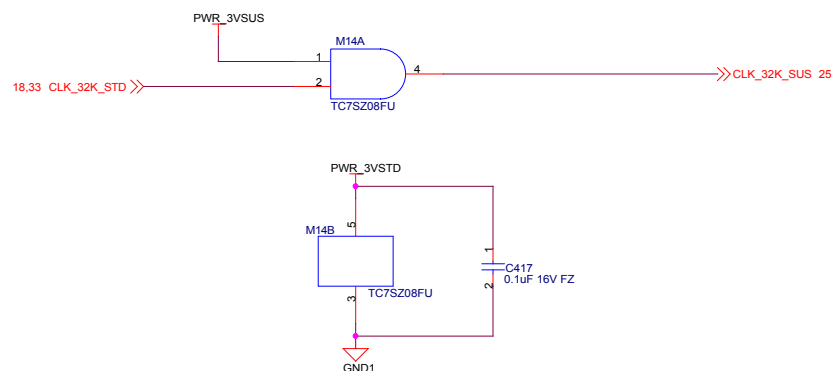
								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAST
								C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description			Sheet	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	25	73



【注意】 JAA16, JAB16はCardBus時クロック信号になるのでGND1にてガードを行うこと。



Rev.	Date	Design	Check	Appr.	Description				Fujitsu Ltd.	Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuvo	26 / 7		



										TITLE	
											VB161AX MAIN
										DRAW. No.	CAS
										C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description						Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo			FUJITSU LTD.	27 / 73

A

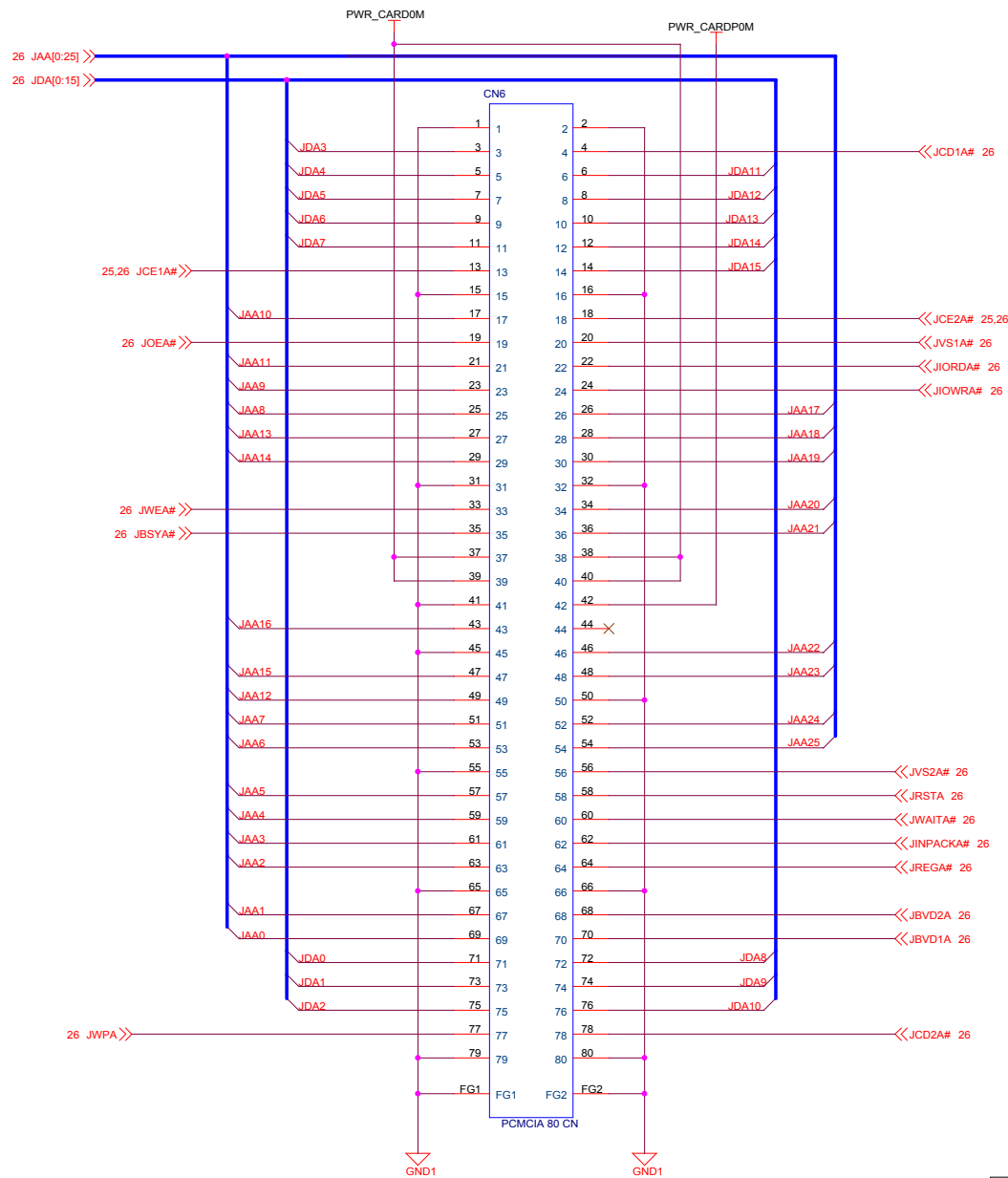
B

C

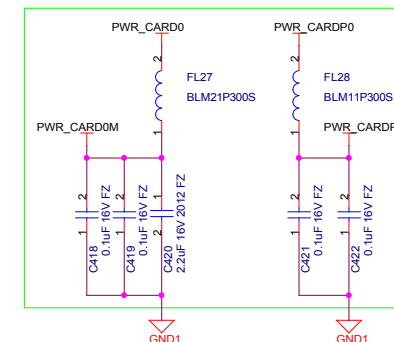
D

E

Nile



コネクタ電源ピンの近傍に配置



										TITLE		
										VB161AX MAIN		
										DRAW. No.		CAS
										C1CP152845-X4		
										Sheet		
Rev.	Date	Design	Check	Appr.	Description					FUJITSU LTD.		
Design	02/05/07	Konaka	Check	Fuchida			Appr.	Fukuyo	28 / 73			

A

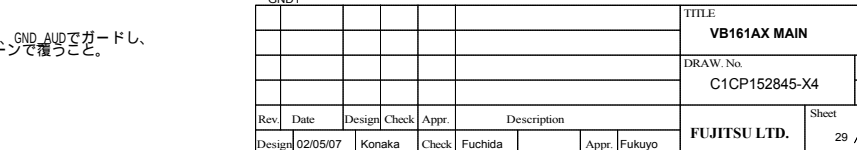
B

C

D

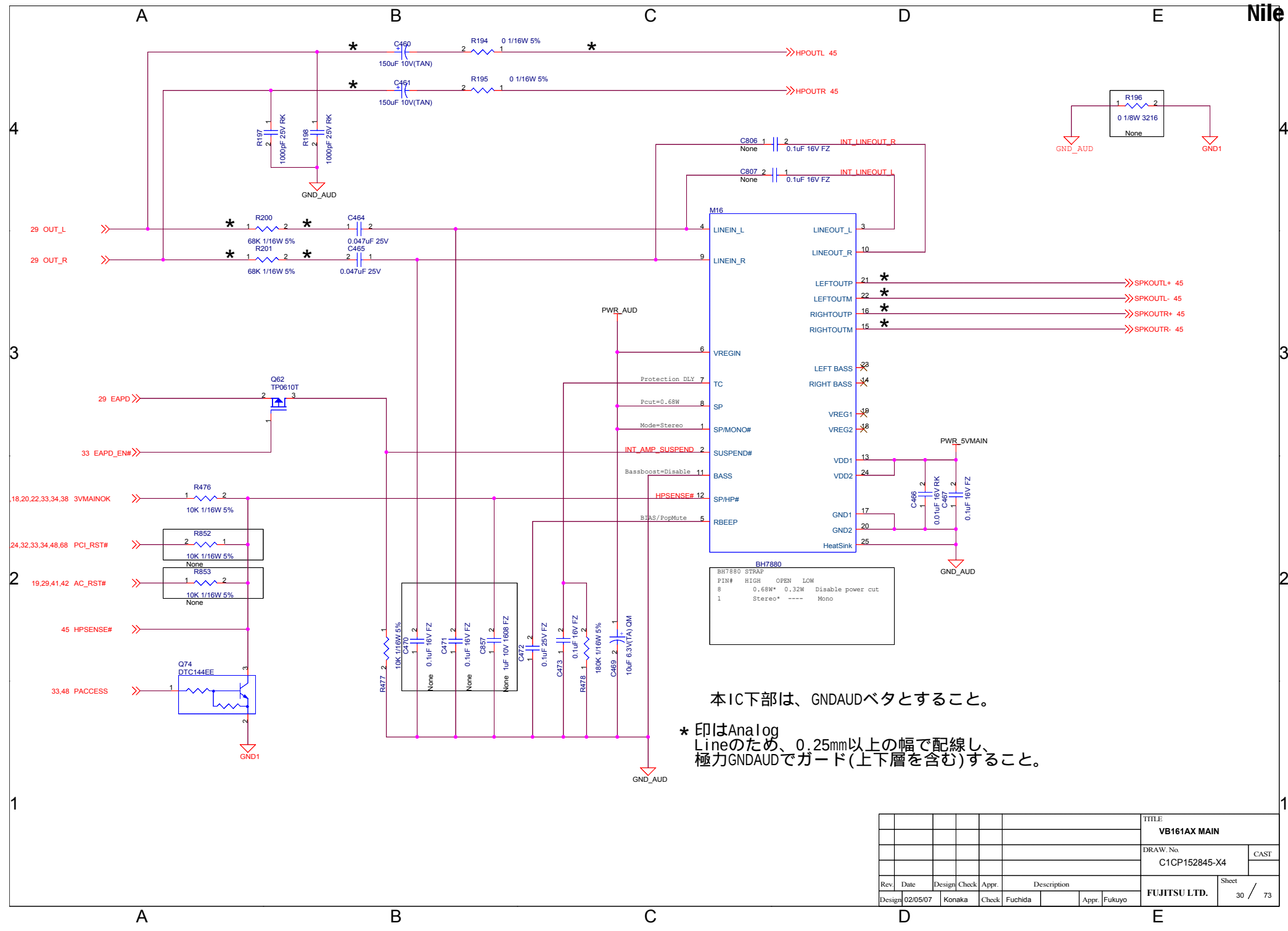
E

全項AUDIO AREA



本項中、印のついたパターンは、GND、AUDでガードし、その上下はGND、AUDのベタパターンで覆うこと。

GND1										TITLE	
										VB161AX MAIN	
										DRAW. No.	CAS
										C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description					Sheet	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	FUJITSU LTD.			29 / 7



本IC下部は、GNDAUDベタとすること。
* 印はAnalog Lineのため、0.25mm以上の幅で配線し、極力GNDAUDでガード(上下層を含む)すること。

TITLE										VB161AX MAIN	
DRAW. No.										C1CP152845-X4	
Rev.										Fujitsu Ltd.	
Date										30 / 73	
Design										Sheet	
02/05/07										30 / 73	
Konaka										Fukuyo	
Check										Fukuyo	
Fuchida										Fukuyo	
Appr.										Fukuyo	
Description										Fukuyo	

A

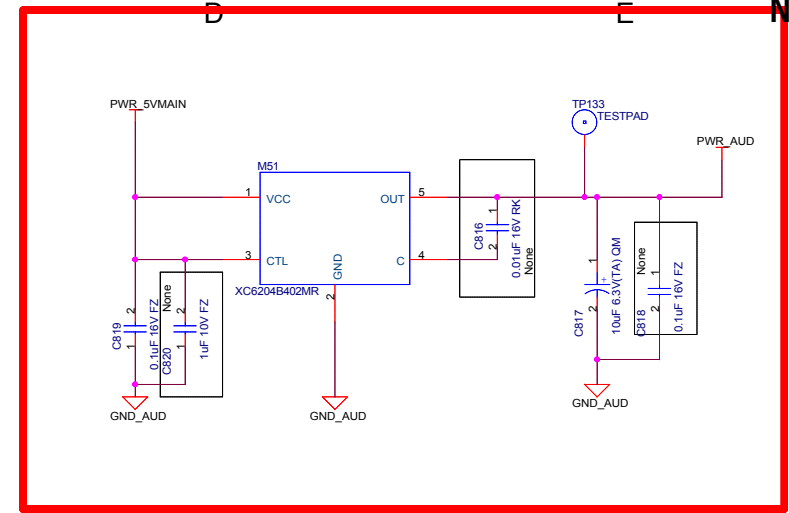
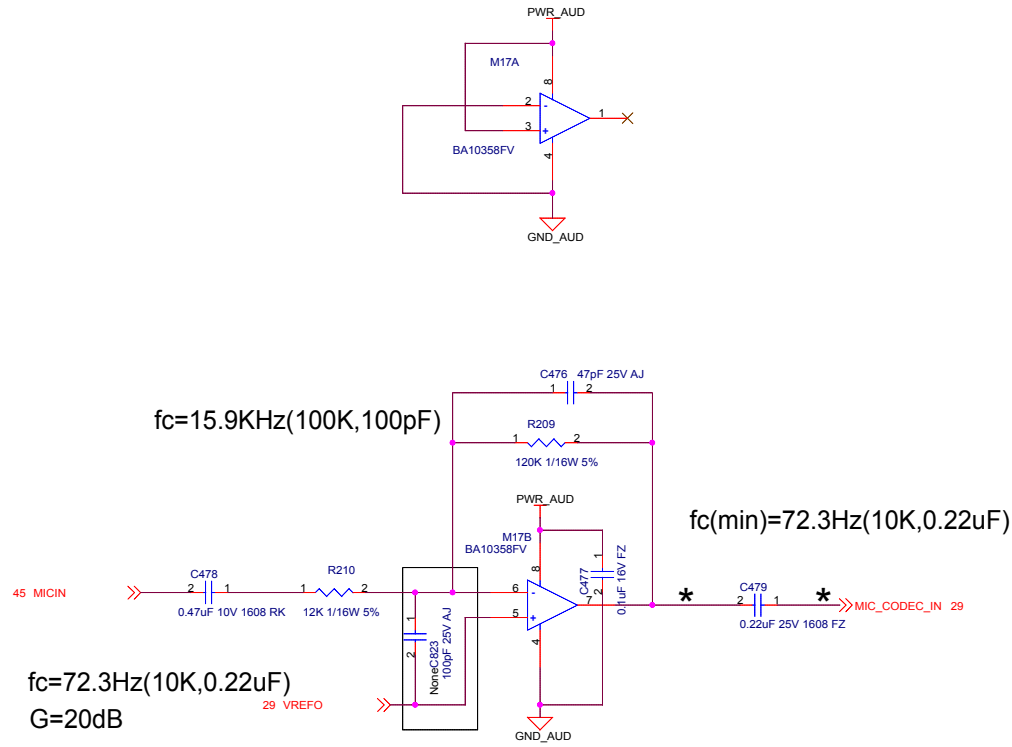
B

C

D

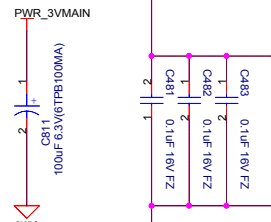
E

Nile

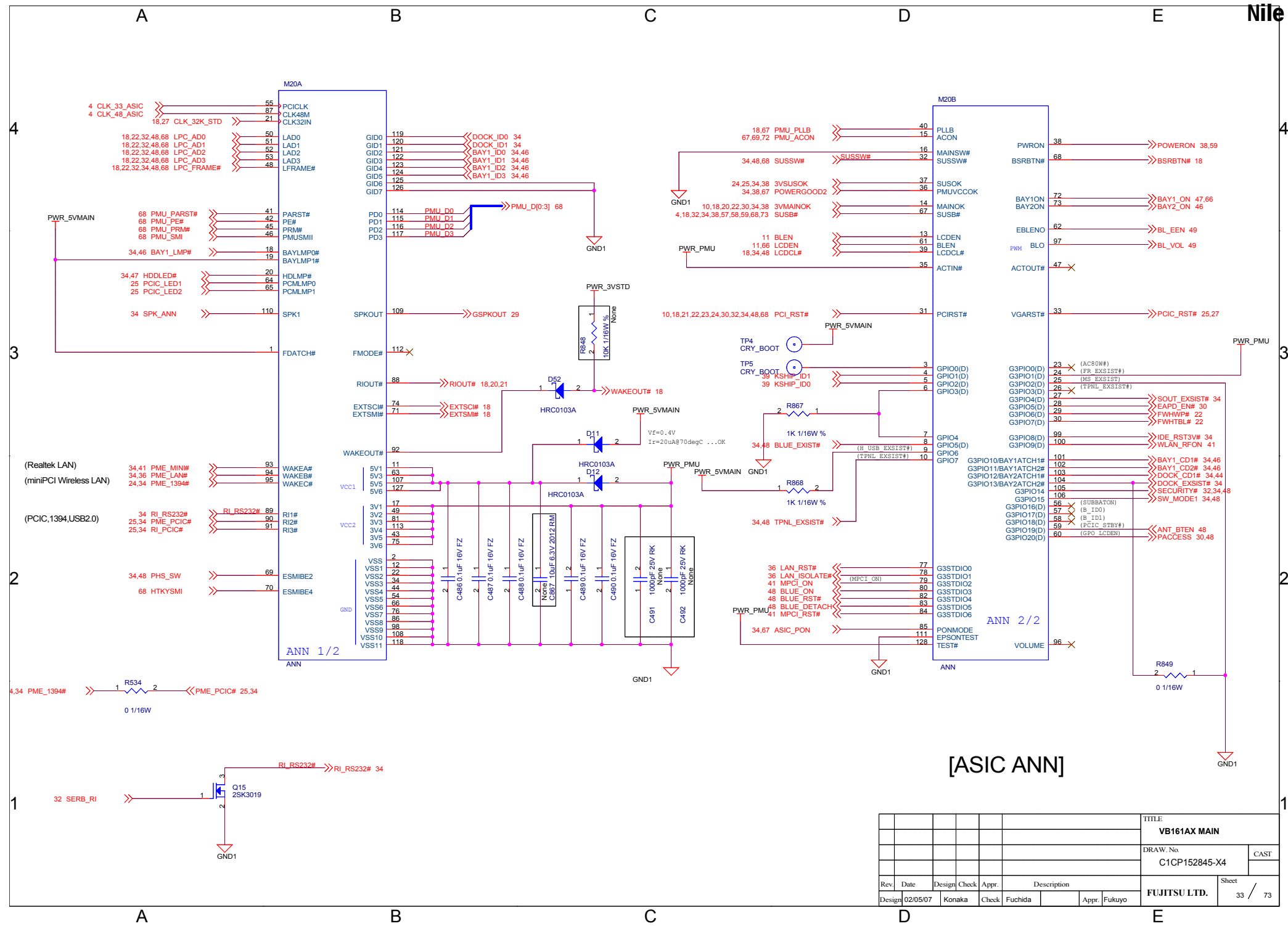


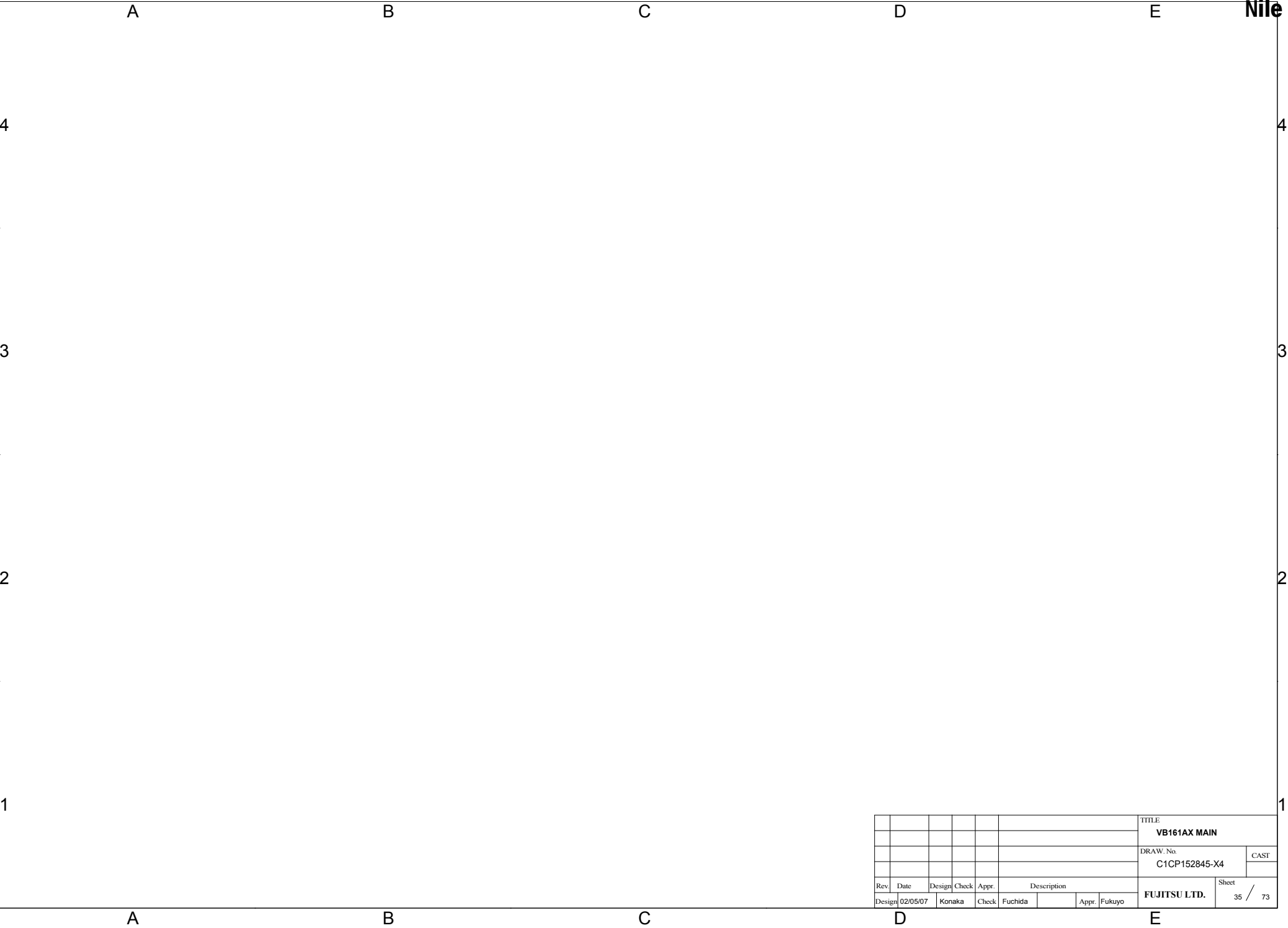
MIC AMP

Rev.	Date	Design	Check	Appr.	Description	TITLE	
						VB161AX MAIN	
						DRAW. No.	CAST
						C1CP152845-X4	
						FUJITSU LTD.	Sheet
							31 / 73



						TITLE	
						VB161AX MAIN	
						DRAW. No.	CAS
						C1CP152845-X4	
Rev	Date	Design	Check	Appr.	Description		Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr. Fukuyo	32 / 7
FUJITSU LTD.							





							TITLE	
							VB161AX MAIN	
							DRAW. No.	
							C1CP152845-X4	CAST
Rev.	Date	Design	Check	Appr.	Description			Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	35 / 73

A

B

C

2SB1197K D

E

Nile

A

B

C

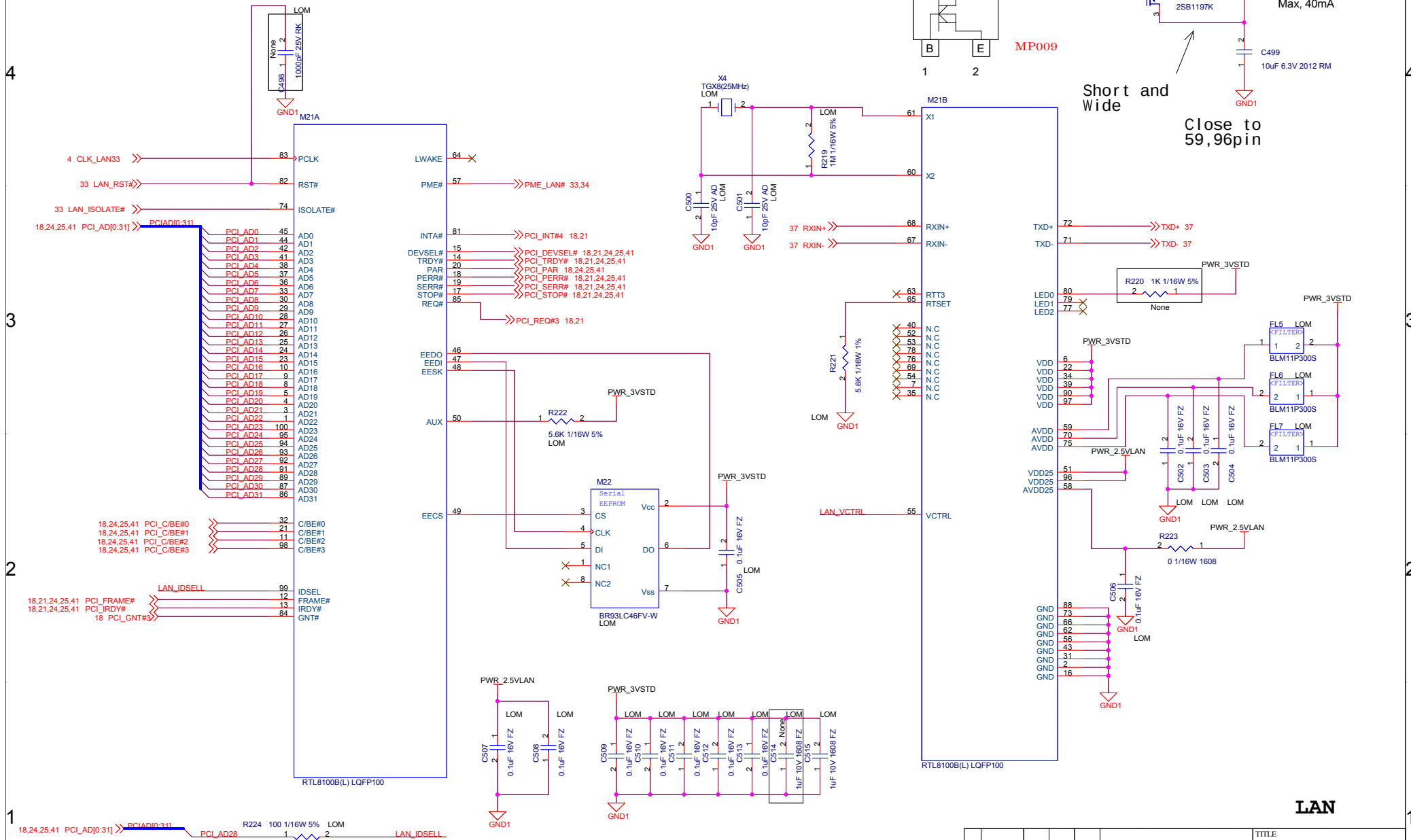
D

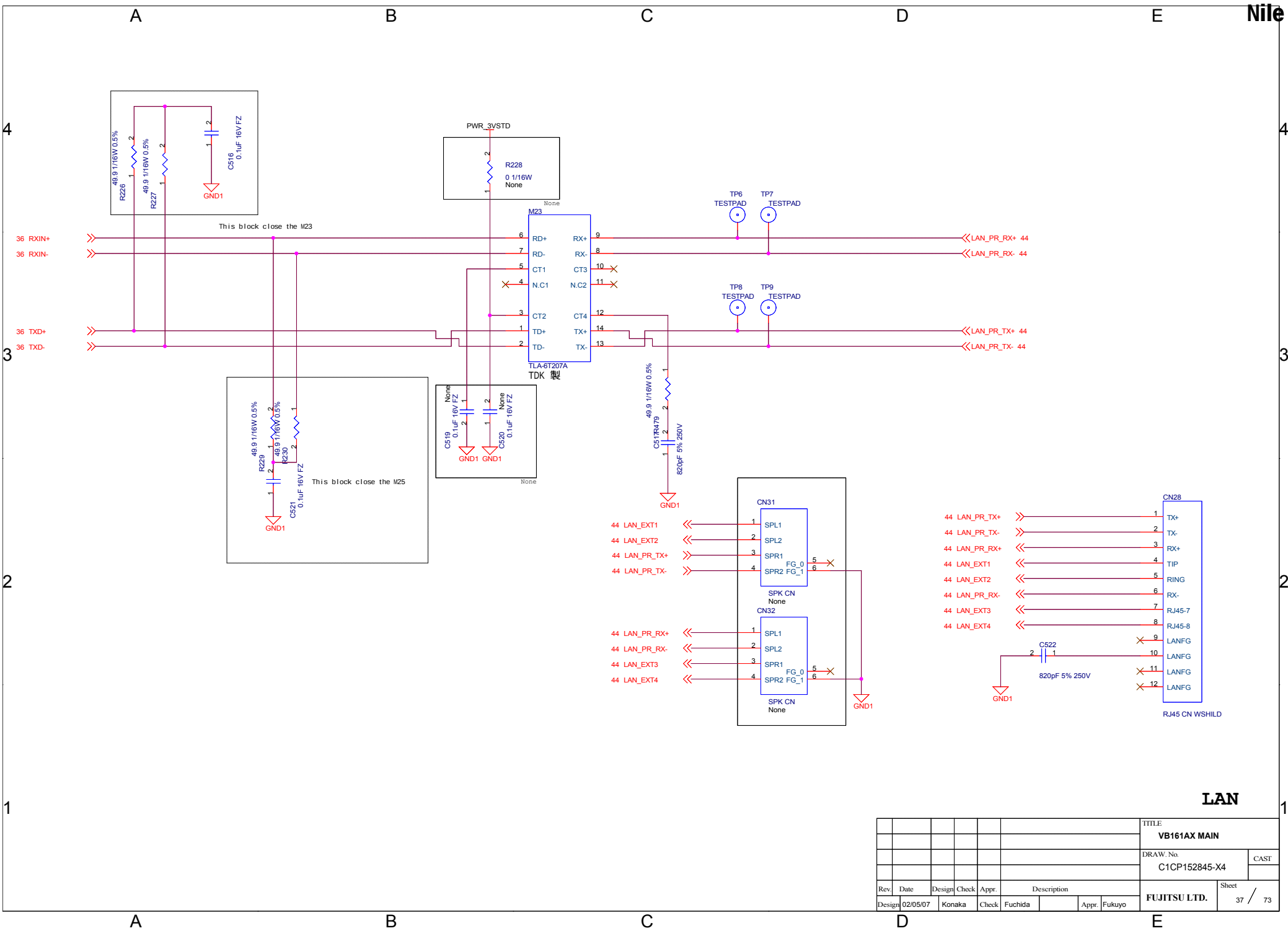
E

[RTL8100B(L)]

LAN

							TITLE	
							VB161AX MAIN	
							DRAW. No.	CAST
							C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description		FUJITSU LTD.	Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	36 / 73





LAN

								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAST
								C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description			FUJITSU LTD.	Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo		37 / 73

A

B

C

D

E

4

4

3

3

2

2

1

1

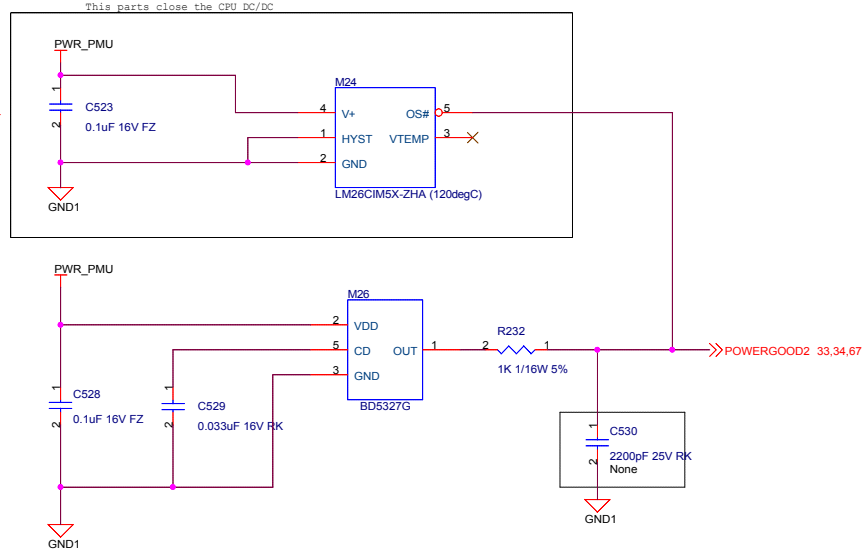
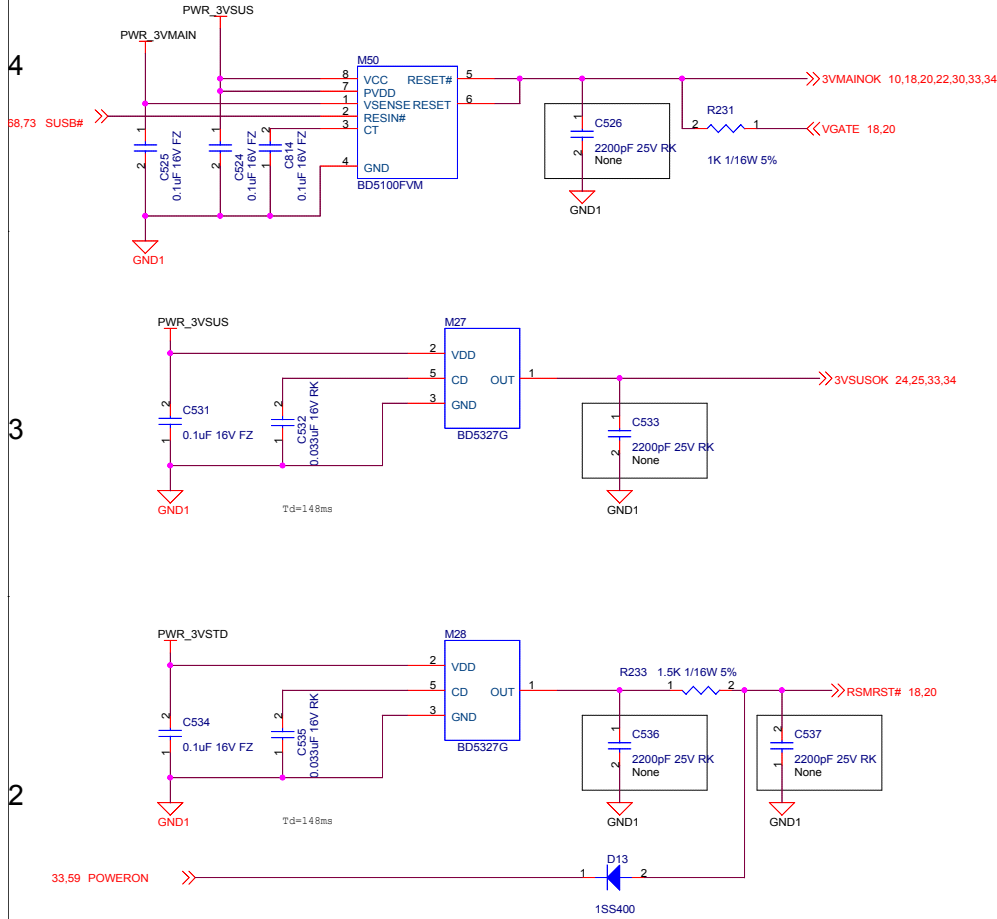
A

B

C

D

E



								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAST
								C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description			Fujitsu Ltd.	Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo		38 / 73

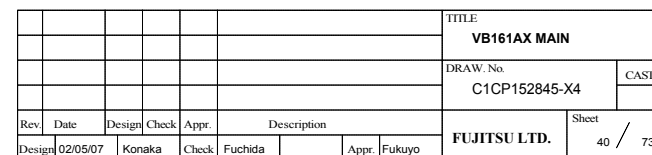


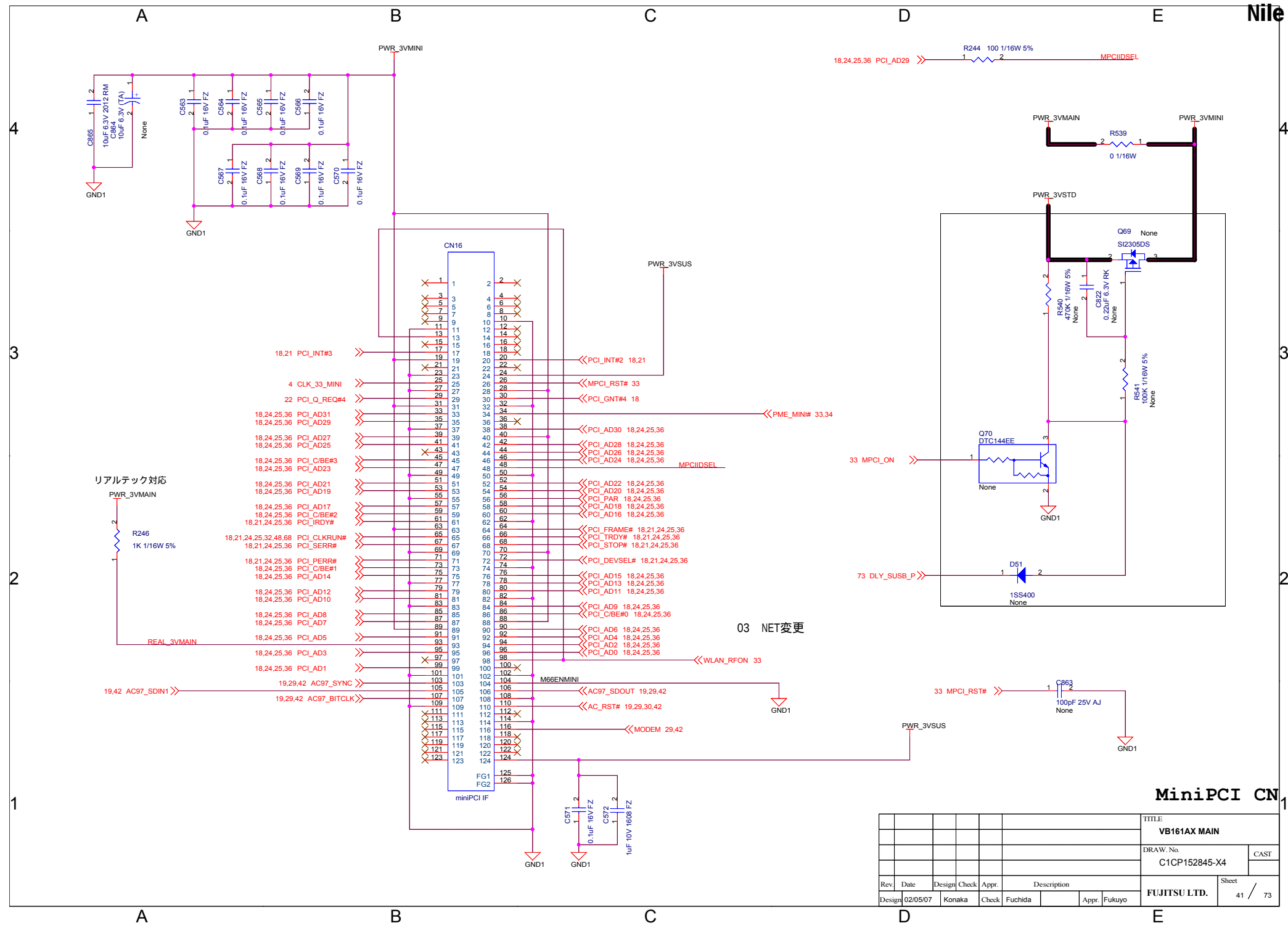
						TITLE		
						VB161AX MAIN		
						DRAW. No.		CAST
						C1CP152845-X4		
Rev.	Date	Design	Check	Appr.	Description			
Design	02/05/07	Konaka	Check	Fuchida		Appr	Fukuyo	
FUJITSU LTD.							Sheet	
							39 /	73



2



C



A

B

C

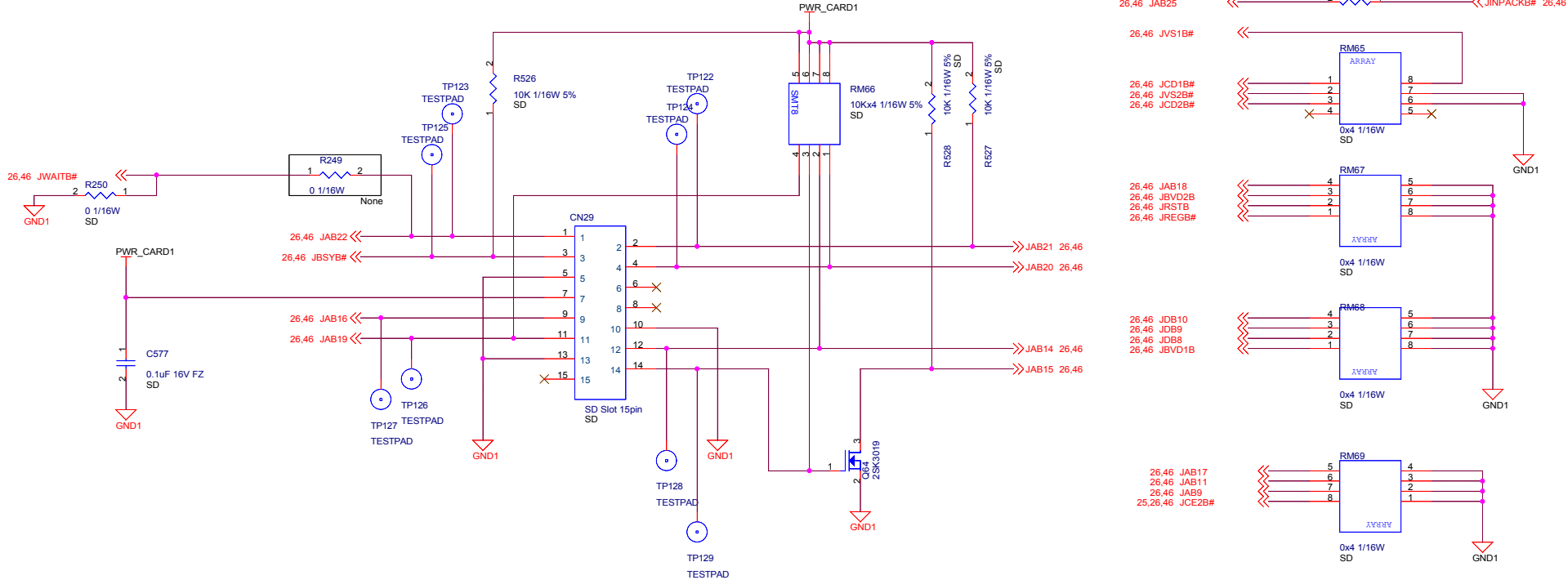
D

E

MISC
JWAITB# SQR3
JAB22 MC_CD#

JAB25 SQRDR
JINPACKB# SQRDRX or SQR4
JCD1B# B_CCD1#
JVS1B# B_CVS1
JCD2B# B_CCD2#
JVS2B# B_CVS2
JAB18 SQR1
JRSTB SQR2
JREGB# SQR5
JBVD2B SQR6
JBVD1B SQR7
JDB8 SQR8
JDB9 SQR9
JDB10 SQR10
JAB17 ISOGND
JAB11 ISOGND
JAB9 ISOGND
JCE2B# ISOGND

SD I/F
JAB14 SD_DATA1
JAB19 SD_DATA0
JAB16 SD_CLK/MS_CLK
JAB20 SD_CMD
JBSYB# SD_DATA3
JAB21 SD_DATA2
JAB15 MC_WP#



[SD SLOT]

								TITLE	
								VB161AX MAIN	
								DRAW. No.	C1CP152845-X4
								CAST	
Rev.	Date	Design	Check	Appr.	Description			FUJITSU LTD.	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	Sheet	43 / 73

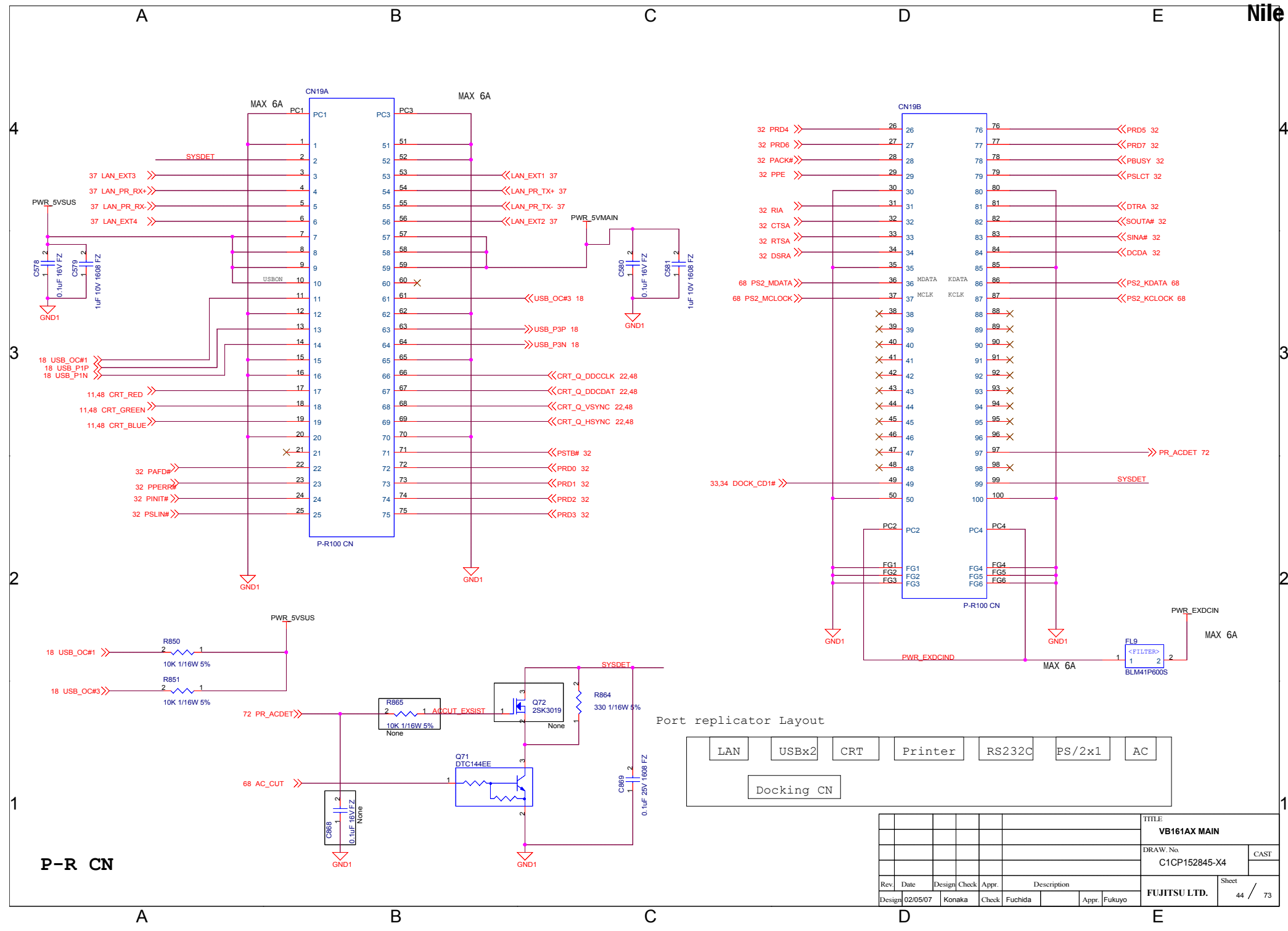
A

B

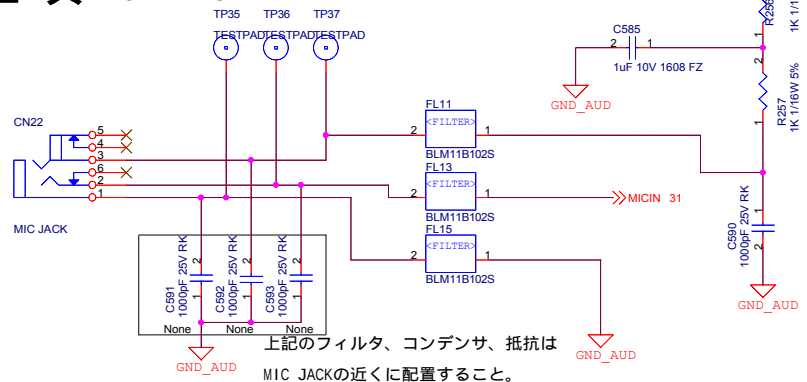
C

D

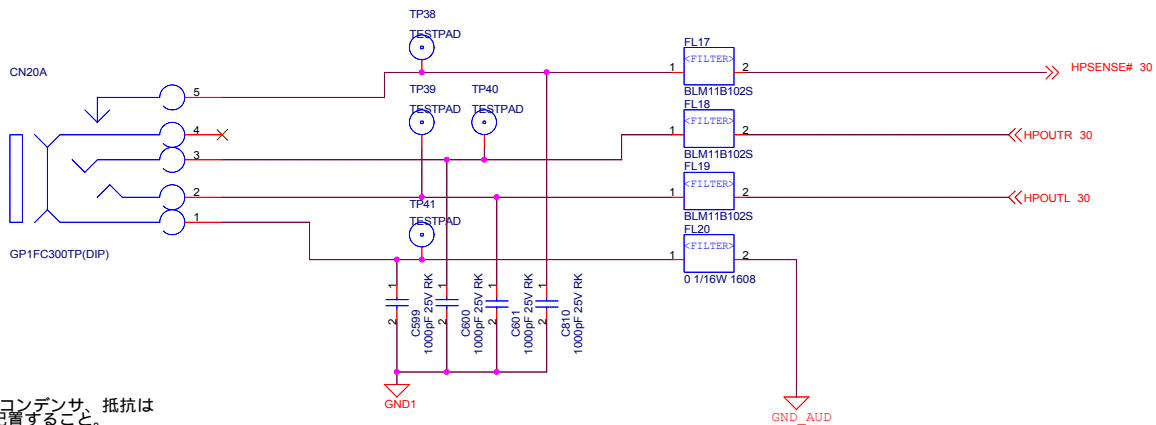
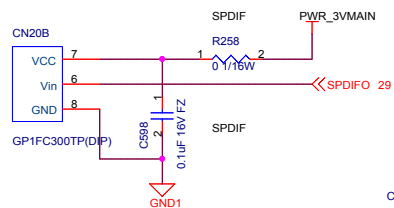
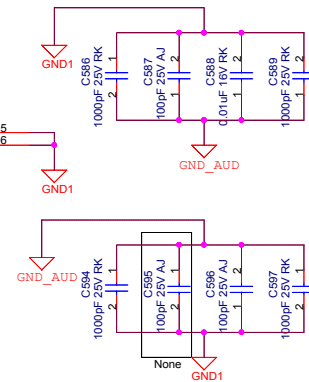
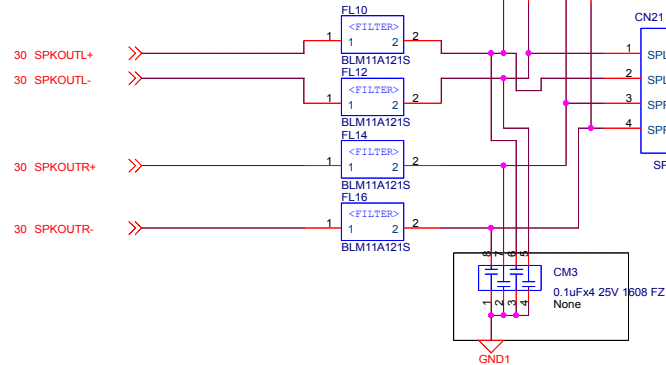
E



全項AUDIO AREA



30 SPKOUTL+
30 SPKOUTL-
30 SPKOUTR+
30 SPKOUTR-

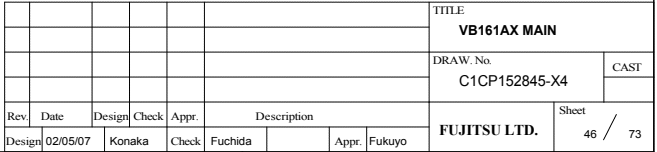


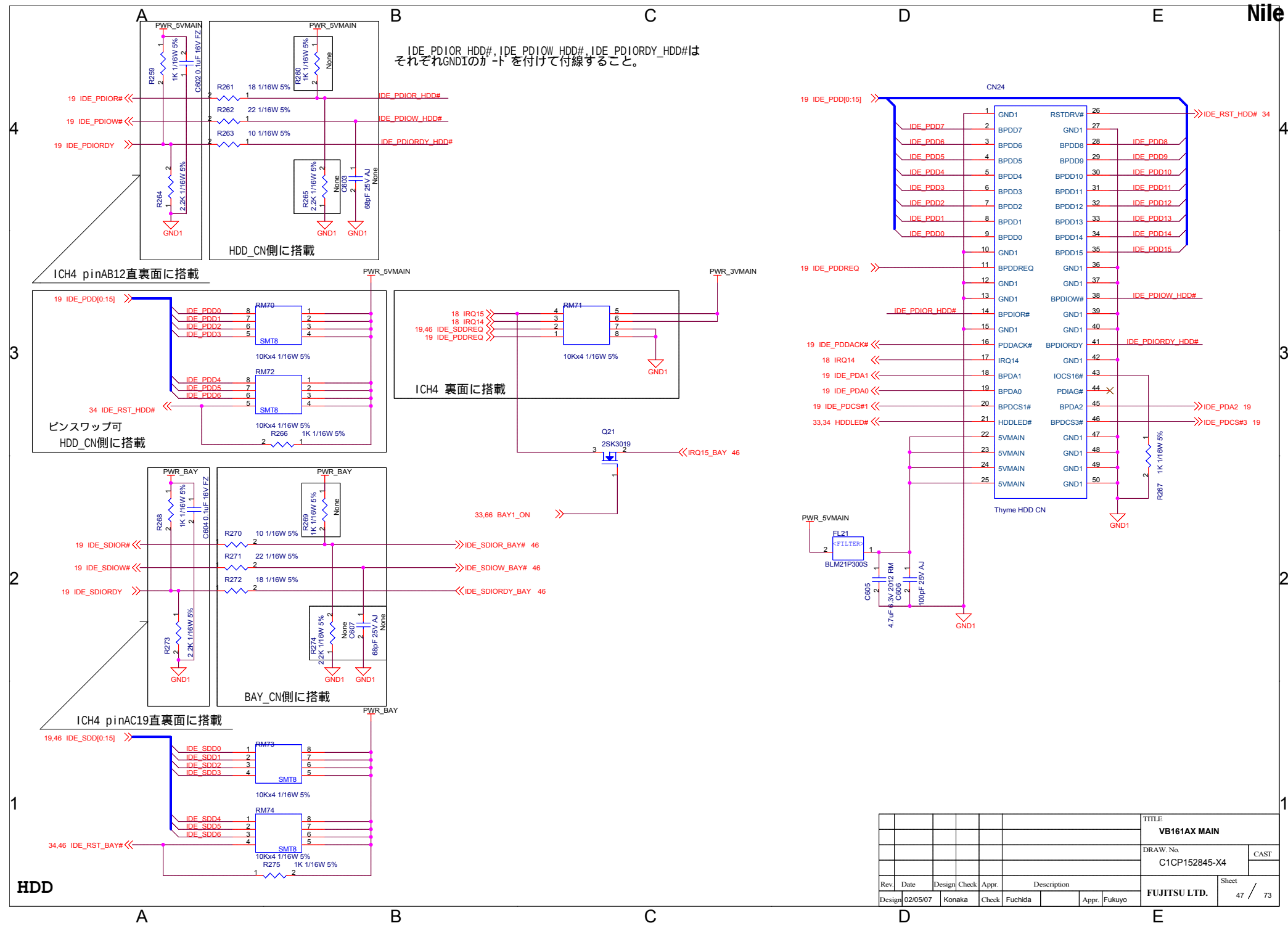
本ページ中に記載されているフィルタ(FLxx)はそれぞれ接続されているコネクタの近くに配置し、フィルターコネクタ間の配線は非常に短く配線すること。
The filters in this page (referred with FLxx) have to be placed near each connector connecting to respective filters. The traces between connector and filter have to be short as much as possible.

本項中、印のついたパターンは、AUDIOGNDでガードし、その上下はAUDIOGNDのベタパターンで覆うこと。また、Mxの下基板面およびその下の層には、デジタル系の信号線を配線しないこと。
The traces marked with have to be guarded both side and both adjacent layer with AUDIOGND. Underneath Mx on surface layer and in one more internal layer don't allow digital traces to be run.

Audio CN

					TITLE		
					VB161AX MAIN		
					DRAW.No.		CAST
					C1CP152845-X4		
Rev.	Date	Design	Check	Appr.	Description		Sheet
Design	02/05/07	Konaka	Check	Fuchida	FUJITSU LTD.		45 / 73





A

B

C

D

E

4

3

2

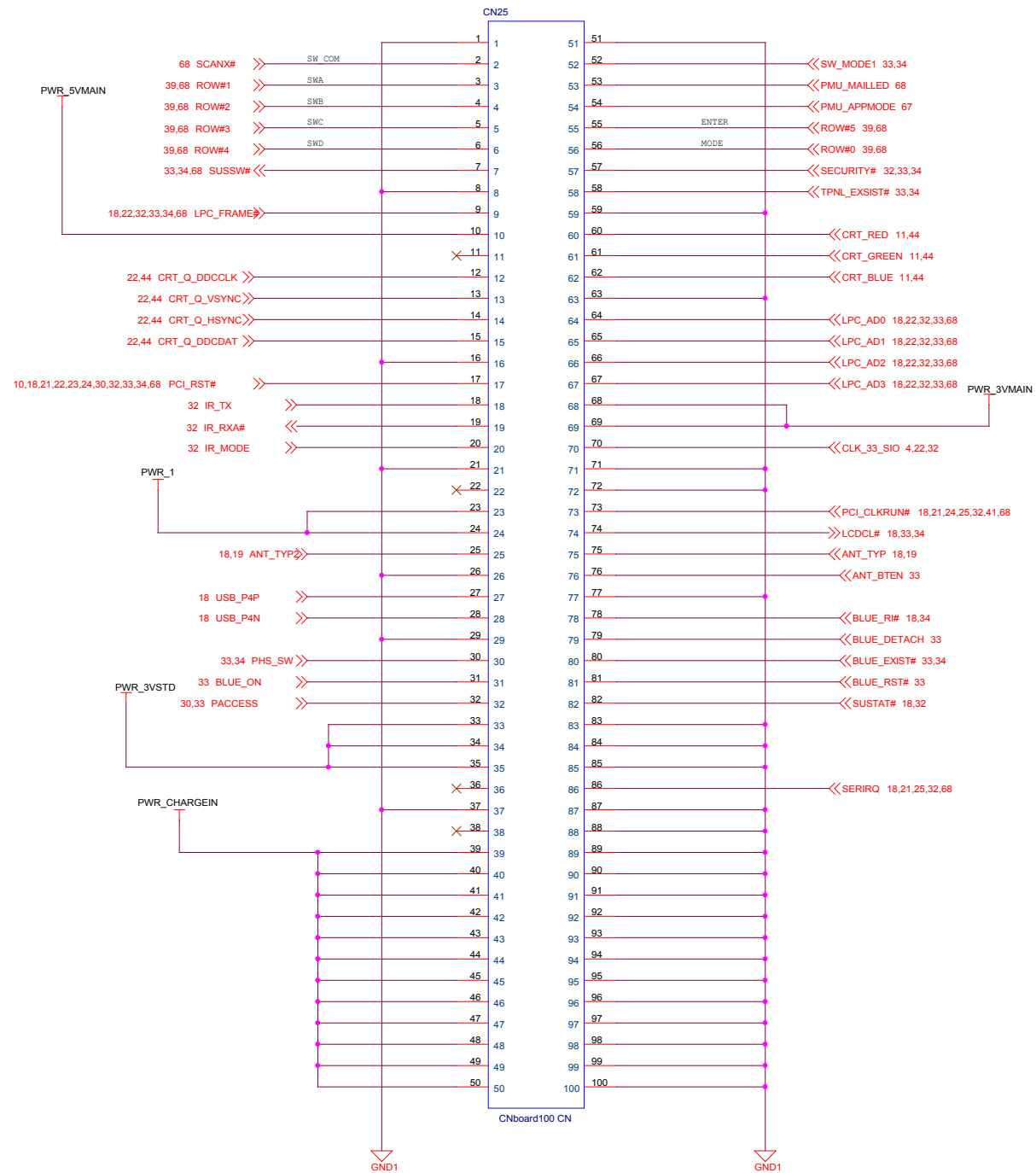
1

4

3

2

1



CN Board

						TITLE		
						VB161AX MAIN		
						DRAW. No.		CAST
						C1CP152845-X4		
Rev.	Date	Design	Check	Appr.	Description	Fujitsu Ltd.		Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	48 / 73

A

B

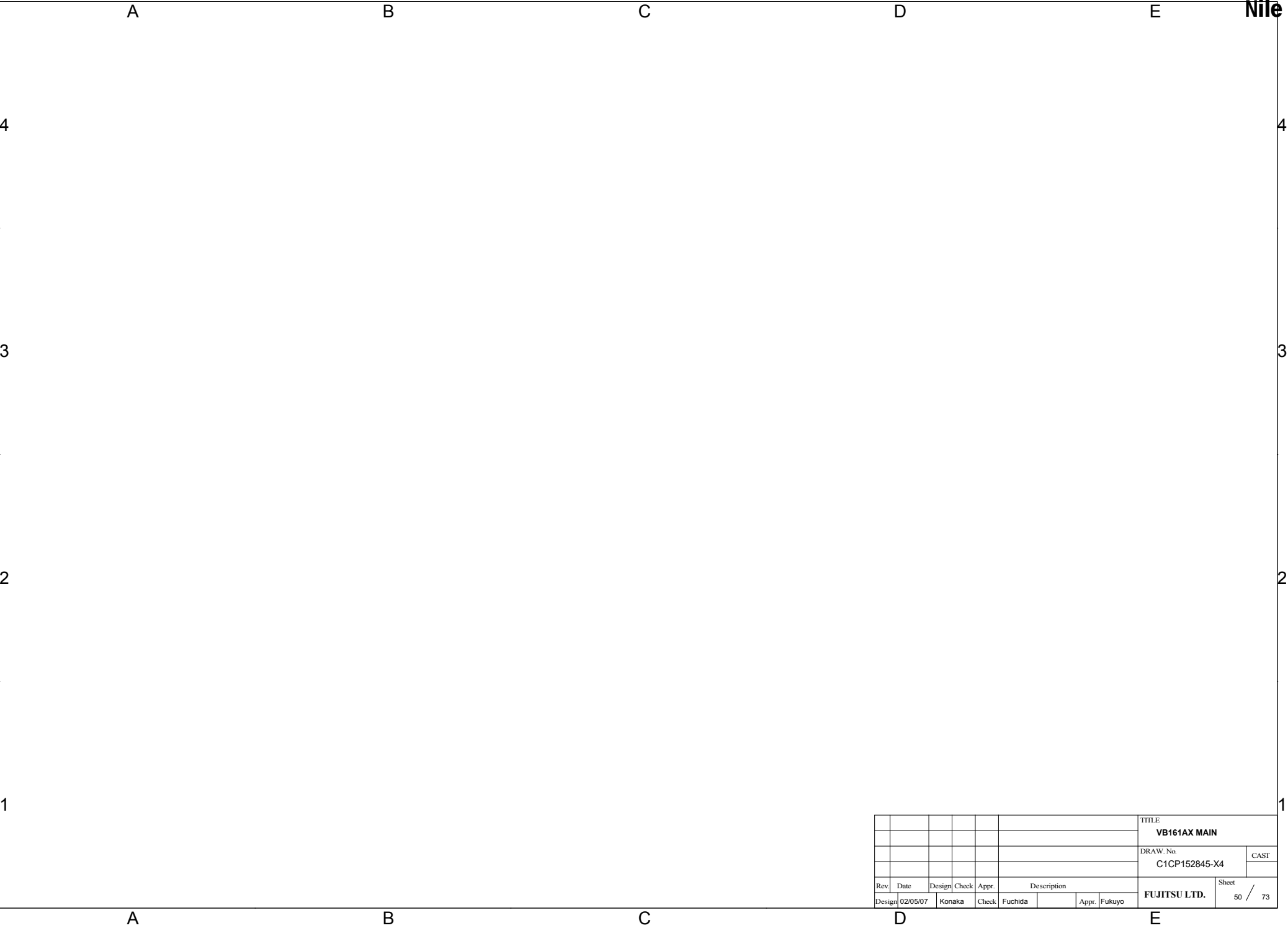
C

D

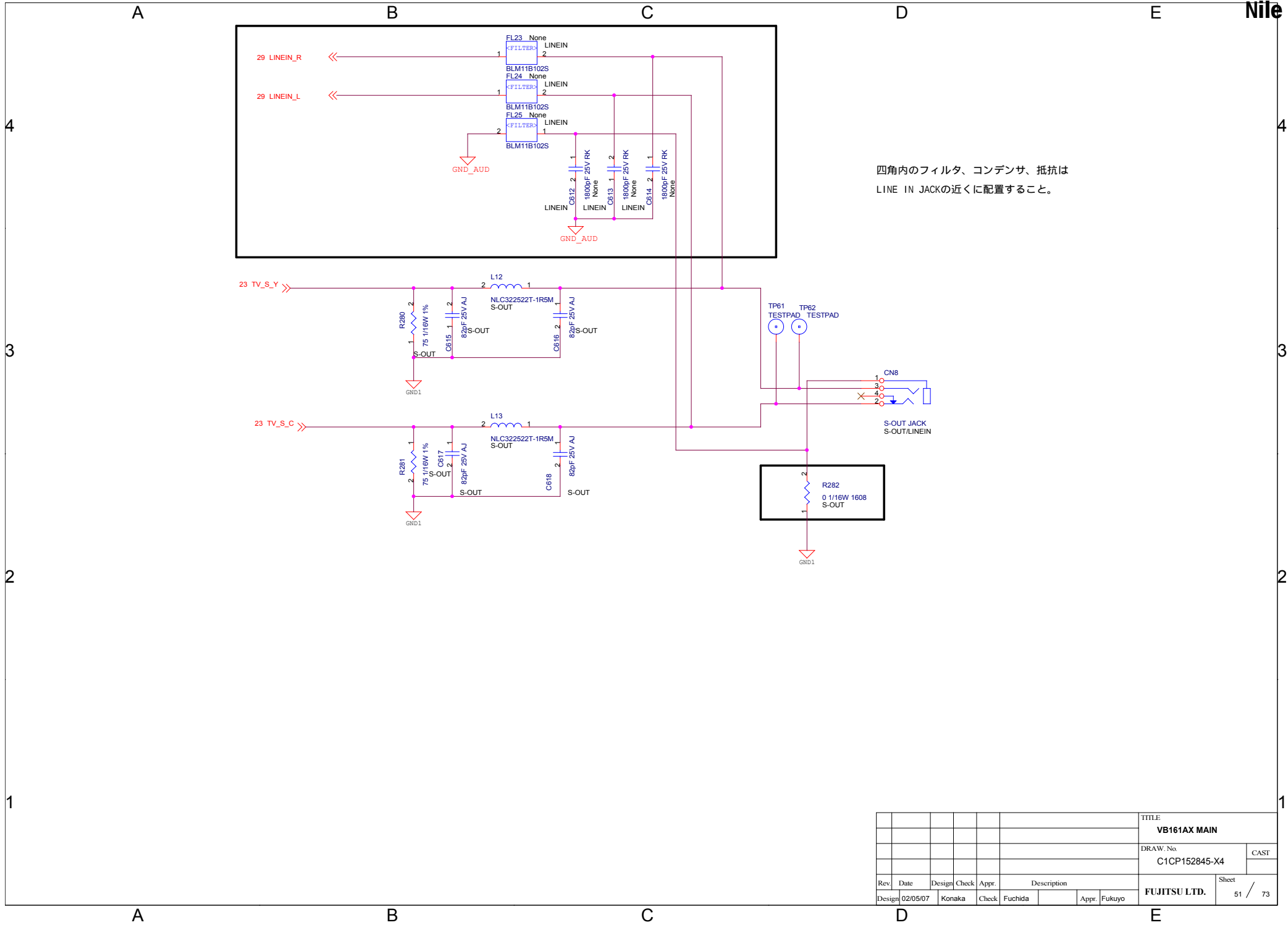
E



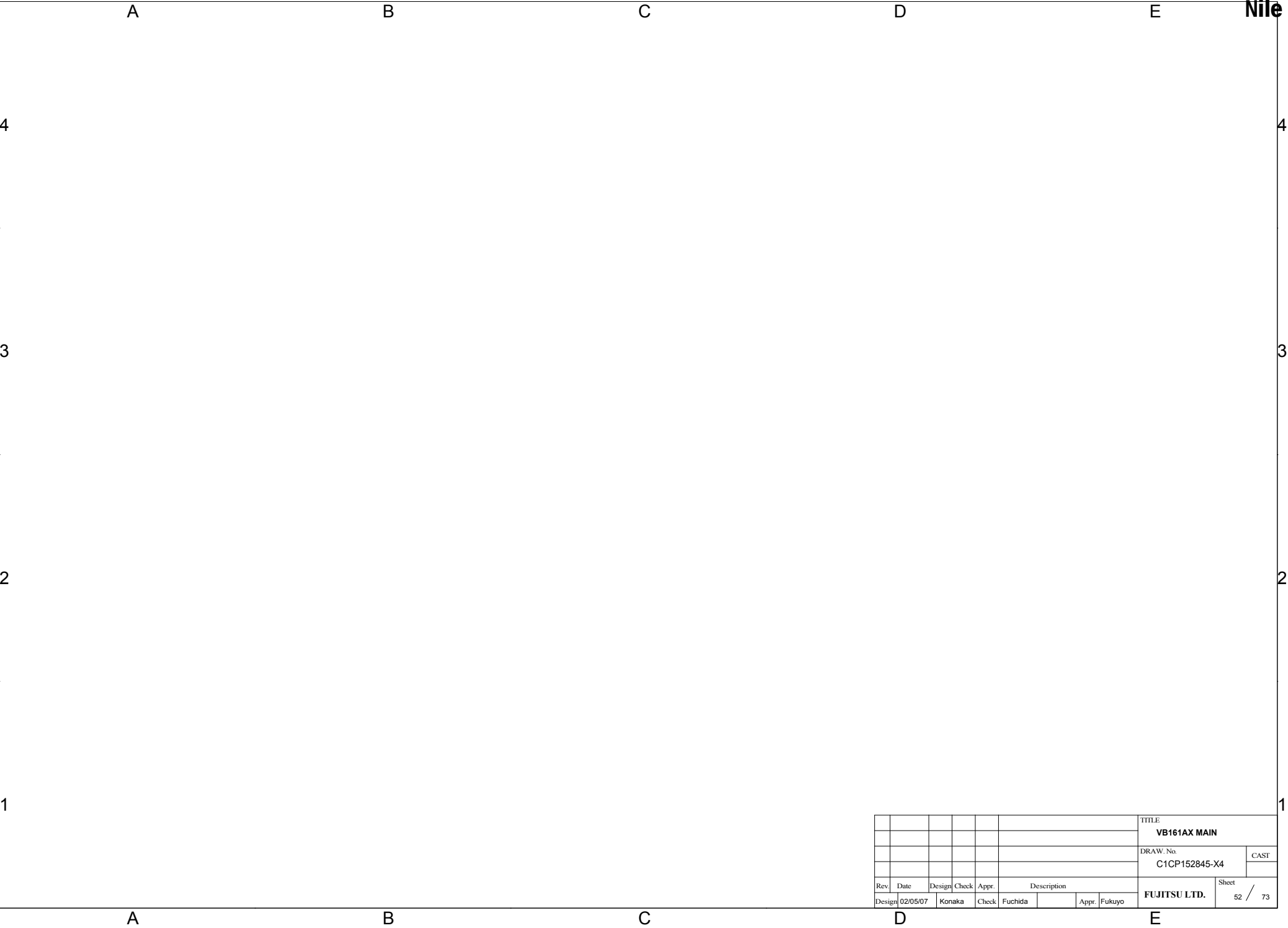
						TITLE	
						VB161AX MAIN	
						DRAW. No.	CAS
						C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description		Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr. Fukuyo	49 / 7
FUJITSU LTD.							



							TITLE	
							VB161AX MAIN	
							DRAW. No.	
							C1CP152845-X4	CAST
Rev.	Date	Design	Check	Appr.	Description			Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	50 / 73



											TITLE
											VB161AX MAIN
											DRAW. No.
											C1CP152845-X4
											CAST
Rev.	Date	Design	Check	Appr.	Description						Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo				51 / 73



							TITLE	
							VB161AX MAIN	
							DRAW. No.	
							C1CP152845-X4	CAST
Rev.	Date	Design	Check	Appr.	Description			Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	52 / 73



Diagram showing the connection of TP63, TP64, TP65, TP66, TP67, and TP130 to video inputs VID0 through VID5. Each TP pin is connected to a corresponding VID input line.

E

4 / 7



--	--

TITLE		VB161AX MAIN	
DRAW. No.		C1CP152845-X4	CAST
FUJITSU LTD.		Sheet	55 / 73

A

B

C

D

E

4

4

3

3

2

2

1

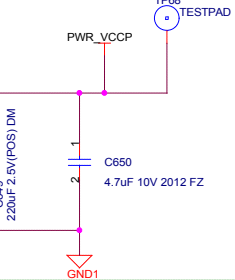
1

Change Net.SUSB# -> DLY_SUSB#

66,73 DLY_SUSB#

Change Net.PWR_1.2VMAIN -> PWR_VCCP

1.05V / 3.5A



Change Parts.R311:11K -> 16K 1/16W 0.5%(CAA34-K1J1602D)
Change Parts.R313:22K -> 51K 1/16W 0.5%(CAA34-K1J5102D)

Power/ DDC/ VCCP

								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAST
								C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description			Sheet	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	56	73

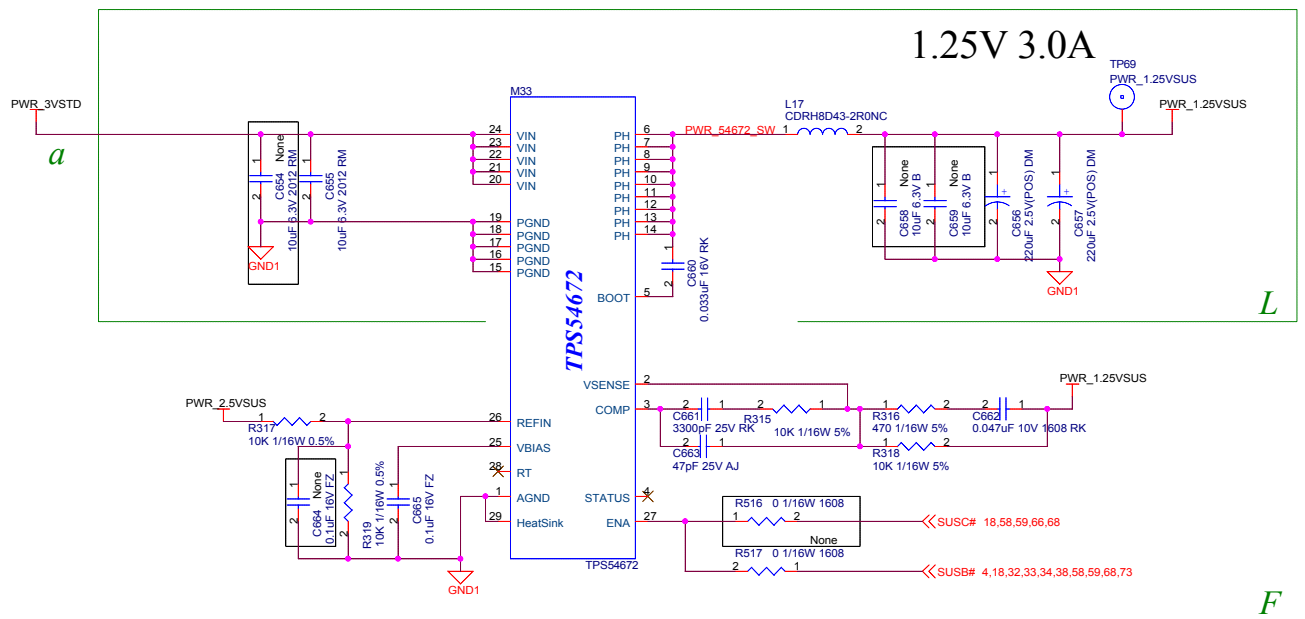
A

B

C

D

E



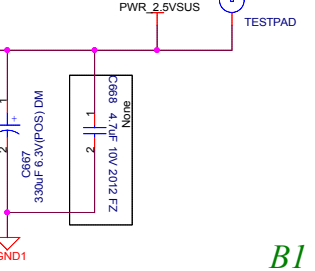
Power/ DDC/ 1.25VSus

								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAST
								C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description			Fujitsu Ltd.	Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo		57 / 73

Power/ DDC/ 2.5VSUS/1.2VMAIN

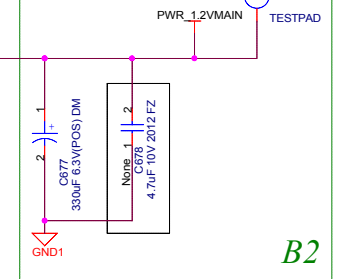
Change Parts.Q31:uPA2702GR(CA47001-1186)
Add Parts.Q63:uPA2702GR(CA47001-1186)
Change Parts.L18:CDEP105-4R3MC(CA53001-2056)

2.5V / 5A



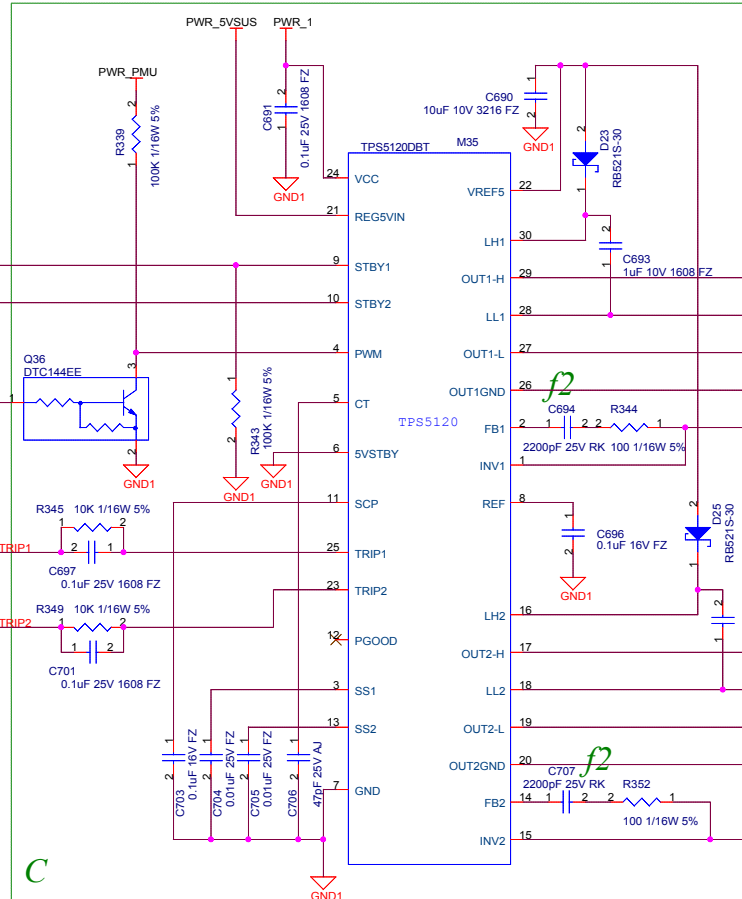
Change Net.PWR_VCCP -> PWR_1.2VMAIN

1.2V 2A



Change Parts.R332:16K -> 22K 1/16W 0.5%(CAA34-K1J2202D)
Change Parts.R335:68K -> 51K 1/16W 0.5%(CAA34-K1J5102D)

								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAST
								C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description			Fujitsu Ltd.	Sheet 58 / 73
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo		



	E
--	---

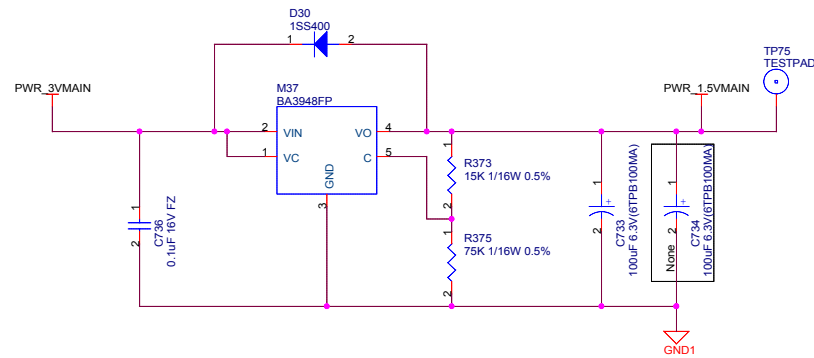
						TITLE	
						VB161AX MAIN	
						DRAW. No.	CAS
						C1CP152845-X4	
Rev	Date	Design	Check	Appr.	Description		Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr. Fukuyo	59 / 7
FUJITSU LTD.							



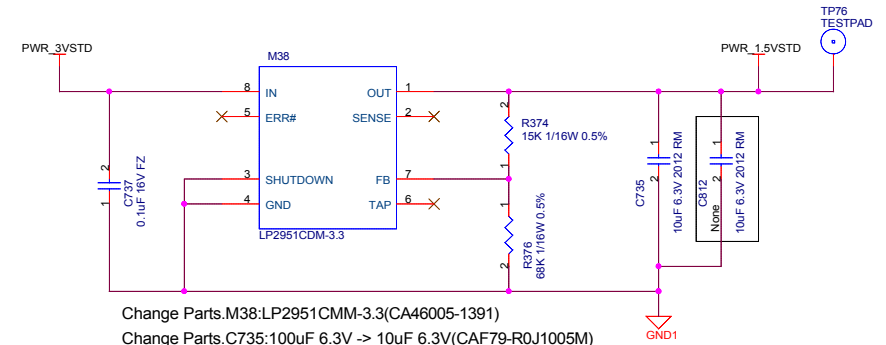
E

F	U	J	I	T	S	U	L	T	D	.																																																																																																																																			
---	---	---	---	---	---	---	---	---	---	---	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--

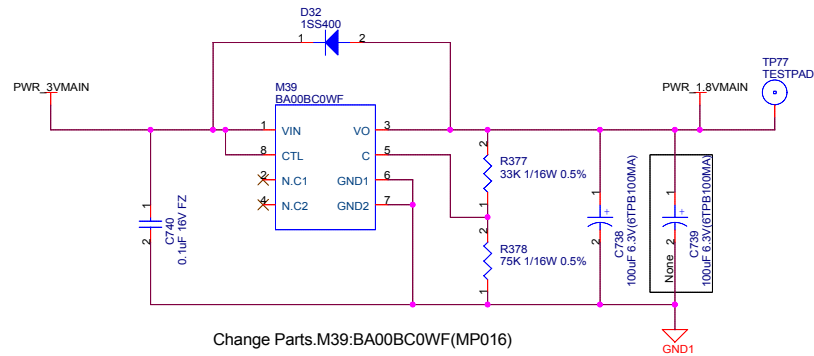
Nile



1.5V 0.74A



Change Parts.M38:LP2951CMM-3.3(CA46005-1391)
Change Parts.C735:100uF 6.3V -> 10uF 6.3V(CAF79-R0J1005M)
Add Parts.C812:10uF 6.3V(CAF79-R0J1005M)Reserve
Change Parts.R376:75K -> 68K 1/16W 0.5%(CAA34-K1J6802D)



1.8V 0.3A

Power/ LDO/ System

										TITLE	
										VB161AX MAIN	
										DRAW. No.	
										C1CP152845-X4	
										CAST	
Rev.	Date	Design	Check	Appr.	Description					Sheet	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	FUJITSU LTD.			61 / 73

A

B

C

D

E

Nile

4

4

3

3

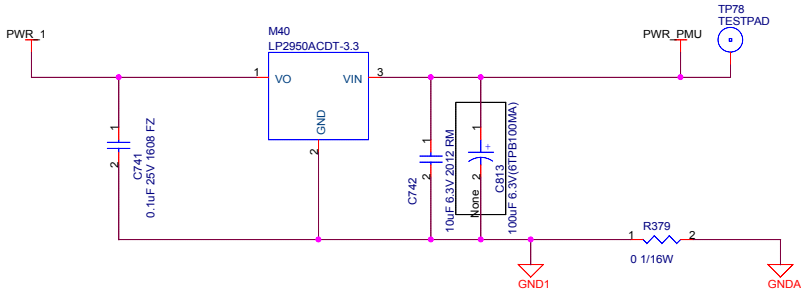
2

2

1

1

Change Parts.M40:LP2950ACDT-3.3



3.3V 50mA

Power/ LDO/ PMU

												TITLE
												VB161AX MAIN
												DRAW. No.
												C1CP152845-X4
												CAST
Rev.	Date	Design	Check	Appr.	Description							Sheet
Design	02/05/07	Konaka	Check	Fuchida				Appr.	Fukuyo			FUJITSU LTD.
											62 / 73	

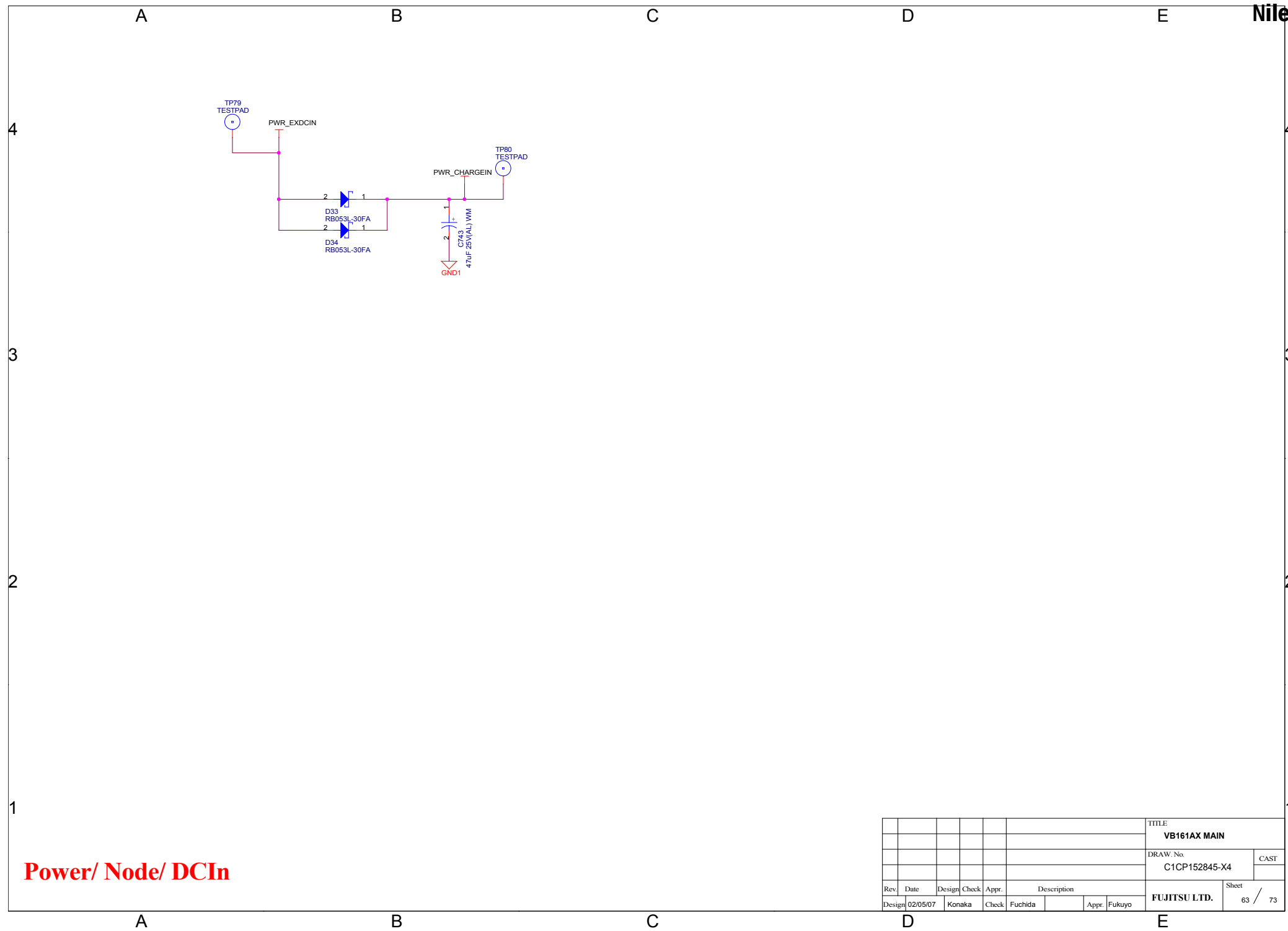
A

B

C

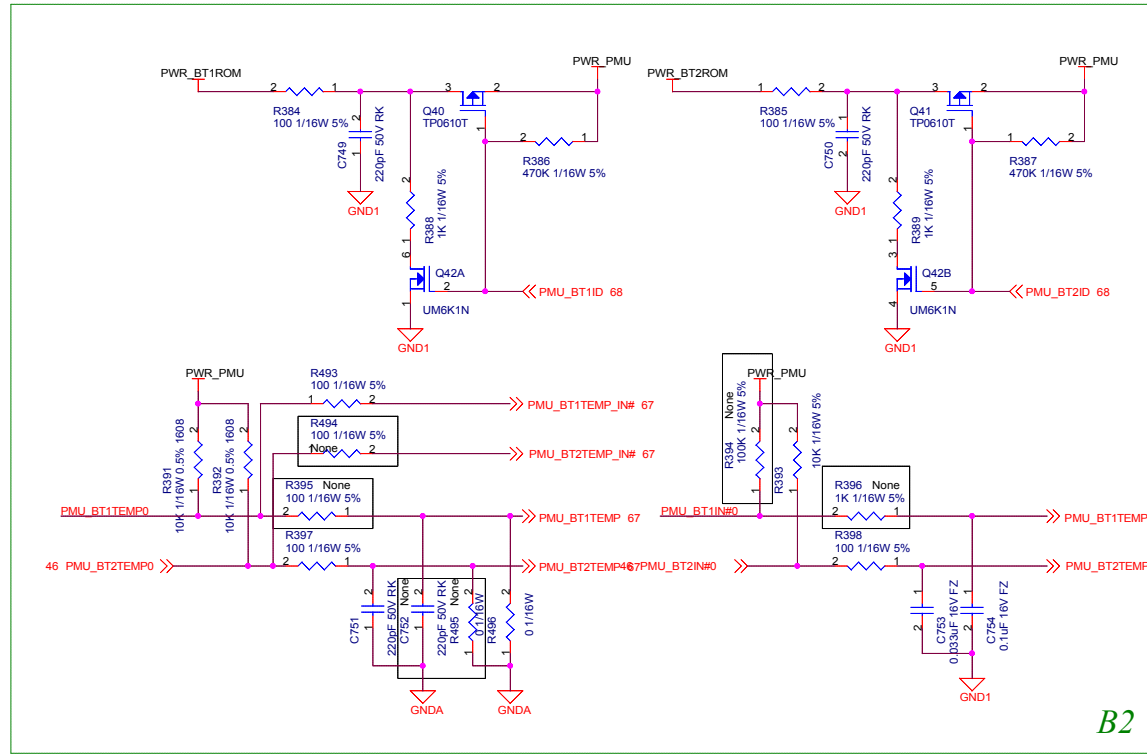
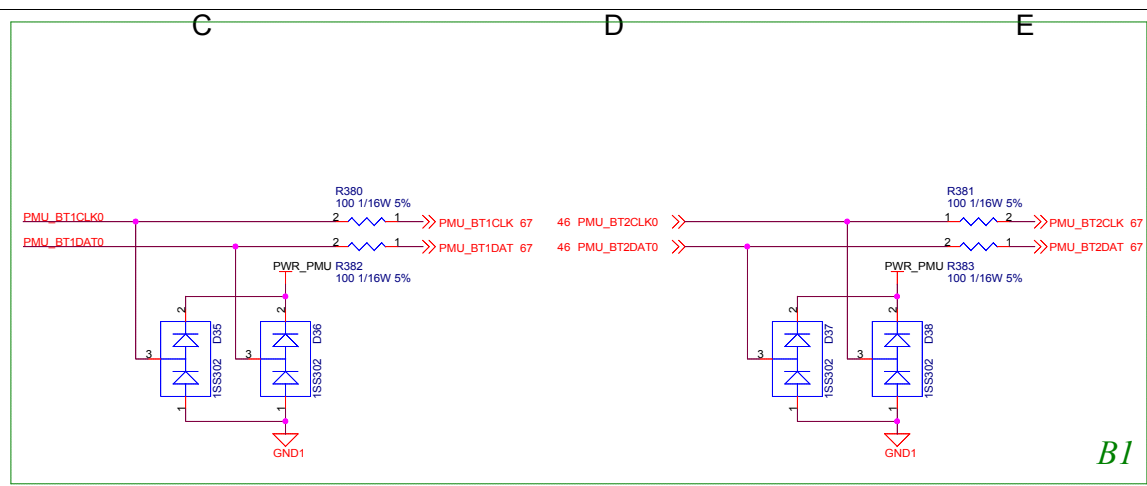
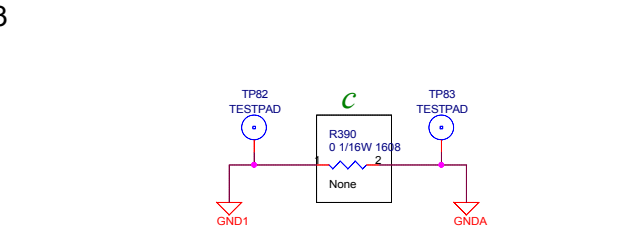
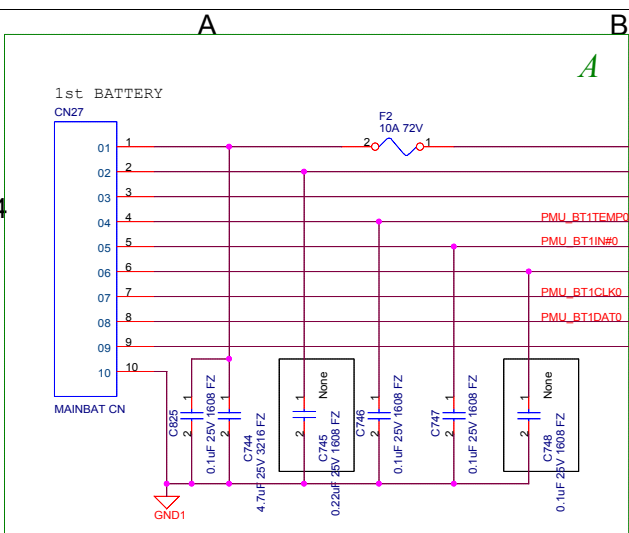
D

E



Power/ Node/ DCIn

										TITLE	
										VB161AX MAIN	
										DRAW. No.	CAST
										C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description					Sheet	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	FUJITSU LTD.		63 / 73	



Power/ Node/ Battery

							TITLE	
							VB161AX MAIN	
							DRAW. No.	C1CP152845-X4
							CAST	
Rev.	Date	Design	Check	Appr.	Description		FUJITSU LTD.	
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	Sheet 64 / 73

										TITLE		
										VB161AX MAIN		
										DRAW. No.		CAST
										C1CP152845-X4		
Rev.	Date	Design	Check	Appr.	Description						Sheet	
Design	02/05/07	Konaka	Check	Fuchida			Appr.	Fukuyo		FUJITSU LTD.	65 /	73

A

B

C

D

E

3.3V 2.0A

3.3V 2.0A

3.3V 1.0A

5.0V 2.0A

5.0V 2.0A

Power/ Node/ Swicth

A

B

C

D

E

												TITLE
												VB161AX MAIN
												DRAW. No.
												C1CP152845-X4
												CAST
Rev.	Date	Design	Check	Appr.	Description							Sheet
Design	02/05/07	Konaka	Check	Fuchida				Appr.	Fukuyo			66 / 73

A

B

C

D

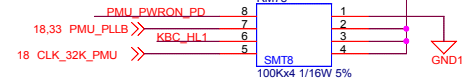
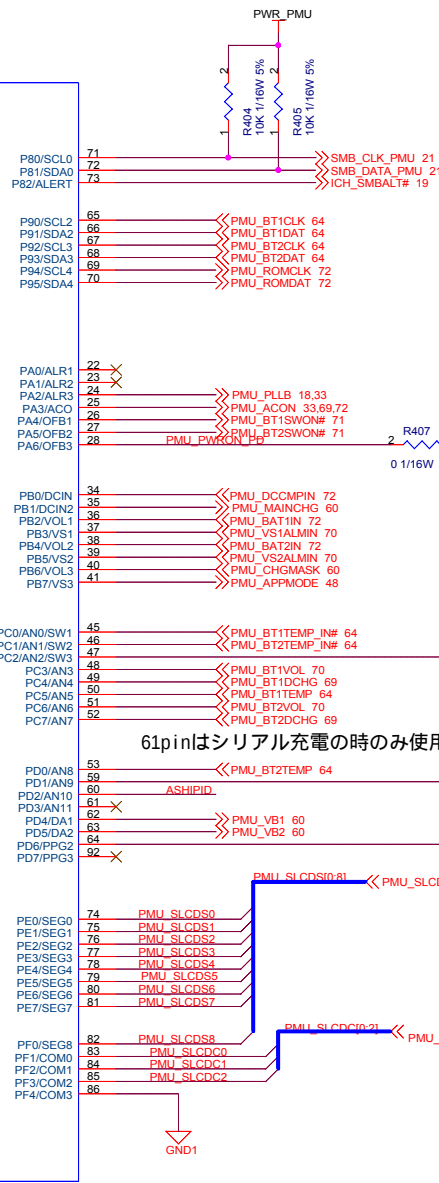
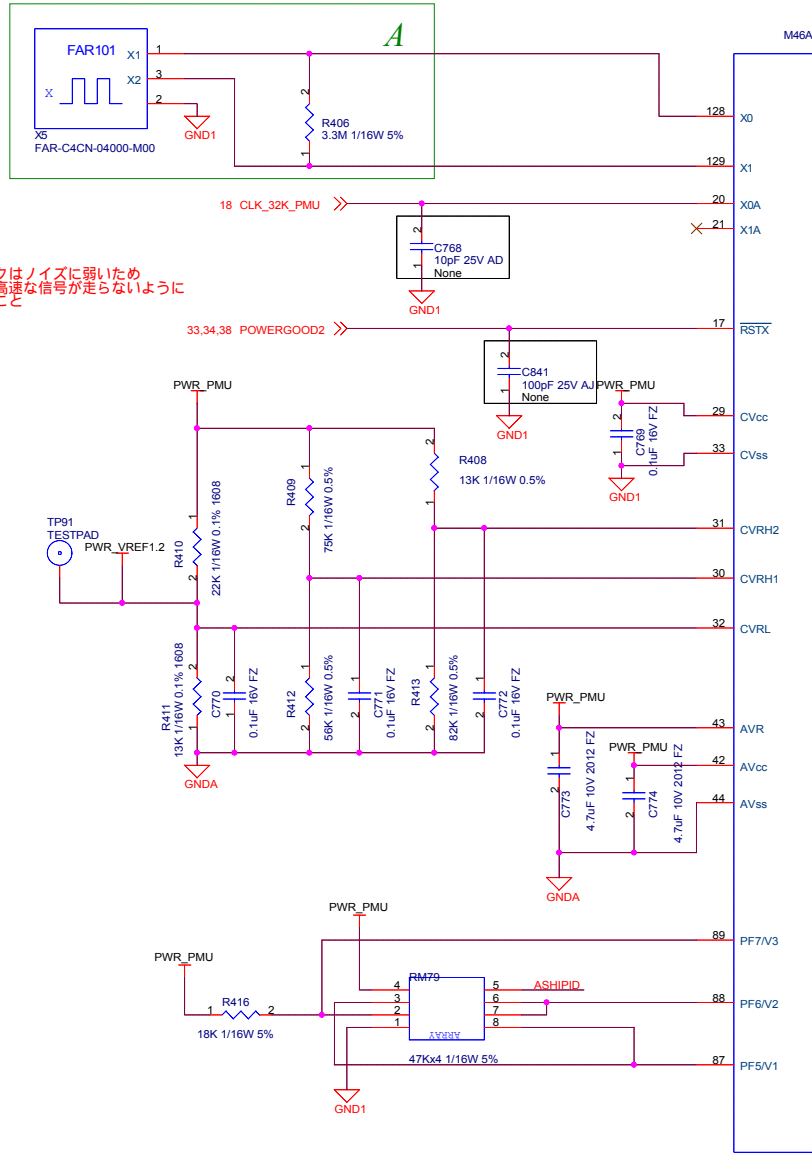
E

4

3

2

1



本クロックはノイズに弱い
ため上下層に高速な信号が走らないように
配線すること

61pinはシリアル充電の時のみ使用するのか？ 要確認

Power/ PMU/ LUNA1

						TITLE	
						VB161AX MAIN	
						DRAW. No.	CAST
						C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description		
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo
						FUJITSU LTD.	Sheet
							67 / 73





								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAS
								C1CP152845-X4	
Rev	Date	Design	Check	Appr.	Description				Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	FUJITSU LTD.	69 / 73

Power/ PMU/ AmMeter

A B C D E

PWR_1

R440
100K 1/16W 5%

64 PMU_VSENSE1

R438
1K 1/16W 5%

R439
330K 1/16W 0.1% 1608

Q49
2SK3019

C793
0.01uF 25V FZ

R441
62K 1/16W 0.1% 1608

R442
82K 1/16W 0.1% 1608

C794
0.1uF 16V FZ

TP94
TESTPAD

PMU_SC1IN 71

PMU_BT1VOL 67

PMU_VS1ALMIN 67

46 PMU_VSENSE2

R443
1K 1/16W 5%

R444
330K 1/16W 0.1% 1608

Q50
2SK3019

C795
0.01uF 25V FZ

R445
62K 1/16W 0.1% 1608

R446
82K 1/16W 0.1% 1608

C796
0.1uF 16V FZ

TP95
TESTPAD

PMU_SC2IN 65,71

PMU_BT2VOL 67

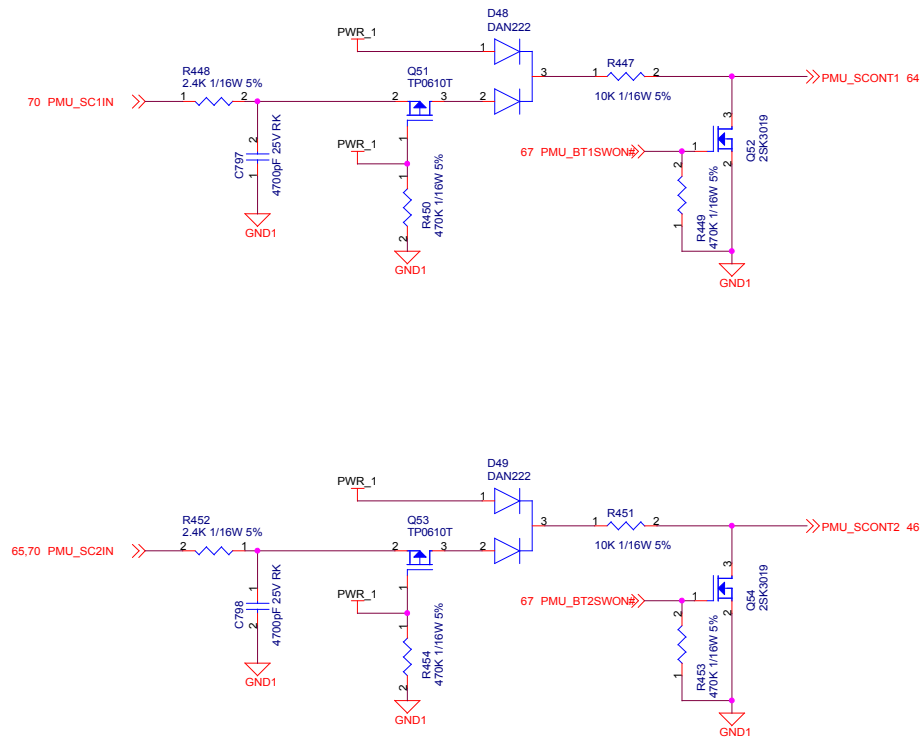
PMU_VS2ALMIN 67

Power/ PMU/ VolMeter

A B C D E

												TITLE
												VB161AX MAIN
												DRAW. No.
												C1CP152845-X4
												CAST
Rev.	Date	Design	Check	Appr.	Description							Sheet
Design	02/05/07	Konaka	Check	Fuchida								FUJITSU LTD.
												70 / 73

E

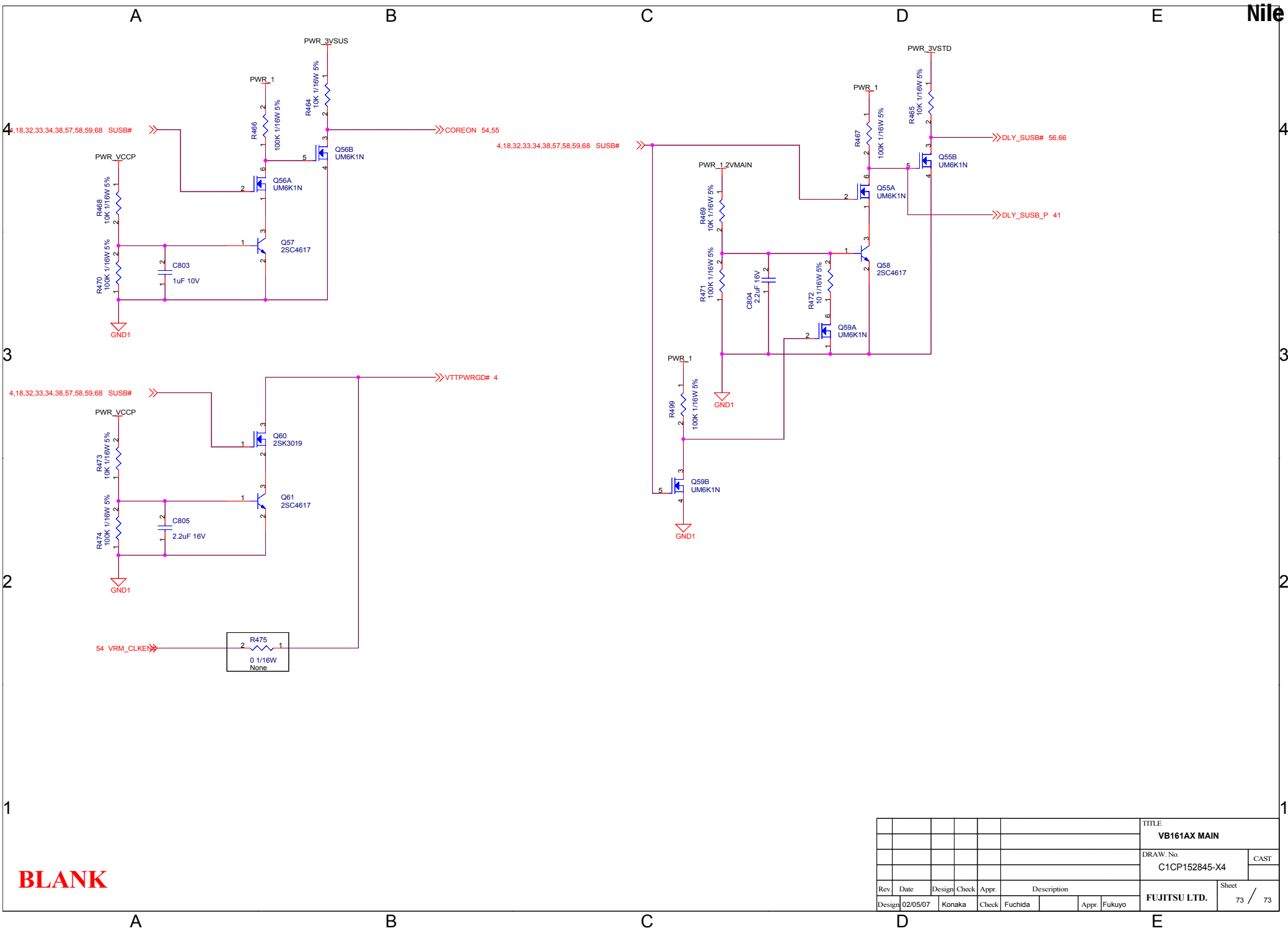


Power/ PMU/ Scont

					TITLE		
					VB161AX MAIN		
					DRAW. No.		CAST
					C1CP152845-X4		
Rev.	Date	Design	Check	Appr.	Description		Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr. Fukuyo	71. / 73



								TITLE	
								VB161AX MAIN	
								DRAW. No.	CAS
								C1CP152845-X4	
Rev.	Date	Design	Check	Appr.	Description				Sheet
Design	02/05/07	Konaka	Check	Fuchida		Appr.	Fukuyo	FUJITSU LTD.	72 / 73



BLANK