

L53RI0 M/B / Pa 2510

01	COVER PAGE
02	BLOCK DIAGRAM
03	MISCELLANEOUS
04	CPU HT INTERFACE
05	CPU DDR2 INTERFACE
06	CPU POWER
07	DDR2 DIMMs
08	DDR2 TERMINATIONS
09	CLOCK GENERATOR
10	RS690MC HT&PCI-E LINK I/F
11	RS690MC PLL&VIDEO I/F
12	RS690MC POWER
13	SB600 PCIE/PCI/CPU/LPC
14	SB600 ACPI/GPIO/USB/AUDIO
15	SB600 SATA/IDE/HWM
16	SB600 POWER
17	FAN/MINI CARD/ODD/SATA CON
18	CRT/LCD/S-VIDEO CON
19	3G/WEB CAM/NEW CARD/USB CON
20	LAN-RTL8101E
21	1394/CARD READER (OZ128)
22	CARD READER/DEBUG/BT/IR
23	AUDIO/LED/SW BD/TP CON
24	EC-IT8512E/BIOS
25	POWER SWITCH
26	SYSTEM POWER (MAX8744)
27	+CPU_CORE (OZ828)
28	+1.2VS/+1.2V_NB (ISL6227)
29	+1.8V_DDR (OZ811)
30	+0.9V_DDR/+1.5V
31	DC_IN/BATT_IN/CHARGER
32	REV. HISTORY
33	
34	

MADE IN TAIWAN
BU2-BK100

FSC Amilo Pa 2510

L53RI0 M/B and Daughter P/N BD List :

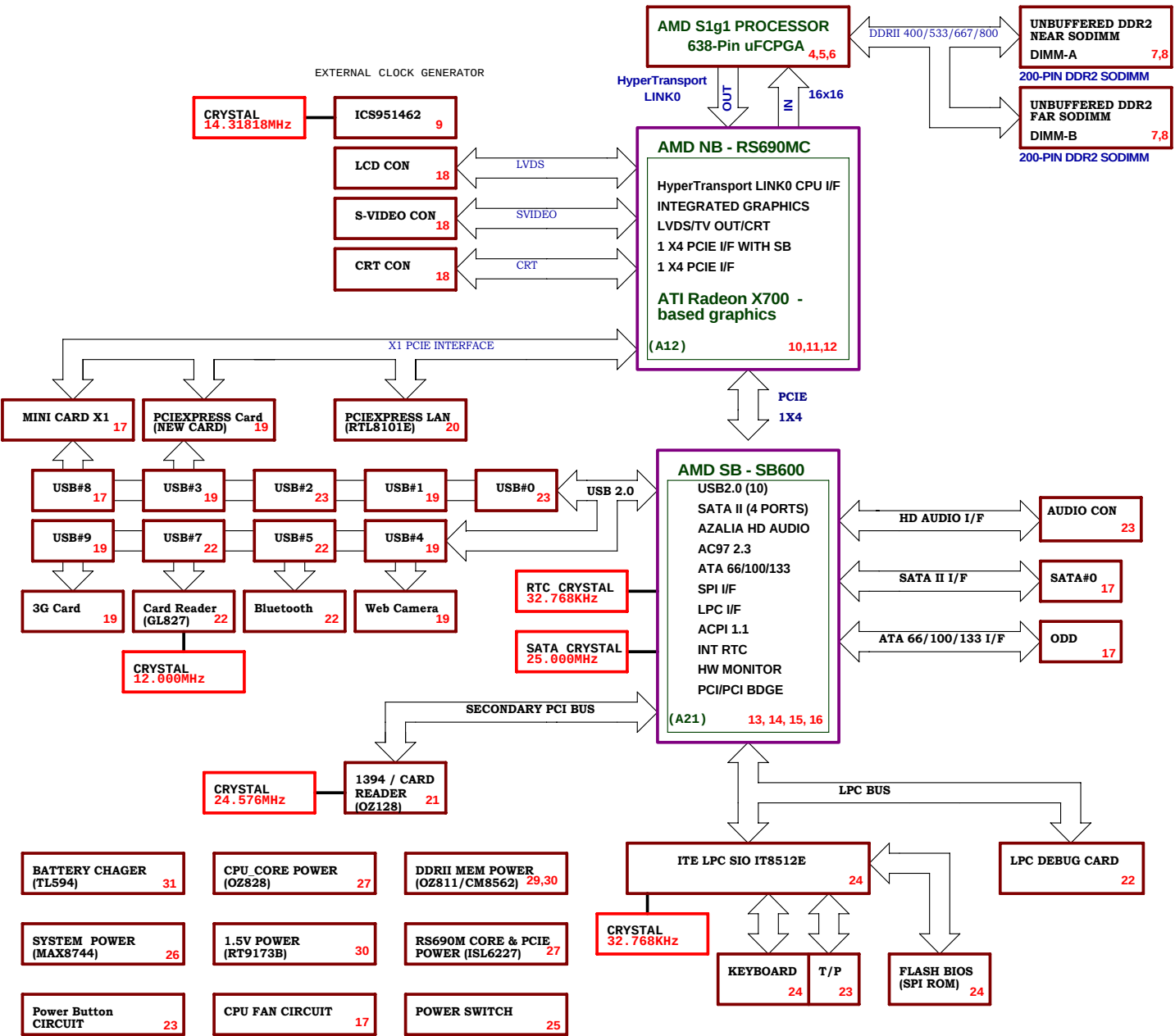
82GL53000-B0F 37GL53000-B0	MAIN BOARD ASSY L53RI0 REV.B FSC PCB MAIN BD FOR L53RI0 REV:B
80G2L7020-C0 35G2L5020-C0	AUDIO BD ASSY L70II0 REV:C PCB AUDIO BD FOR L50II0 REV:C
80G8L5000-C0 35G8L5000-C0	TOUCHPAD BD FOR L50RI0 R:C PCB TOUCHPAD BD FOR L50RI0 R:C
80G9L5000-C0 35G9L5000-C0	MODEM BD FOR L50RI0 R:C PCB MODEM BD FOR L50RI0 R:C
80G5L5000-C0 35G5L5000-C0	SWITCH BD FOR L50RI0 R:C PCB SWITCH BD FOR L50RI0 R:C
80GPL5000-C0 35GPL5000-C0	ODD BD FOR L50RI0 R:C PCB ODD BD FOR L50RI0 R:C

L53RI0 Other Daughter BD P/N List:

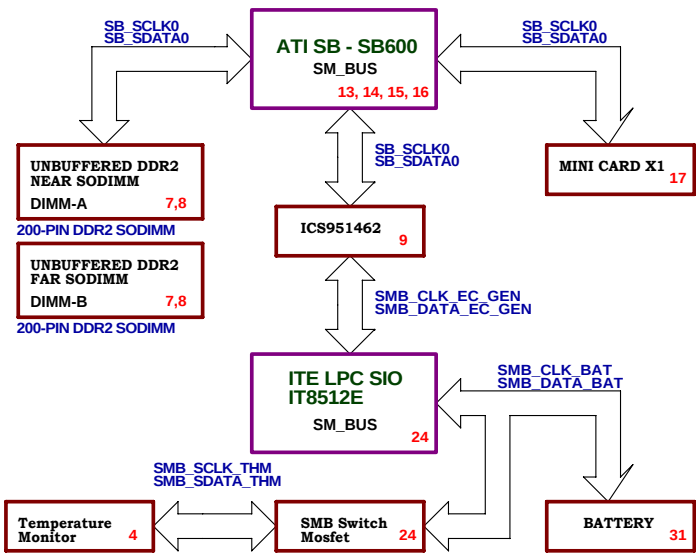
80GJL5100-B0 35GJL5100-B0	3G BD ASSY L51AIX REV:B PCB 3G BD FOR L51AIX REV:B
80GYL5310-A0 35GYL5310-A0	IR BD ASSY L53IIX VER:A PCB IR BD FOR L53IIX VER:A

L53RI0 BLOCK DIAGRAM

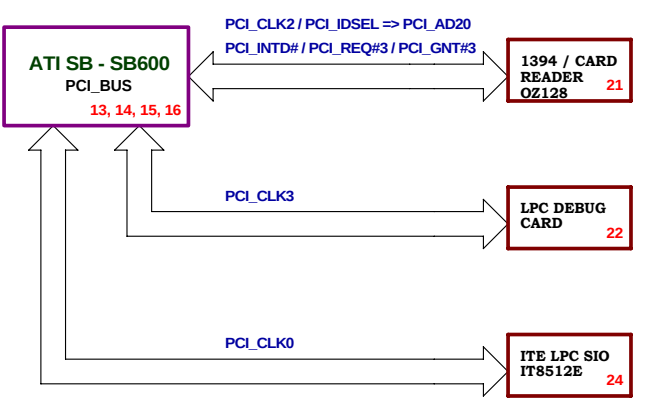
SYSTEM BLOCK DIAGRAM



SB_BUS BLOCK DIAGRAM



PCI_BUS BLOCK DIAGRAM



Elitegroup Computer System

BLOCK DIAGRAM

Size	Document Number	Rev
3721	L53RI0	BF.1
Date:	Tuesday, February 27, 2007	Sheet 2 of 32

CPU AMD S1g1 POWER RAIL		LAPTOP MODE				
		S0	S1	S3	S4	S5
+CPU_CORE	VID[5:0]	ON	ON	OFF	OFF	OFF
CPU_VDDA	+2.5V	ON	ON	ON	OFF	OFF
VLDI	+1.2V	ON	ON	ON	OFF	OFF
VDDIO	+1.8V	ON	ON	ON	OFF	OFF
VTT	+0.9V	ON	ON	ON	OFF	OFF

NB RS690MC POWER RAIL		LAPTOP MODE				
		S0	S1	S3	S4	S5
AVDD	+3.3V	ON	ON	ON	OFF	OFF
AVDDDI	+1.8V	ON	ON	ON	OFF	OFF
AVDDQ	+1.8V	ON	ON	ON	OFF	OFF
HTPVDD	+1.8V	ON	ON	ON	OFF	OFF
LPVDD	+1.8V	ON	ON	ON	OFF	OFF
LVDDR18D	+1.8V	ON	ON	ON	OFF	OFF
LVDDR33	+3.3V	ON	ON	ON	OFF	OFF
PLLVD12	+1.2V	ON	ON	ON	OFF	OFF
PLLVD18	+1.8V	ON	ON	ON	OFF	OFF
VDD_HT	+1.2V	ON	ON	ON	OFF	OFF
VDD18	+1.8V	ON	ON	ON	OFF	OFF
VDDA_12	+1.2V	ON	ON	ON	OFF	OFF
VDDC	+1.0/1.2V	ON	ON	OFF	OFF	OFF
VDDPLL	+1.2V	ON	ON	ON	OFF	OFF
VDDR	+1.8V	ON	ON	ON	OFF	OFF
VDDR3	+3.3V	ON	ON	ON	OFF	OFF

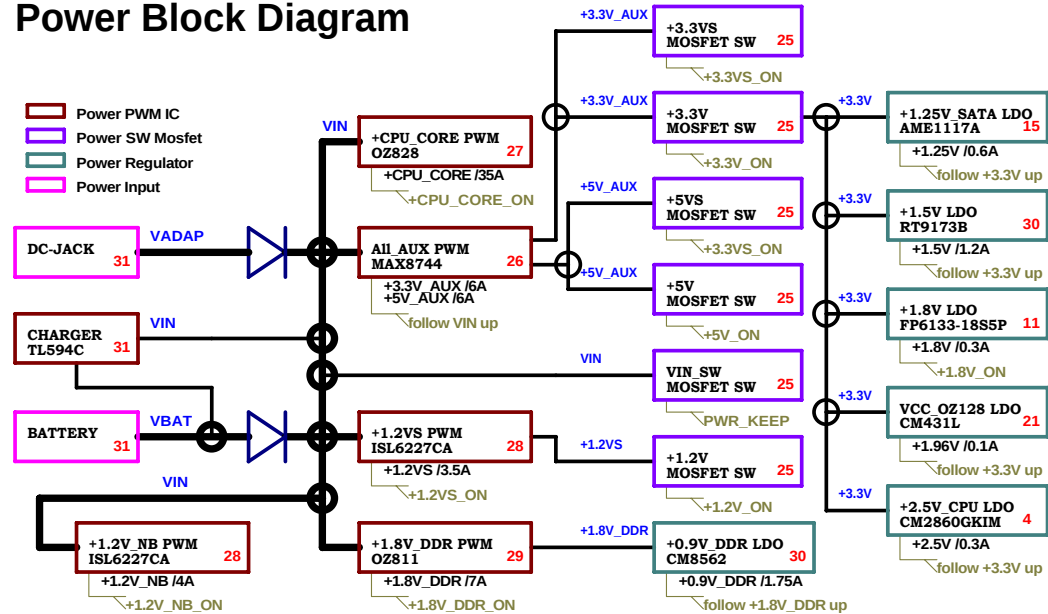
SB SB600 POWER RAIL		LAPTOP MODE				
		S0	S1	S3	S4	S5
PCIE_PVDD	+1.2V	ON	ON	ON	OFF	OFF
PCIE_VDDR	+1.2V	ON	ON	ON	OFF	OFF
PLLVD SATA	+1.25V	ON	ON	ON	OFF	OFF
AVDD SATA	+1.25V	ON	ON	ON	OFF	OFF
XTLVDD SATA	+3.3V	ON	ON	ON	OFF	OFF
AVDD_HWM	+3.3V	ON	ON	ON	OFF	OFF
VDDQ	+3.3V	ON	ON	ON	OFF	OFF
VDD	+1.2V	ON	ON	ON	OFF	OFF
CPU_PWR	+1.8V	ON	ON	ON	OFF	OFF
V5_VREF	+5V	ON	ON	ON	OFF	OFF
AVDDCK_3.3V	+3.3V	ON	ON	ON	OFF	OFF
AVDDCK_1.2V	+1.2V	ON	ON	ON	OFF	OFF
AVDD_USB	+3.3V	ON	ON	ON	OFF	OFF
AVDDC	+3.3V	ON	ON	ON	OFF	OFF
S5_3.3V	+3.3V	ON	ON	ON	OFF	OFF
S5_1.2V	+1.2V	ON	ON	ON	OFF	OFF
USB_PHY_1.2V	+1.2V	ON	ON	ON	OFF	OFF
SB_VBAT	+3.0V	ON	ON	ON	ON	ON

PCI DEVICES IRQ ROUTING TABLE				
DEVICE	IDSEL#	REQ/GNT#	PCI INT	CLOCK
LPC DEBUG	N/A	N/A	N/A	PCI_CLK3
EC ITE8512EX	N/A	N/A	N/A	PCI_CLK0
1394 OZ128	AD20	3 / 3	H	PCI_CLK2

SYSTEM POWER RAIL		LAPTOP MODE				
Voltage Name	Control Pin	S0	S1	S3	S4	S5
+1.2V	+1.2V_ON	ON	ON	ON	OFF	OFF
+1.2V_NB	+1.2V_NB_ON	ON	ON	OFF	OFF	OFF
+1.25V SATA	follow +3.3V up	ON	ON	ON	OFF	OFF
+1.5V	follow +3.3V up	ON	ON	ON	OFF	OFF
+1.8V	+1.8V_ON	ON	ON	ON	OFF	OFF
+2.5V_CPU	follow +3.3V up	ON	ON	ON	OFF	OFF
+3.3V	+3.3V_ON	ON	ON	ON	OFF	OFF
+5V	+5V_ON	ON	ON	ON	OFF	OFF
+CPU_CORE	+CPU_CORE_ON	ON	ON	OFF	OFF	OFF
VCC_OZ128	follow +3.3V up	ON	ON	ON	OFF	OFF
+0.9V_DDR	follow +1.8V_DDR up	ON	ON	ON	OFF	OFF
+1.2VS	+1.2VS_ON	ON	ON	ON	OFF	OFF
+1.8V_DDR	+1.8V_DDR_ON	ON	ON	ON	OFF	OFF
+3.3VS	+3.3VS_ON	ON	ON	ON	OFF	OFF
+5VS	+3.3VS_ON	ON	ON	ON	OFF	OFF
VIN_SW	PWR_KEEP	ON	ON	ON	OFF	OFF
+3.3V_AUX	follow VIN up	ON	ON	ON	ON	ON
+5V_AUX	follow VIN up	ON	ON	ON	ON	ON

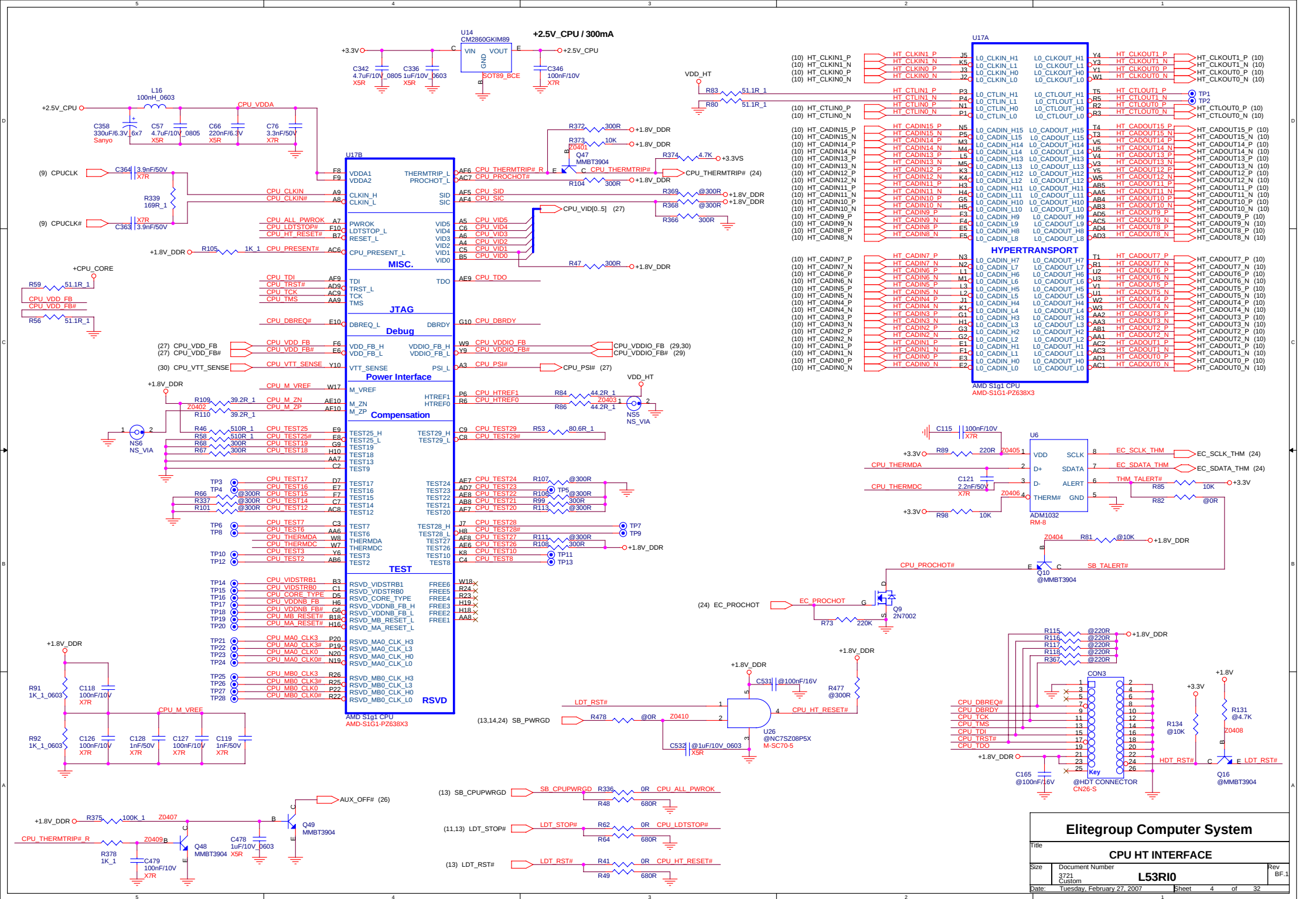
PS. xxxxxx USB S3 Wake-Up BIOS Default Setup

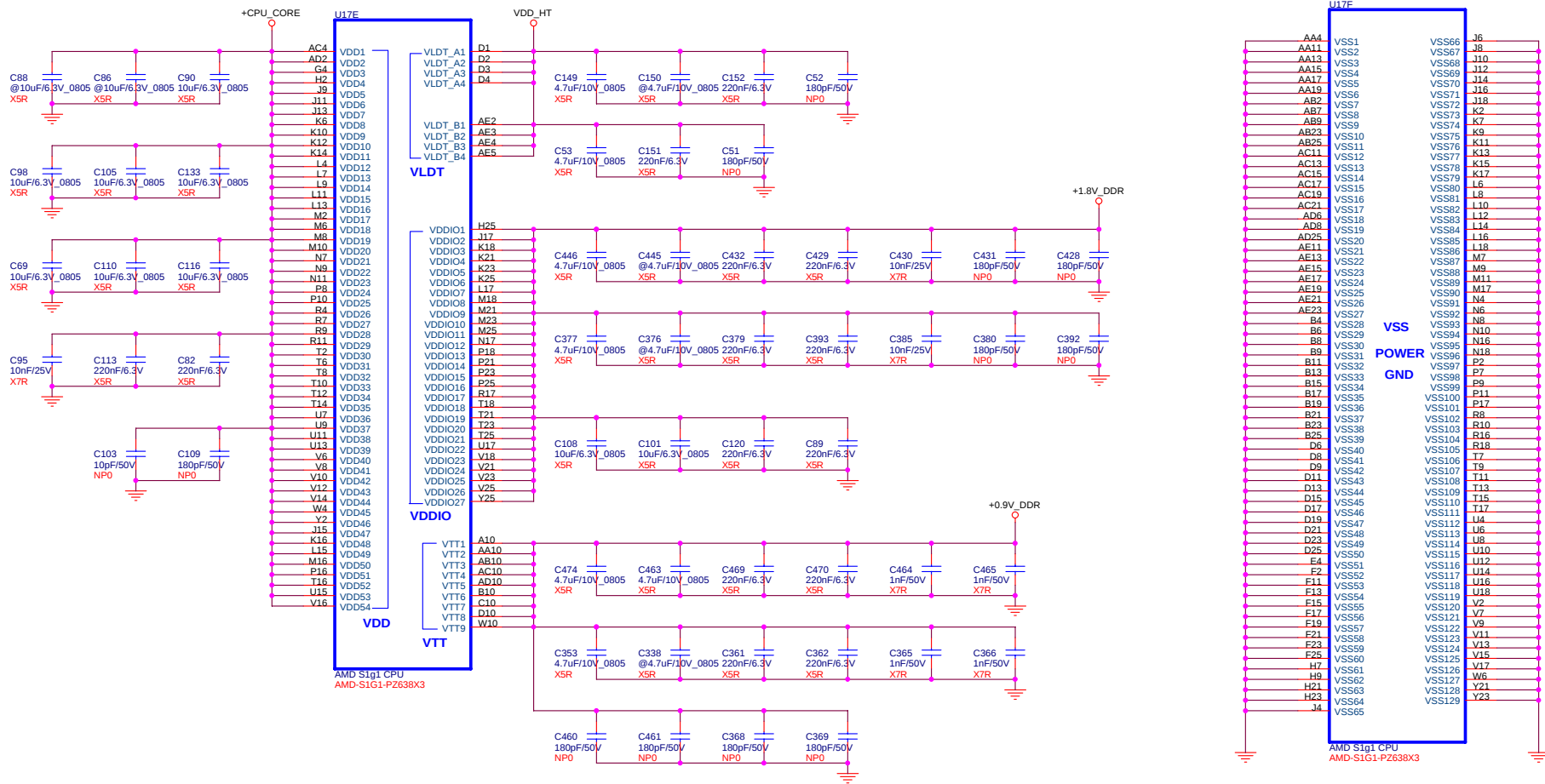
Power Block Diagram

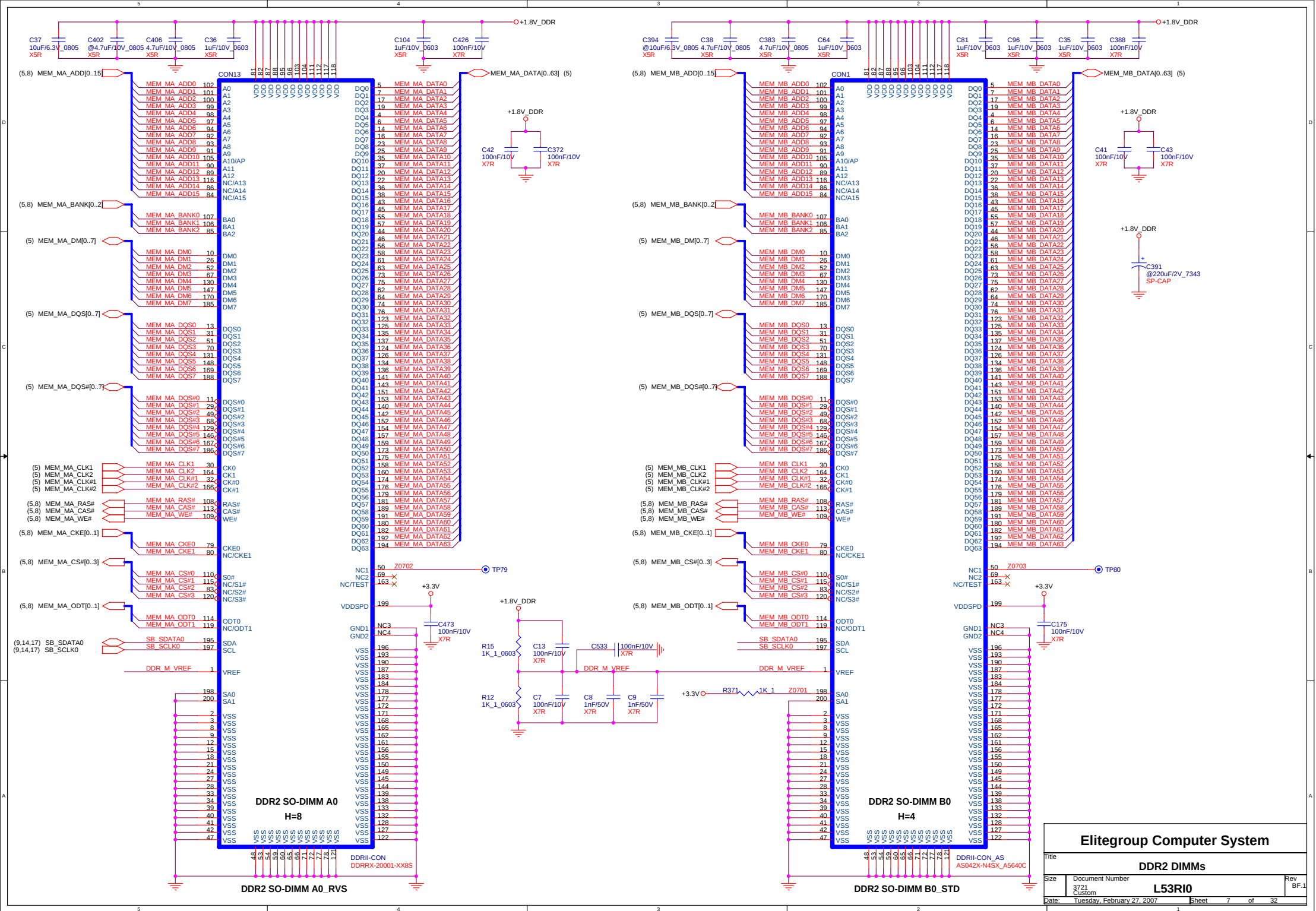


USB PORTS DEVICES TABLE										
USB Ports	USBP0 USBN0	USBP1 USBN1	USBP2 USBN2	USBP3 USBN3	USBP4 USBN4	USBP5 USBN5	USBP6 USBN6	USBP7 USBN7	USBP8 USBN8	USBP9 USBN9
Devices	Audio BD USB_0	M/B USB_1	Audio BD USB_2	New Card USB_3	Webcam USB_4	Bluetooth USB_5	N/A	Card Reader USB_7	Mini Card USB_8	3G BD USB_9

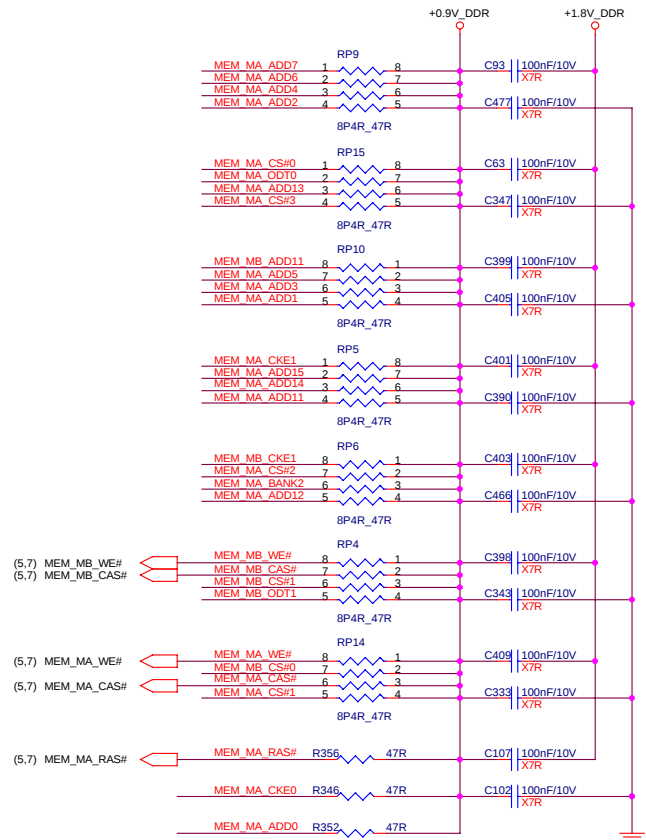
Elitegroup Computer System		
MISCELLANEOUS		
Title		
Size	Document Number 3721 Custom	Rev BF.1
Date	Tuesday, February 27, 2007	
	Sheet 3 of 32	





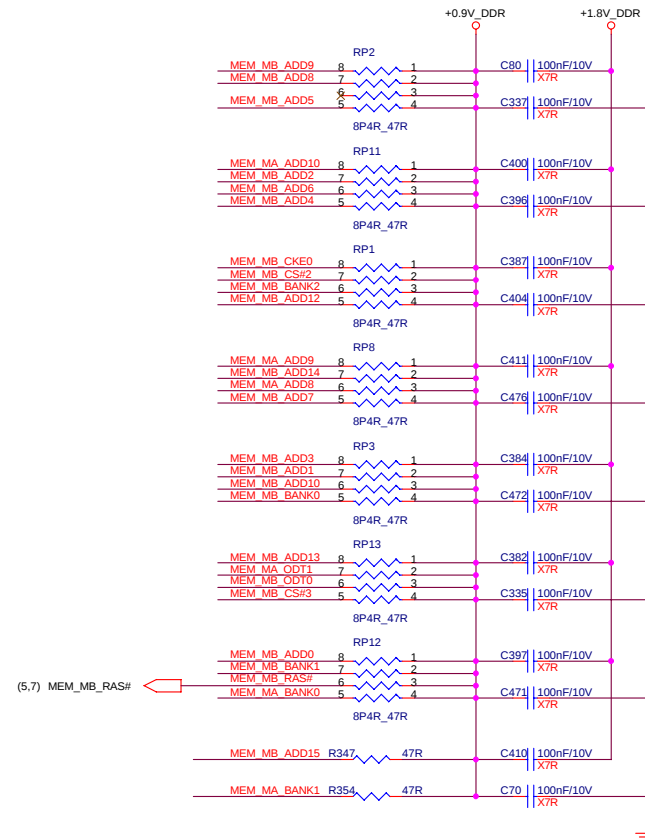


(5,7) MEM_MA_ADD[0..15] MEM_MA_ADD[0..15]
(5,7) MEM_MA_CS#[0..3] MEM_MA_CS#[0..3]
(5,7) MEM_MA_ODT[0..1] MEM_MA_ODT[0..1]
(5,7) MEM_MA_CKE[0..1] MEM_MA_CKE[0..1]
(5,7) MEM_MA_BANK[0..2] MEM_MA_BANK[0..2]



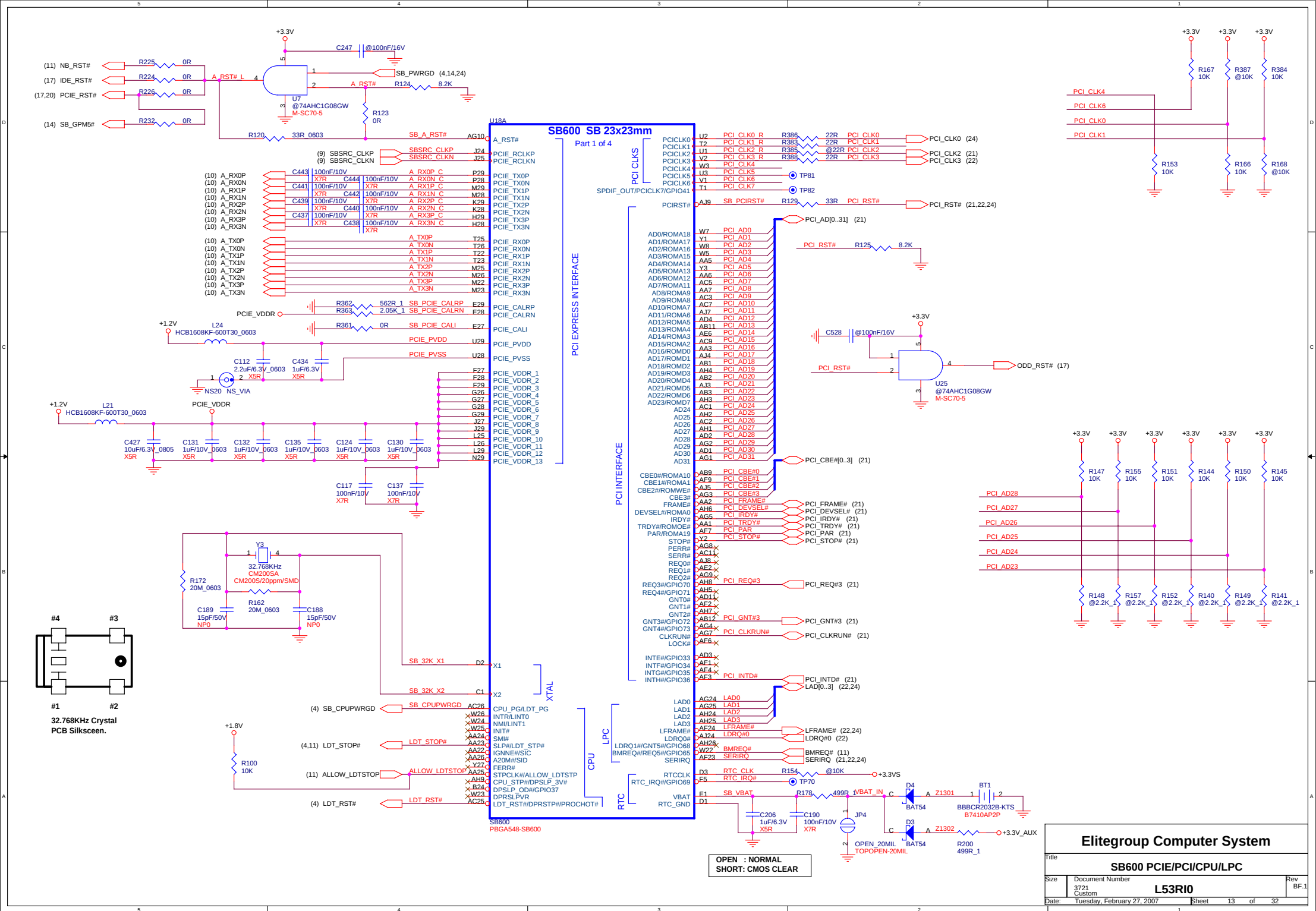
DDR2 SO-DIMM A0

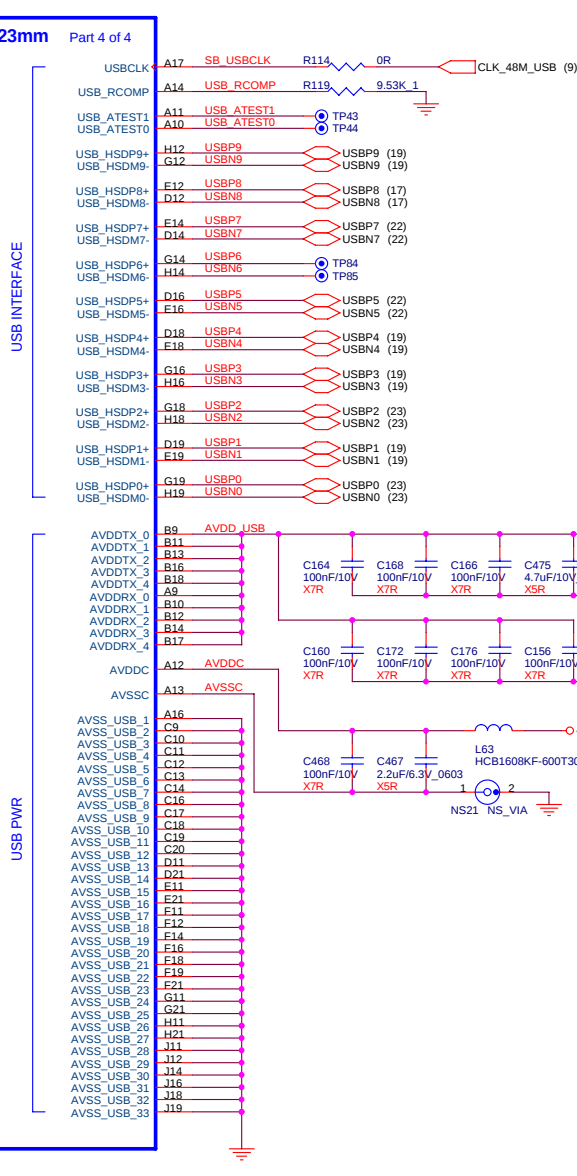
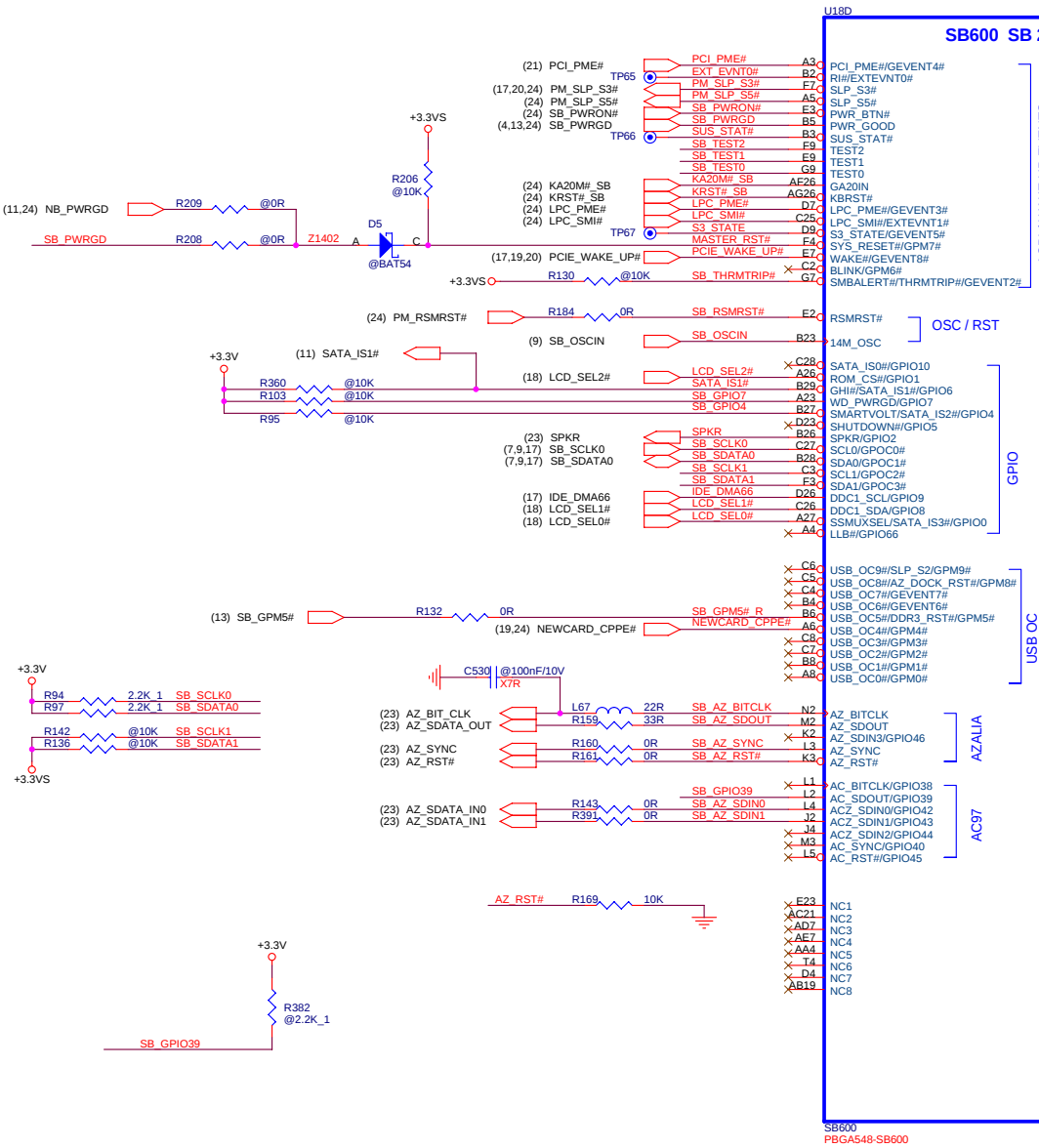
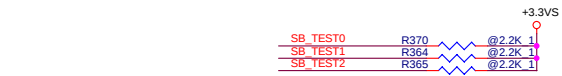
(5,7) MEM_MB_ADD[0..15] MEM_MB_ADD[0..15]
(5,7) MEM_MB_CS#[0..3] MEM_MB_CS#[0..3]
(5,7) MEM_MB_ODT[0..1] MEM_MB_ODT[0..1]
(5,7) MEM_MB_CKE[0..1] MEM_MB_CKE[0..1]
(5,7) MEM_MB_BANK[0..2] MEM_MB_BANK[0..2]

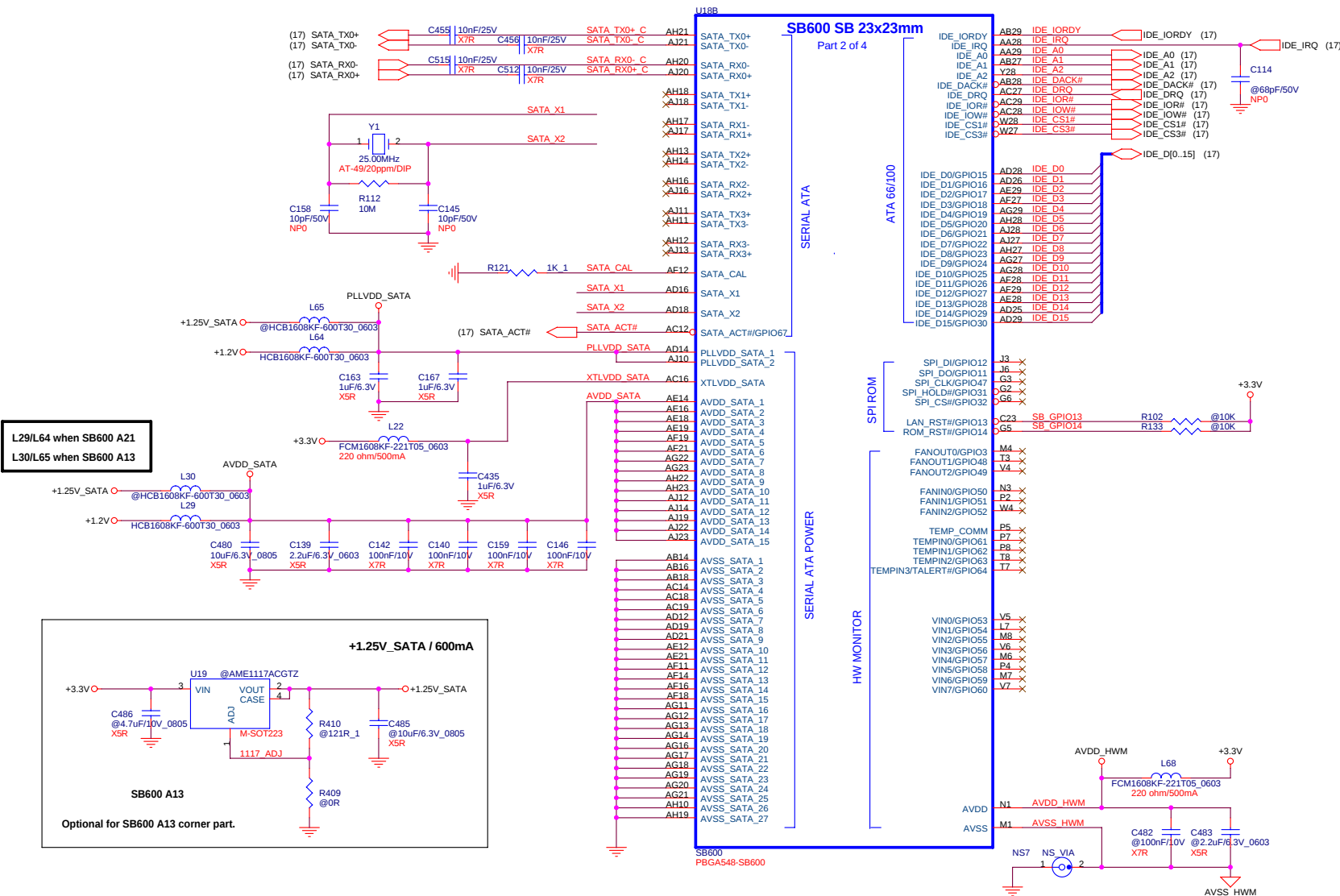


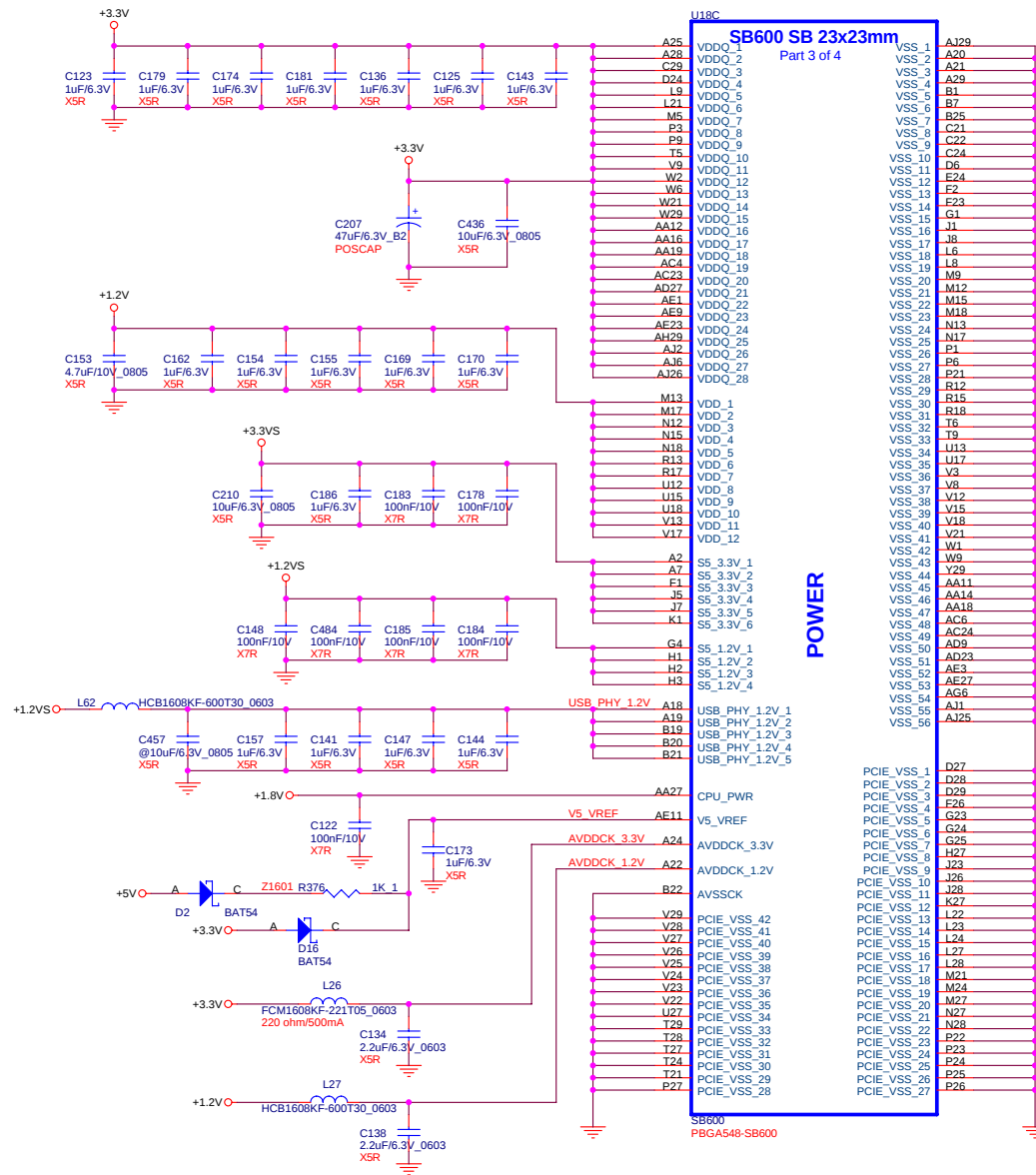
DDR2 SO-DIMM B0



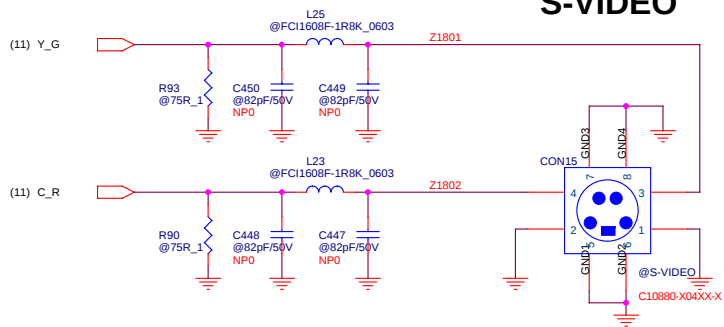






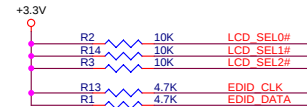


S-VIDEO

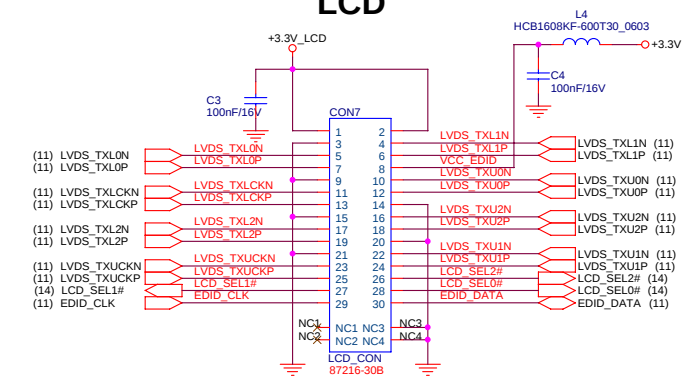


LCD Panel SELECT

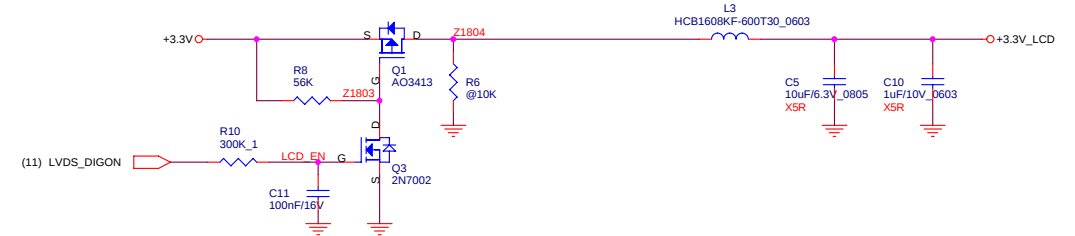
LCD_SEL2#	LCD_SEL1#	LCD_SEL0#	
L	L	L	reserved
L	L	H	1024X768
L	H	L	1400X1050
L	H	H	1280X800
H	L	L	reserved
H	L	H	1680X1050
H	H	L	1920X1200
H	H	H	1440X900



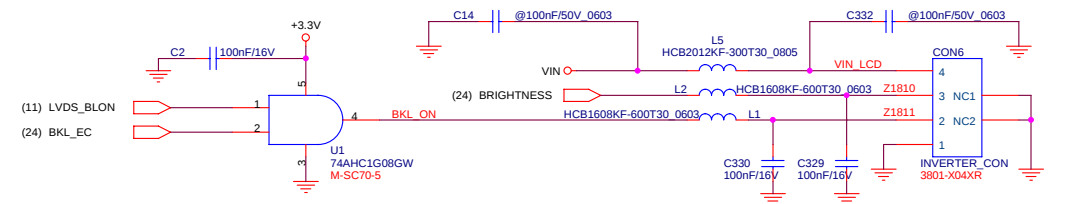
LCD



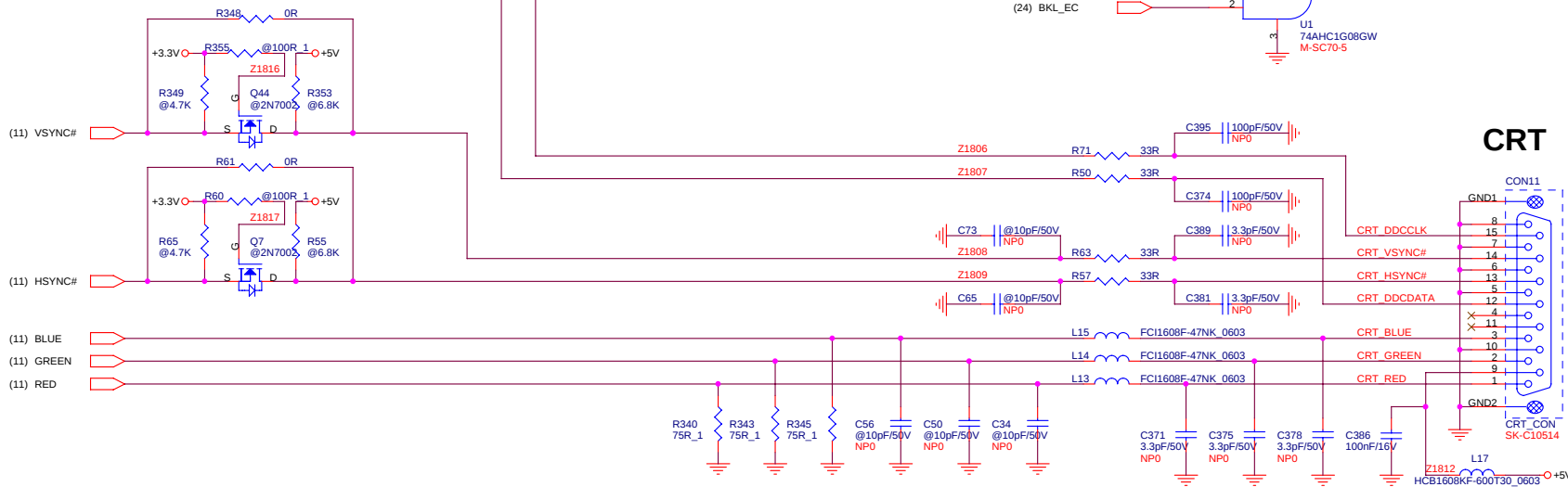
LCD VCC



INVERTER



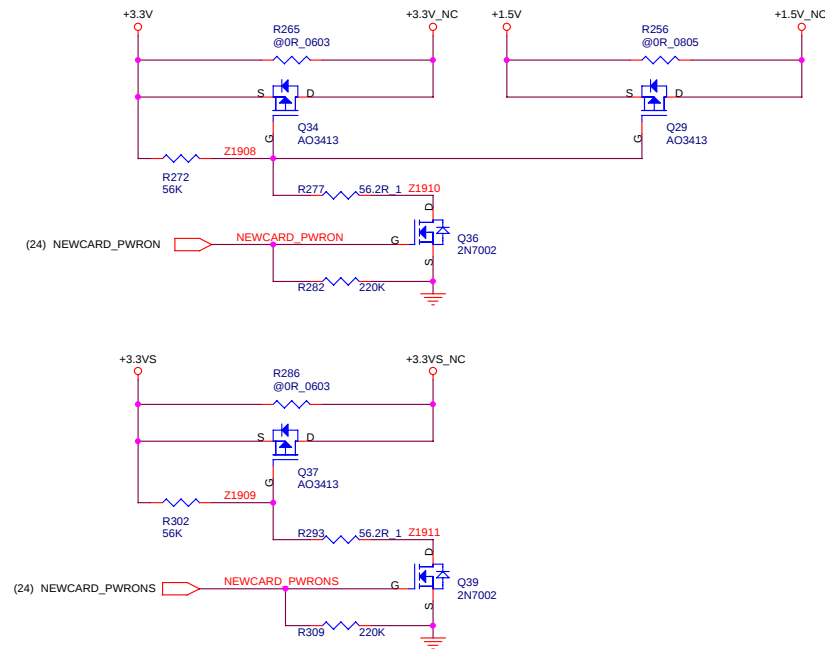
CRT



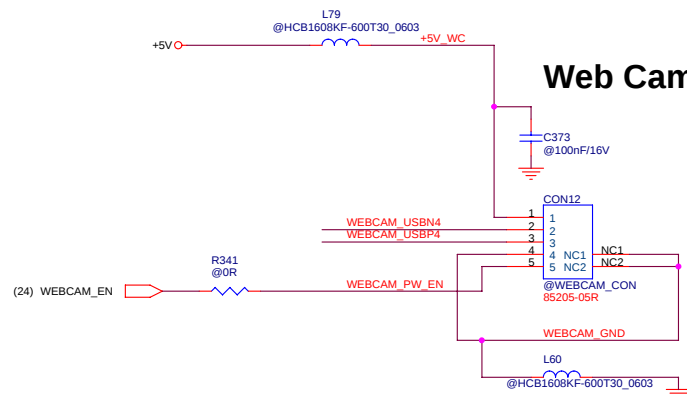
Elitegroup Computer System

CRT/LCD/S-VIDEO CON		
Title	3721	Rev
Size	Document Number	BF.1
	Custom	
Date:	Tuesday, February 27, 2007	Sheet 18 of 32

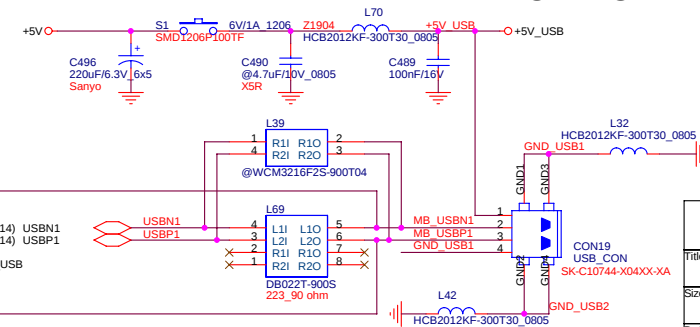
NEW CARD POWER



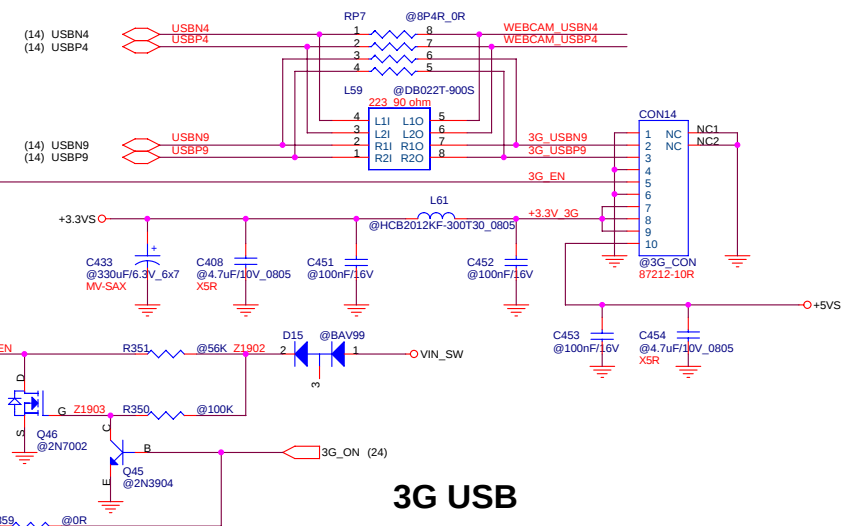
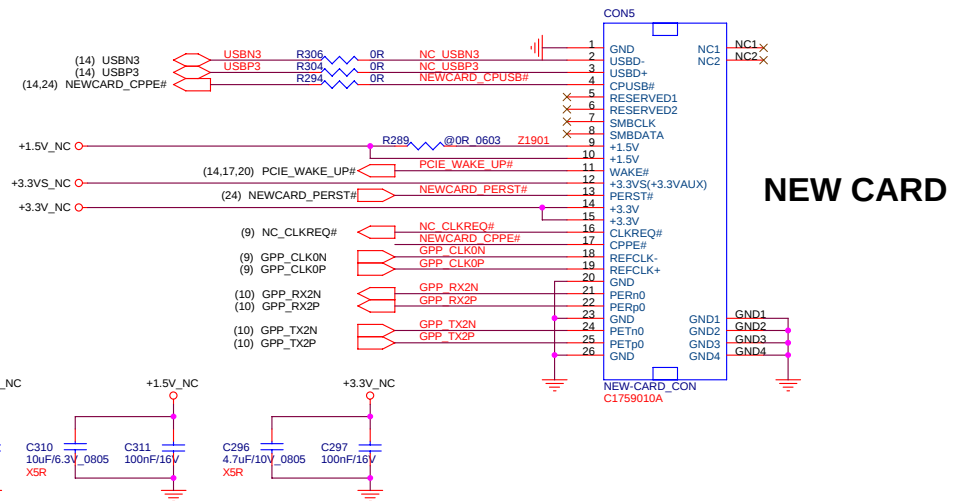
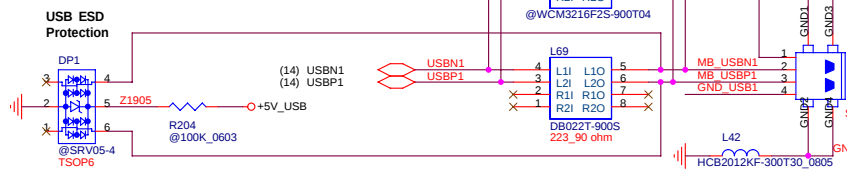
Web Camera



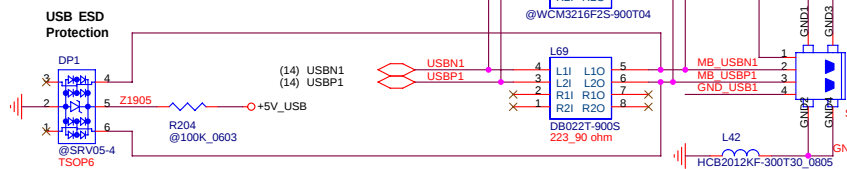
ON BOARD USB



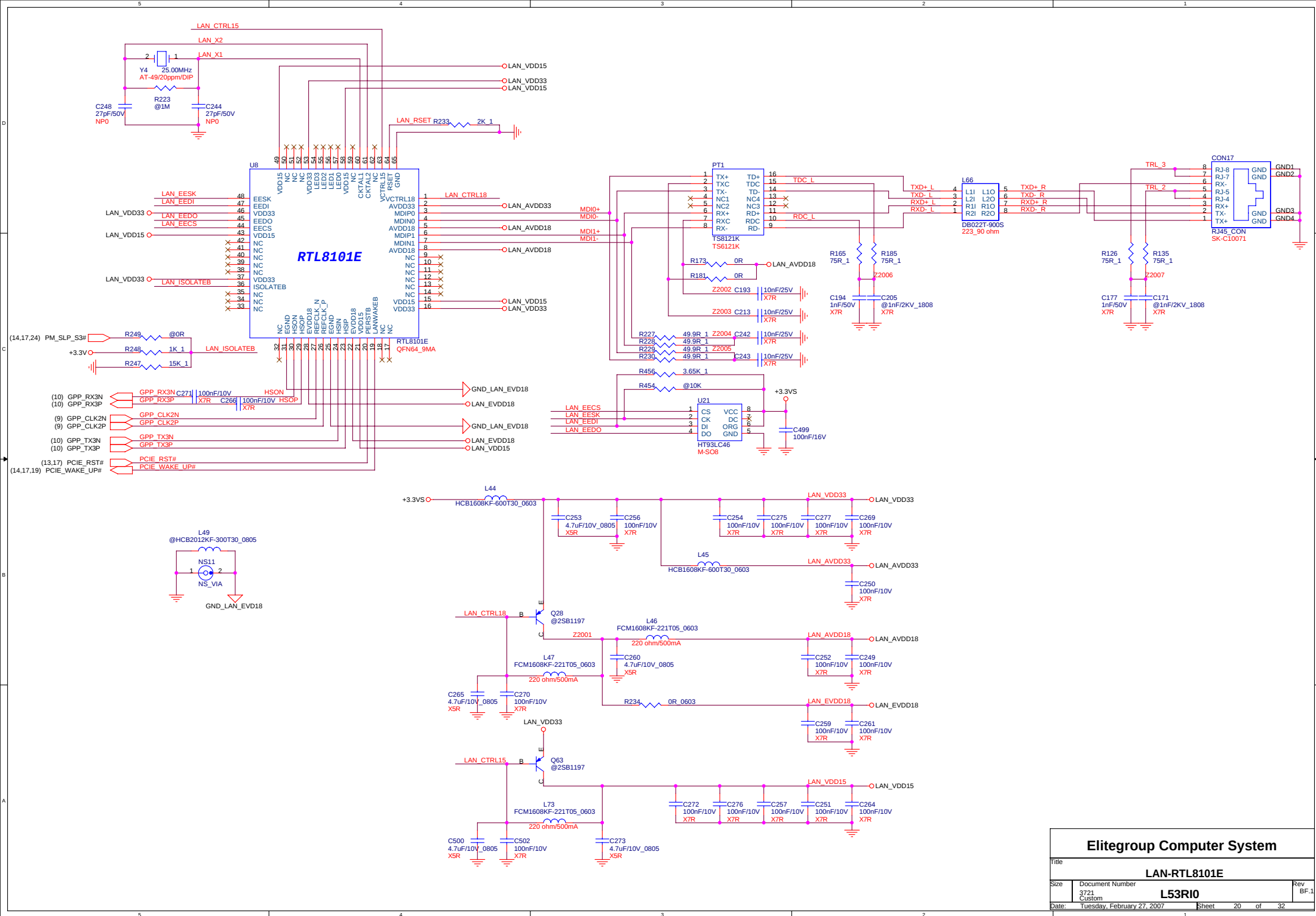
ON BOARD USB

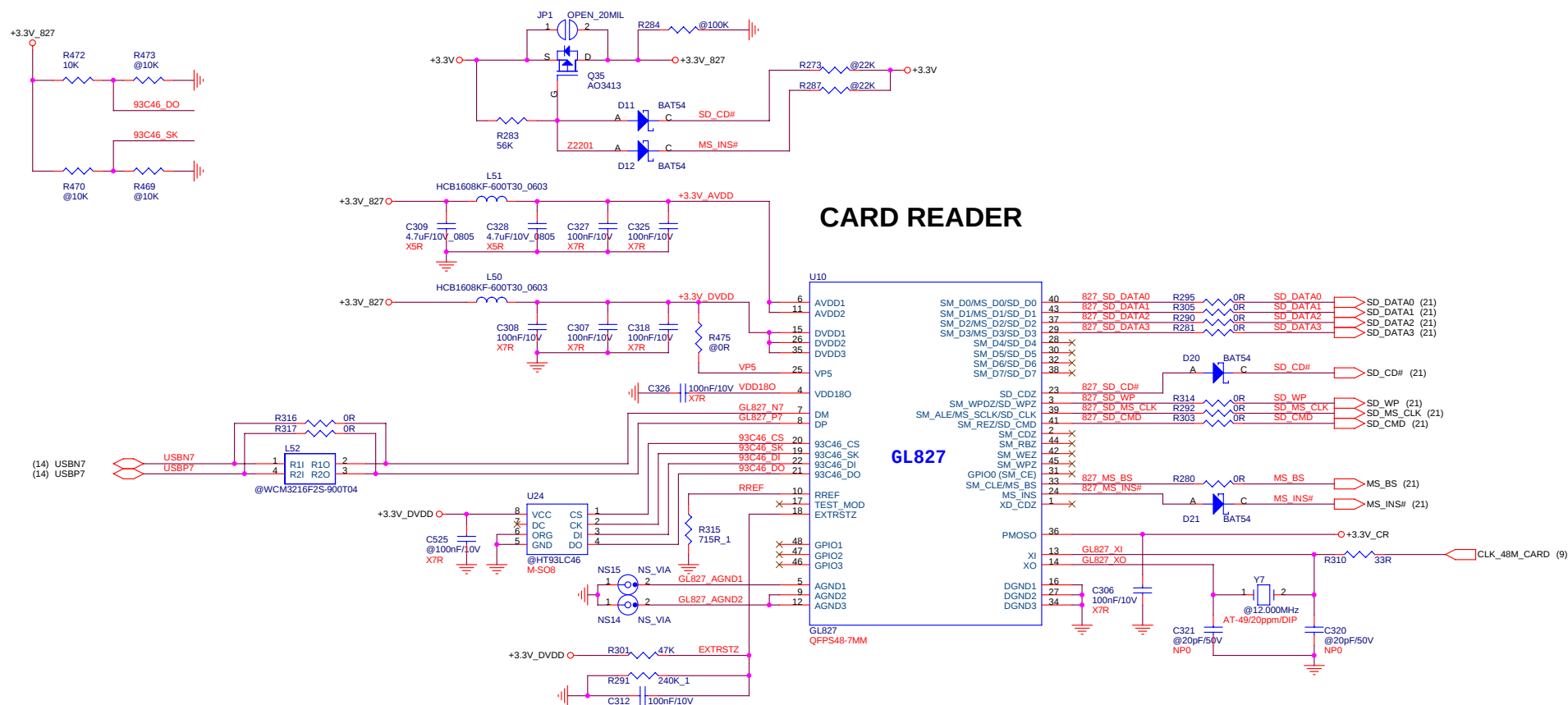


ON BOARD USB

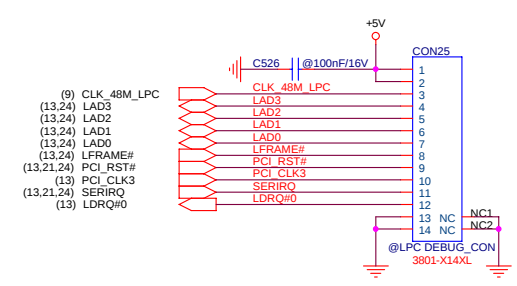


Elitegroup Computer System			
Title 3G/WEB CAM/NEW CARD/USB CON			
Size	Document Number 3721 Custom	L53RI0	Rev BF.1
Date:	Tuesday, February 27, 2007	Sheet	19 of 32

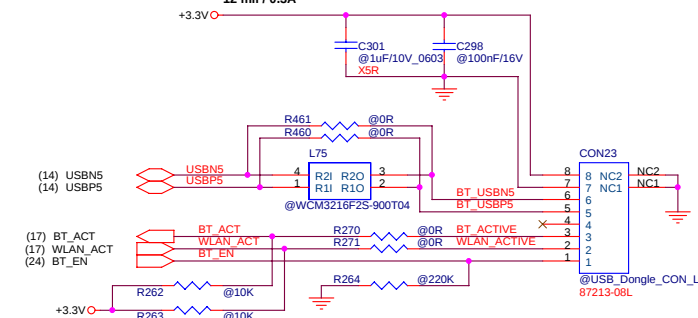




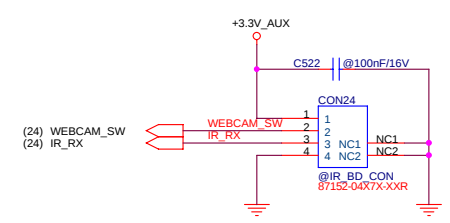
LPC DEBUG CARD



USB Dongle (Bluetooth)



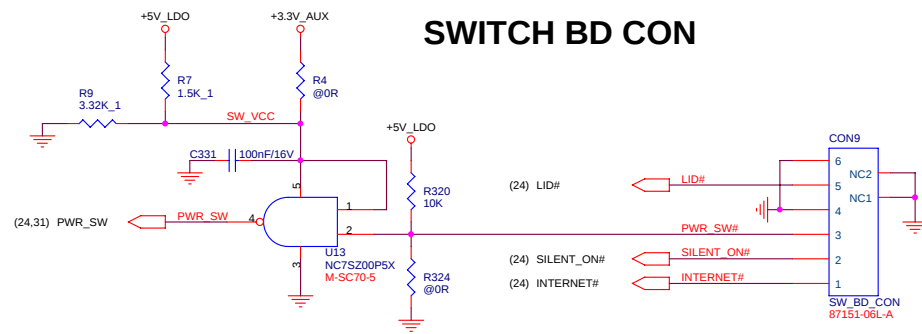
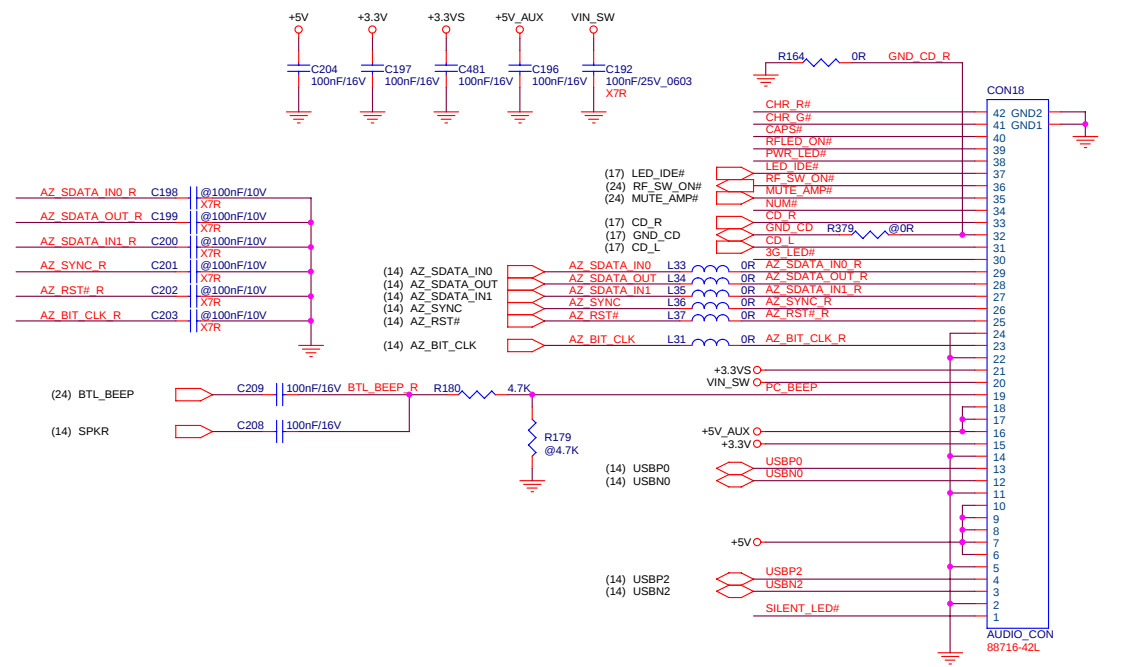
IR



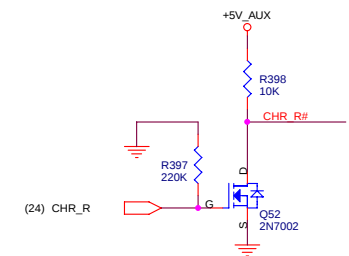
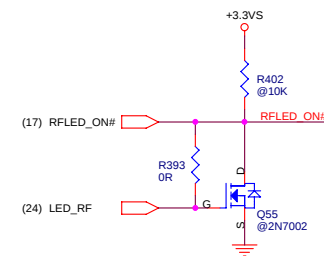
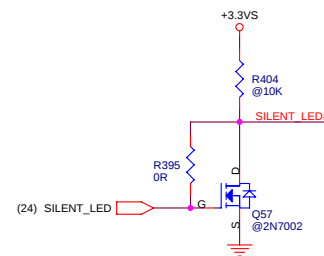
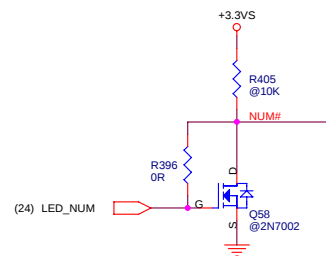
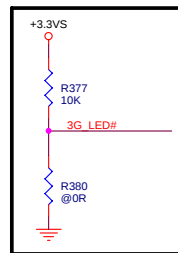
Elitegroup Computer System			
Title			
CARD READER/DEBUG/BT/IR			
Size	Document Number	Rev	
3721	Custom	L53R10	
Date:	Tuesday, February 27, 2007	Sheet	22 of 32

SWITCH BD CON

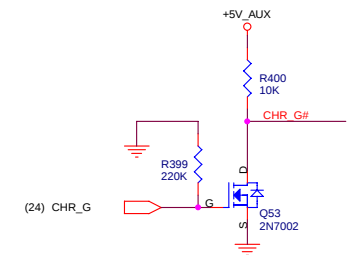
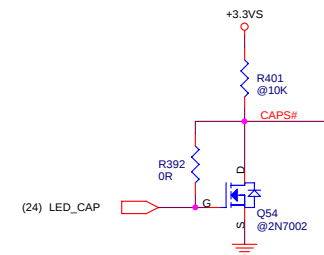
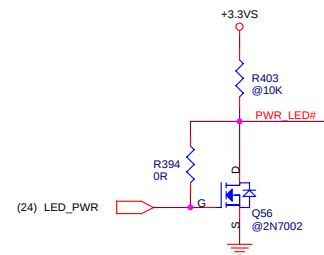
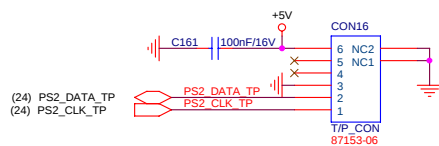
The schematic diagram illustrates the SWITCH BD CON circuit. It features a 5V_LDO and +3.3V_AUX power supply rails. A 3.32K₁ resistor (R9) is connected to the 5V_LDO rail, and a 1.5K₁ resistor (R7) is connected to the +3.3V_AUX rail. A 100nF/16V capacitor (C331) is connected to the SW_VCC rail. A 10K resistor (R320) is connected to the 5V_LDO rail, and a 0R resistor (R324) is connected to the SW_VCC rail. The circuit is controlled by a PWR_SW signal (24,31) and a LID# signal (24). The circuit is connected to a SW_BD_CON module (87151-06L-A) via a CON9 connector. The module has pins 1, 2, 3, 4, 5, and 6, which are labeled LID#, SILENT_ON#, INTERNET#, PWR_SW#, and NC1, NC2 respectively.

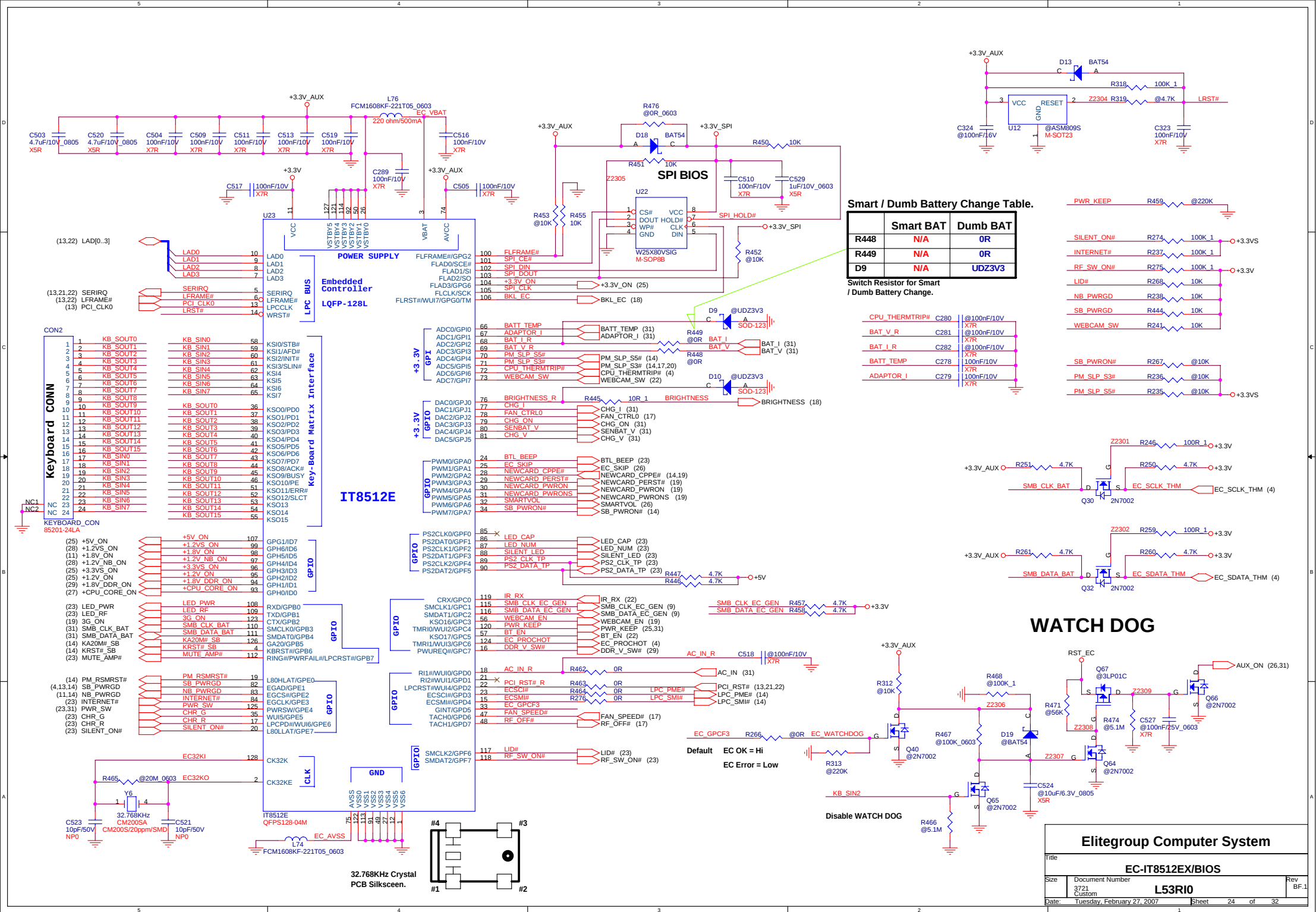
[illegible]

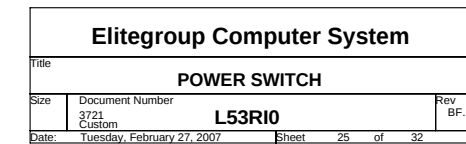
	L50IIx Audio BD	L51AII Audio BD
R377 10K	Add	Remove
R380 0R	Remove	Add



TOUCH PAD CON







MAX8744 shutdown current : 8~ 20 uA(SHDN# pin = low)
 MAX8744 standby current : 65 ~ 120 uA
 MAX8744 idle power : 3.5mW ~ 4.5mW

+3.3V_AUX

**+3.3V/6A
PK=8A**

Vout=3.331V

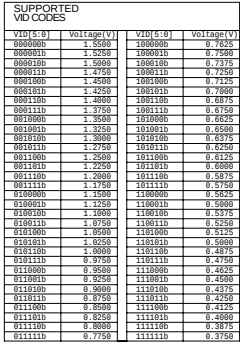
+5V_AUX

**+5V/6A
PK=8A**

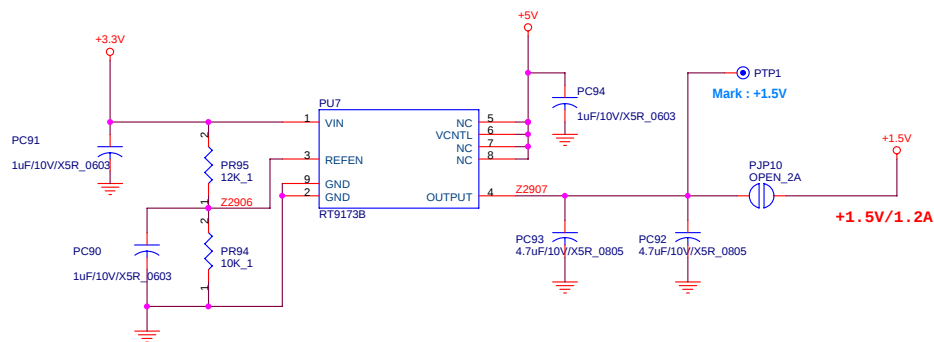
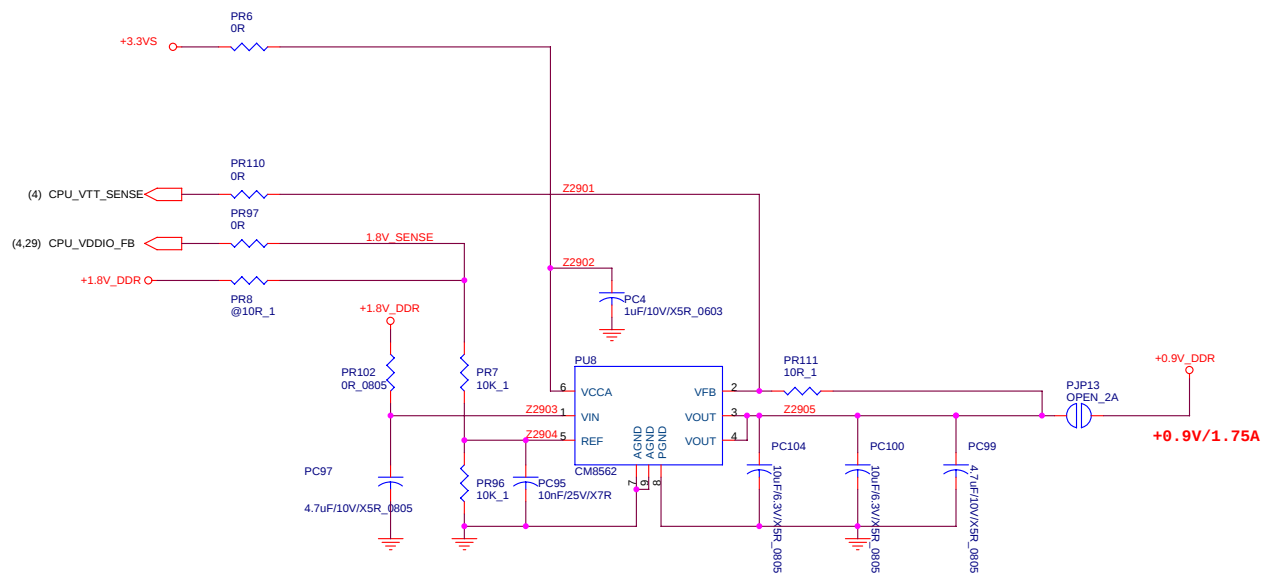
Vout= 5.065V

"High"=5.065V
"Low"=4.865V

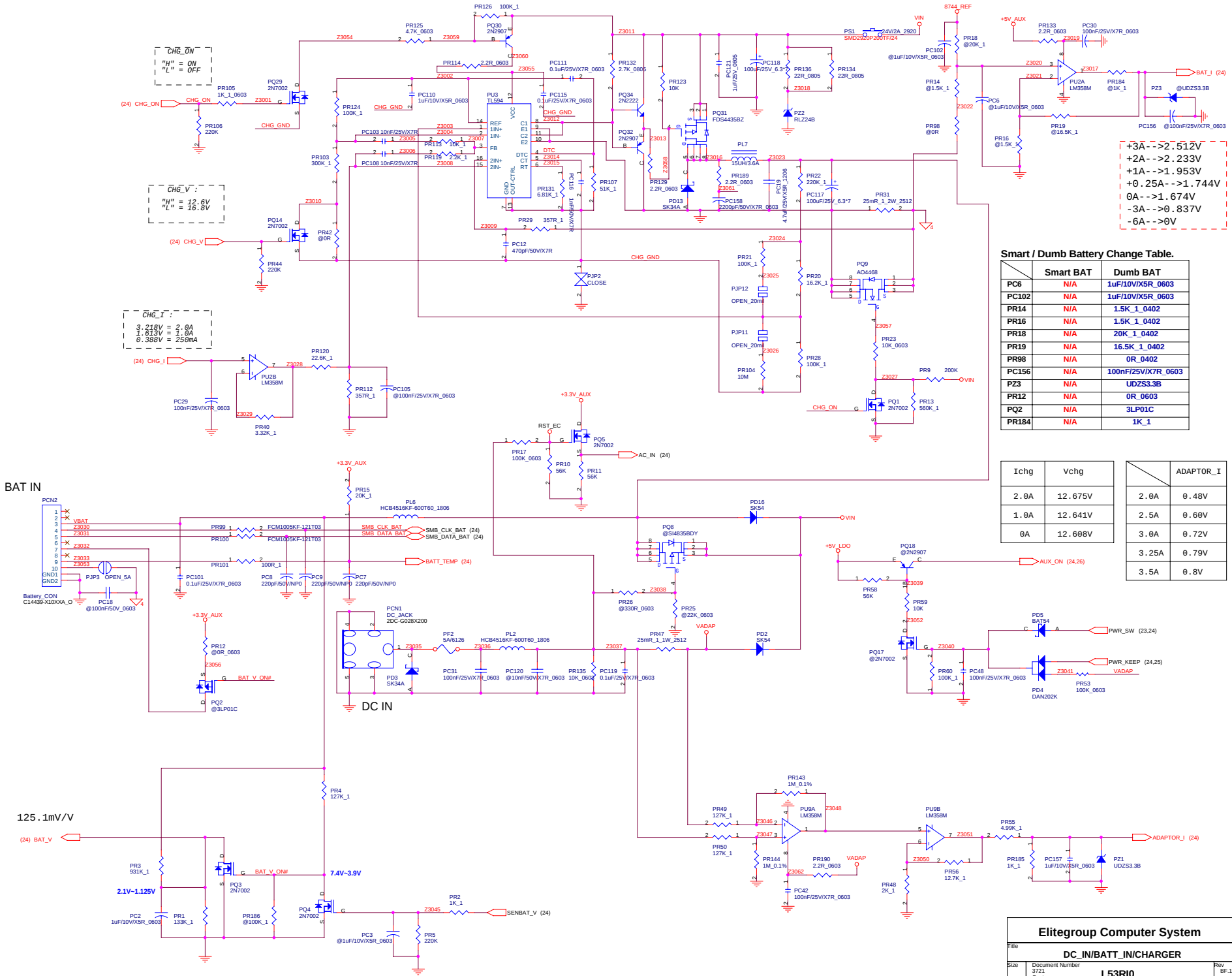
Elitegroup Computer System			
Title			
SYSTEM POWER (MAX8744)			
Size	Document Number	Rev	
	3721	BF.1	
	Custom	L53R10	
Date:	Tuesday, February 27, 2007	Sheet	26 of 32



DDR Termination Power



Elitegroup Computer System			
Title		+0.9V_DDR/+1.5V	
Size	Document Number	L53R10	
	3721 Custom	Rev BF.1	
Date:	Tuesday, February 27, 2007	Sheet	30 of 32



+3A-->2.512V
+2A-->2.233V
+1A-->1.953V
+0.25A-->1.744V
0A-->1.674V
-3A-->0.837V
-6A-->0V

Smart / Dumb Battery Change Table.

	Smart BAT	Dumb BAT
PC6	N/A	1uF/10V/X5R_0603
PC102	N/A	1uF/10V/X5R_0603
PR14	N/A	1.5K_1_0402
PR16	N/A	1.5K_1_0402
PR18	N/A	20K_1_0402
PR19	N/A	16.5K_1_0402
PR98	N/A	0R_0402
PC156	N/A	100nF/25V/X7R_0603
PZ3	N/A	UDZS3.3B
PR12	N/A	0R_0603
PQ2	N/A	3LP01C
PR184	N/A	1K_1

Ichg	Vchg	ADAPTOR_I	
2.0A	12.675V	2.0A	0.48V
1.0A	12.641V	2.5A	0.60V
0A	12.608V	3.0A	0.72V
		3.25A	0.79V
		3.5A	0.8V

Sch Rev.	PCB Rev.	Data	REVISION DESCRIPTION	Sch Rev.	PCB Rev.	Data	
A.01	37GL53000-A0	2006/10/14	1. Change R311/R314/R319 2.2K 1 to @2.2K 1 (Page.14) 2. Modify System Block Diagram (Page.2) 3. Change R426 @220K to 91K 1 (Page.23) 4. Add PCI DEVICES IRQ ROUTING TABLE (Page.3) 5. Add USB PORTS DEVICES TABLE (Page.3) 6. Add Power Block Diagram (Page.3) 7. Add CPUINB/SB/SYSTEM POWER RAIL TABLE(Page.3) 8. Add Disable WATCH DOG circuit WQ1 2N7002 / WR1 5.1M (Page.23) 9. Change PC8 @1uF/10VX5R 0603 to 100K 0603, PR16 @1.5K 1 to 100K, PR22 @16.5K 1 to 100K (Page 30.) 10. Add Smart / Dumb Battery Change Table (Page.30) 11. Change U2IU6/U18 @NC7SZ08M5 to @NC7SZ08P5X (Page.4) 12. Change U4 74HC1G32GW to NC7SZ32P5X_U27 @74AHC1G126DCKR to @NC7SZ126P5X (Page.11) 13. Change U3U5 74AHC126DCKR to NC7SZ126P5X (Page.18) 14. Change U15 74HC1G00 to NC7SZ00P5X (Page.22) 15. Change R202 10K to @10K (Page.14) 16. Modify Battery mode +xV AUX ON/OFF TABLE (Page.25) 17. Add Battery mode +xV AUX ON/OFF TABLE (Page.22) 18. Change R428 10K to @10K (Page.23) 19. Change R108/R337 @100R 0603 to 100R 0603, Q11/Q53 @2N7002 to 2N7002 (Page.24) 20. U13 pin10 (NEWCARD_CPPE#) jump the line to connect CON5 pin17 (EXPCARD_CPPE#) (Page.19) 21. Add Add to stop leakage voltage circuit, WD1 BAT54 (Page.16) 22. Change PR180 1mR 1 2W 2512 to 0.5mR 1 2W 2512, PR121 30K 1 0603 to 24K 1 0603 (Page.26) 23. Change R325 @100K 1 to 100K 1 (Page.4)	B0.0	37GL53000-B0	2006/12/27	1. Add C531 @100nF/16V, C532 @1uF/10V/X5R 0603, R477 @300R, R478 @0R, U26 @NC7SZ08P5X. (Page.04) 2. Change U14 AME8800DEFTZ to CM2860GKM89, C358 220uF/6.3V 6*5 to 330uF/6.3V 6*7. (Page.04) 3. Change R91 / R92 1K 1 to 1K 1 0603, R48 / R49 / R64 10K to 680R. (Page.04) 4. Add C533 100nF/10V/X7R. (Page.7) 5. Change R12 / R15 1K 1 to 1K 1 0603. (Page.7) 6. Change C492 220uF/6.3V 6*5 to 330uF/6.3V 6*7. (Page.9) 7. Change C97 100pF/50V/NP0 to 100nF/10V/X7R, R76 2.05K 1 to @220K, U5 AME804EEYZ to FP6133-18S5P. (Page.11) 8. Del R75 1K 1. (Page.11) 9. Add C528 @100nF/16V, U25 @74AHC1G08GW. (Page.13) 10. Change Y3 PCB Footprint CM200S to CM200SA, R385 22R to @22R, R178 1K 1 to 499R 1. (Page.13) 11. Change C188 / C189 18pF/50V/NP0 to 15pF/50V/NP0, C112 10uF/6.3V/X5R 0805 to 2.2uF/6.3V/X5R 0603. (Page.13) 12. Add C530 @100nF/10V/X7R. (Page.14) 13. Change R119 11.9K 1 to 9.76K 1. (Page.14) 14. Change R322 Net PCI_RST# to ODD_RST#. (Page.17) 15. Originally above parts change to not above parts : C447 / C448 / C449 / C450 / CON15 / L23 / L25 / R90 / R93. (Page.18) 16. Add Net WEBCAM_GND, L79 HCB1608KF-600T30 0603. (Page.19) 17. Change L60 @HCB1608KF-600T30 0603 to HCB1608KF-600T30 0603, link net WEBCAM_PW_EN to WEBCAM_GND. (Page.19) 18. Del Net Z1907, JP2 OPEN 20MIL, Q42 @2N7002, Q43 @AO3413, R342 @56K, R344 @220K. (Page.19) 19. Change R248 Pull-Hi voltage +3.3VS to +3.3V. (Page.20) 20. Add L77 / L78 @HCB2012KF-300T30 0805. (Page.21) 21. Originally above parts change to not above parts : C262 / C263 / C267 / C268 / C274 / C283 / C284 / C285 / C286 / C287 / C293 / C294 / C295 / C303 / C305 / C313 / C314 / C315 / C316 / C317 / C322 / CON20 / L48 / L72 / Q33 / Q38 / R239 / R240 / R242 / R243 / R244 / R269 / R279 / R296 / R297 / R298 / R299 / R307 / R308 / R311 / U9 / U11 / Y5. (Page.21) 22. Add D20 / D21 BAT54, R475 @0R. (Page.22) 23. Change CON2 pin17 Net RF_SW_ON# to RF_SW_ONH. (Page.22) 24. Change Change C522 100nF/16V to @100nF/16V, IR_BD_CON to @IR_BD_CON. (Page.22) 25. Del R285 / R288 @0R. (Page.22) 26. Swap Q35 pin.S / D. (Page.22) 27. Originally not above parts change to above parts : D11 / D12 / R280 / R281 / R283 / R290 / R292 / R295 / R303 / R305 / R314 / U10. (Page.22) 28. Change CON9 PCB Footprint GB1006X-X051-7F_87151 to 87151-06L-A. (Page.23) 29. Change CON18 PCB Footprint 6701-X42N-00X to 88716-42L. (Page.23) 30. Change R7 15K 1 to 1.5K 1, R9 30.1K 1 to 3.32K 1. (Page.23) 31. Add C529 1uF/10V/X5R 0603, R476 @0R 0603. (Page.24) 32. Change Y6 PCB Footprint CM200S to CM200SA. (Page.24) 33. Change U22 S25FL008A to W25X80VSI6. (Page.24) 34. Change U23 pin.17 Net RF_SW_ON# to CHR_R_U23 pin.35 Net LID# to CHR_G. (Page.24) 35. Change U23 pin.17 Net CHR_G to LID#, U23 pin.118 Net CHR_R to RF_SW_ONH. (Page.24) 36. Change H10 PCB Footprint C315D158A-UW to C315B237D158-UW. (Page.25) 37. Originally not above parts change to above parts : EF2 / EF3 / EF7. (Page.25) 38. Change PC56 1uF/10V/X5R 0603 to 0.1uF/25V/X7R 0603, PC58 4.7uF/25V/X5R 1206 to @4.7uF/25V/X5R 1206. (Page.26) 39. Change PC58 1uF/10V/X5R 0603 to 0.1uF/25V/X7R 0603, PC69 4.7uF/25V/X5R 1206 to @4.7uF/25V/X5R 1206. (Page.26) 40. Change PC139 PCB Footprint @220uF/6.3V/QSC 6*5.7 to @4.7uF/10V/X5R 0805. (Page.26) 41. Change PC148 4.7uF/10V/X5R 0805 to @4.7uF/10V/X5R 0805. (Page.26) 42. Change PR138 63.4K 1 to 56.2K 1, PR145 @6.49K 1 to 9.09K 1. (Page.29) 43. Add PTP1 +1.5V Testpoint (Mark +1.5V). (Page.30) 44. Add PC157 1uF/10V/X5R 0603, PR184 @1K 1, PR186 @100K 1, PR190 2.2R 0603. (Page.31) 45. Change PR29 / PR112 576R 1 to 357R 1, PR48 10K to 2K 1, PR55 10K to 4.99K 1, PR56 10K to 12.7K 1. (Page.31) 46. Change PR31 40mR 1 1W 2512 to 25mR 1 2W 2512, PR47 40mR 1 1W 2512 to 25mR 1 1W 2512. (Page.31) 47. Change PR49 / PR50 47K 0.5% to 127K 1, PR143 / PR144 560K 0.5% to 1M 0.1%. (Page.31) 48. Change PS1 Net VADAP to VIN, PR123 link Net Z3013 to Z3058. (Page.31) 49. Del Net Z3017 / Z3049, PC38 1uF/10V/X5R 0603, PD1 SK34A, PF1 @3A 1206. (Page.31)
A.02	37GL53000-A0	2006/10/23	1. Change R41 @2K to 2K (Page.11) 2. Modify SB600 Required Straps (Page.13) 3. Change R411 33R to 22R (Page.21) 4. Change PR180 10K 0603 to @560K 0603 (Page.25)				
A.03	37GL53000-A0	2006/10/26	1. Change PR55 @10mR 1 1W 2512 to @40mR 1 1W 2512 (Page.26) 2. Change PR124 2.2R 0603 to 0R 0603 (Page.26) 3. Change PR72 15K 0603 to 1K 1 0603, PR166 51K 0603 to @51K 0603 (Page.28) 4. Change PR167 100K 1 to 220K (Page.28)				
A.04	37GL53000-A0	2006/11/11	1. Change C105 @10uF/6.3V 0805 X5R to 10uF/6.3V 0805 X5R (Page.6) 2. Change C208 / C209 22pF/50V NP0 to 30pF/50V NP0 (Page.9) 3. Change R36 @2K to 2K 1, R37 / R40 / R42 150R 1 to @150R 1 (Page.11) 4. Change L60 QT1608RL060H-CA to FCM1608JF-221T05 (Page.15) 5. Modify U22 SPX11A7M3 to AME1117ACGT2 (Page.15) 6. Change L14 / L15 / L16 100H 0603 to FCM1608JF-221T05 0603, R291 / R292 / R293 150R 1 to 75R 1 (Page.18) 7. Modify L25 / L26 QT1608-1R8K-LF 0603 to FCI1608F-1R8K 0603 (Page.18) 8. Change PR181 9.76K 1 to 6.98K 1, PR188 15K 1 to 10K 1, PR193 120K 1 to 100K 1 (Page.25) 9. Modify PR184 @20K to @22K (Page.25) 10. Change PC8 22nF/2V/330 0603 to CMO5 330uF/2V/7343 SPCAP, PR26 0R to @0R (Page.26) 11. Change PR86 1.33K 1 to 1K 1, PR195 10K 1 to 3.01K 1, PR199 6.04K 1 to 4.53K 1 (Page.27) 12. Change PR63 560K 1 to 174K 1, PR158 20K 1 to 12K 1, PR159 41.2K 1 to 26.7K 1 (Page.28) 13. Change PC88 4.7uF/10V/X5R 0805 to 10uF/6.3V/X5R 0805 (Page.29)				
A1.0	37GL53000-A1	2006/11/21	1. Because the revision scope is widespread, all components again rename. 2. Modify Cover Page. (Page.1) 3. Modify Block Diagram. (Page.2) 4. Modify Miscellaneous. (Page.3) 5. Del Voltage level switch circuit. (Page.4) 6. Add EC control CPU_PROCHOT# circuit, CPU_SIC Pull-low resistor. (Page.4) 7. Add Net CLK_48M_CARD resistor, Net GPP_CLK2P / GPP_CLK2N for PCIE-LAN. (Page.9) 8. Add Net GPP_CLK2P / GPP_CLK2N Pull-low resistor / Capacitors. (Page.9) 9. Change Net GPP_TX3P / GPP_TX3N / GPP_RX3P / GPP_RX3N for PCIE-LAN. (Page.10) 10. Modify RS698MC_BMREQ# / Power shift circuit. (Page.11) 11. Add Net GPP_TX0P / GPP_TX0N / GPP_RX0P / GPP_RX0N for MINI Card. (Page.11) 12. Modify SB_A_RST# / CMOS BAT / PCI_CLK4 circuit. (Page.13) 13. Change SB 32.768KHz crystal / CMOS Reset Jump PCB Footprint. (Page.13) 15. Change SB600 pin.B27 Net SB_SMARTVOL to SB_GPIO4, SB600 pin.B29 SB_GPIO6 to SATA_IS1#. (Page.14) 16. Change SB600 pin.A6 Net EXPCARD_CPPE# to NEWCARD_CPPE#. (Page.14) 17. Del SB600 pin.C4 Net NEWCARD_OC#, SB600 RSMRST# HW circuit, SB_TEST0 1 / 1 2 Pull-low resistor. (Page.14) 18. Add SB600 Net USB4 / USB5 / USB6 / USB7 / USBN7 / USBN7. (Page.14) 19. Change Net MASTER_RST# Pull-Hi resistor voltage +3.3V to +3.3VS. (Page.14) 20. Add Prevention leakage voltage schottky diode. (Page.16) 21. Add Net BT_ACT, WLAN_ACT. (Page.17) 22. Del Net PCIE_WAKE_UP# Pull-Hi resistor. (Page.17) 23. Change FAN power a wave filter capacitor 10uF/6.3V/X5R 0805 to 33uF/6.3V B2. (Page.17) 24. Change Net GPP_RX3N / GPP_RX3P / GPP_TX3P to GPP_TX0N / GPP_TX0P / GPP_RX0P. (Page.17) 25. Modify CRT_VSYNC# / HSYNC# level voltage circuit. (Page.18) 26. Del LCD_VCC protect fuse. (Page.18) 27. Modify NEW_CARD power switch circuit, 3G control circuit, USB ESD protect circuit. (Page.19) 28. Modify 3G Power +3.3VS Trace wide 110mil / 2.6A to 130 mil / 3.25A (Page.19) 29. Change 3G control voltage +12V to VIN_SW, 3G power +5V to +5VS. (Page.19) 30. Add Web Camera control circuit. (Page.19) 31. Rename NEWCARD_Con_pin.17 EXPCARD_CPPE# to NEWCARD_CPPE#. (Page.19) 32. Change LAN_IC RTL8100C to RTL8101E. (Page.20) 33. Co-Layout Card Reader IC GL827 circuit. (Page.22) 34. Add USB Dongle (Bluetooth) and IR_SW circuit. (Page.22) 35. Change LPC Debug Con PCB Footprint 3801-X14Xk to 3801-X14XL. (Page.22) 36. Modify PWR_SW circuit. (Page.23) 37. Del Passive type watch-dog circuit. (Page.23) 38. Change SW_Con PCB Footprint to GB1006X-X051-7F_87151. (Page.22) 39. Change Audio_Con PCB Footprint to 6701-X42N-00X. (Page.22) 40. Change Audio_Con pin.20 +12V to VIN_SW. (Page.22) 41. Change EC IC ITE8510Rx to ITE8512E. (Page.24) 42. Modify Power Switch circuit. (Page.25) 43. Del +12V power switch circuit, +1.5V / +12V / +CPU_CORE / +1.2V_NB Discharge circuit. (Page.25) 44. Add VIN_SW power switch circuit. (Page.25) 45. Change System power IC MAX8734 to MAX8744. (Page.26) 46. Change CPU power IC MAX8760 to OZ828. (Page.27) 47. Modify ISL6227 power circuit. (Page.28) 48. Add RS690MC IGP Power Shift Level Table. (Page.28) 49. Modify OZ811 power circuit. (Page.29) 50. Modify RT9173B / CM8562 power circuit. (Page.30) 51. Modify TL594C charger circuit. (Page.31) 52. Modify Dumb Battery circuit. (Page.31)				
BF.0	37GL53000-B0	2007/01/26	1. Change R368 300R to @300R, R366 @300R to 300R. (Page.04) 2. Change R200 1K 1 to 499R 1. (Page.13) 3. Change L67 33R to 22R, R119 9.76K 1 to 9.53K 1, R160 33R to 0R. (Page.14) 4. Change R55 / R353 @6.8K to 6.8K. (Page.18) 5. Originally above parts change to not above parts : C373 / CON12 / L60 / L79 / R341. (Page.19) 6. Originally above parts change to not above parts : C408/C433/C451/C452/C453/C454 / CON14 / L61 / R359 / PR7. (Page.19) 7. Originally above parts change to not above parts : C298/C301/C526 / CON23/CON25 / R270/R271/R460/R461. (Page.22) 8. Change R180 10K to 4.7K, R179 4.7K to @4.7K. (Page.23) 9. Change PC61 1uF/10V/X5R 0603 to 0.1uF/25V/X7R 0603, PR69 @100K to 100K 1. (Page.26) 10. Change PC63 1uF/10V/X5R 0603 to 0.1uF/25V/X7R 0603, PR156 @4.7K 0603. (Page.26) 11. Change PR115 / PR116 300R 1 to 365R 1, PR121 1R 0603 to 2.2R 0603, PR122 2.2R 0603 to 1R 0603. (Page.27) 12. Change PC54 @220uF/2V_7343 to 330uF/2V_7343, PR127 8.45K 1 to 24.9K 1, PR128 30.1K 1 0603 to 16.9K 1 0603. (Page.27)	BF.1	37GL53000-B0	2007/02/27	1. Change R55 6.8K to @6.8K, R60 100R 1 to @100R 1, R61 @0R to 0R, Q7 2N7002 to @2N7002. (Page.18) 2. Change R348 @0R to 0R, R353 6.8K to @6.8K, R355 100R 1 to @100R 1, Q44 2N7002 to @2N7002. (Page.18) 3. Change PC61 0.1uF/25V/X7R 0603 to @0.1uF/25V/X7R 0603. (Page.26) 4. Change PR115 / PR116 365R 1 to 374R 1, PR127 24.9K 1 to 25.5K 1, PR128 16.9K 1 0603 to 17.4K 1 0603. (Page.27) 5. Change R9 3.48K 1 to 3.6K 1. (Page.28)
A1.1	37GL53000-A1	2006/12/12	1. Add Y3 32.768KHz Crystal PCB Silksceen. (Page.13) 2. Change R16 @0R to 0R, R17 0R to @0R. (Page.18) 3. Change R173 / R181 @0R to 0R. (Page.20) 4. Change R248 link power +3.3VS to +3.3V. (Page.20) 5. Change CON24 pin1 +5V to +3.3V AUX. (Page.22) 6. Add Y6 32.768KHz Crystal PCB Silksceen. (Page.24) 7. Change H16 C237B98D63-UW to @C237B98D63-UW. (Page.25) 8. Change PC148 @4.7uF/10V/X5R 0805 to 4.7uF/10V/X5R 0805, PR163 @0R 0603 to 0R 0603. (Page.26) 9. Change PR128 9.1K 1 0603 to 12.4K 1 0603. (Page.27) 10. Change PL7 4.7UH6A to 15UH5.6A. (Page.31)				

Elitegroup Computer System

REV. HISTORY		
Title		
Size	Document Number	Rev
3721	Custom	BF.1
L53R10		
Date:	Tuesday, February 27, 2007	Sheet 32 of 32