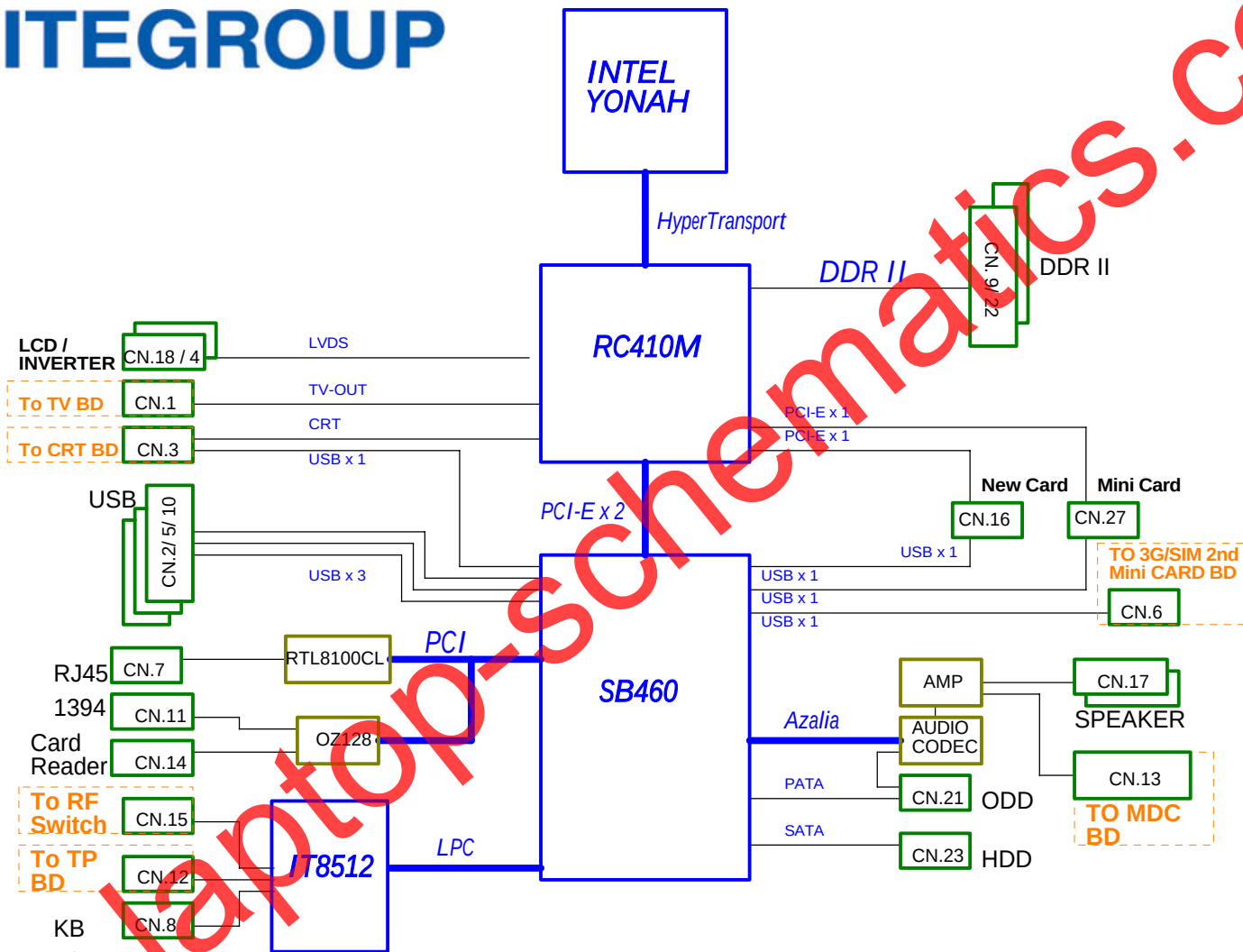
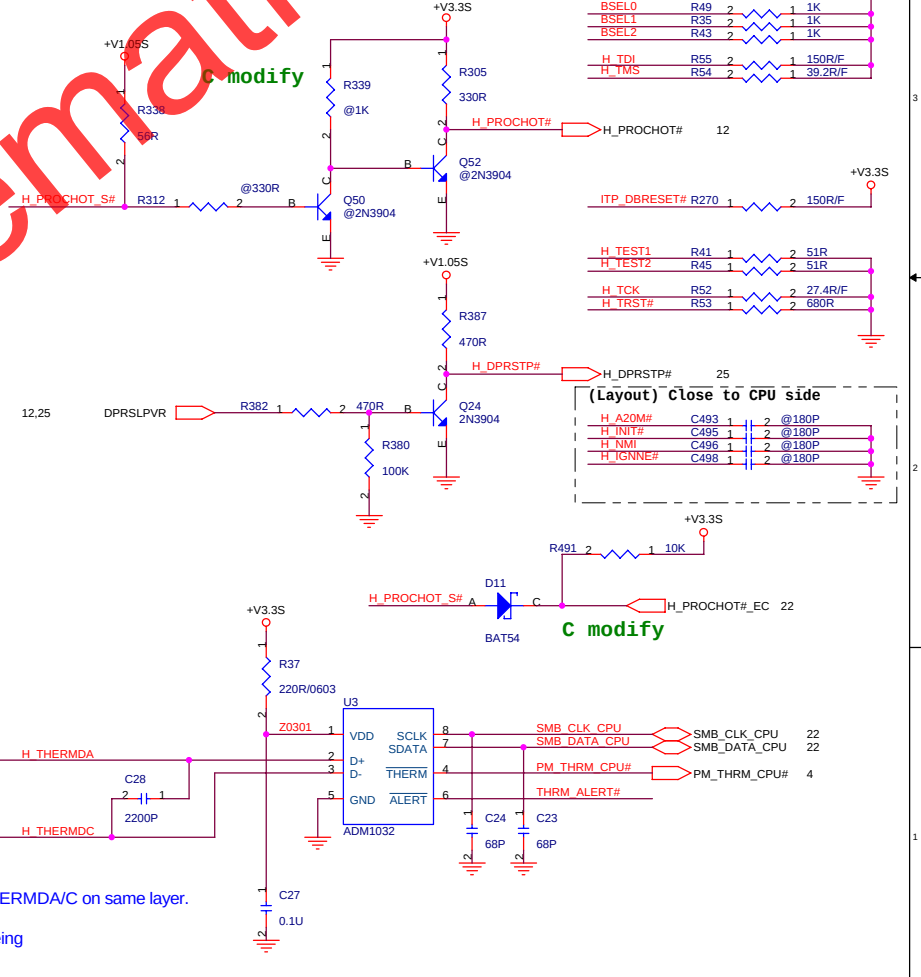
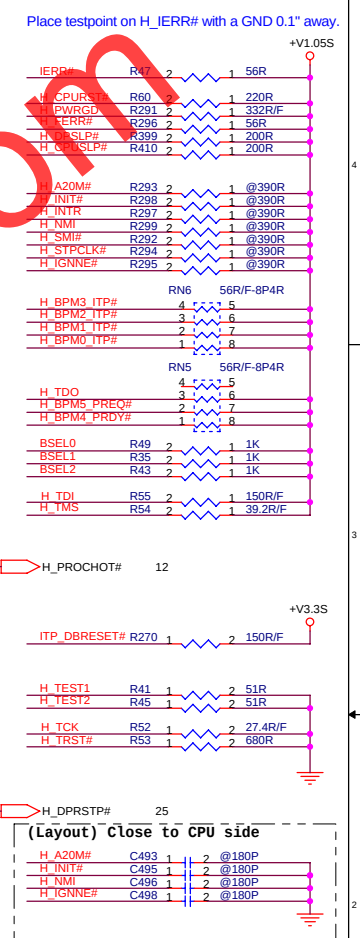
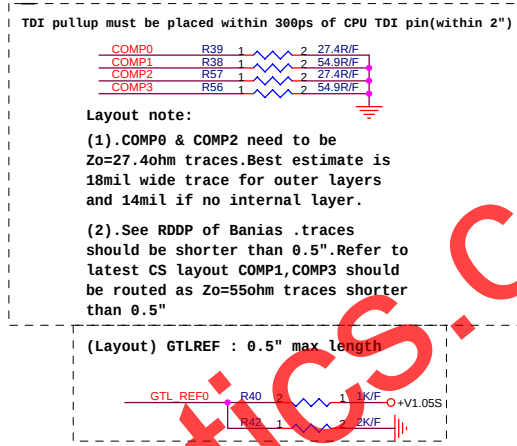
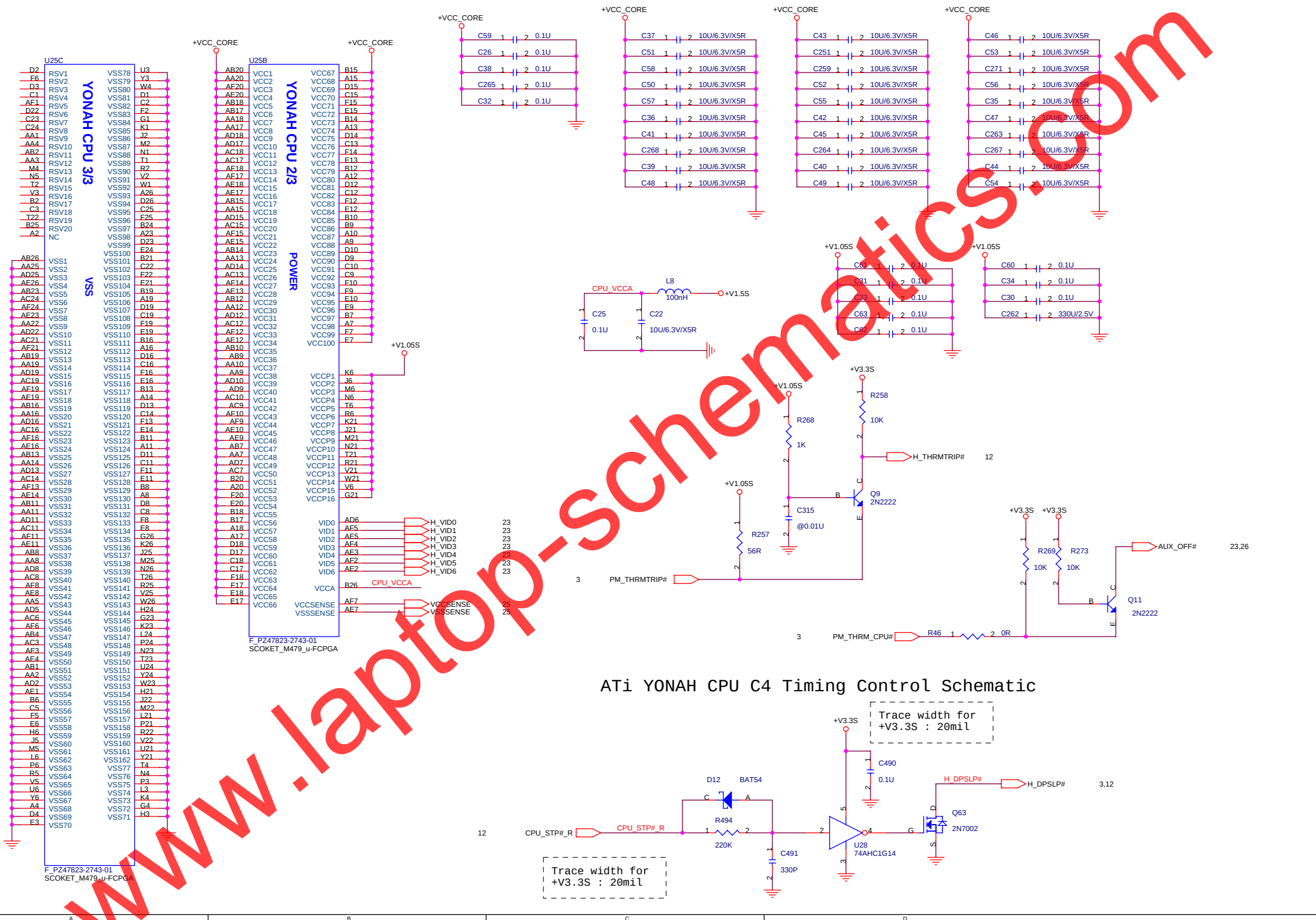
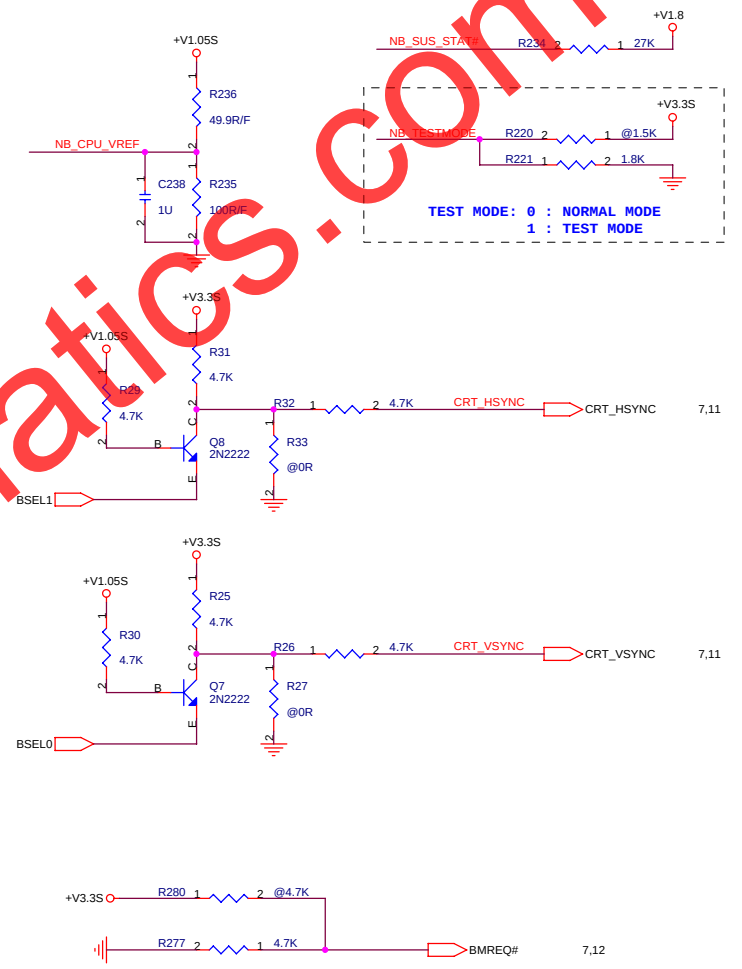
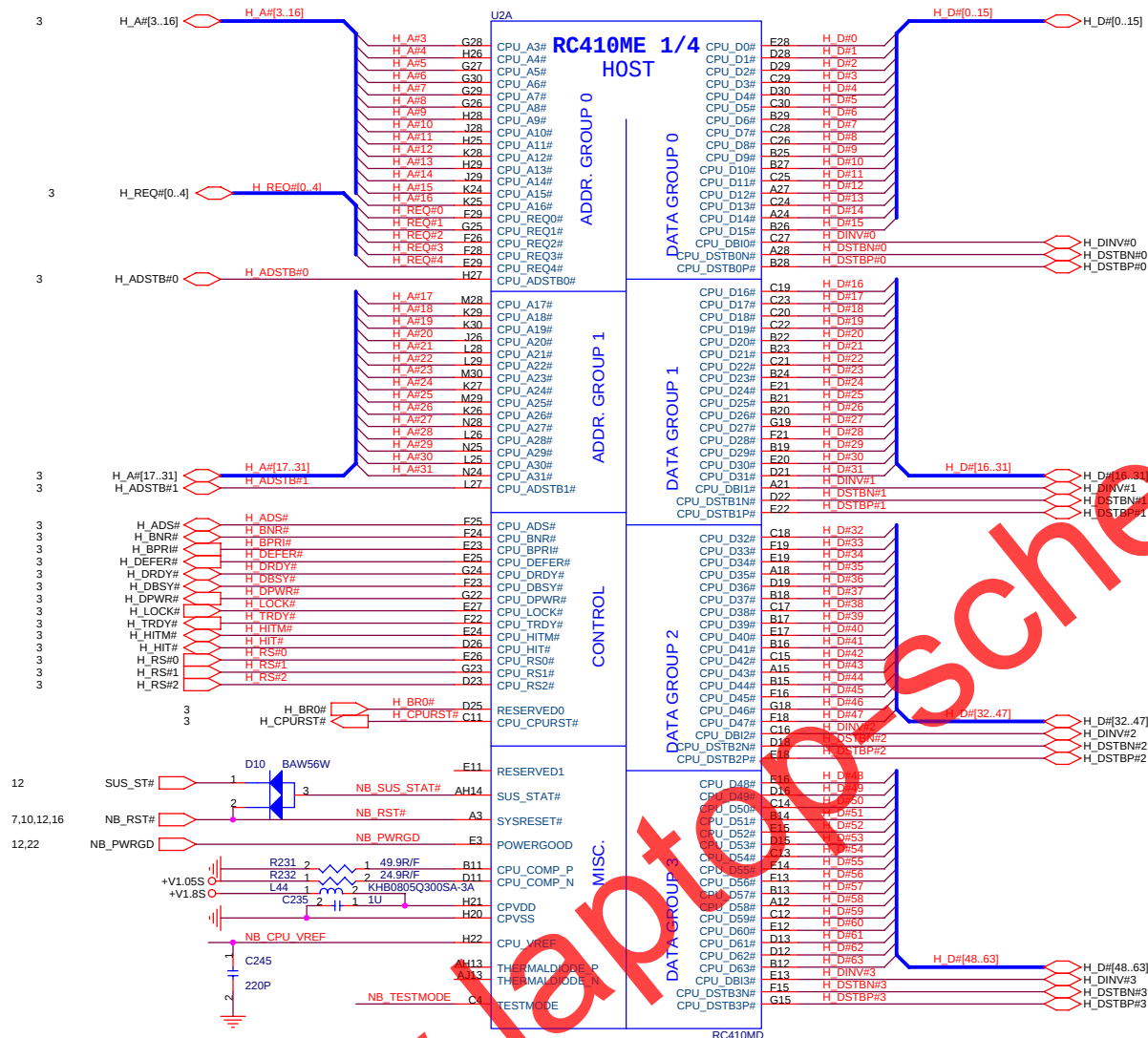






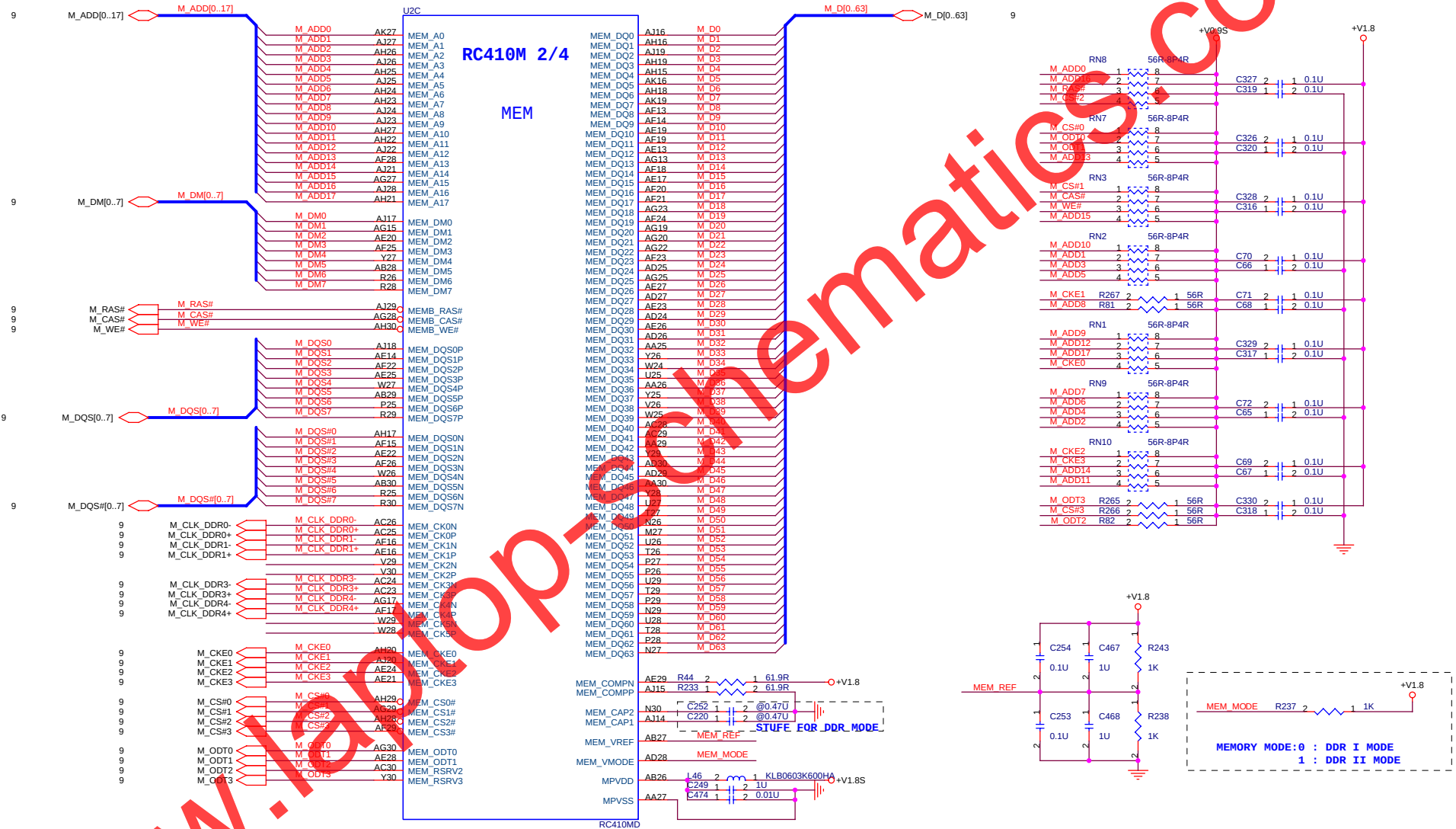
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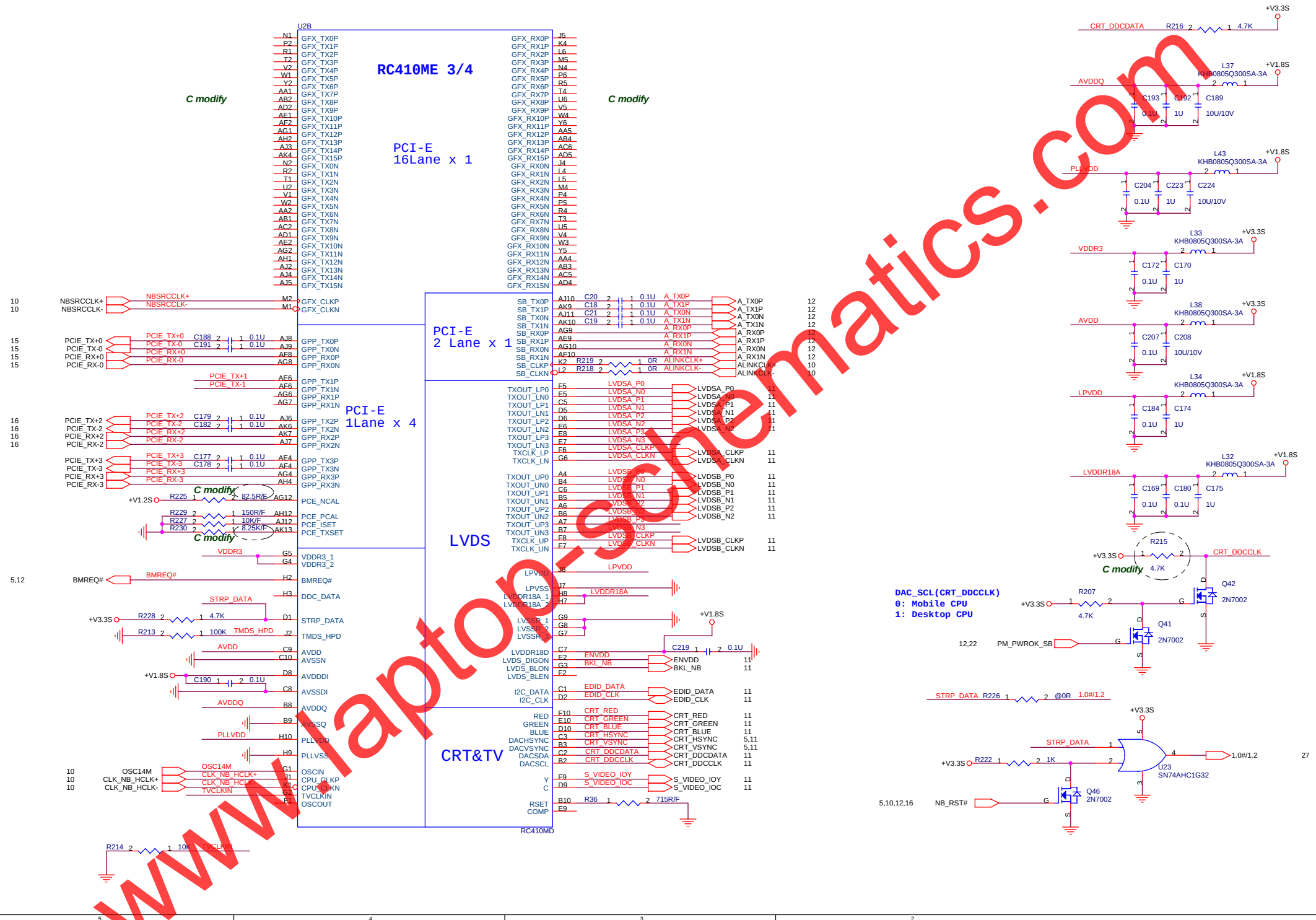


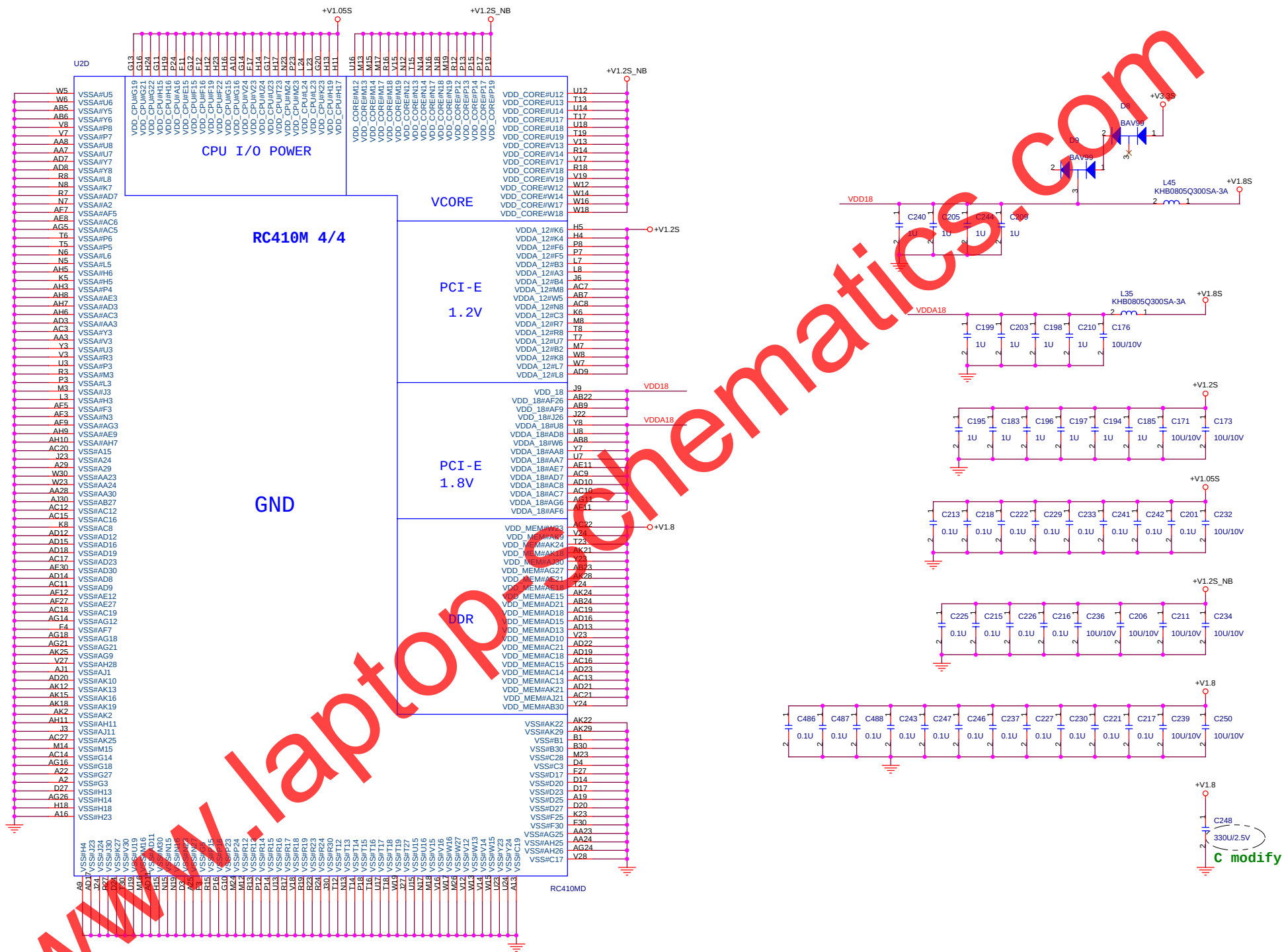


BMREQ#	HSYNC	VSYNC	CLK SPEED
0	0	0	100MHZ
0	0	1	133MHZ
0	1	1	166MHZ

DDR2 TERMINATION



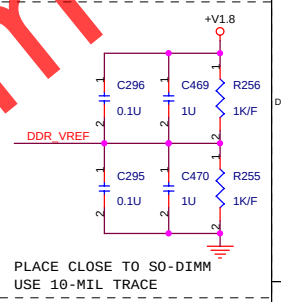




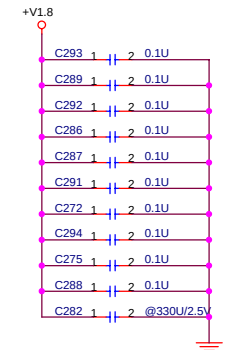
DDR2 SO-DIMM A0



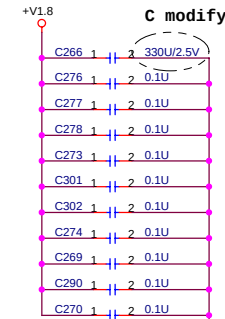
DDR2 SO-DIMM B0



PLACE CLOSE TO SO-DIMM
USE 10-MIL TRACE

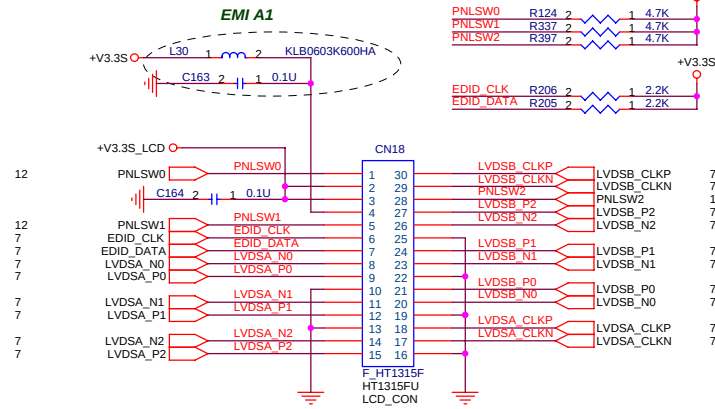
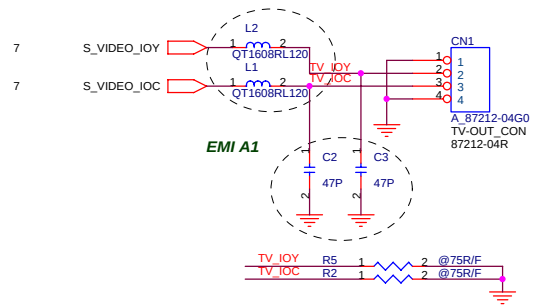


DE-COUPLING FOR SODIMM 1



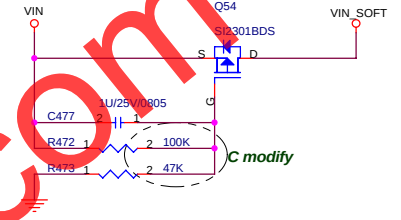
DE-COUPLING FOR SODIMM 2

Extended TV-OUT Connector

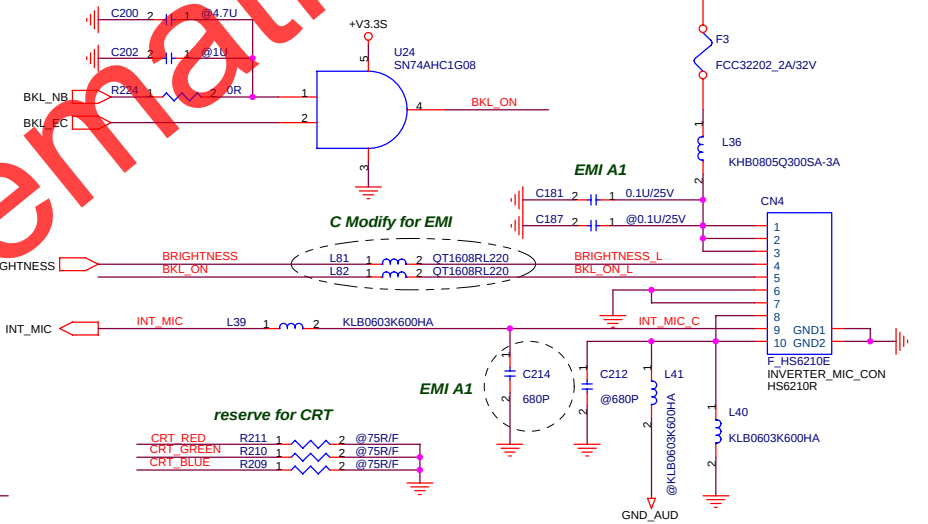


PANNEL ID SELECT

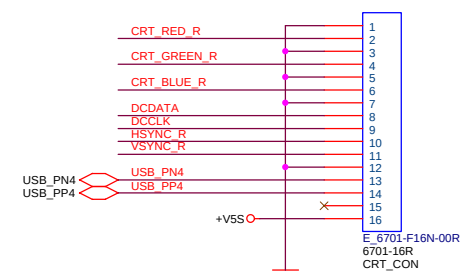
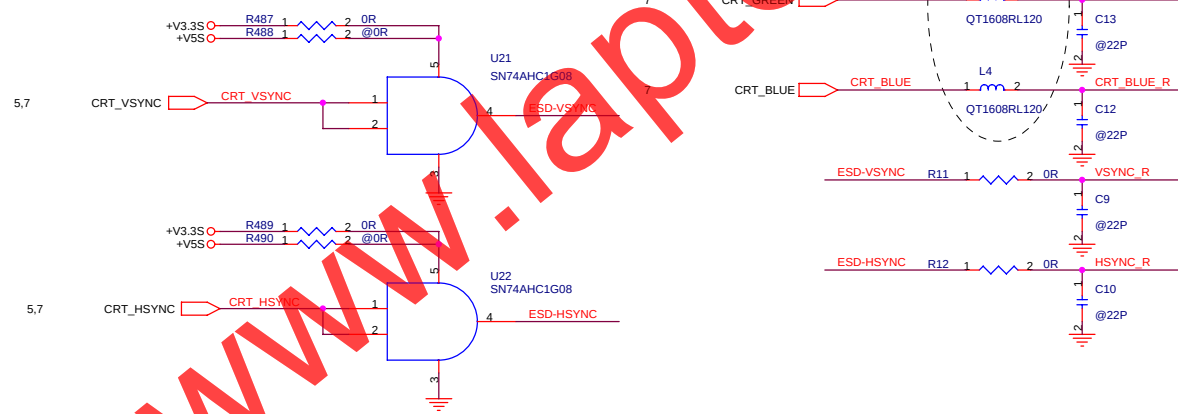
PNLSW2	PNLSW1	PNLSW0	reserved
L	L	L	1680X1050
L	H	L	1600X1200
L	H	H	1400X1050
H	L	L	1280X1024
H	L	H	1280X854
H	H	L	1280X800
H	H	H	1024X768

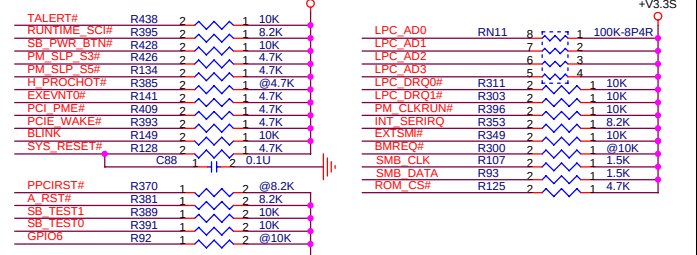
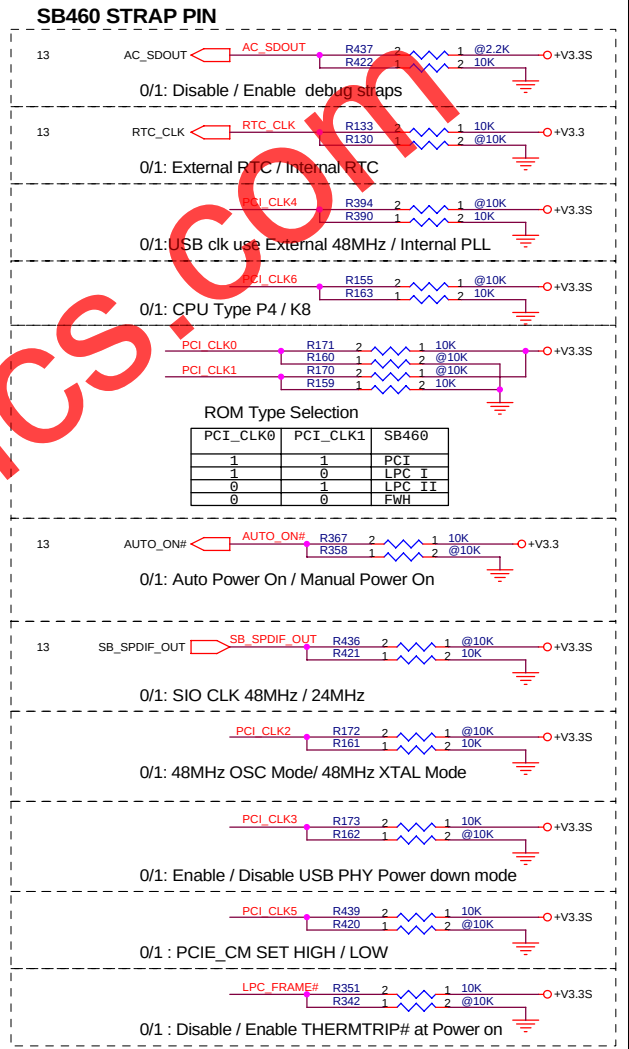
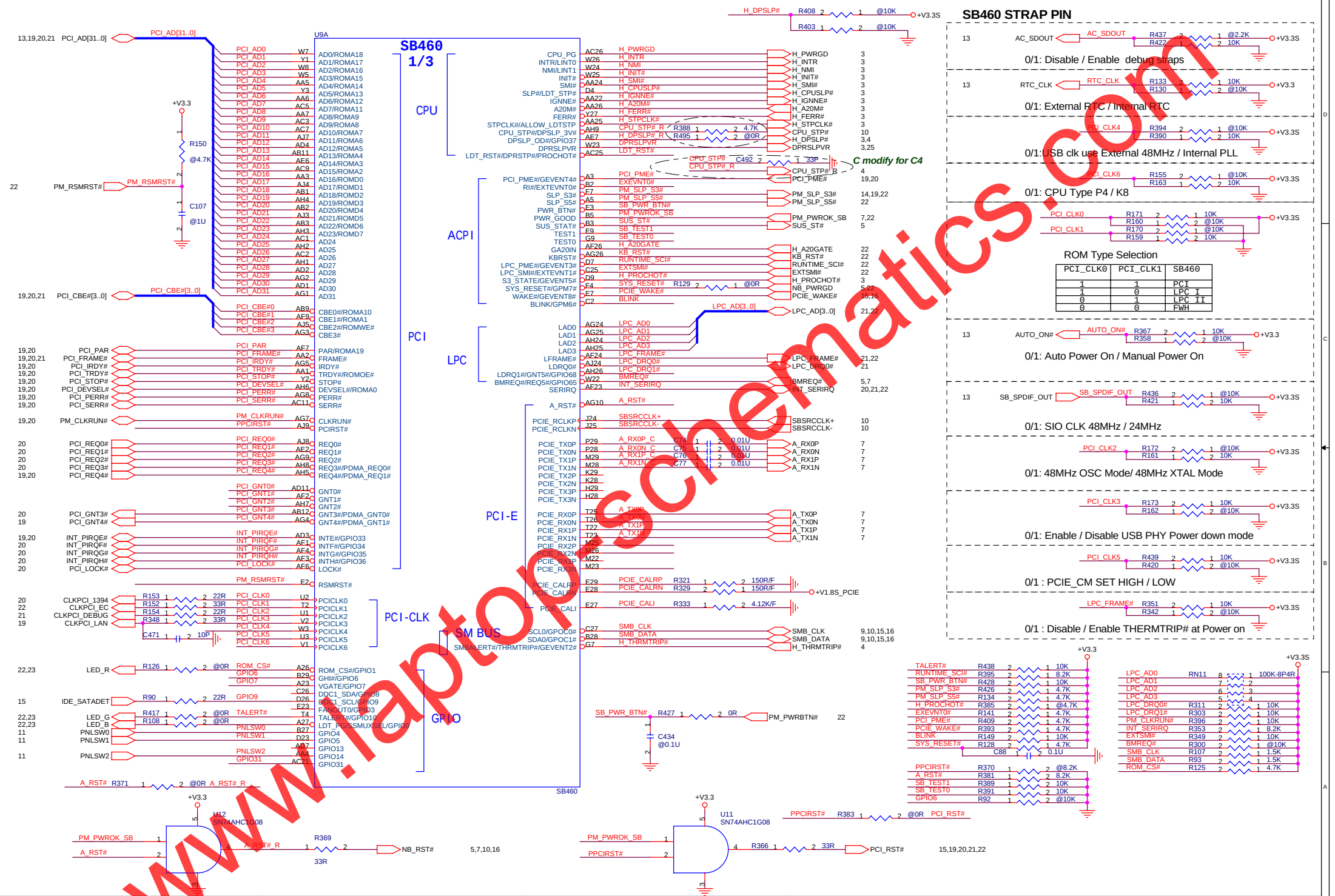


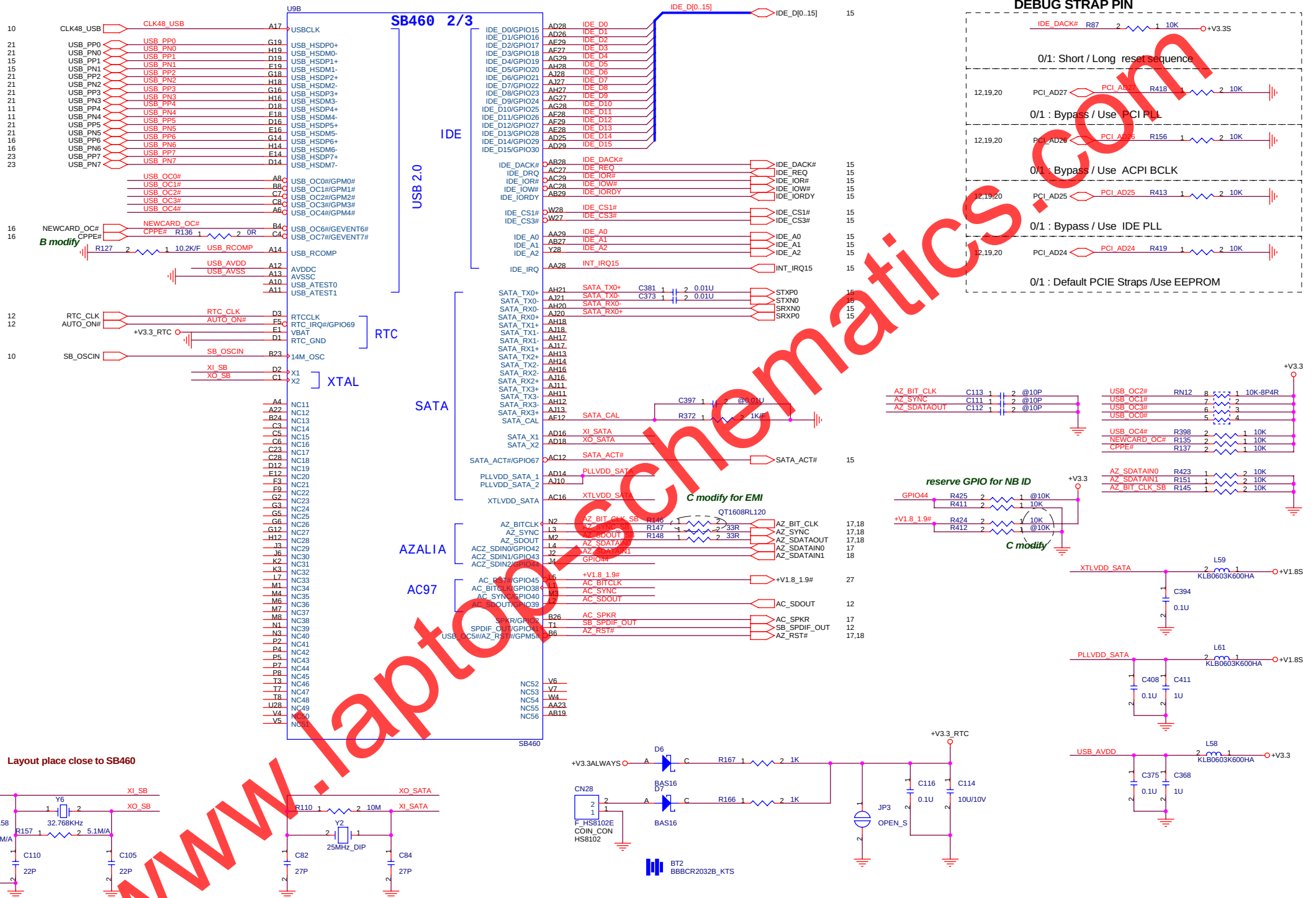
INVERTER

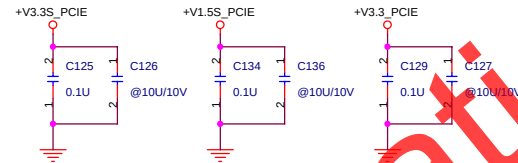
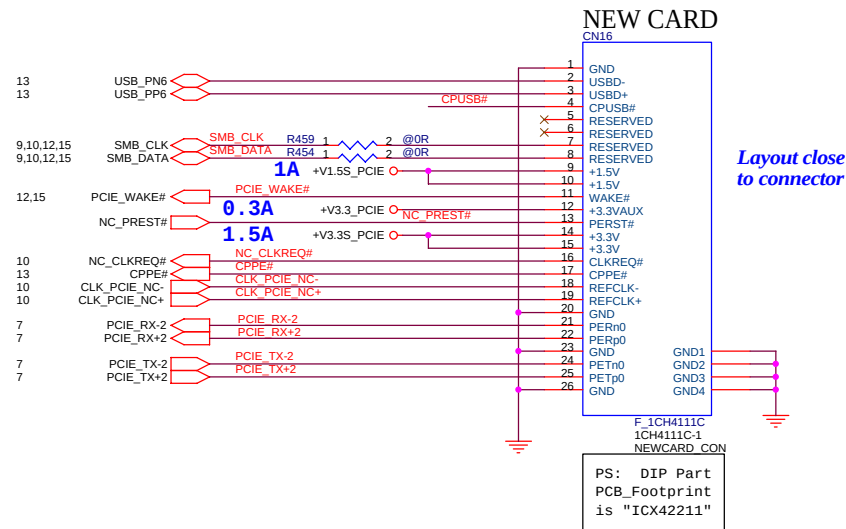


CRT CON

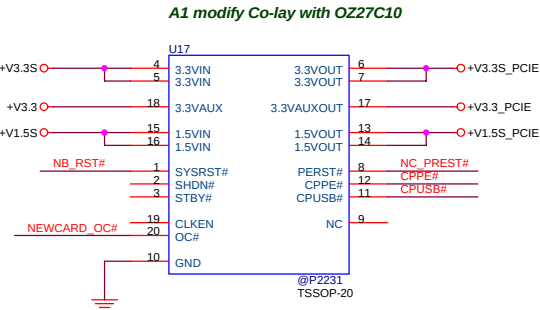
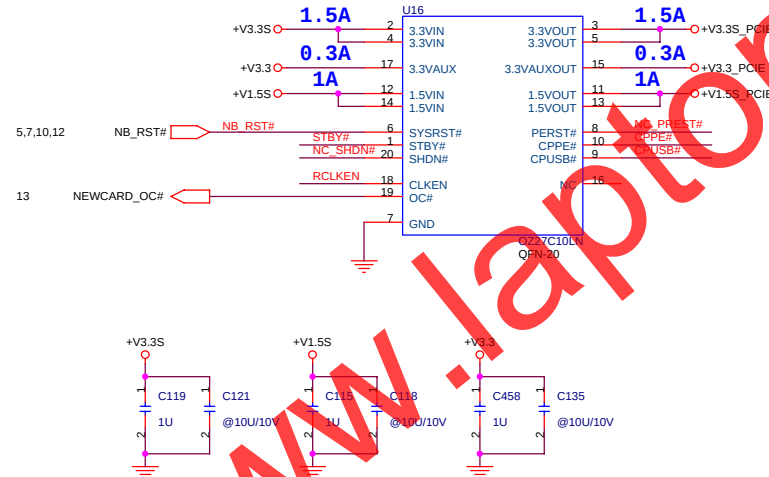


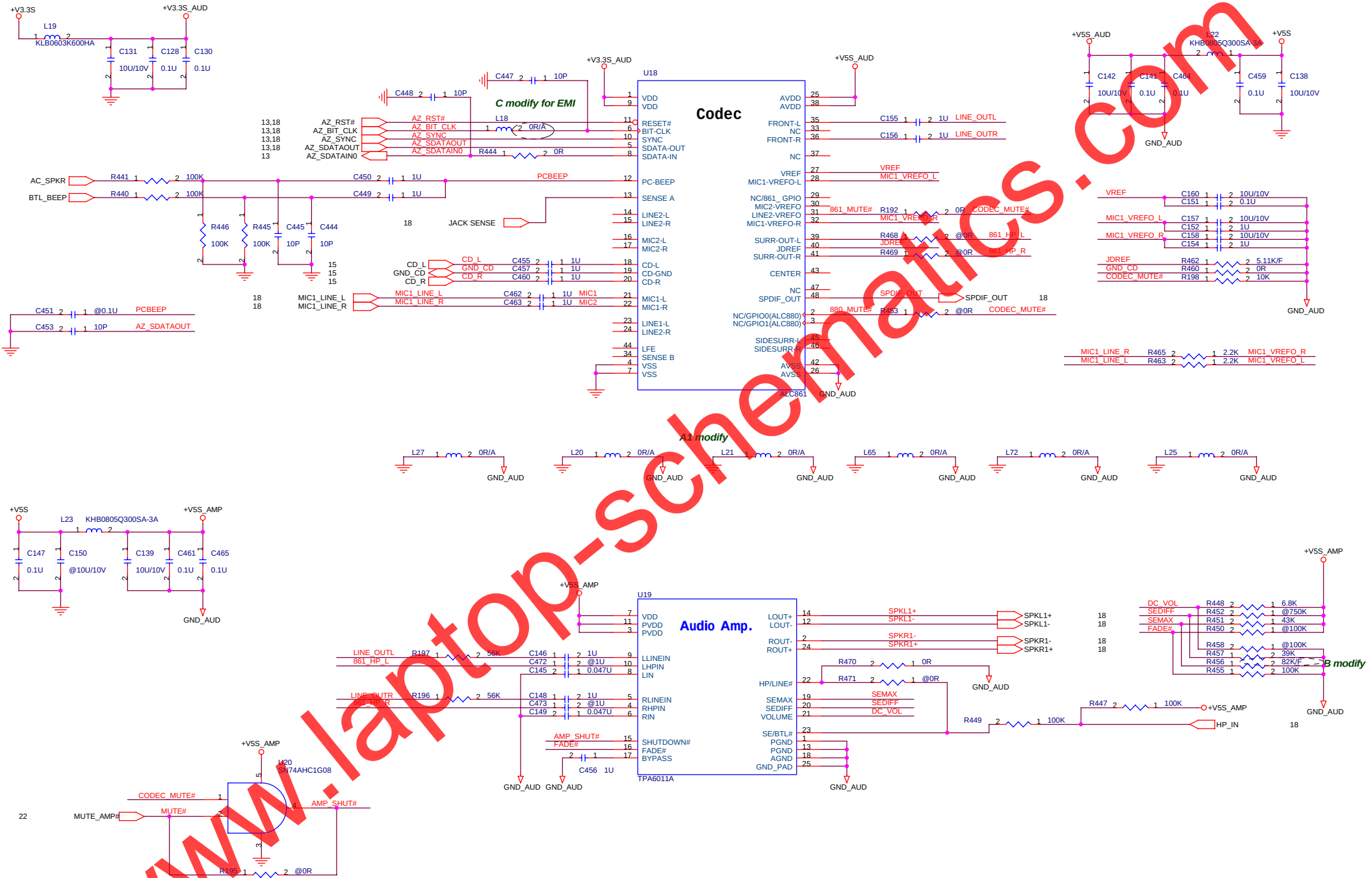






NEW CARD POWER SWITCH

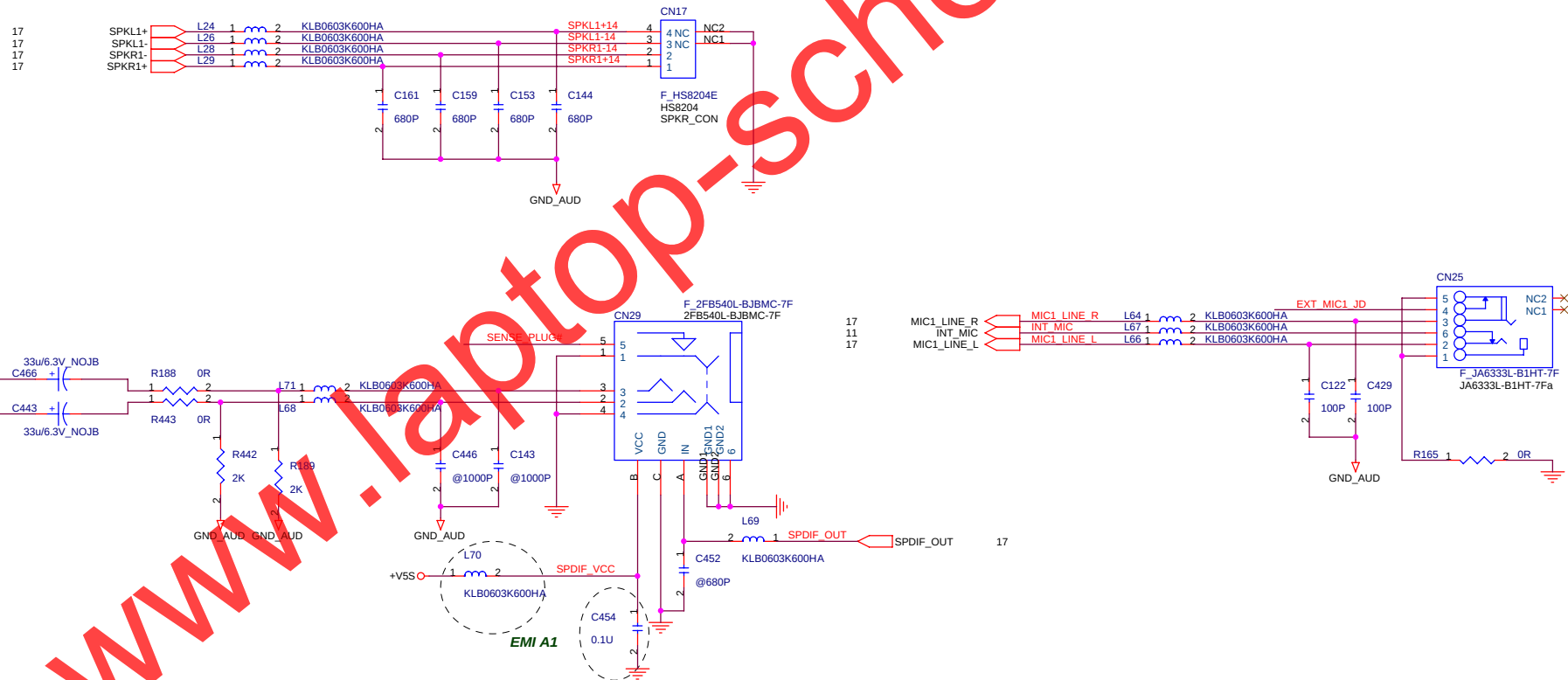


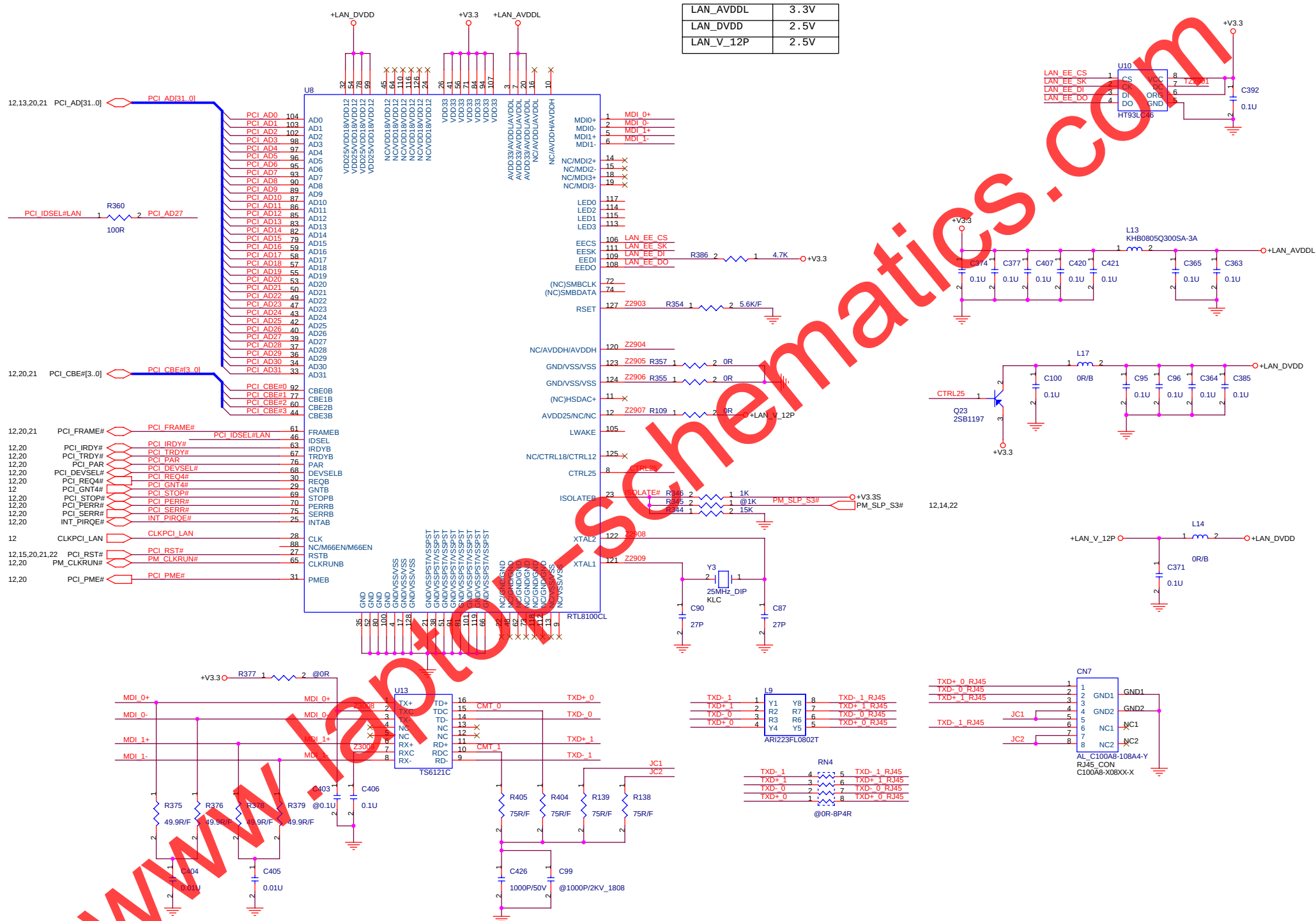


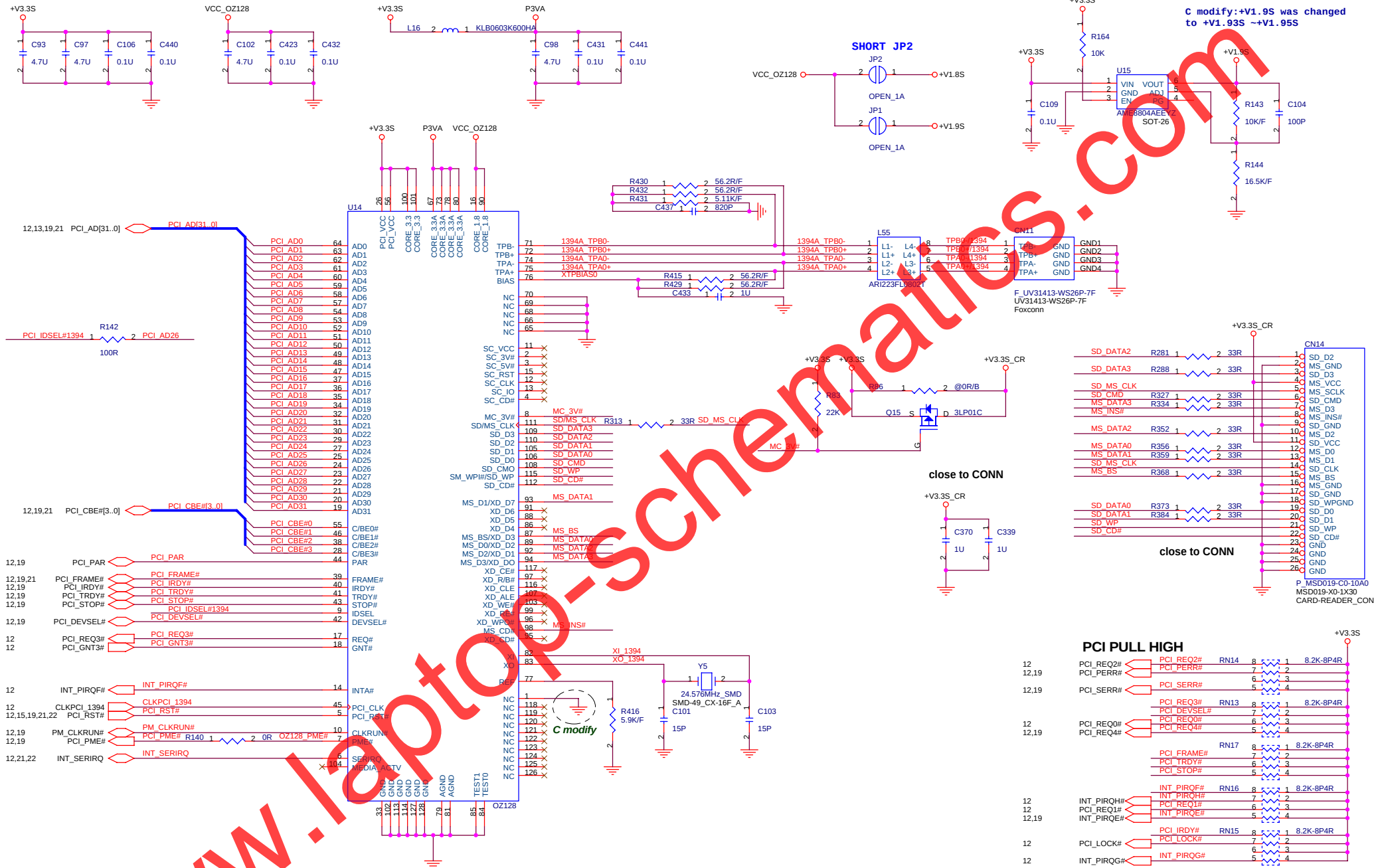
MDC Connector (X40 only)



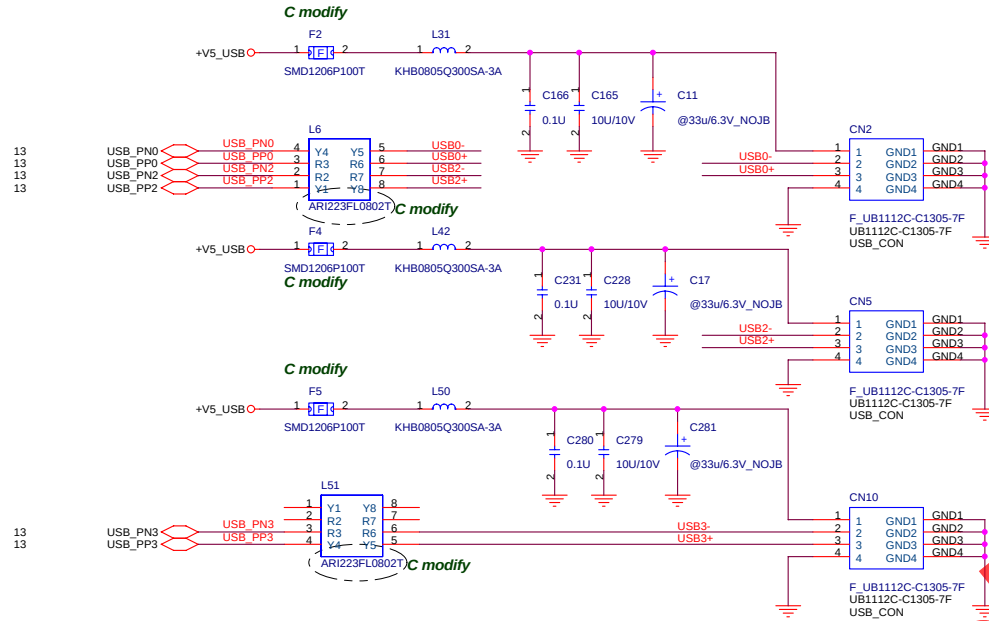
INTERNAL SPEAKER



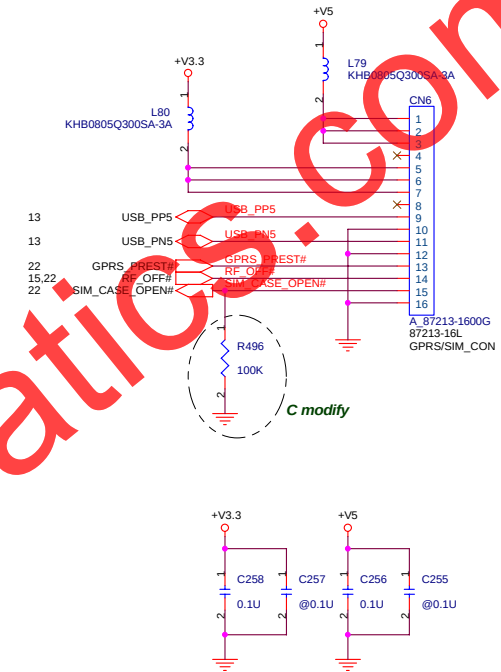




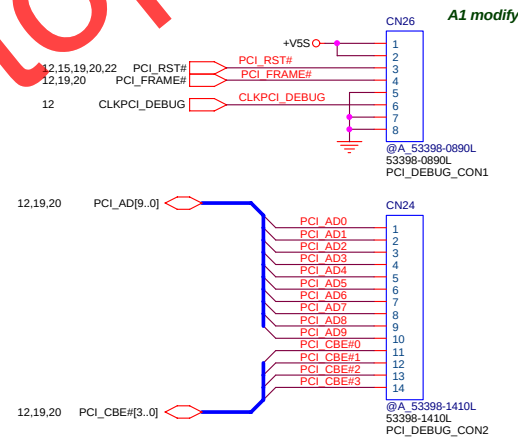
USB Connector



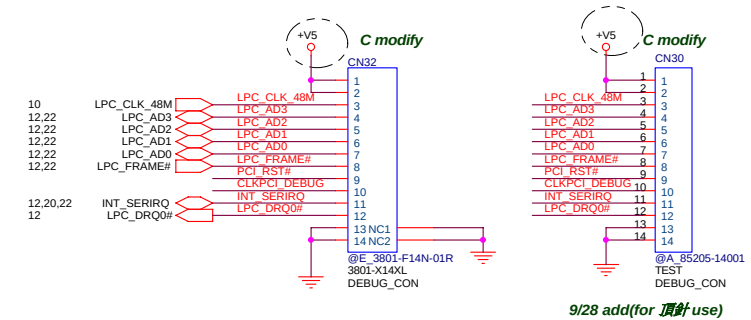
3G/SIM card Connector

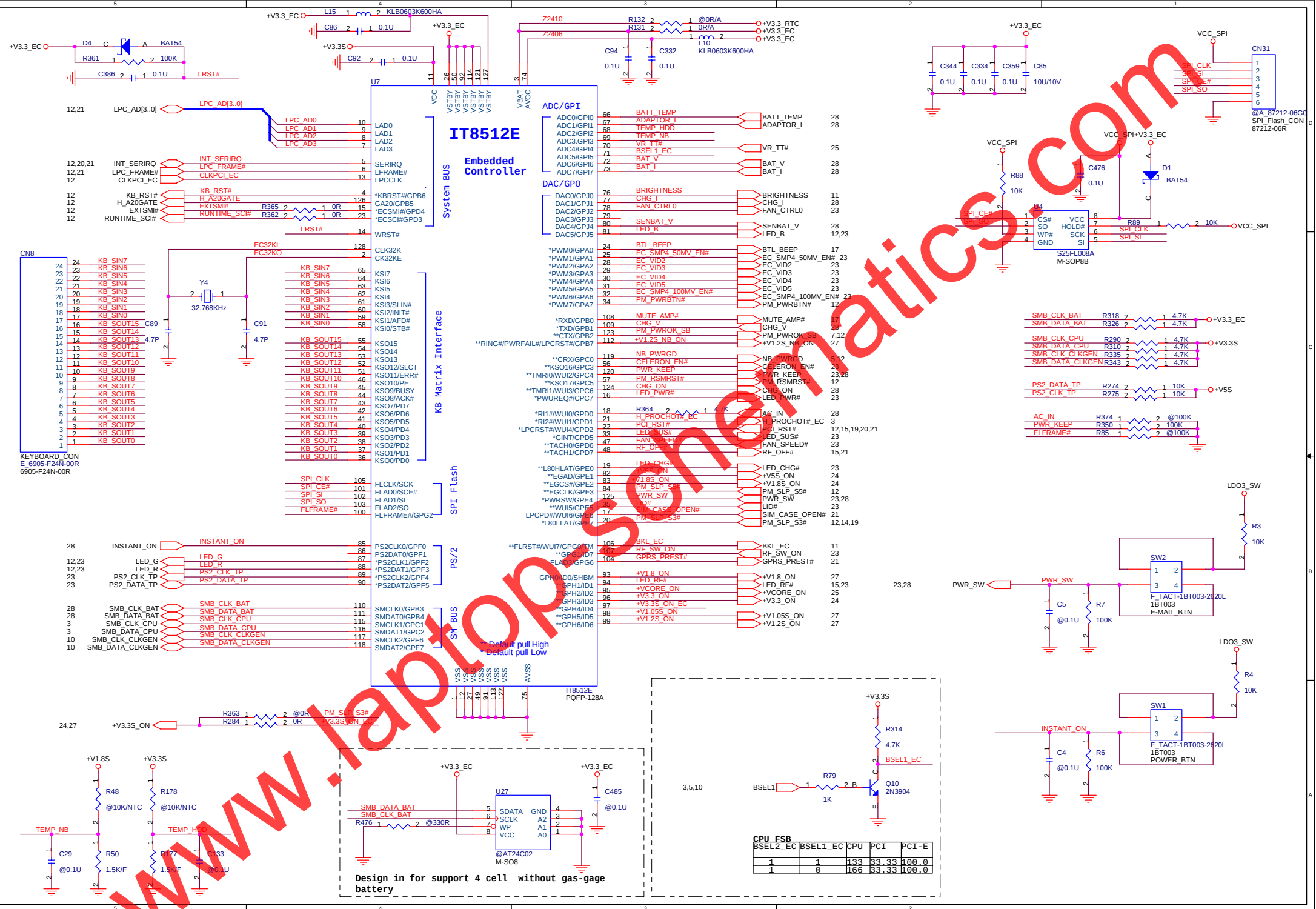


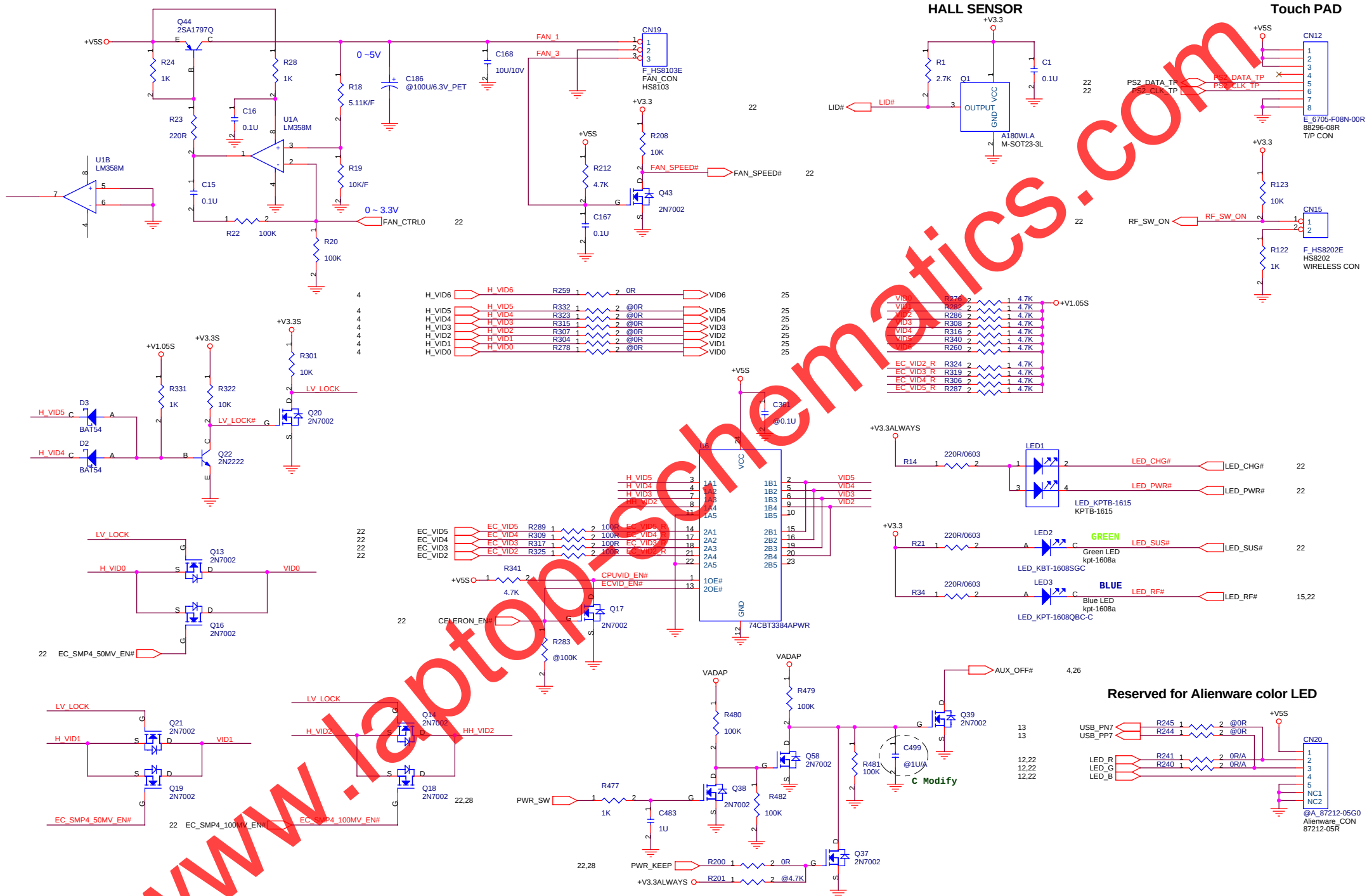
PCI Debug Card Connector

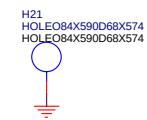
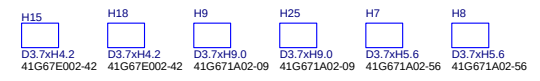
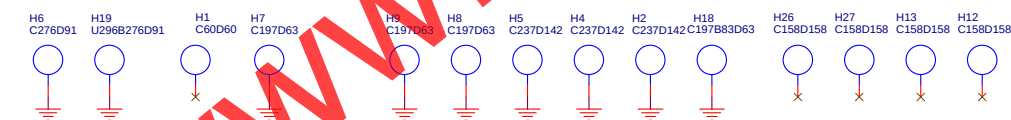
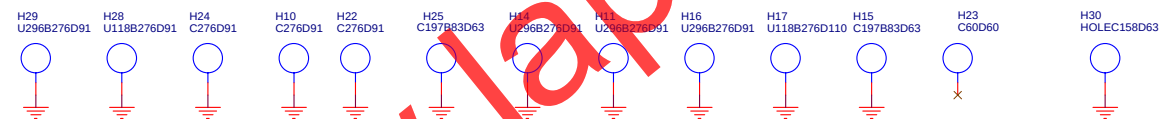
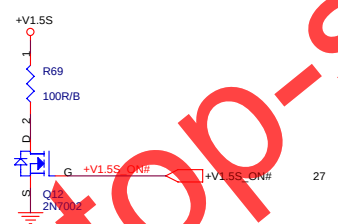
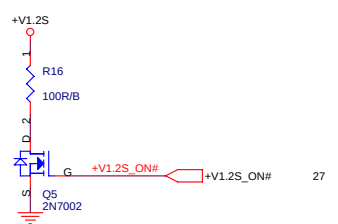
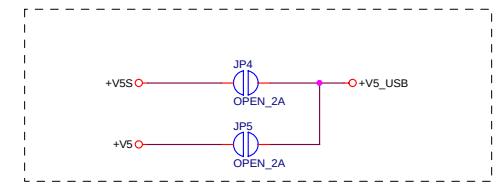
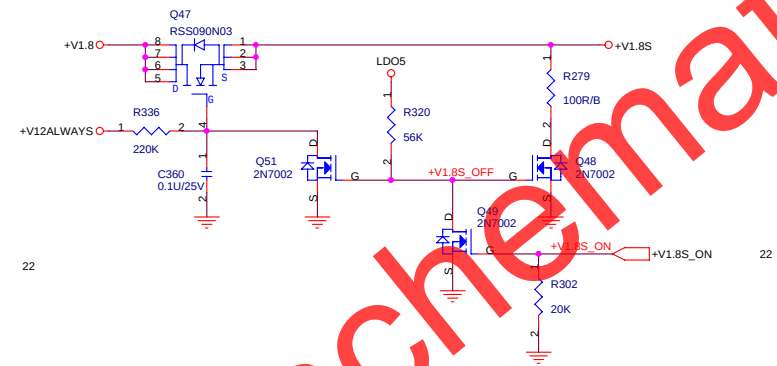
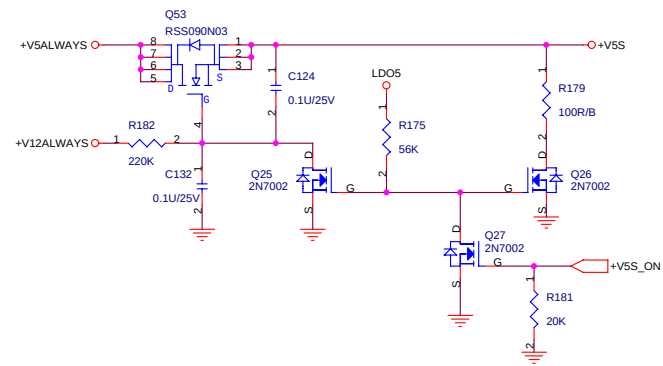
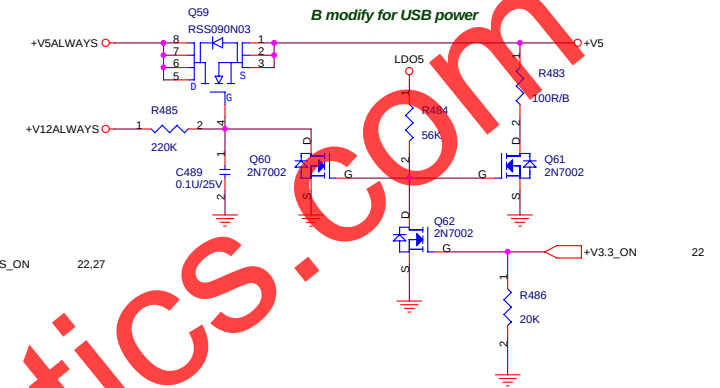
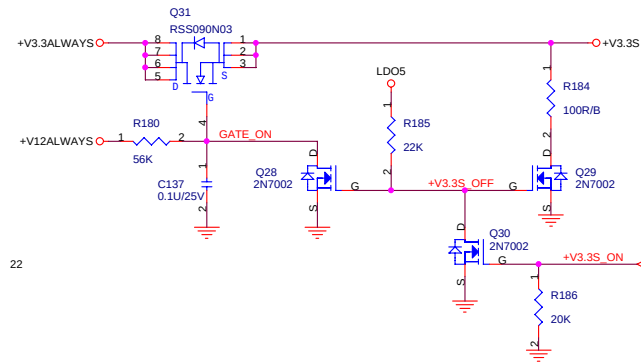
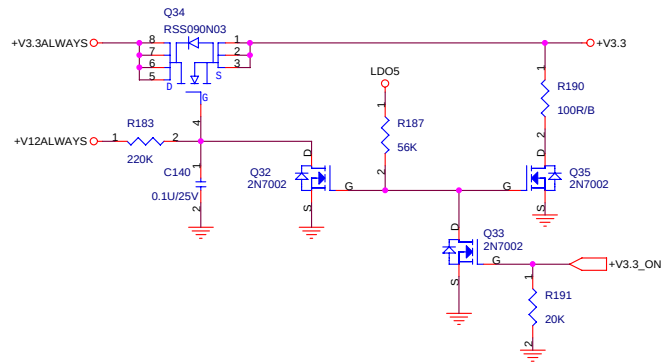


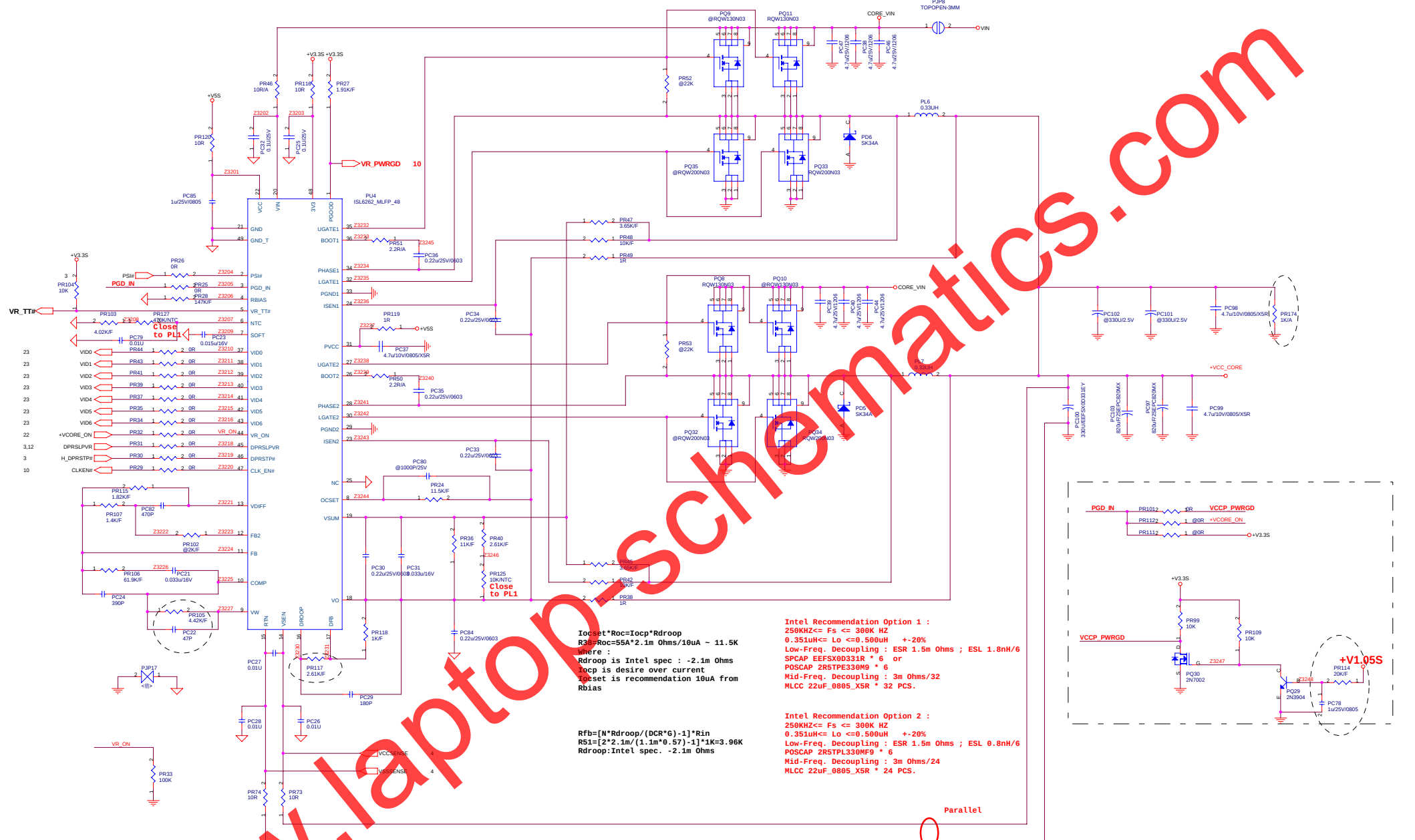
LPC Debug Card Connector

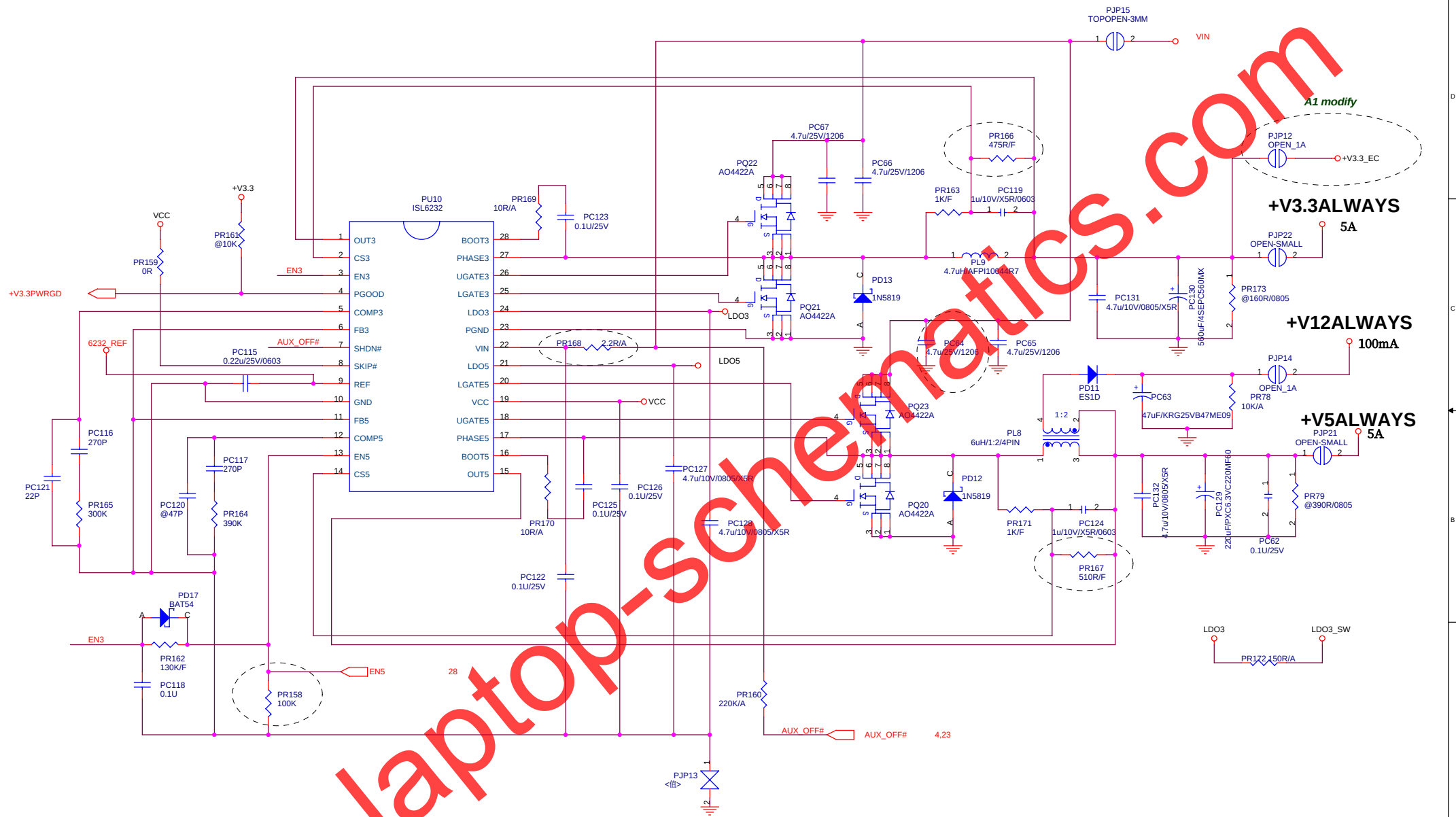








[illegible]



+V1.2S_NB
4A

+V1.05S
3A

+V1.2S
1.2A

+V1.5S
1.2A

+V0.9S
1.0A

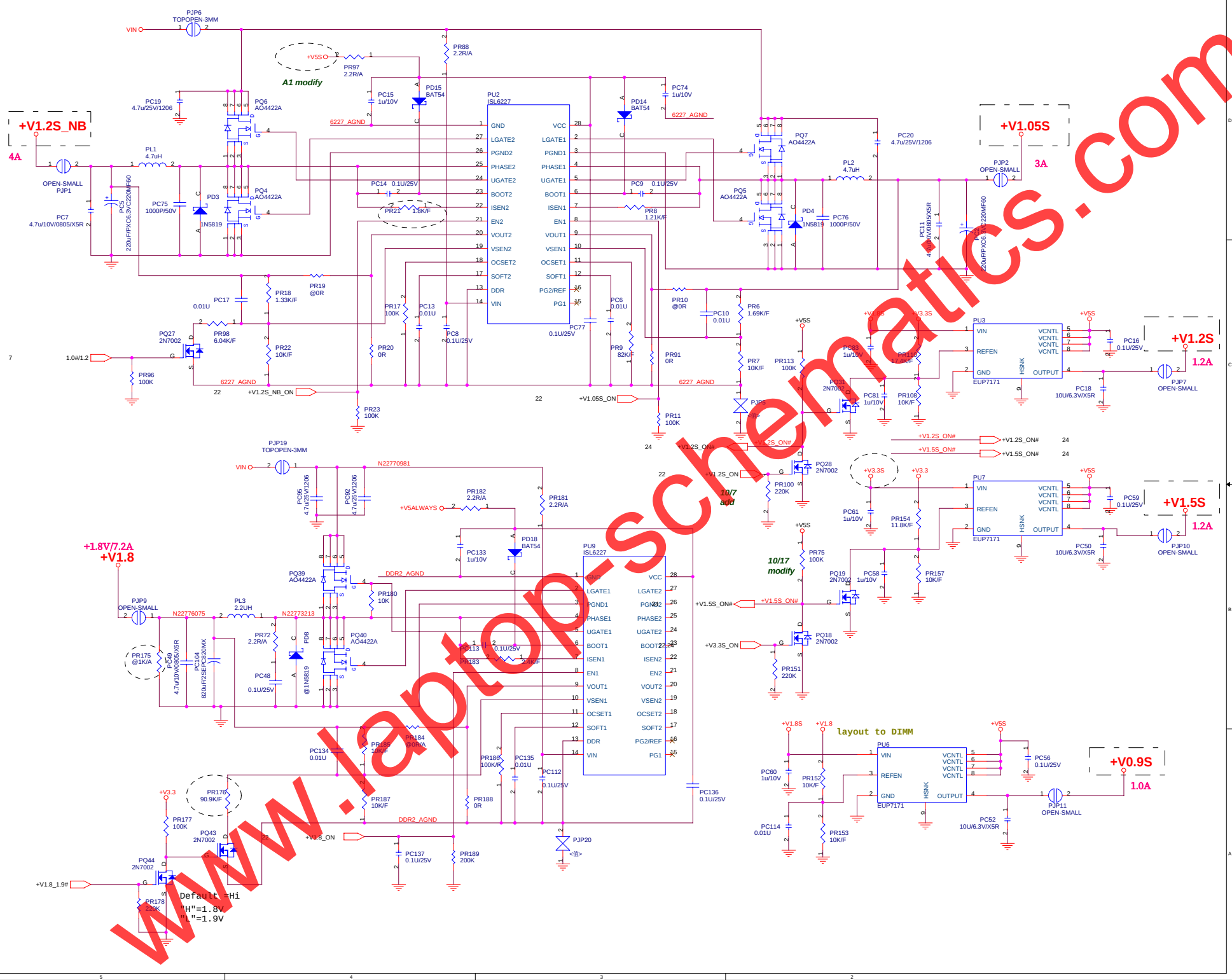
Default = Hi
"H"=1.8V
"L"=1.9V

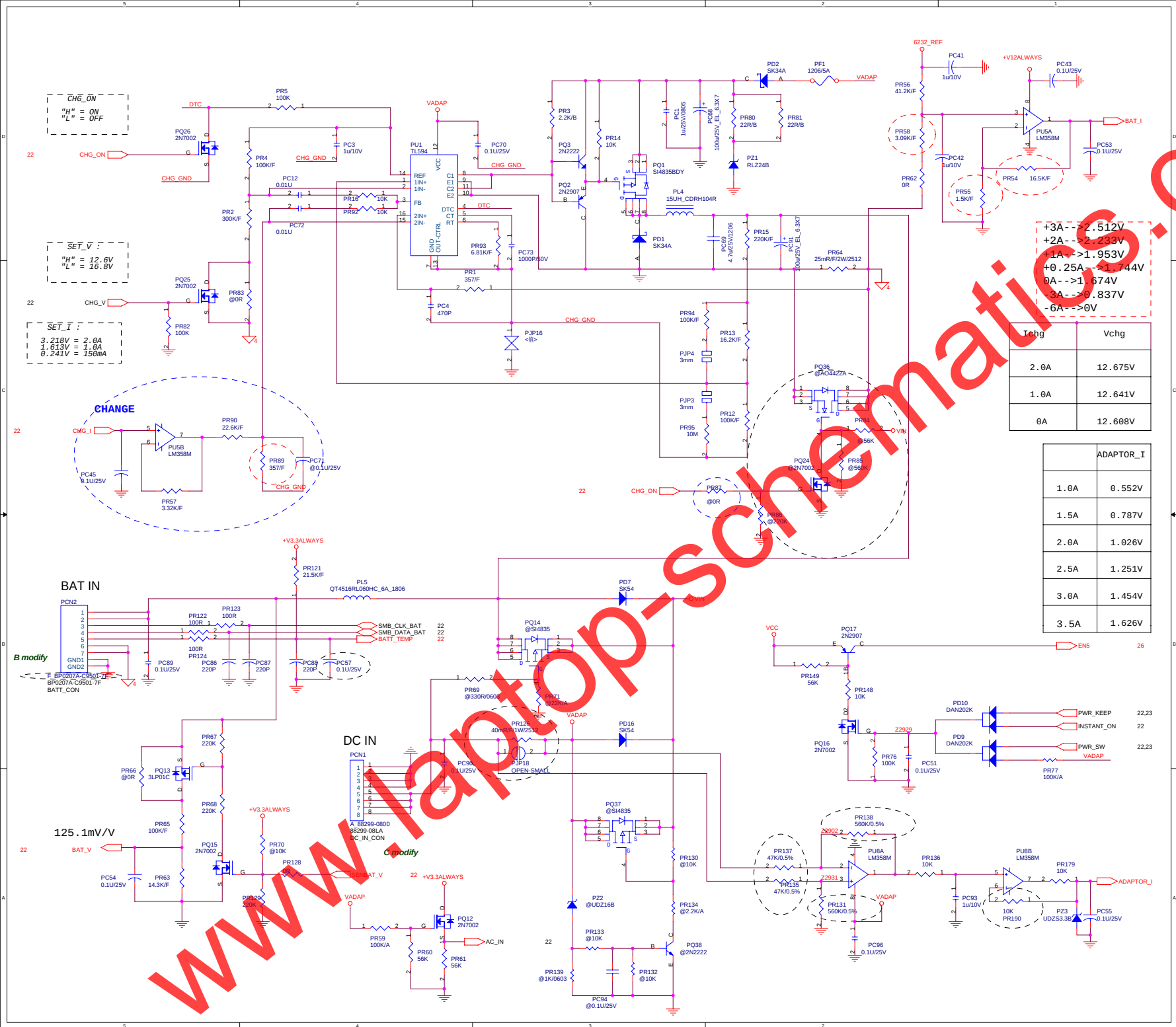
layout to DIMM

10/17 modify

10/17 add

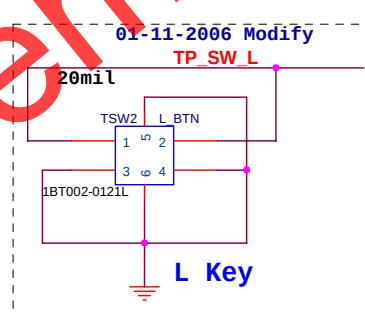
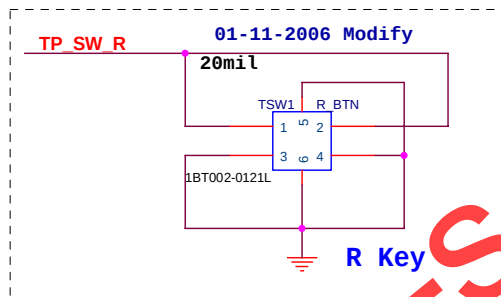
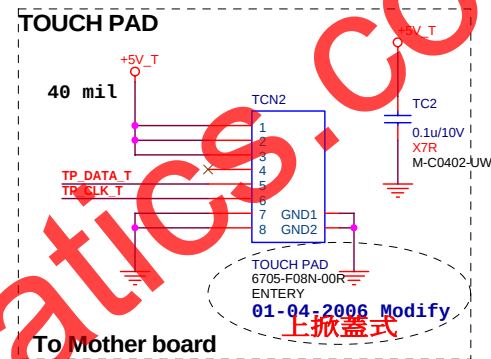
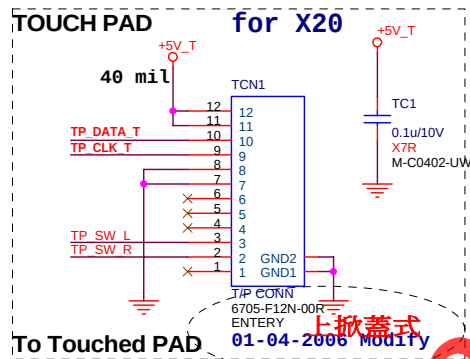
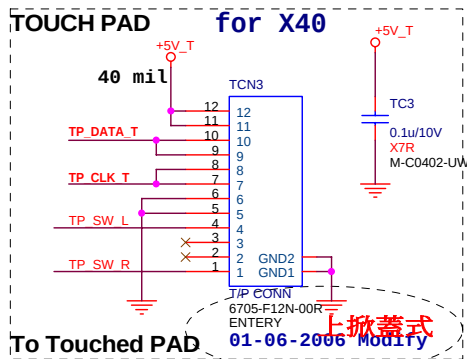
A1 modify

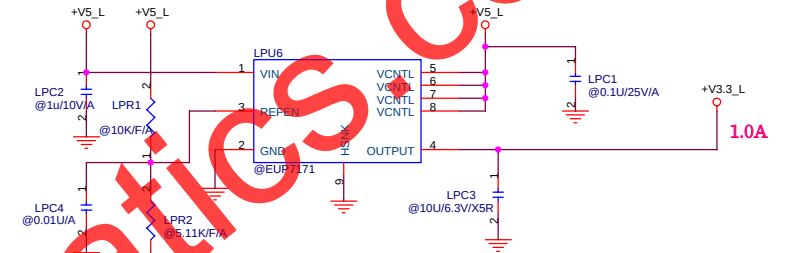
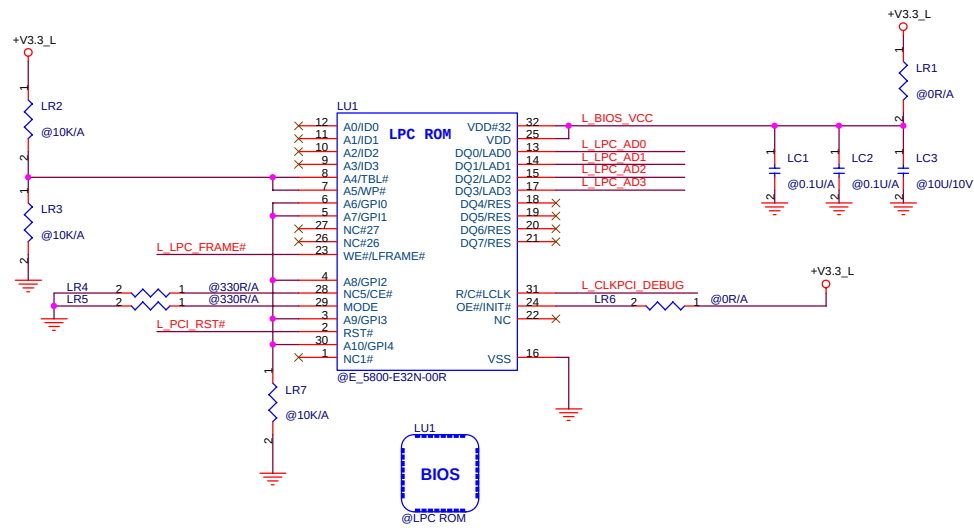




LAYER :8

LAYER1 : TOP
LAYER2 : GND
LAYER3 : IN1
LAYER4 : IN2
LAYER5 : GND
LAYER6 : IN3
LAYER7 : VCC
LAYER8 : BOT





LPC Debug Card Connector

