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MS(6797)

Version 100

Intel (R) Springdale (GMCH) + ICH5 Chipset
Intel Northwood & Prescott mPGA478B Processor

CPU:

Intel Northwood/Prescott

System Chipset:

Intel Springdale - GMCH (North Bridge)
Intel ICH5 (South Bridge)

On Board Chipset:

BIOS -- FWH EEPROM
AC'97 Codec --RealTek ALC655
LPC Super I/O -- SMSC 47M997
LAN --Broadcom BCM4401/5705
CLOCK --ICS 952617

Main Memory:

DDR * 2 (Max 2GB)

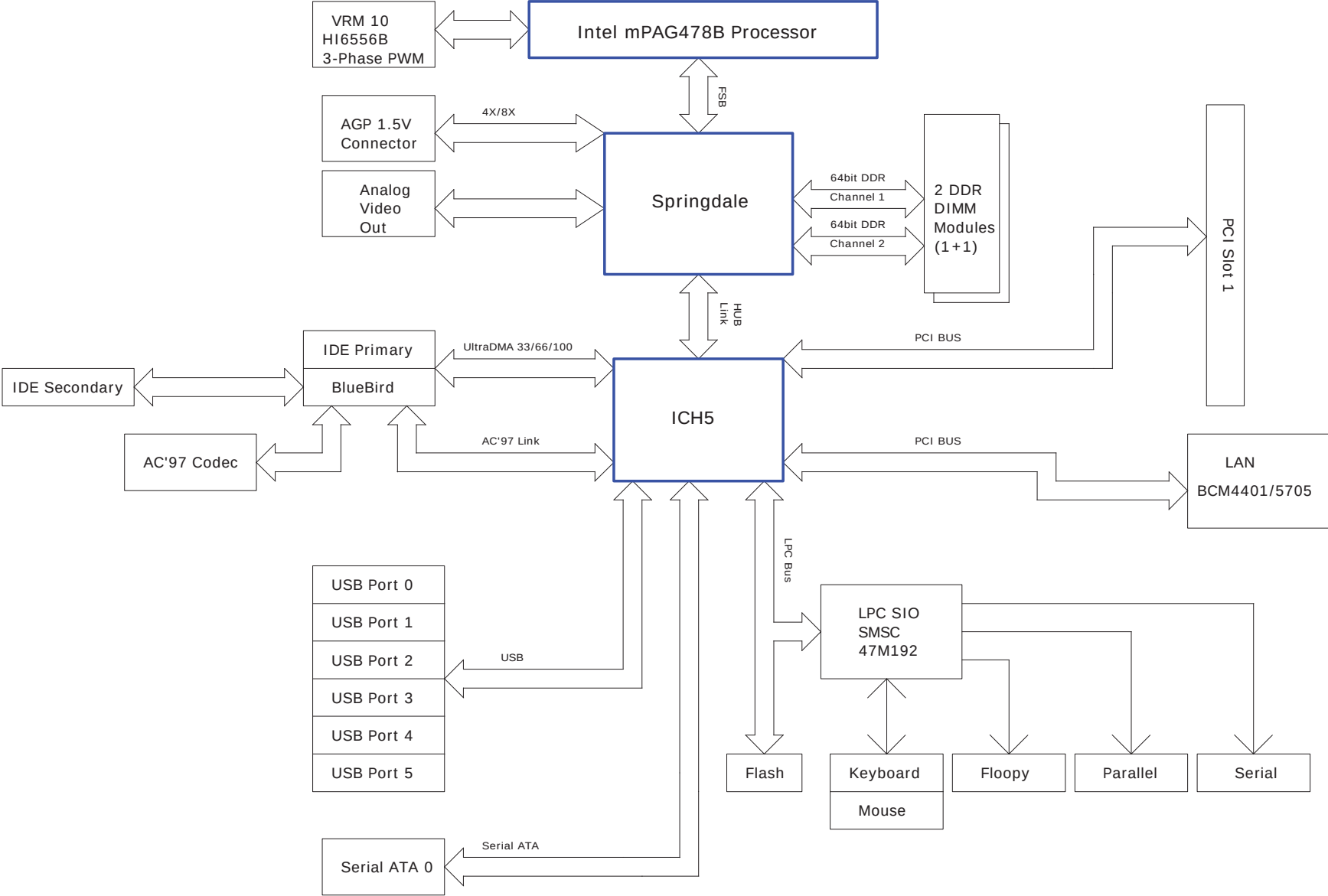
Expansion Slots:

PCI2.3 SLOT *1

PWM:

Controller: Intersil 6556B

Block Diagram



ICH5

GPIO Pin	Type	Function	Power well
GPIO 0	I	ISA_REQ#	MAIN
GPIO 1	I	PREQ#5	MAIN
GPIO 2	I	PIRQ#E	MAIN
GPIO 3	I	PIRQ#F	MAIN
GPIO 4	I	PIRQ#G	MAIN
GPIO 5	I	PIRQ#H	MAIN
GPIO 6	I	SIO_SMI#	MAIN
GPIO 7	I	GPI17	MAIN
GPIO 8	I	GPI18	RESUME
GPIO 9	I	OC4#	RESUME
GPIO 10	I	OC5#	RESUME
GPIO 11	I	RECOVER#	RESUME
GPIO 12	I	GPI12	RESUME
GPIO 13	I	SIO_PME#	RESUME
GPIO 14	I	OC#6	RESUME
GPIO 15	I	OC#7	RESUME
GPIO 16	O	ISA_GNT#	MAIN
GPIO 17	O	PGNT#5	MAIN
GPIO 18	O	GPO18	MAIN
GPIO 19	O	GPO19	MAIN
GPIO 20	O	GPO20	MAIN
GPIO 21	O	ISA_NOGO	MAIN
GPIO 22	OD	GPO22	MAIN
GPIO 23	O	GPO23	MAIN
GPIO 24	I/O	CD_SMI#	RESUME
GPIO 25	I/O	GPIO25	RESUME
GPIO 27	I/O	GPO27	RESUME
GPIO 28	I/O	GPIO28	RESUME
GPIO 32	I/O	BIOS_WP#	MAIN
GPIO 33	I/O	SATA_LED	MAIN
GPIO 34	I/O	GPIO34	MAIN
GPIO 40	I	PREQ#4	MAIN
GPIO 41	I	GPI41	MAIN
GPIO 48	O	PGNT#4	MAIN
GPIO 49	OD	CPUPWRGD	MAIN

default output
default output
default output
default output
default output
default output

FWH

GPIO Pin	Type	Function
GPI 0	I	PD_DET
GPI 1	I	SD_DET
GPI 2	I	Pull down through 1K ohms (unused)
GPI 3	I	Pull down through 1K ohms (unused)
GPI 4	I	Pull down through 1K ohms (unused)

SIO

GPIO Pin	Type	Function
GP26	I	VID5
GP43	I	VID4

PCI Config.

DEVICE	MCP1 INT Pin	REQ# / GNT#	IDSEL	CLOCK	CLK GEN PIN OUT
PCI Slot 1	INTA# INTB# INTC# INTD#	PCI_REQ#0 PCI_GNT#0	AD16	PCICLK0	13 (PCI_CLK0)
MPCI	INTB# INTC#	PCI_REQ#3 PCI_GNT#3	AD21	PCICLK1	14 (PCI_CLK1)

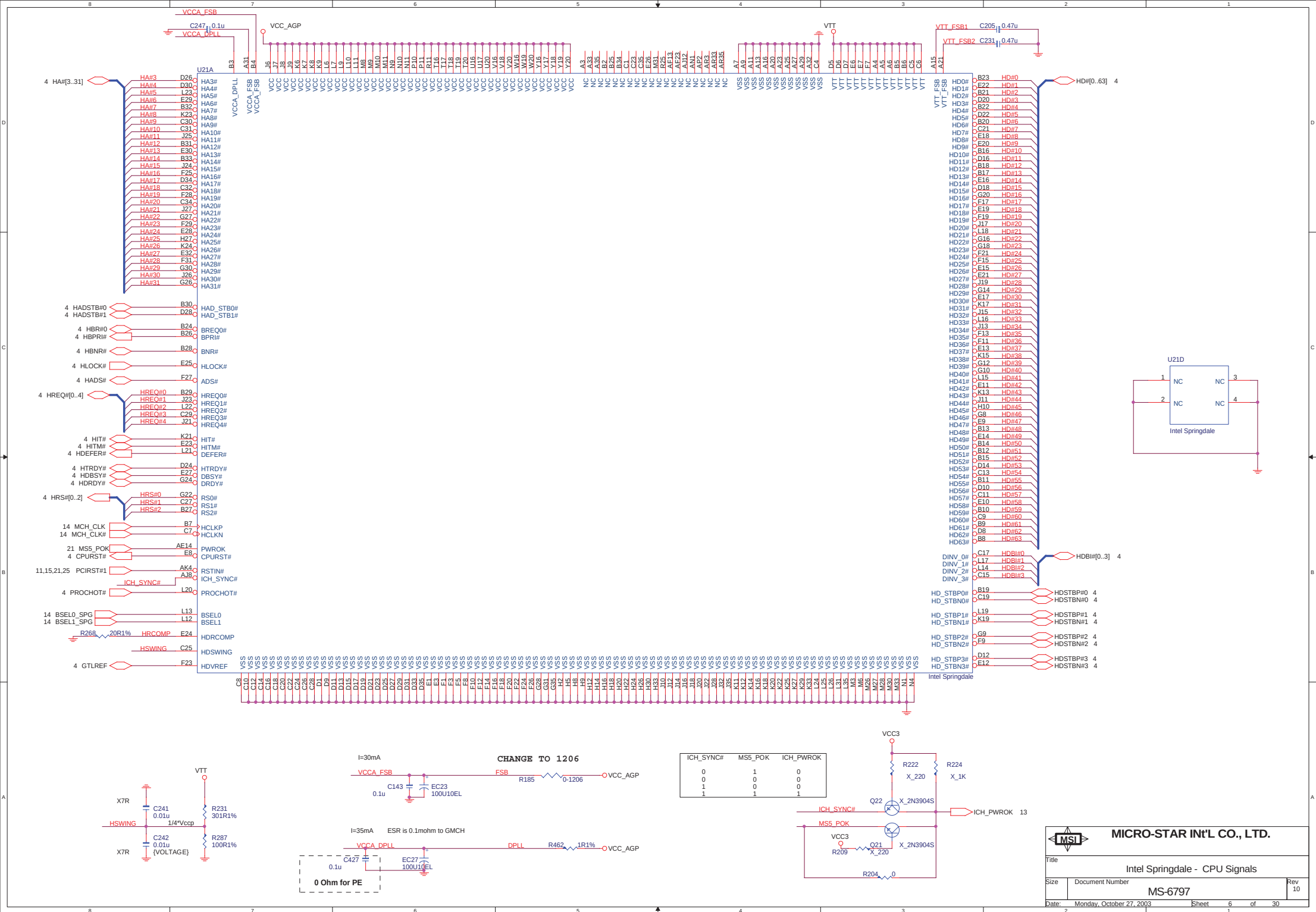
DDR DIMM Config.

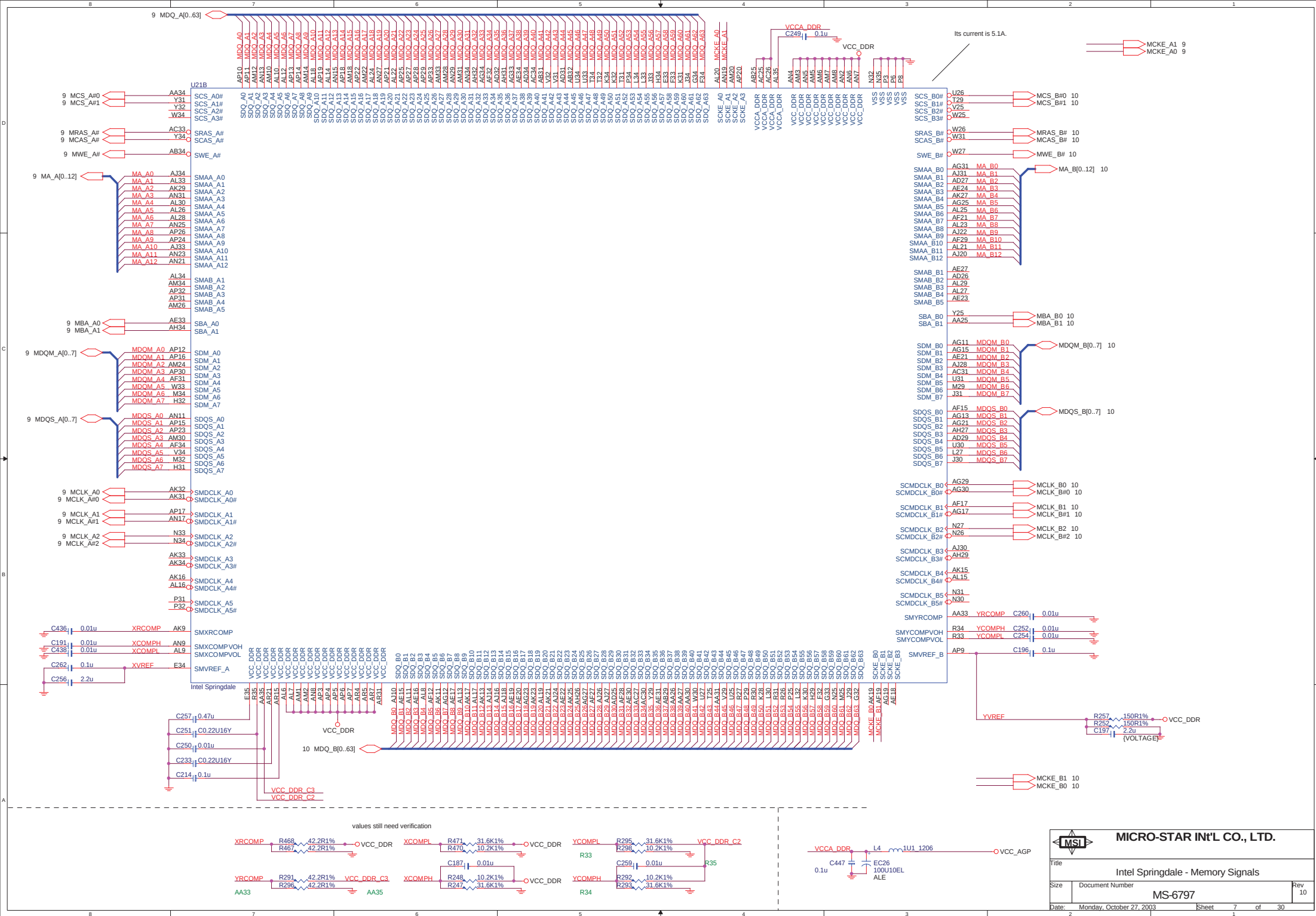
DEVICE	ADDRESS	CLOCK
DIMM 1	1010000B	MCLK_A0/MCLK_A0# MCLK_A1/MCLK_A1# MCLK_A2/MCLK_A2#
DIMM 3	1010010B	MCLK_B0/MCLK_B0# MCLK_B1/MCLK_B1# MCLK_B2/MCLK_B2#

PCI RESET DEVICE

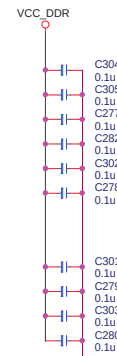
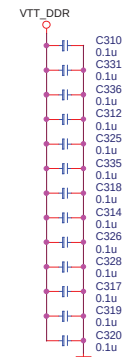
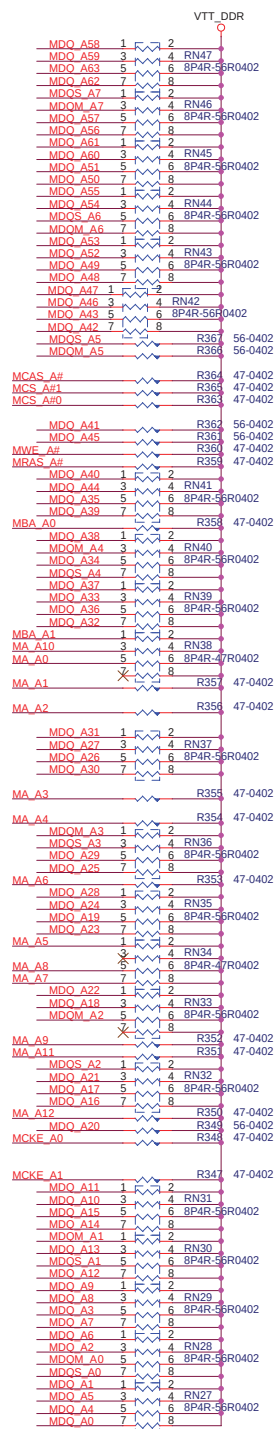
Signals	Target
PCIRST# ICH5	AGP slot, FWH, MS5,
PCIRST#1	Springdale,LAN, Super I/O,1394,TPM
PCIRST#2	PCI slot 1, ext PCI slot
HD_RST#	Primary, Scoundary IDE

Figure 1 shows schematic diagrams of the power supply circuit for the 100W and 150W modules. The diagrams illustrate the VCCP input, decoupling capacitors (C212, C234, C206, C238, C236, C224, C240, C211, C215, C478, C480, C235, C226, C479), and the output voltage (220u-2V). The 100W module uses capacitors C212, C234, C206, and C238. The 150W module uses capacitors C236, C224, C240, C211, C215, C478, C480, C235, C226, and C479. The output voltage is 220u-2V.





DDR Terminational Resisitors



ADDR.=1010000B



Title			
DDR DIMM 1 & 2			
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DDR DIMM3

SIGNALS

POWER

ADDR.=1010010B

DDR Termination Resistor

VTT_DDR

VTT_DDR

VTT_DDR

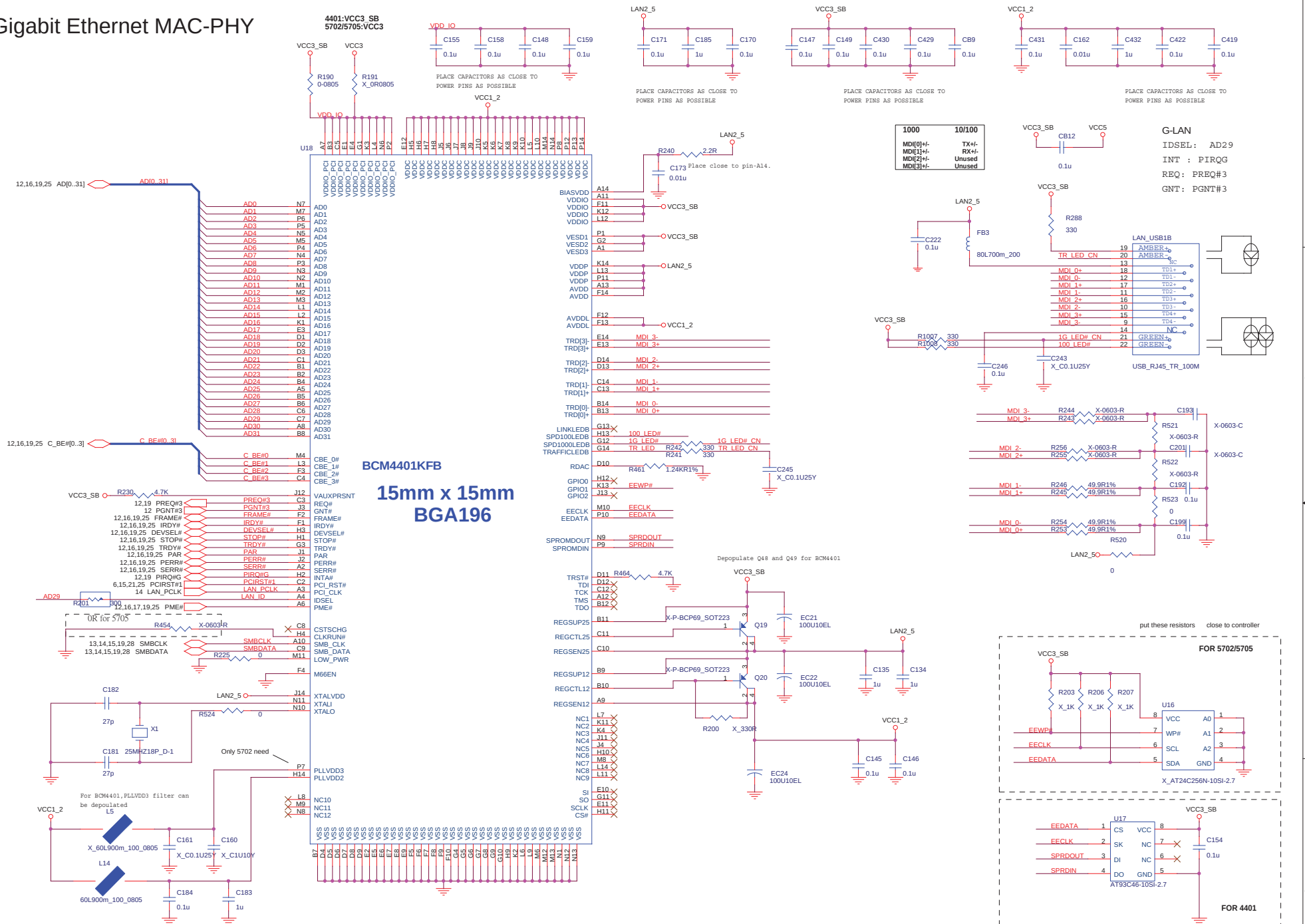
VCC_DDR

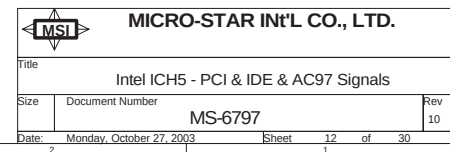


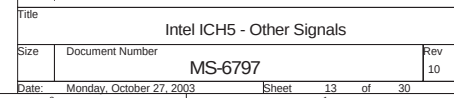
MICRO-STAR INT'L CO., LTD.

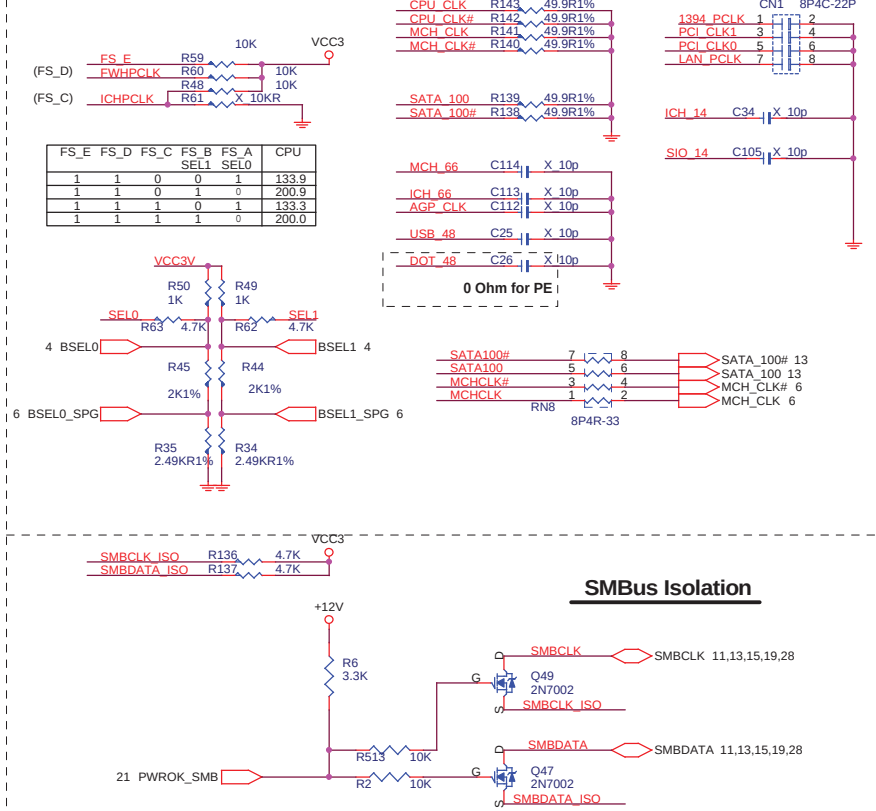
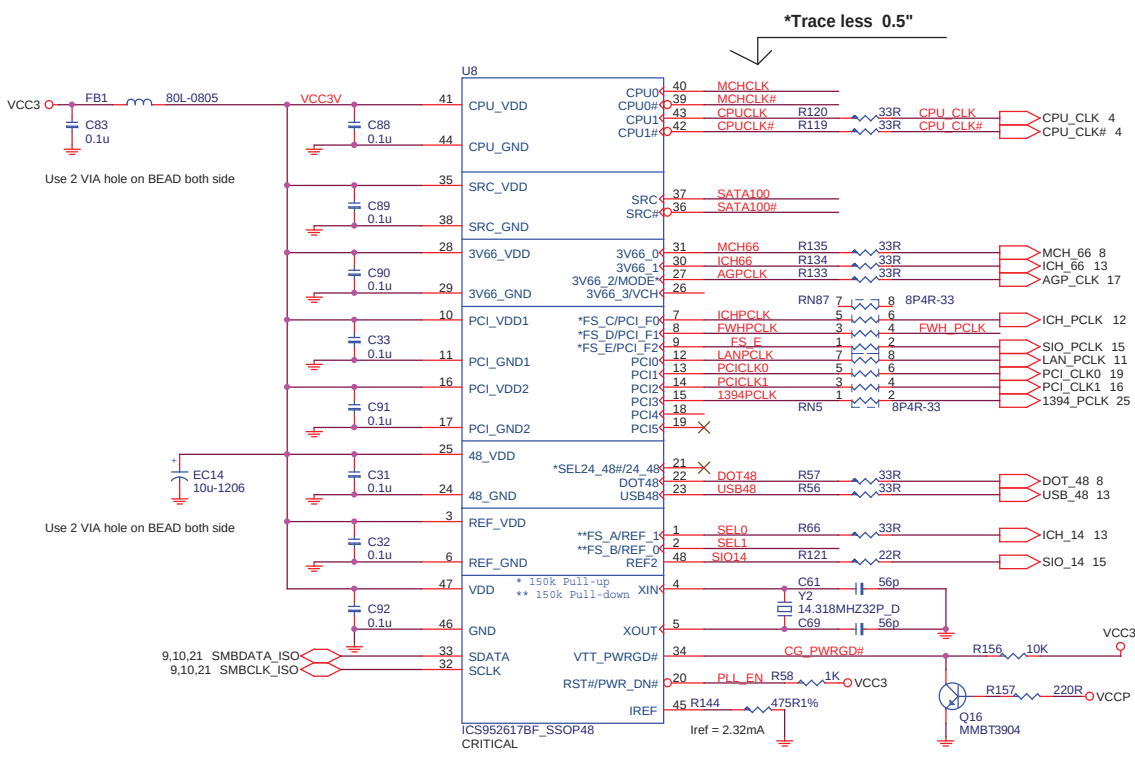
Title		DIMM 3.4	
Size	Document Number	MS-6797	Rev 10
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Gigabit Ethernet MAC-PHY





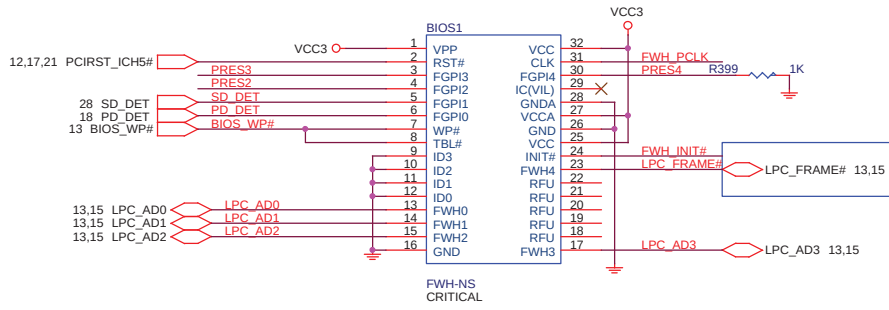




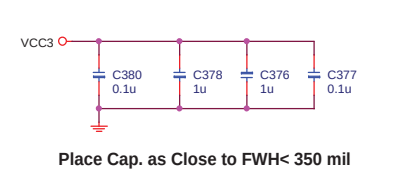
BIOS PROTECT BLOCK

BIOS Update Config.		
HIGH	Un_protected	
LOW	Protected	Default

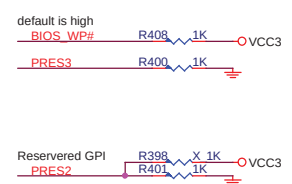
Firmware Hub (FWH)



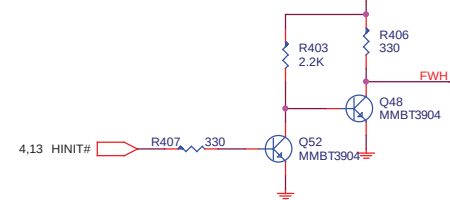
FWH DECOUPLING CAPACITORS



FWH Resistors



INIT signal voltage translation



MICRO-STAR INT'L CO., LTD.

Title CY28404 & FWH & TPM		
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6.11.21.25 PCIRST#1 14 SIO_PCLK 13 SERRIQ 13 LPC_DRQ0# 13.14 LPC_FRAME#

13.14 LPC_AD0 13.14 LPC_AD1 13.14 LPC_AD2 13.14 LPC_AD3

4.23 VID5

22 CPU_CTRL 22 CPU_FAN 22 SYS_FAN

13 SIO_SMI# 13 SIO_PME#

4.23 VID4

11.13.14.19.28 SMBDATA 11.13.14.19.28 SMBCLK

4.23 VID[0..3]

VCCP 0 VCC3 0 VCC3_AGP 0 VCC3_SB 0 VCC5 0 VCC3 0 VCC_DDR 0

13 SUSCLK 14 SIO_14 4 THERM+

VCC3 0 CB7 0.1u VCC3 0

W/Game port VREF=5V
W/O Game port VREF depends on layout

U4

26 PCI_RESET# 29 PCI_CLK 30 SER_IRQ 32 LDRQ# 34 LFRAME# 36 LPC_PDI# 20 LAD0 22 LAD2 23 LAD3 32 J1B1/GP10 33 J1B2/GP11 34 J2B1/GP12 36 J2B2/GP13 37 JX1/GP14 38 JY1/GP15 39 JX2/GP16 41 JZ/GP17 46 GP20/P17 47 MIO_INGP25 48 MIO_OUT/GP26 51 FAN_CTL1/GP33 54 FAN_CTL2/GP32 52 FAN_CTL3/GP32 51 FAN_SEN1/GP31 51 FAN_SEN2/GP30 48 LED1/GP60 45 LED2/GP61 51 SYSPORT/GP24 50 IO_SMI#/GP27 50 IO_PME#/GP42 51 DDRIC/GP43 103 SDA 104 SCLK 106 VID0 107 VID1 108 VID2 109 VID3 109 +12V_IN/VID4 112 HGND 118 VCCP_IN 119 NC1(+1.8V_IN) 120 NC2(+1.5V_IN) 121 HVCC 122 D0-(XNOR_IN) 124 D0+ 125 +5V_IN 126 +3.3V_IN 127 +2.5V_IN 128 A0(RESET#)THERM#(XNOR_OUT) 6 SUSCLK 19 CLK_14M 123 NC3(D1+) 124 NC4(D1-) 44 VREF 45 VCC 46 VCC 93 VCC 101 HGND 125 NC5(HGND) 126 NC6(HGND) 127 HGND 128 HGND 101 HGND 125 NC5(HGND) 126 NC6(HGND) 127 HGND 128 HGND

DRVDENO/GP40 1 DRVDENO 2 DRVDENO/GP41 3 INDEX# 3 MOT_A# 3 MOT_B# 3 DRV_A# 3 DRV_B# 3 DIR# 3 MOT_B# 3 DIR# 3 STEP# 3 WT_DT# 3 WT_EN# 3 TRACK0# 3 FDD_WP# 3 TRACK0# 3 HEAD# 3 DSKCHG# 68 PRD0 69 PRD1 70 PRD2 71 PRD3 72 PRD4 73 PRD5 74 PRD6 75 PRD7 77 RSLCT# 78 RPE 79 RBUS# 80 RACK# 81 RSLIN# 82 RINT# 83 RERR# 84 RACD# 85 RSTB# 61 IRRX2/GP34 62 IRTX2/GP35 90 R1# 91 R2# 98 R0X1 99 CTS# 98 DCD# 98 DCD# 98 DSR# 98 TXD1 98 RTS# 98 DTR# 92 R1# 93 R2# 98 R0X2/GP52/IRRX 99 CTS# 98 DCD# 98 DCD# 98 DSR# 98 TXD2/GP53/IRTX 99 RTS# 98 DTR# 64 GA20/GP37 66 KBRST#/GP38 53 KBDATA# 57 KBCLK# 58 MSQAT# 59 MSCLK# 18 VTR 111 HVCC 122 HVCC 102 HVCC 40 AGND 7 GND 31 GND 60 GND 76 GND

FDD1

Pin	Signal
1	
2	DRV DEN0
3	
4	INDEX#
5	
6	MOT A#
7	
8	DRV B#
9	
10	MOT B#
11	
12	DRV A#
13	
14	MOT B#
15	
16	DIR#
17	
18	STEP#
19	
20	WT DTH
21	
22	WT EN#
23	
24	TRACK0#
25	
26	FDD WP#
27	
28	RDATA#
29	
30	HEAD#
31	
32	SKCHG#
33	
34	

CONN-FDD(4)(5)(6)V

Pin connection diagram for the 8P4R-150 connector. The diagram shows a 15-pin connector with pins numbered 1 to 15. The pins are connected to various signals: INDEX# (pin 7), TRACK0# (pin 5), FDD_WP# (pin 3), RDATA# (pin 1), DSKCHG# (pin 15), and VCC5 (pin 8). The connector is labeled RN4 and R14.

SMBus address strapping

Hi=5A
Low=50

13 THRM#

RECOVER#

1N4148

R526

X-0603-R

R5

4.7K

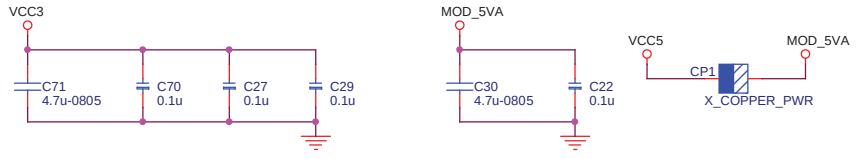
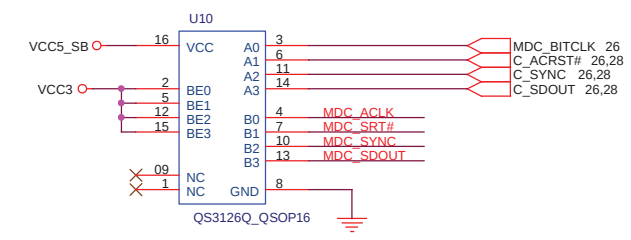
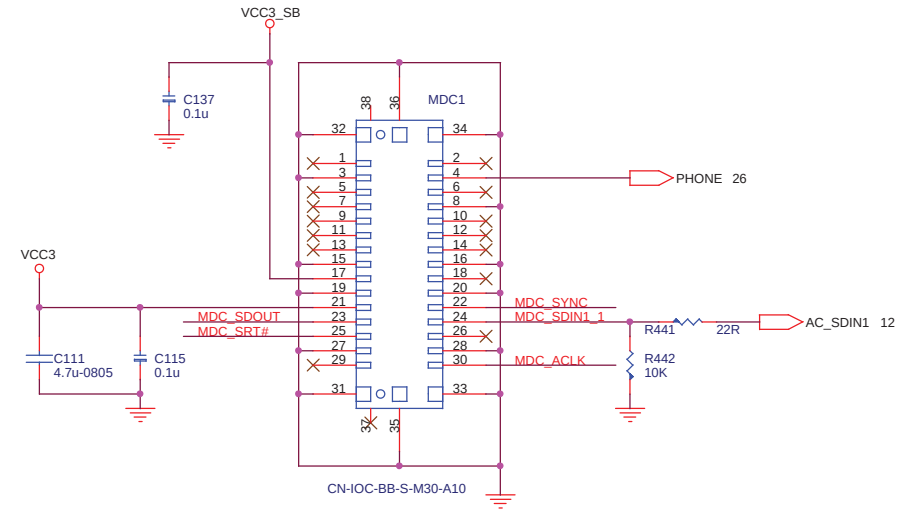
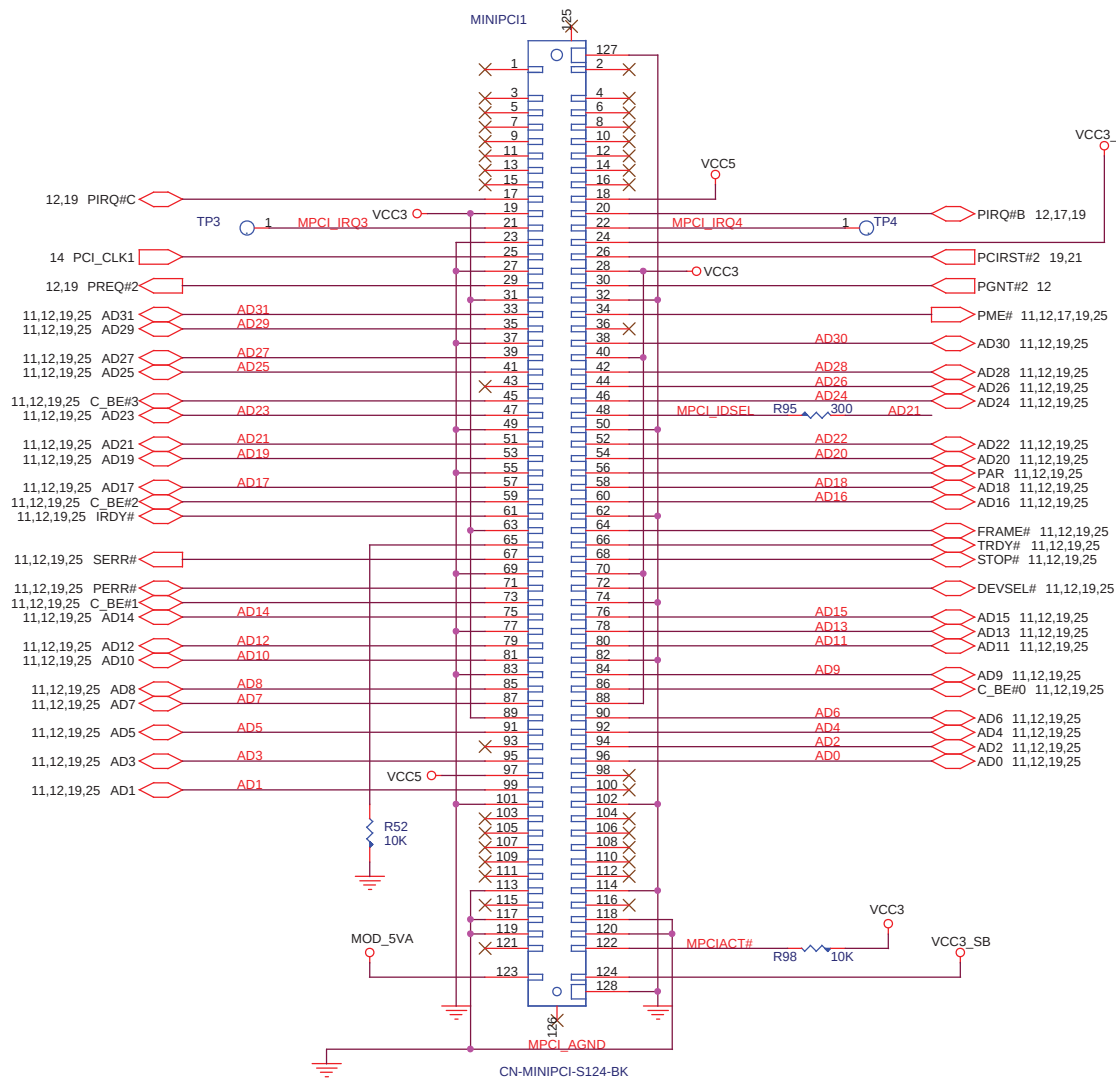
VCC3_SB

OVT#

OVT# power plane belongs to HV

SIO_ADDR

SIO_ADDR
H: 0x04E
L: 0x02E (DEFAULT)



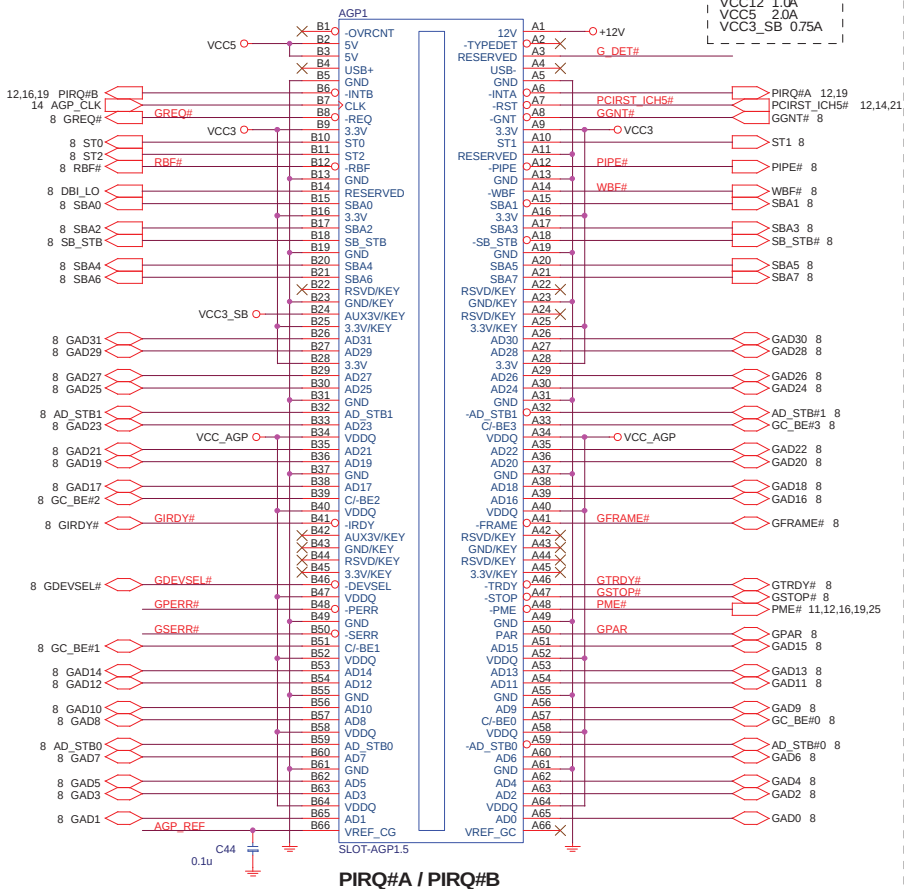
MICRO-STAR INT'L CO., LTD.			
Title MPCI & MDC			
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AGP 1.5V 4X/8X SLOT(AGP VER:3.0)

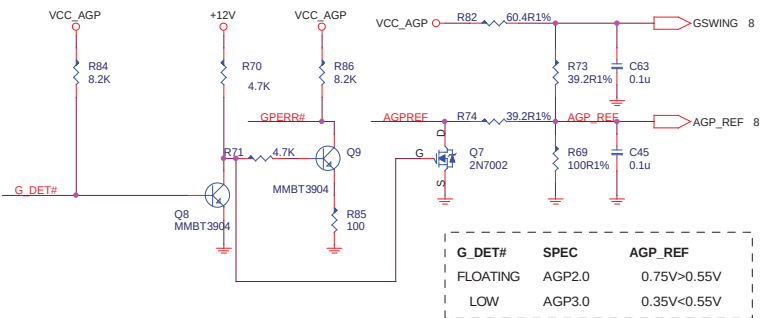
VCC5 = 60mils trace / 15 mils space

AGP Slot I_max

VCC3 2.0A
VCC3 6.0A
VCC12 1.0A
VCC5 2.0A
VCC3_SB 0.75A



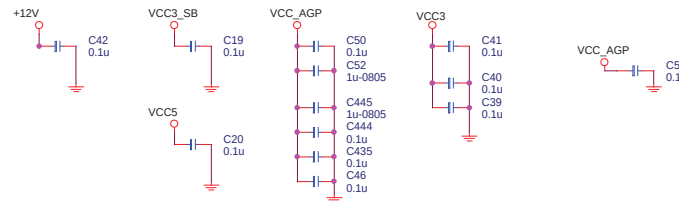
Springdale Reference & Swing Voltage Circuit



AGP TERMINATION RESISTORS

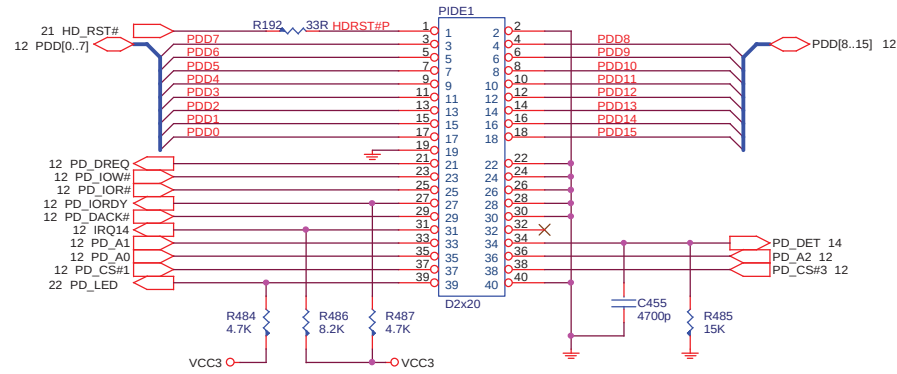


AGP SLOT DECOUPLING CAPACITORS

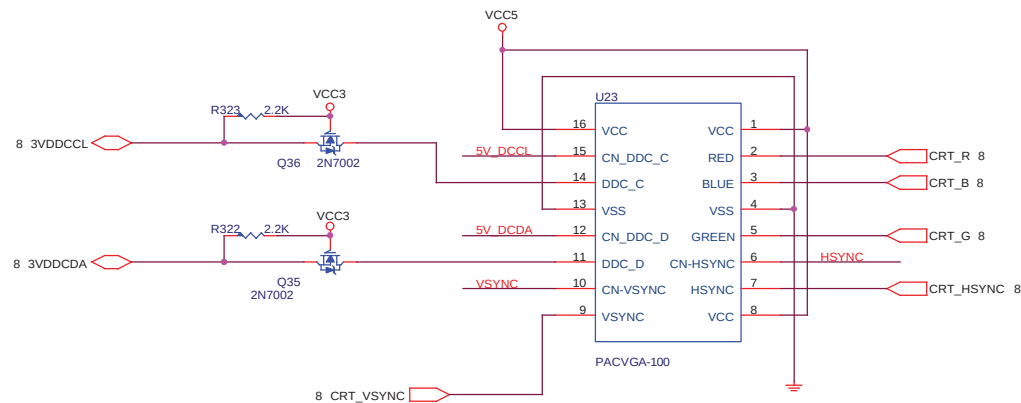
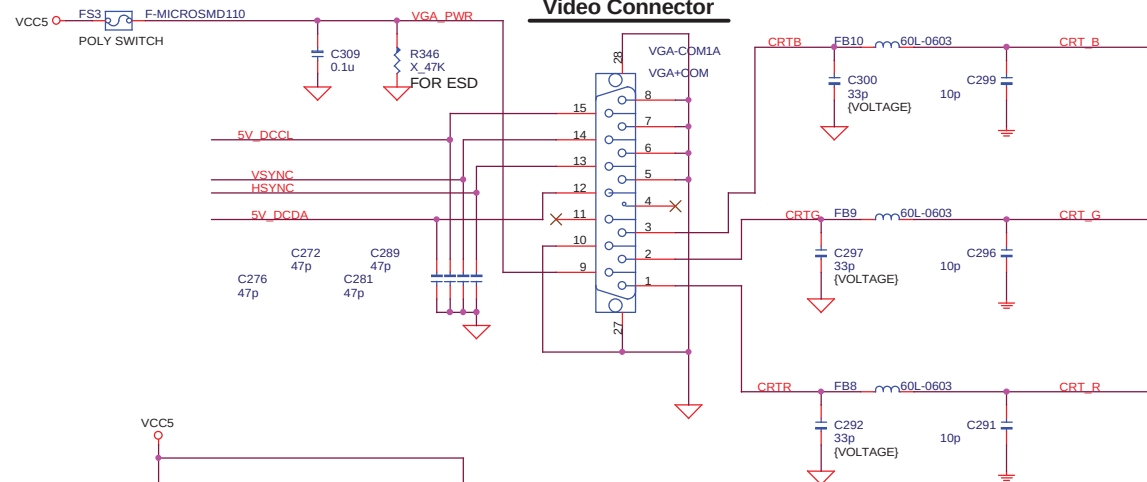


PRIMARY IDE BLOCK

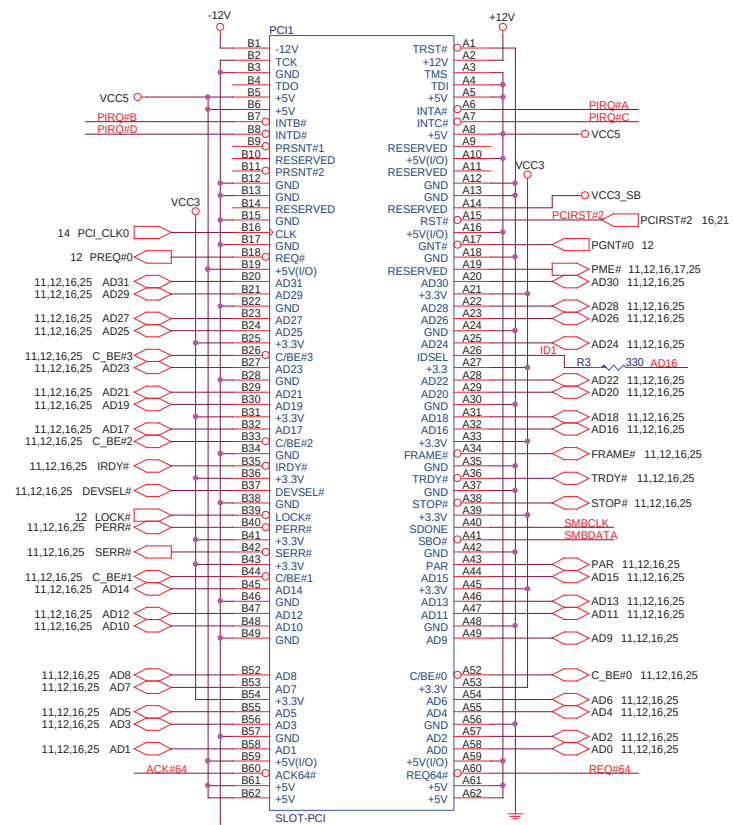
ATA 33/66/100 IDE Connectors



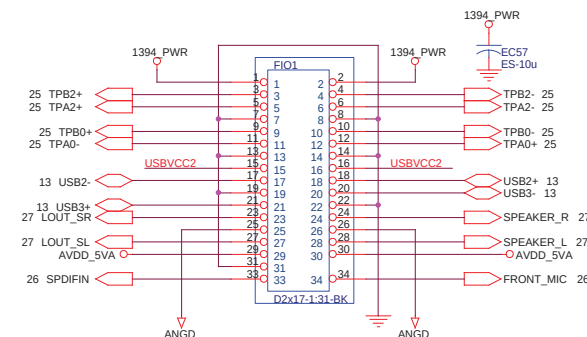
Video Connector



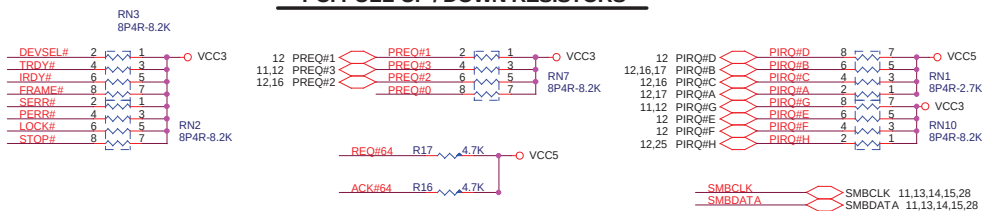
PCI SLOT 1 (PCI VER: 2.2 COMPLY)

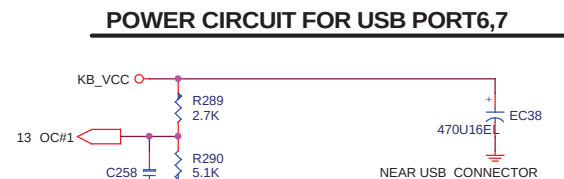
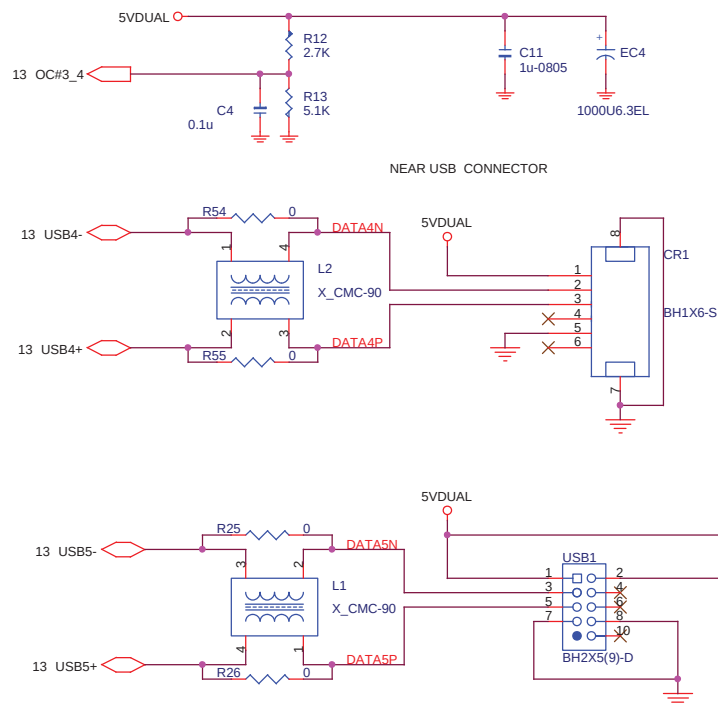


IDSEL = AD16
MASTER = PREQ#0
PIRQ#A

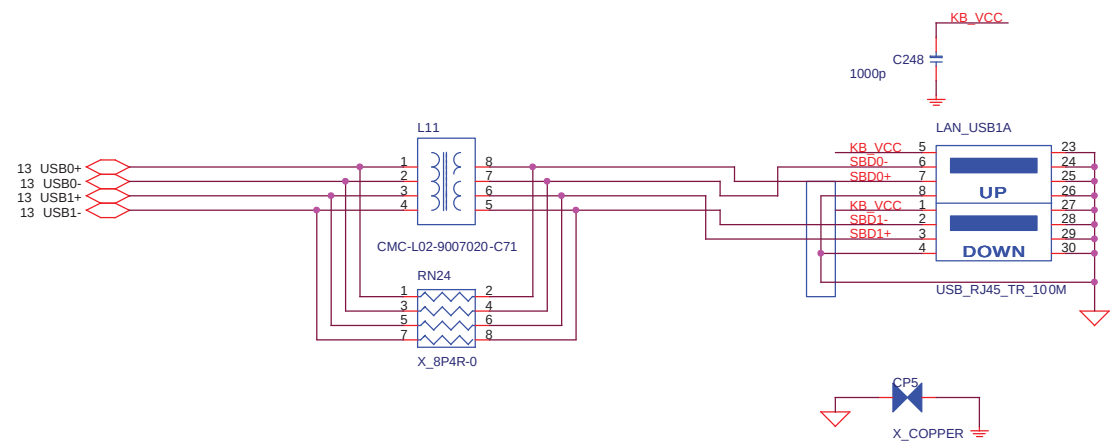


PCI PULL-UP / DOWN RESISTORS





REAR PANEL USB CONNECTOR FOR USB PORT 6,7



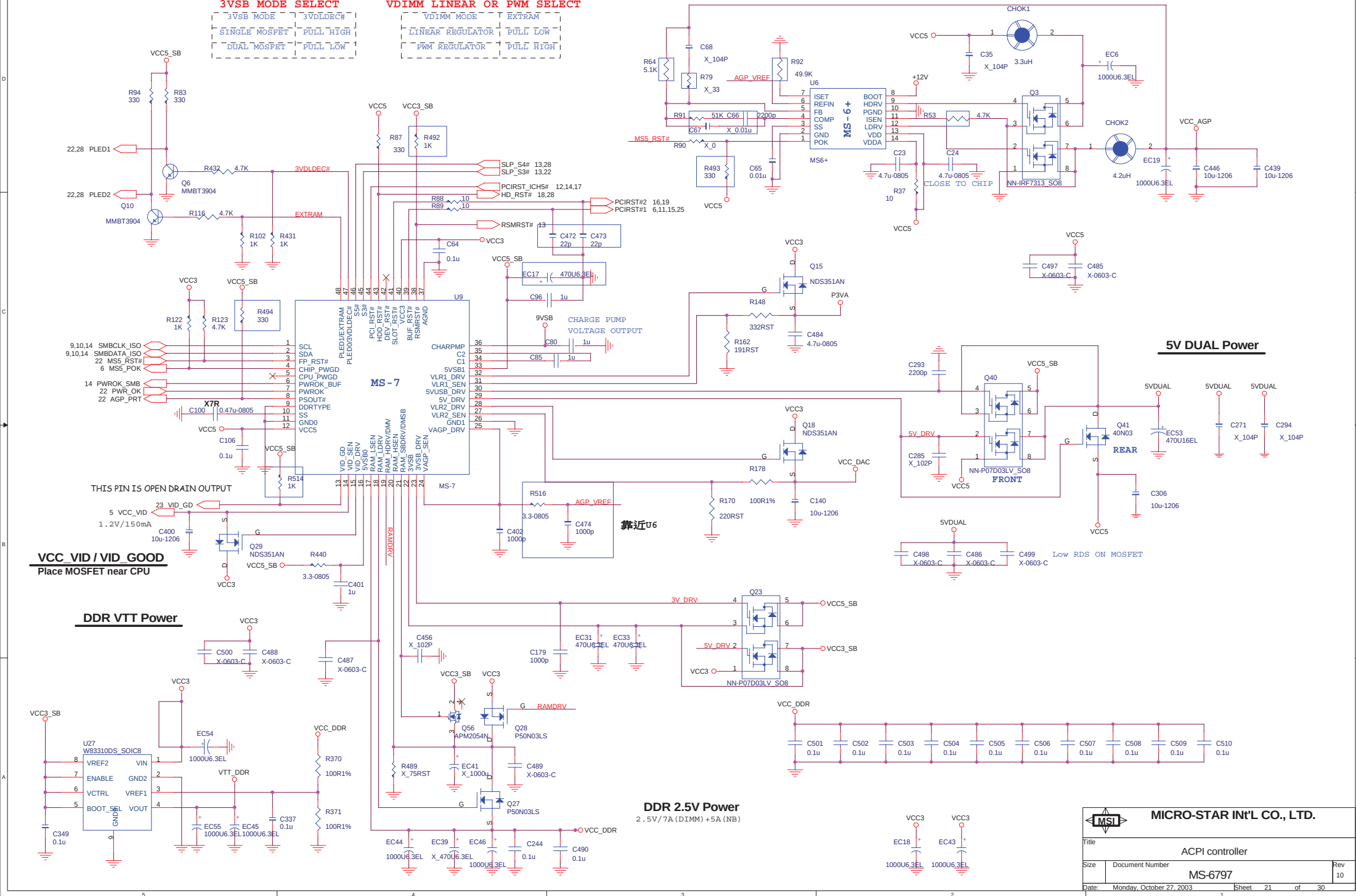
ACPI Controller

3VSB MODE SELECT

3VSB MODE	3VbLDEC#
SINGLE MOSFET	PULL HIGH
DUAL MOSFET	PULL LOW

VDIMM LINEAR OR PWM SELECT

VDIMM MODE	EXTRAM
LINEAR REGULATOR	PULL LOW
PWM REGULATOR	PULL HIGH



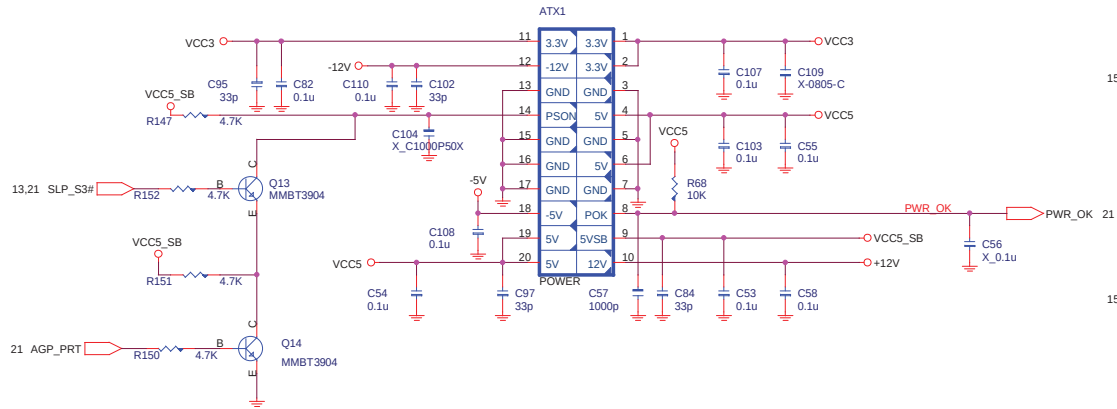
VCC_VID / VID_GOOD
Place MOSFET near CPU

DDR VTT Power

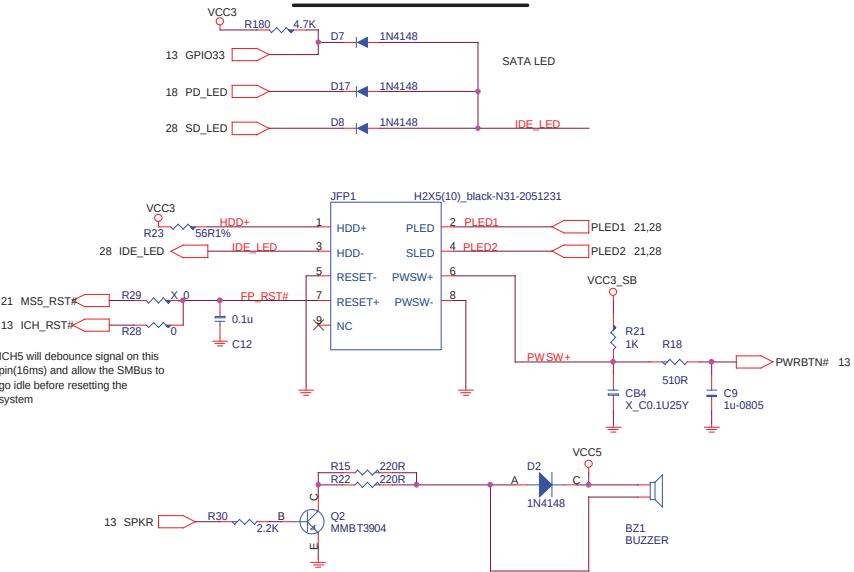
DDR 2.5V Power
2.5V/7A (DIMM) +5A (NB)

MICRO-STAR INT'L CO., LTD.		
Title: ACPI controller		
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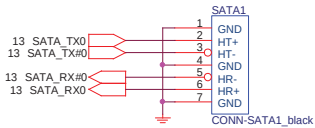
ATX Connector



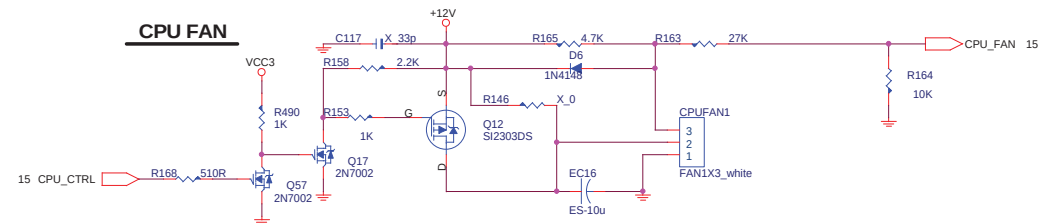
Front Panel Connector



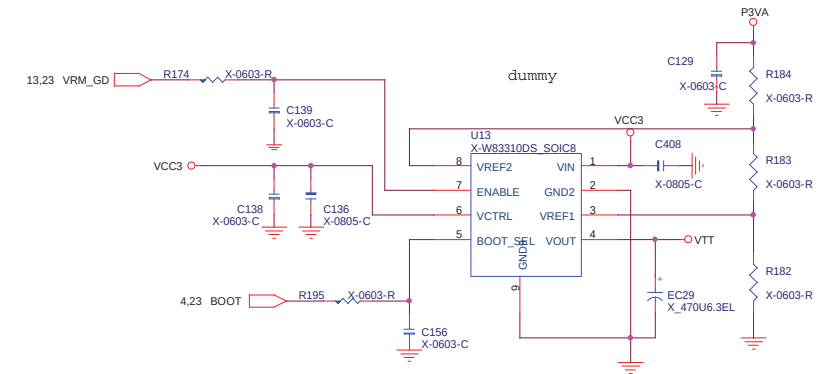
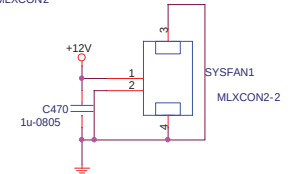
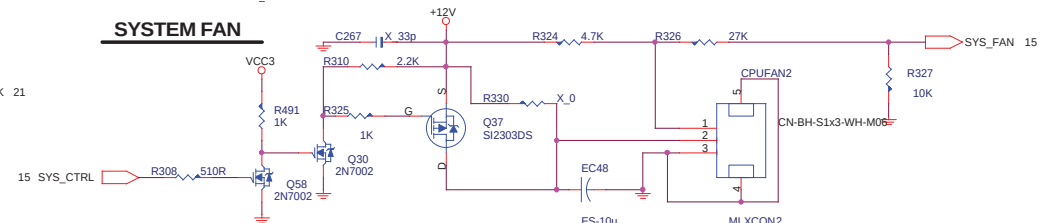
SERIAL ATA CONNECTOR BLOCK



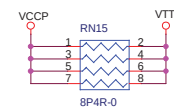
CPU FAN



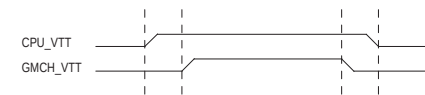
SYSTEM FAN



Intel reference GMCH VTT power circuit



BOOT	BOOT#	CPU	Voltage
LOW	LOW	Northwood	1.45V
HIGH	HIGH	Prescott	1.225V



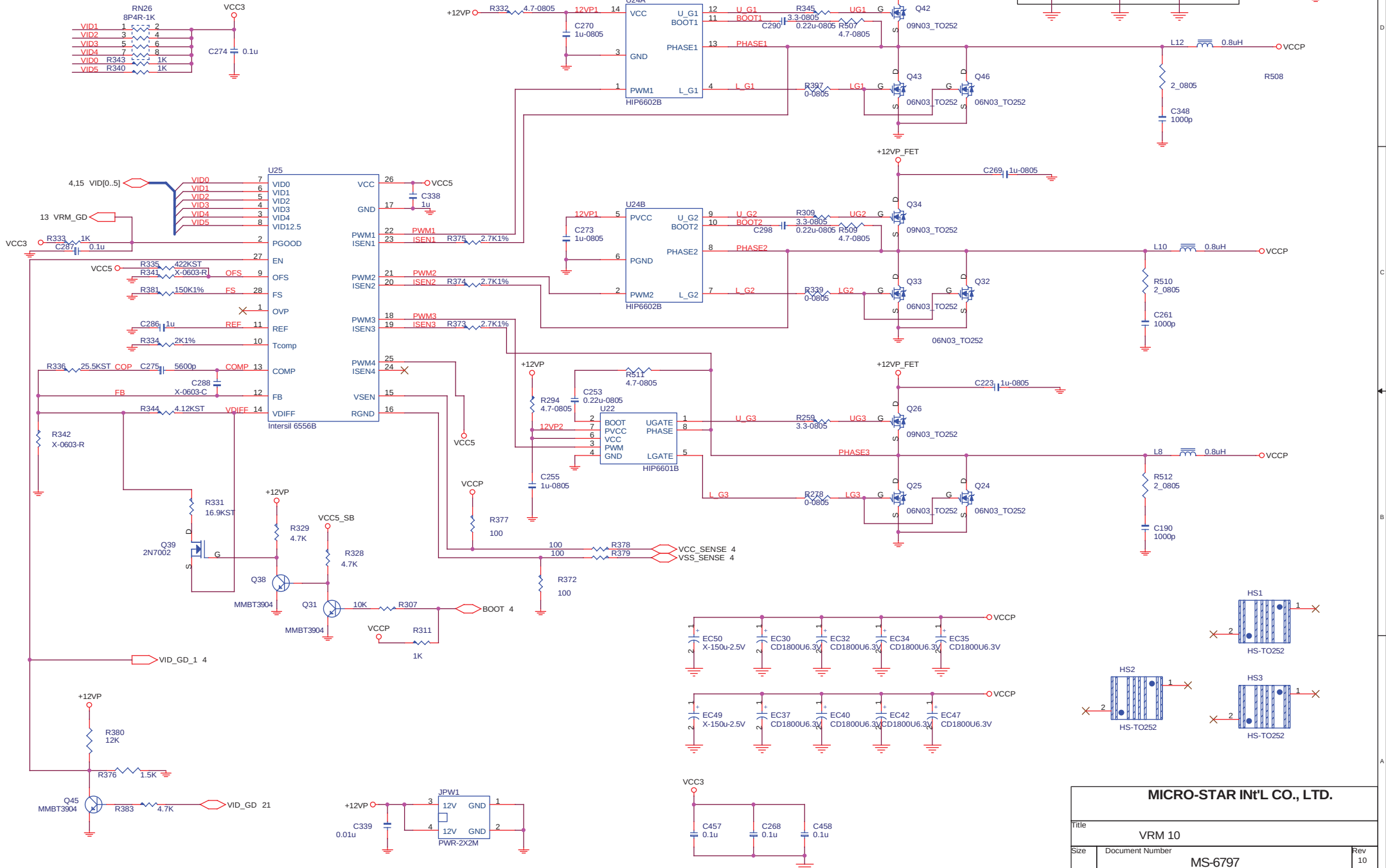
Icc=2A
NORTHWOOD: 1.45V
PRESCOTT: 1.2V

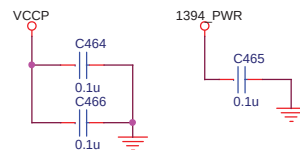
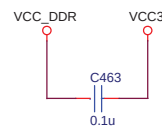
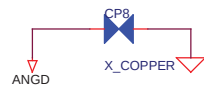
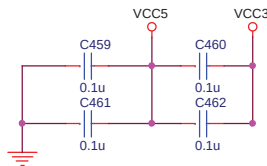
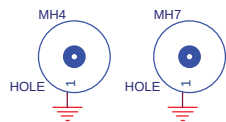
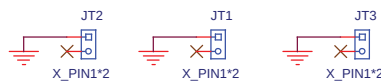
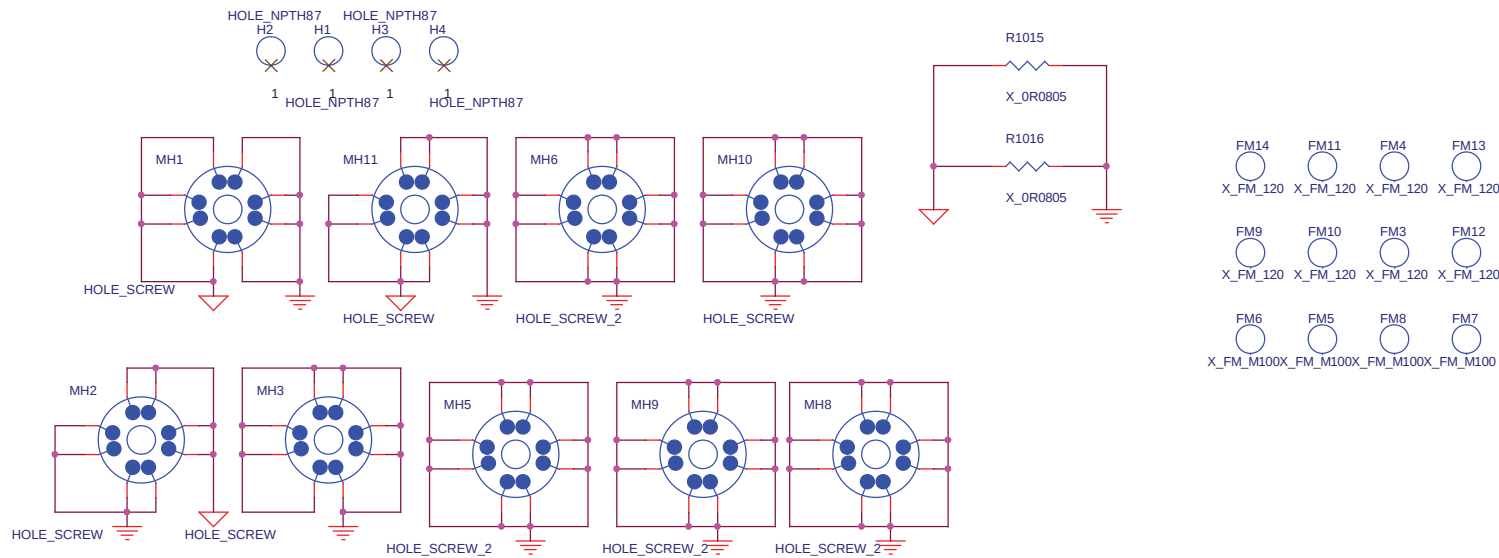


MICRO-STAR INT'L CO., LTD.

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VID Pull-Up Resistor





NBFAN
NBFAN

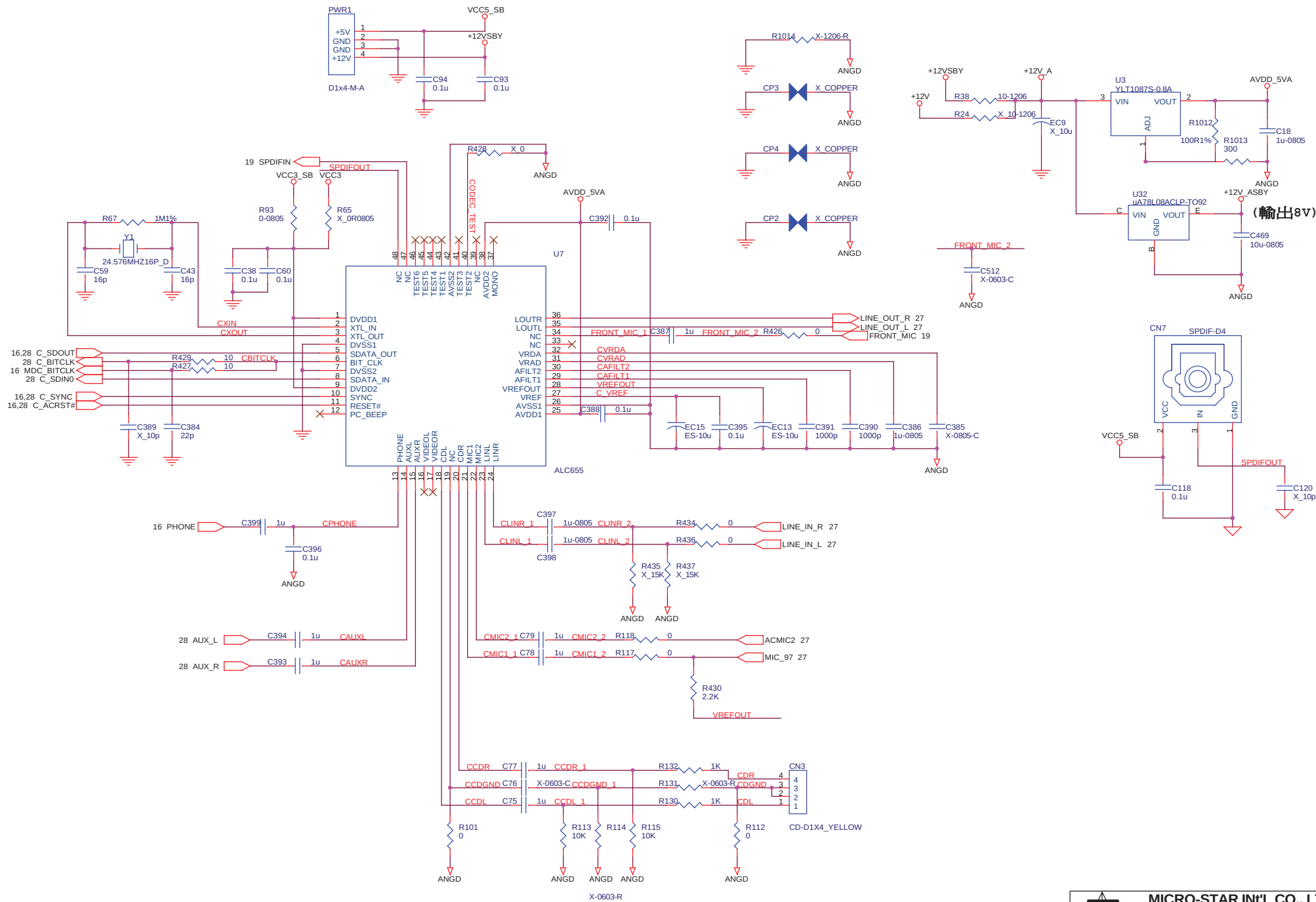
PCB
PCB

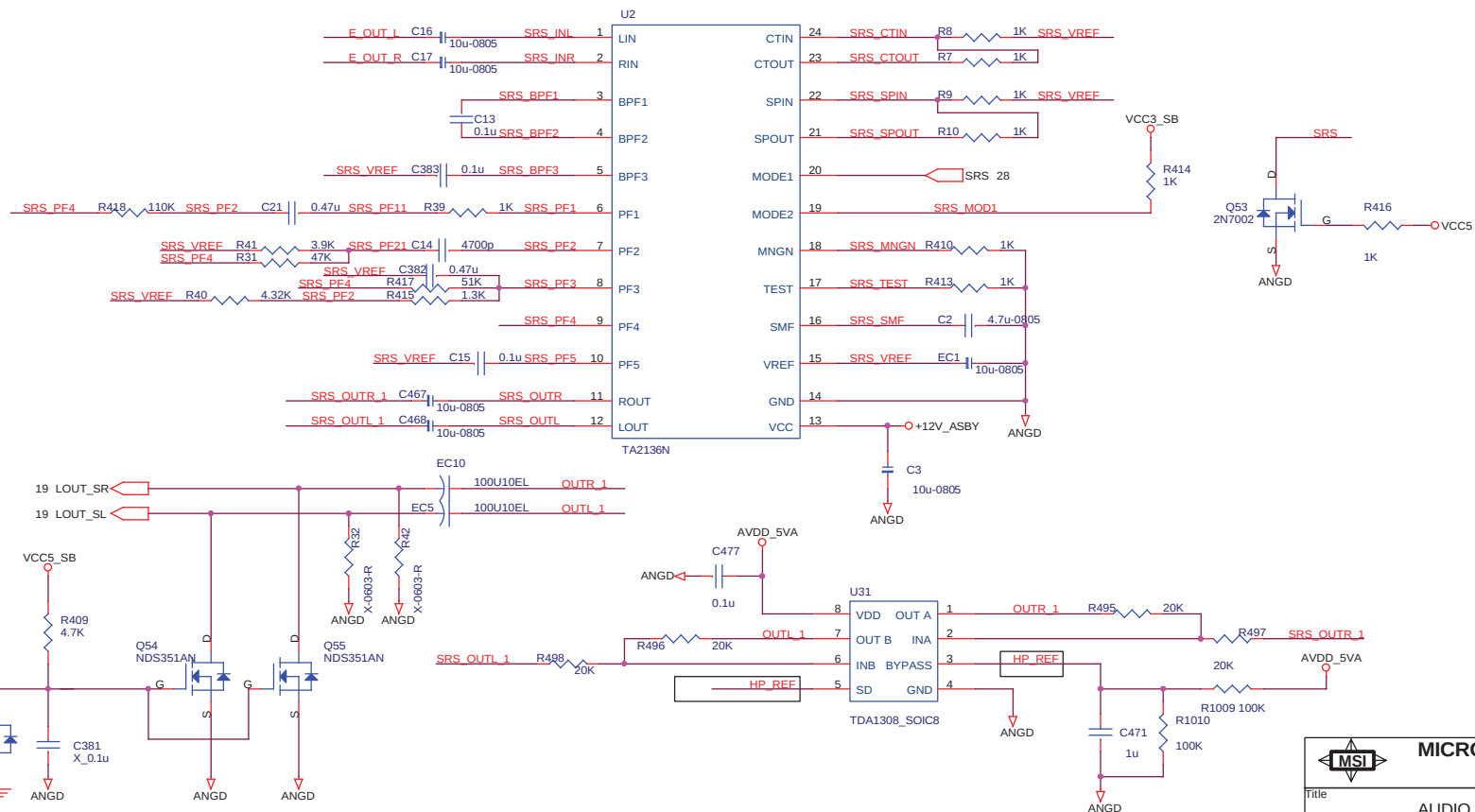
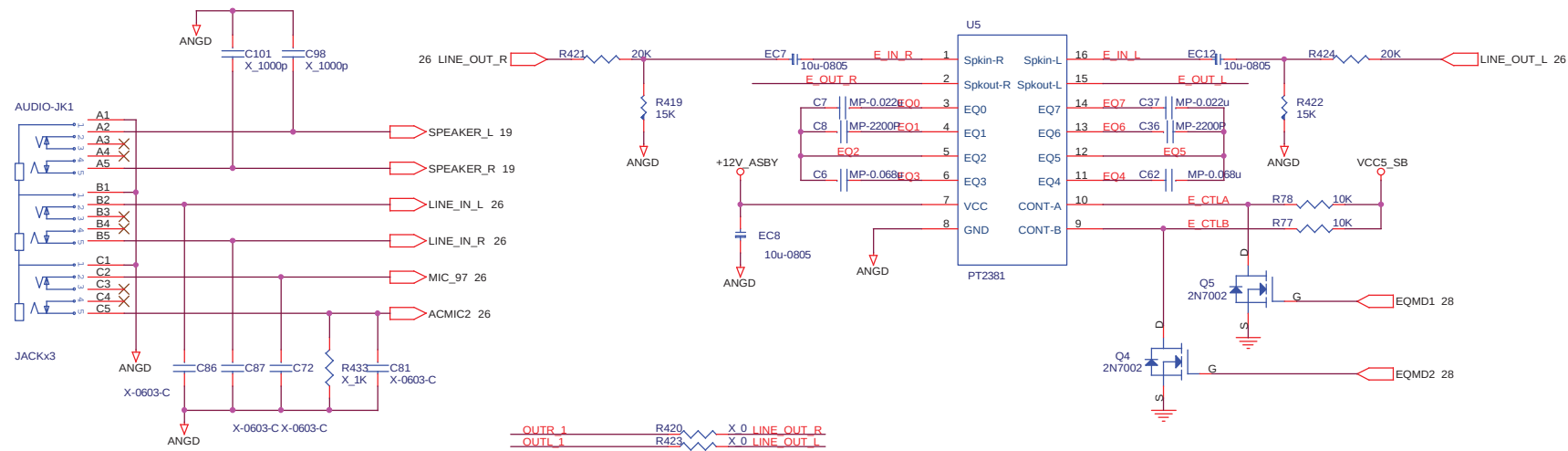
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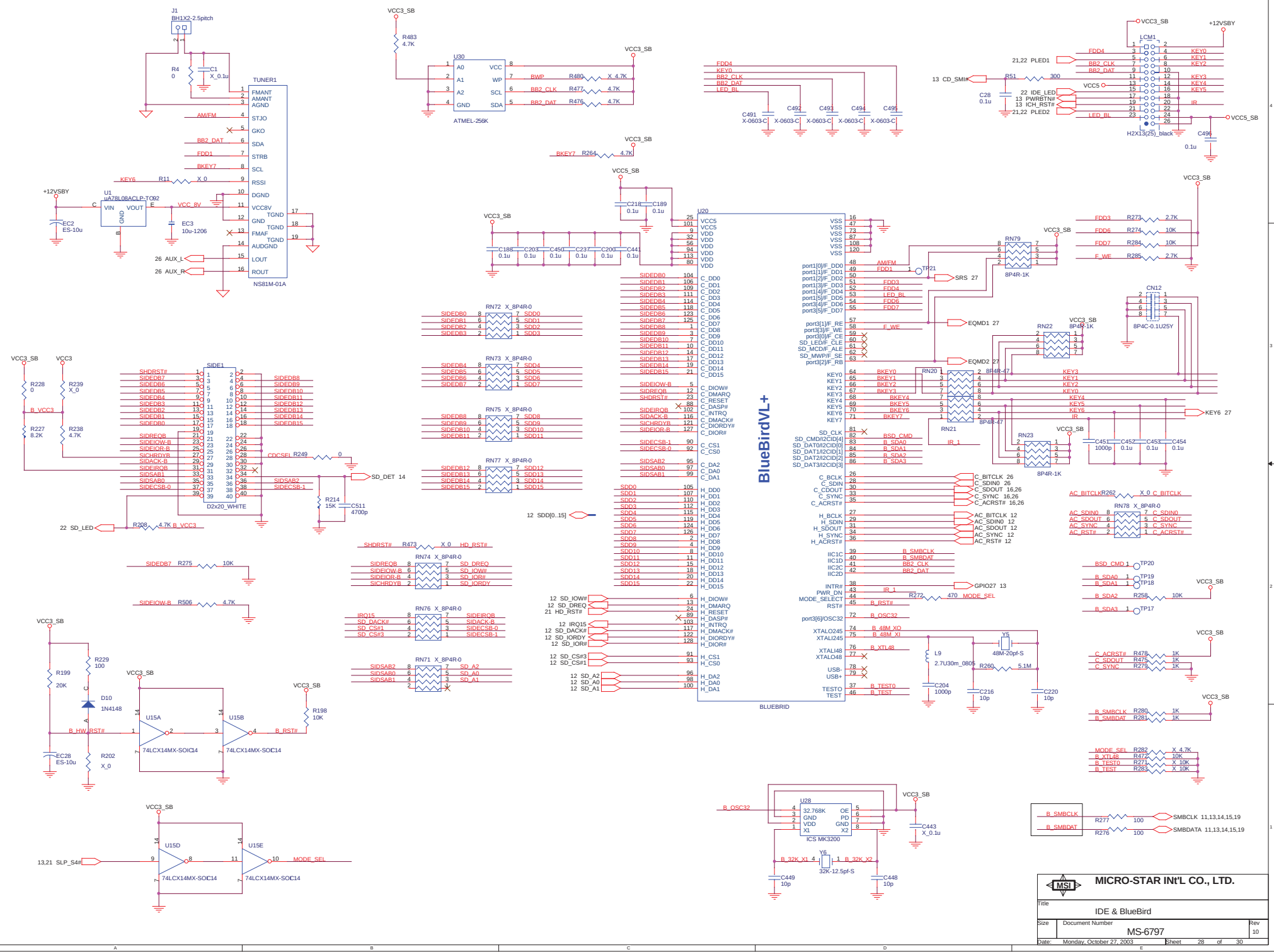
JMCH1
HEATSINK_HOOK

JMCH2
HEATSINK_HOOK

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