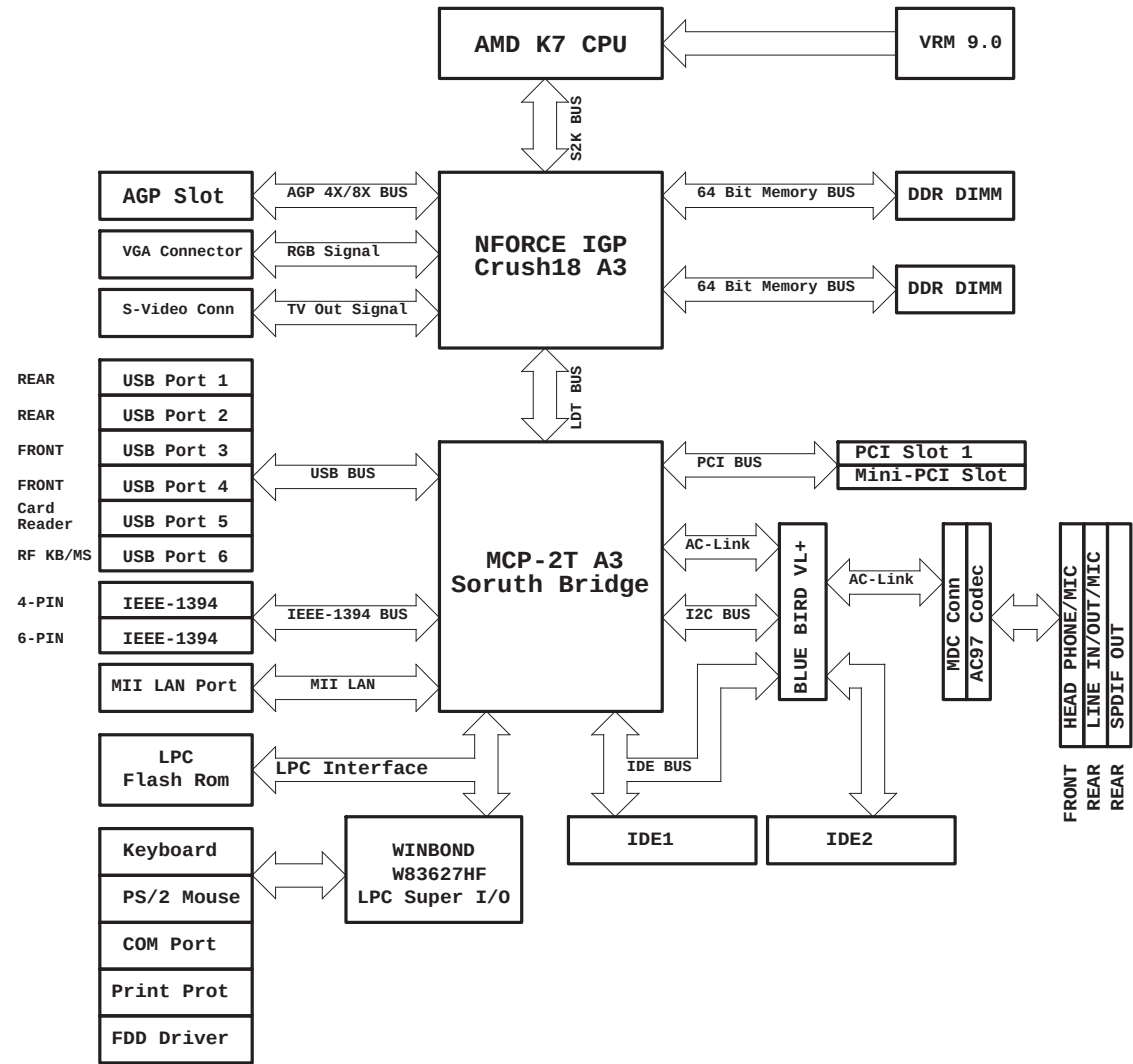
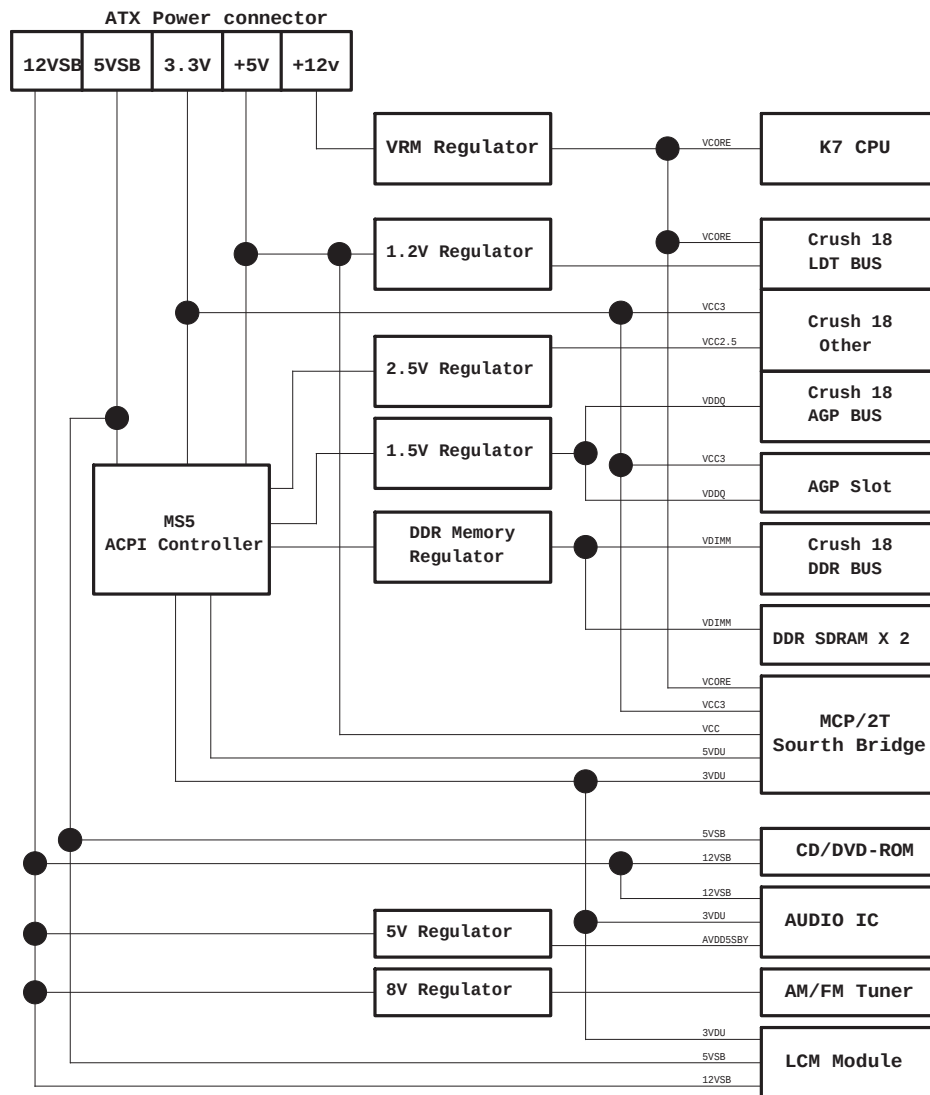


Gerber Out Version

MS-6796 Version: 10

- 001-Block Diagram 1
- 002-Power and Specification
- 003-K7 CPU Part 1
- 004-K7 CPU Part 2
- 005-VRM 9.0
- 006-Crush18G Part 1
- 007-Crush18G Part 2
- 008-Crush18G Part 3
- 009-Dual VGA & S-Video Connector
- 010-AGP Slot
- 011-DDR DIMM1 & 2
- 012-DDR Terminator Resistor
- 013-MCP/2T Part 1
- 014-MCP/2T Part 2
- 015-MCP/2T Part 3
- 016-N/A
- 017-MDC Conn & BIOS
- 018-MII LAN & USB Connector
- 019-IEEE 1394 PHY
- 020-N/A
- 021-PCI Slot1 & Mini-PCI
- 022-N/A
- 023-N/A
- 024-N/A
- 025-IDE Connector
- 026-SMSC LPC47M292
- 027-LPT & FDD & KBM
- 028-MS5 ACPI Controller
- 029-Front Panel & ATX Power
- 030-Block Diagram 2
- 031-BlueBird & LCM Conn
- 032-AC'97 Codec
- 033-Audio IC & Connector
- 034-Tuner & Front I/O
- 035-Decoupling Capacitor
- 036-MS-6796 History





MS-6796 Specification

CPU: AMD K7 Serial Socket 462

North Bridge: Nvidia Crush 18G A3

South Bridge: Nvidia MCP/2T A3

On Board Device: IEEE1394 PHY

On Board Device: ICS MII LAN

On Board DDR SDRAM: X2

On Board AGP Slot: X1

On Board PCI Slot: X2

On Board MDC Conn: X1

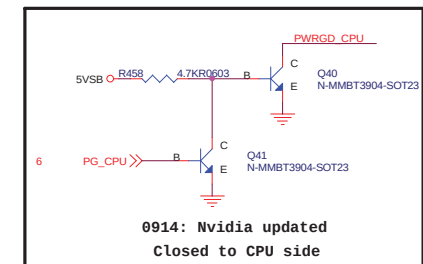
On Board Device: LCM/Blue Bird Module

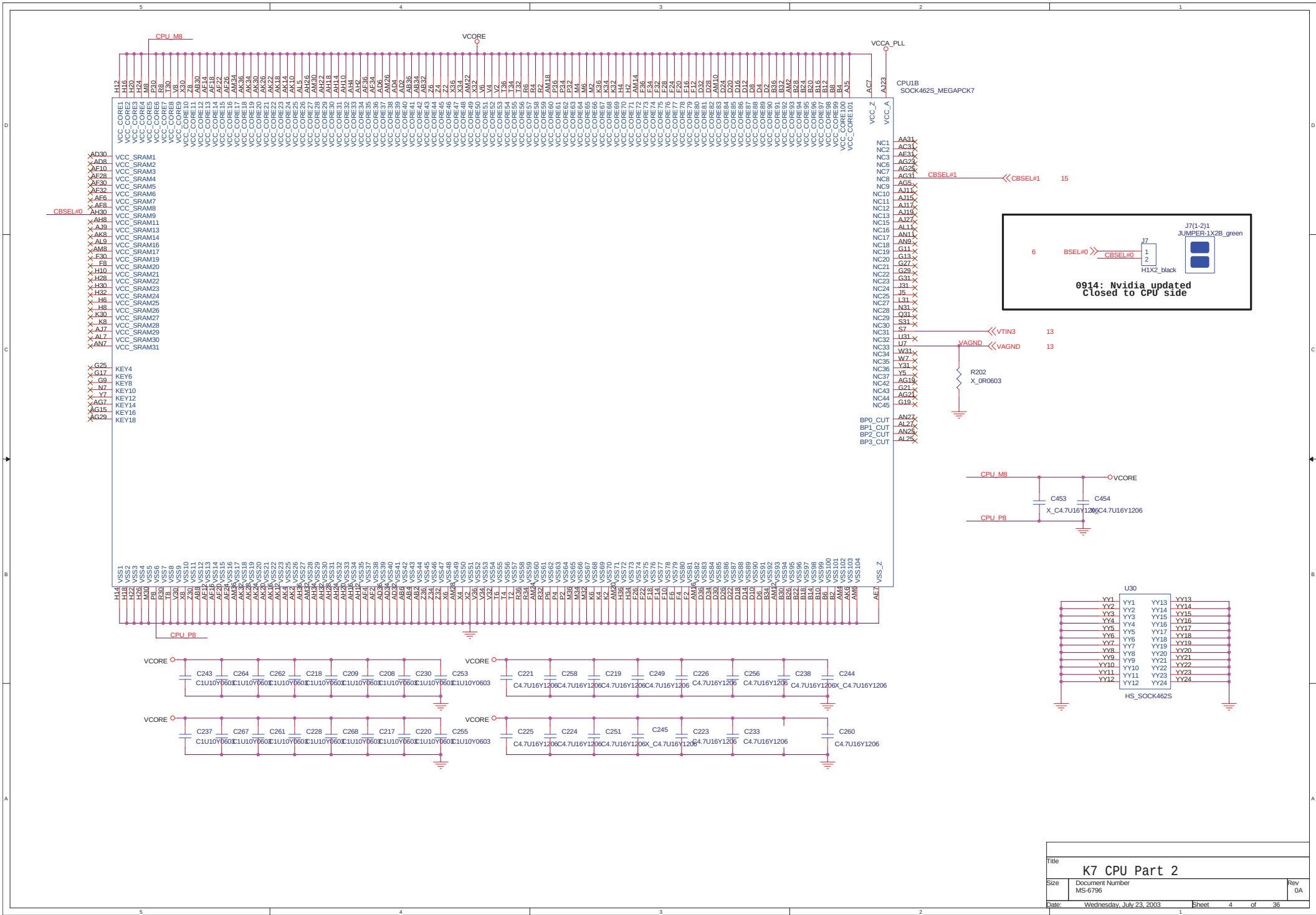
On Board Device: ALC650

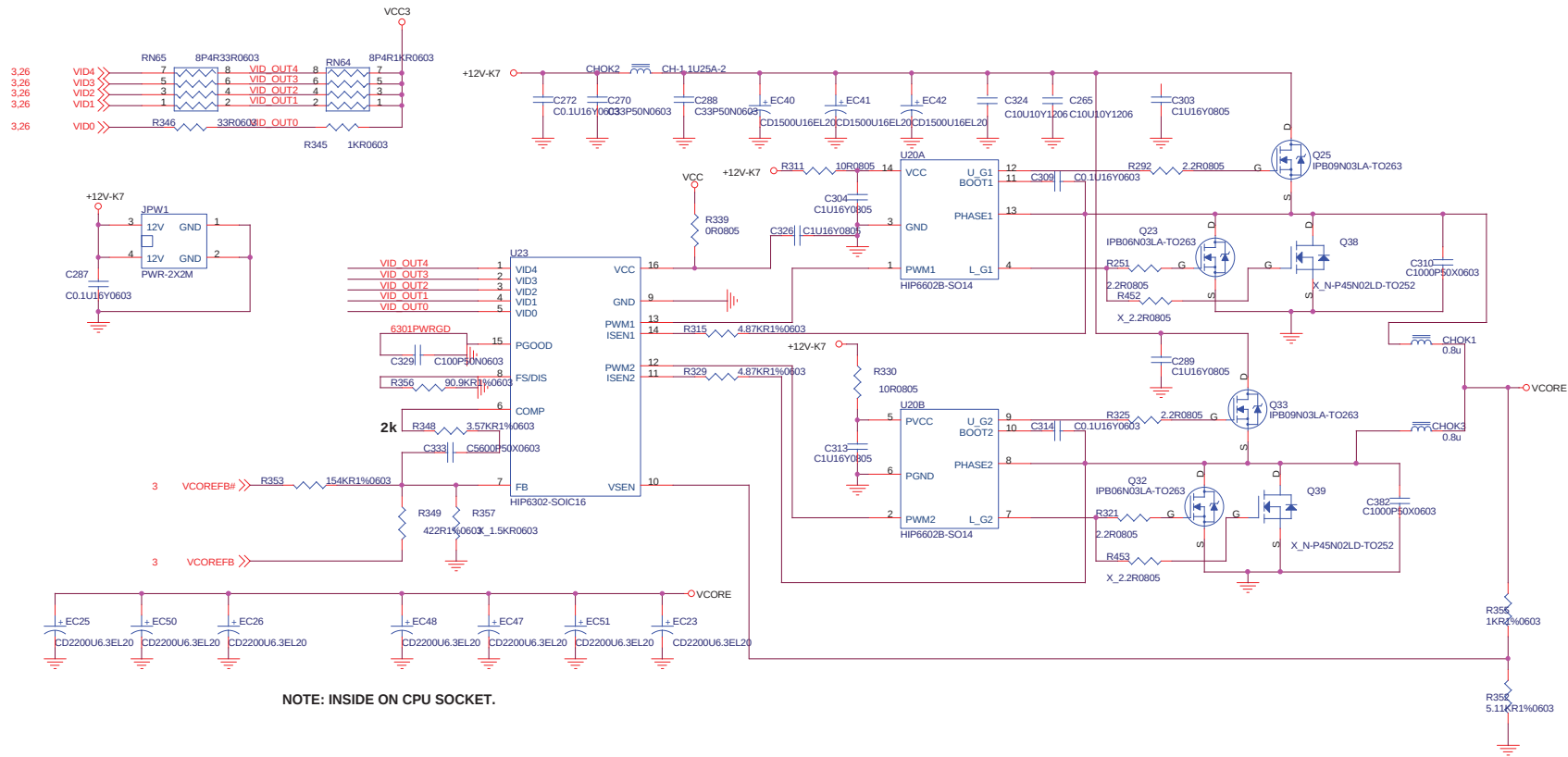
On Board Device: SRS/EQ IC

GPIO and PCI BUS Address

Title			
Power and specification			
Size	Document Number	Rev	
	MS-6796	0A	
Date	Tuesday, July 22, 2003	Sheet	2 of 36



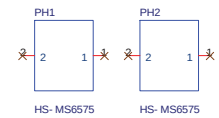




NOTE: INSIDE ON CPU SOCKET.

VID4	VID3	VID2	VID1	VID0	VDC(V)
1	0	1	1	0	1.30
1	0	0	0	1	1.425
1	0	0	0	0	1.45
0	1	1	1	1	1.475
0	1	1	1	0	1.500
0	1	1	0	1	1.525
0	1	1	0	0	1.550
0	1	0	1	1	1.575
0	1	0	1	0	1.600
0	1	0	0	1	1.625

VID4	VID3	VID2	VID1	VID0	VDC(V)
0	1	0	0	0	1.650
0	0	1	1	1	1.675
0	0	1	1	0	1.70
0	0	1	0	1	1.725
0	0	1	0	0	1.75
0	0	0	1	1	1.775
0	0	0	1	0	1.80
0	0	0	0	1	1.825
0	0	0	0	0	1.85
1	1	1	1	1	OFF



FDB6670 Specification:

Qg= 35nC; Trise= 15ns; Tfall= 42ns

Hi-Side MOSFET:

Switching loss=

$$1/2 * I_o * V_{in} * (T_{rise} + T_{fall}) * F_s$$

$$1/2 * 22.5 * (15ns + 42ns) * 200K$$

$$P_{switching loss} = 0.64W$$

$$\text{Conduction loss} = I_o^2 * R_{ds(on)} * D$$

$$= (22.5 * 22.5) * 0.0065 * 0.33 = 1.1W$$

$$P_{total} = P_{switching loss} + P_{conduction loss}$$

$$= 0.64W + 1.1W = 1.74W$$

Low-Side MOSFET:

$$P_{total} = (22.5 * 22.5) * 0.0065 * 0.67$$

$$= 2.2W$$

AMD CPU specification:

2GHz CPU: Vcore= 1.65V, P= 74.1W

Vcore_DC= -50mv ~ 50mv

Vcore_AC= -100mv ~ 150mv

I_ripple= I_o * sqrt(D/(1-D))

D= 1.65/5= 33%; I_ripple= 10.58A

The ripple current for each input

Based on temperature coefficient of Rubycon:

漣波電流 溫度係數 溫度

2.55A 1 105

4.3A 1.7 85

5.4A 2.1 under 65

Total 4 pcs input capacitance= 17.2A @85

ESR= V_o(delta)/I_o(delta)

ESR= 100mv/45= 2.22m_ohm

ESR for each capacitance is 13m_ohm

Total 10 pcs capacitance are 1.3 m_ohm

1.3 m_ohm < 2.2 m_ohm, so it OK!

Rubycon 2200uF/6.3V capacitor

Rated voltage: 6.3V

Surge voltage: 8V

Leakage current: 415uA/2min

DF= 0.24@120Hz

Ripple current= 2.55A(max)

Vender guaranteed: 3000 Hr/105 degree.C

Actual operating temperature: 55 degree.C @45A

Ambient temperature: 25 degree.C

實際壽命= L1*2^[(T1-Ta)/10]*2^[(Ta-T2)/K]

K= 10, if ripple current within specification

K= 5, if ripple current over specification

實際壽命= L1*2^[(T1-T2)/10]

實際壽命= 3000*2^[(105-55)/10]

實際壽命= 96000 Hr

Ripple current of Vender's design:

Iac=

$$\frac{\Delta(T) * B * A * W C}{\tan G}$$

Delta(T): 溫升; A= 0.785*[(D(D+4L))/10]

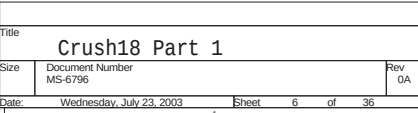
B: 散熱係數; WC= 6.28*f*C

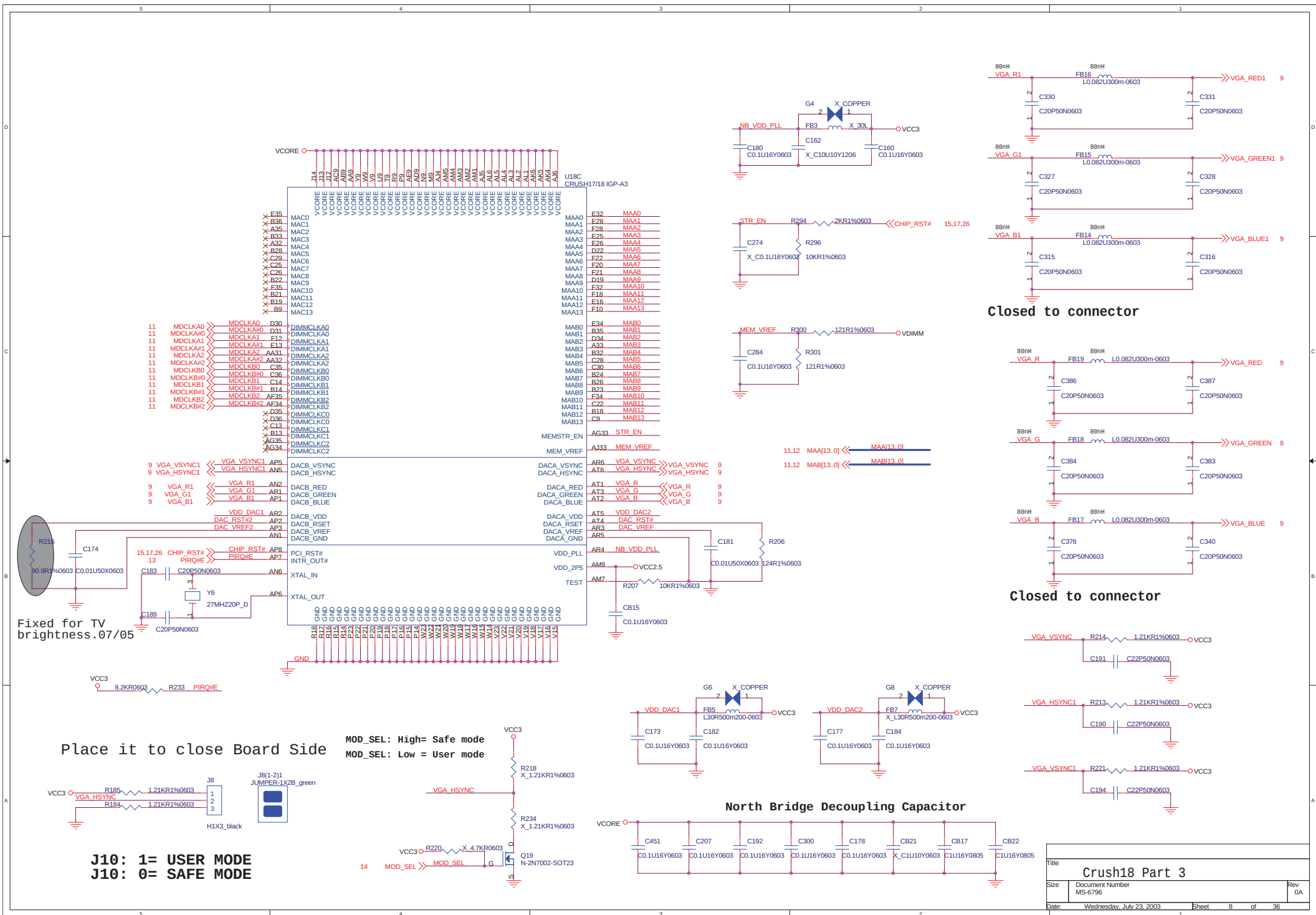
D: 電容外徑; L: 電容長度

tanG= R/Xc;

DF(電容損失角)= tanG*100%

Title		
VRM 9.0		
Size	Document Number	Rev
MS-6796		0A
Date	Wednesday, July 23, 2003	Sheet 5 of 36





Fixed for TV
brightness.07/05

Place it to close Board Side

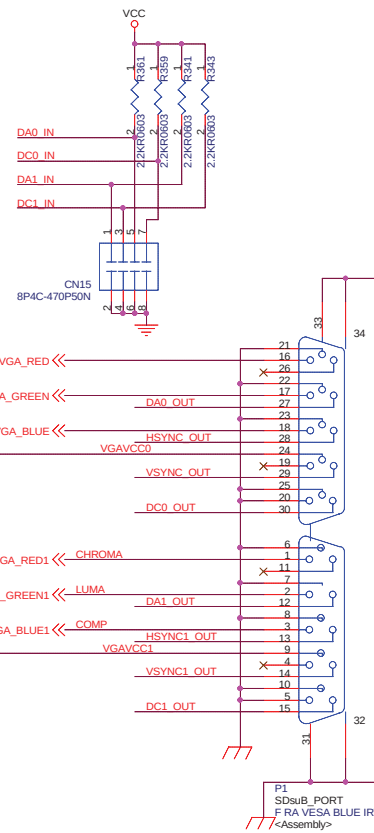
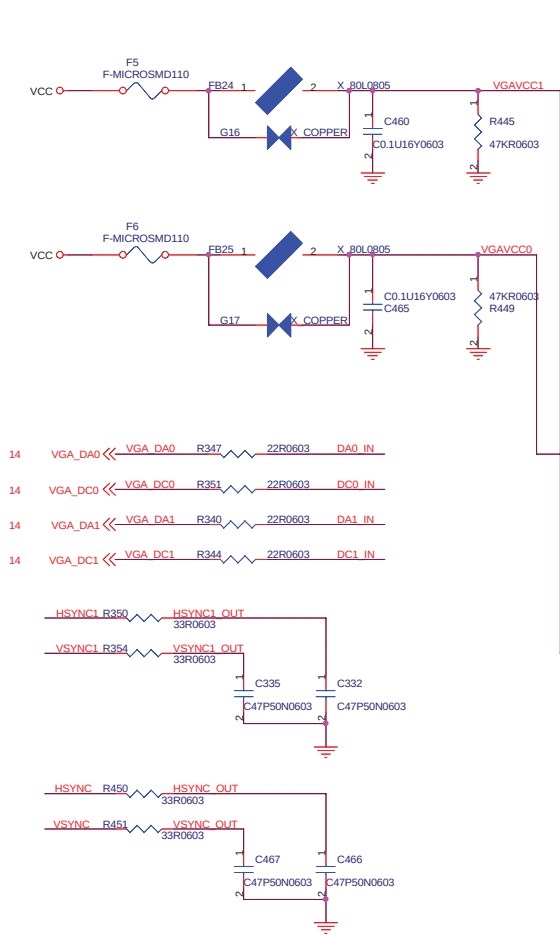
MOD_SEL: High= Safe mode
MOD_SEL: Low = User mode

J10: 1= USER MODE
J10: 0= SAFE MODE

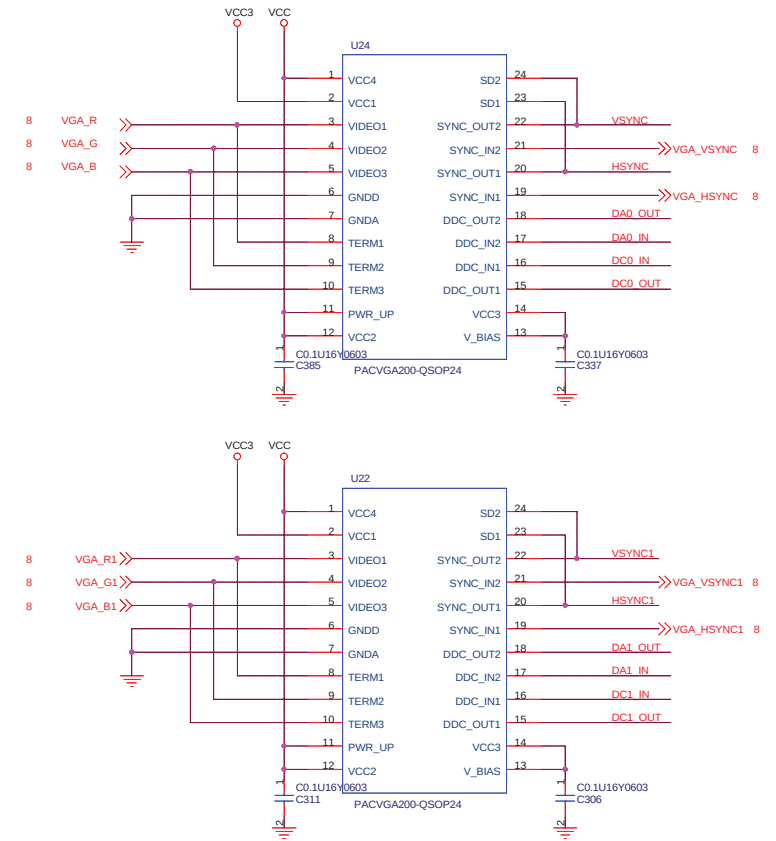
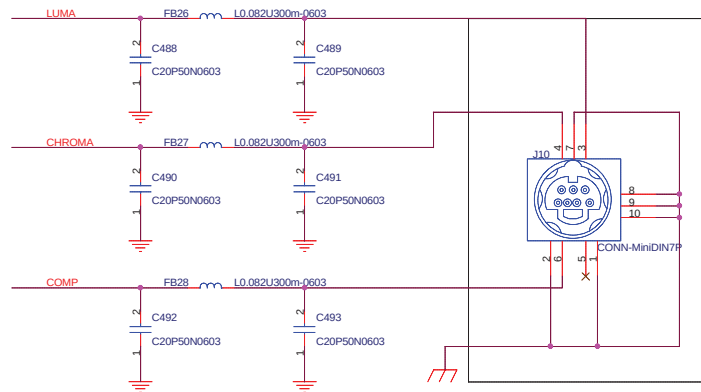
North Bridge Decoupling Capacitor

Closed to connector

Closed to connector

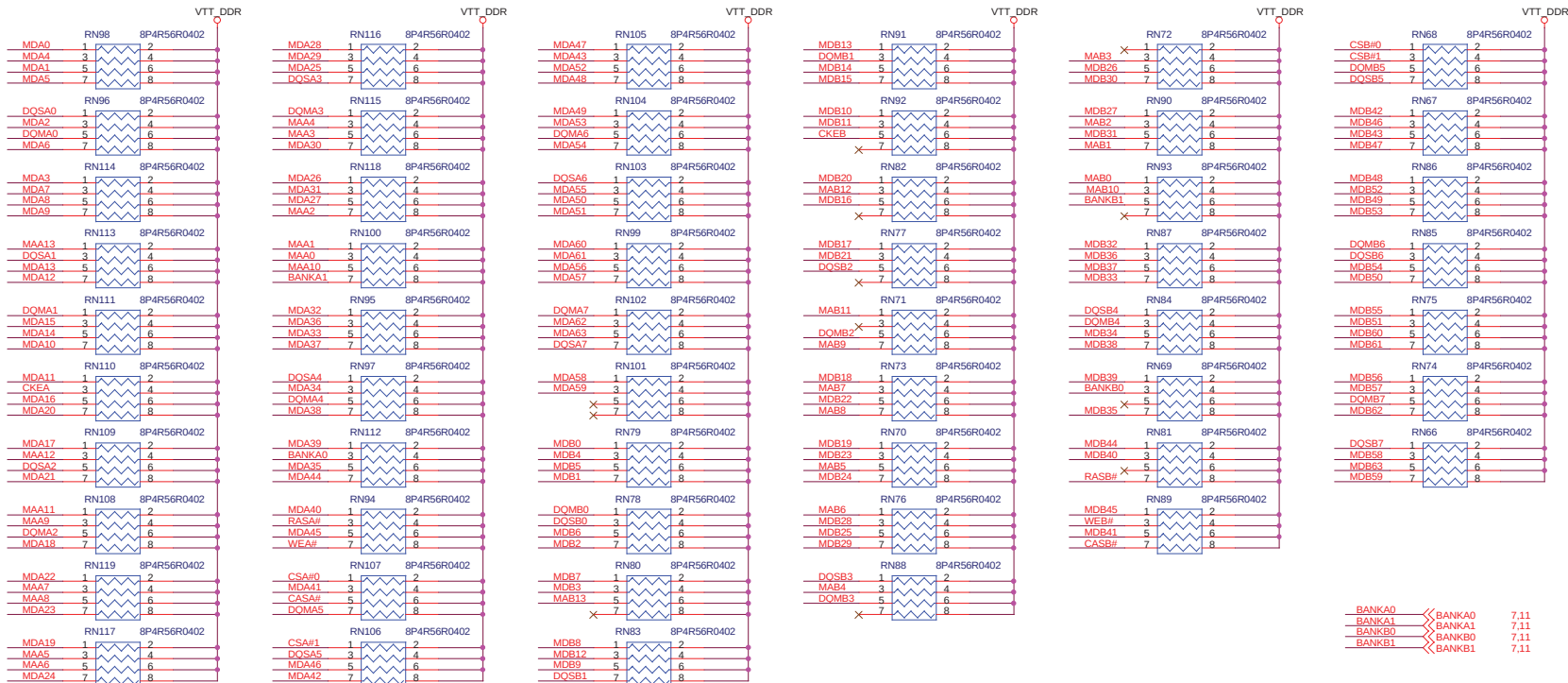


TV OUT Circuit



Title		<Title>	
Size	Document Number	Rev	
Date	Wednesday, July 23, 2003	Sheet	9 of 36

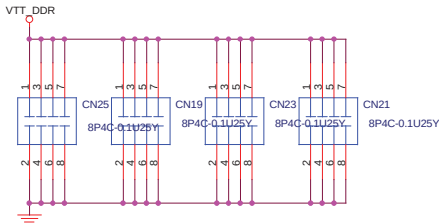
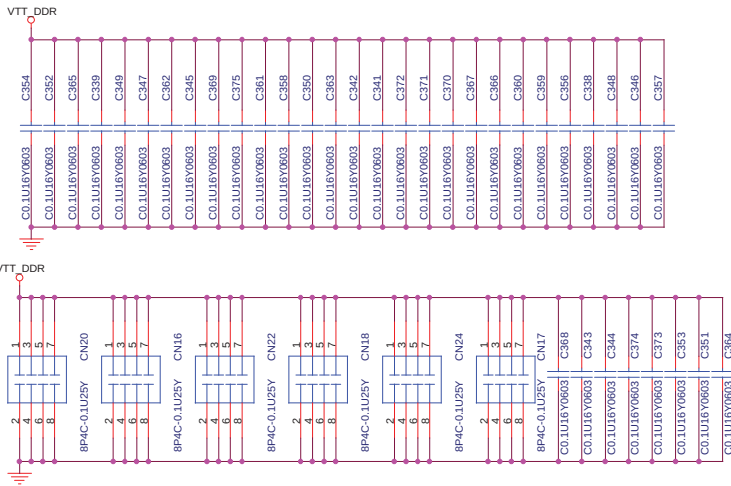
Check Size & Number



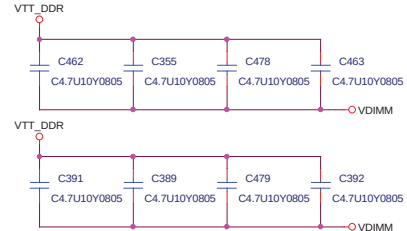
BANKA0 << BANKA0 7.11
BANKA1 << BANKA1 7.11
BANKB0 << BANKB0 7.11
BANKB1 << BANKB1 7.11

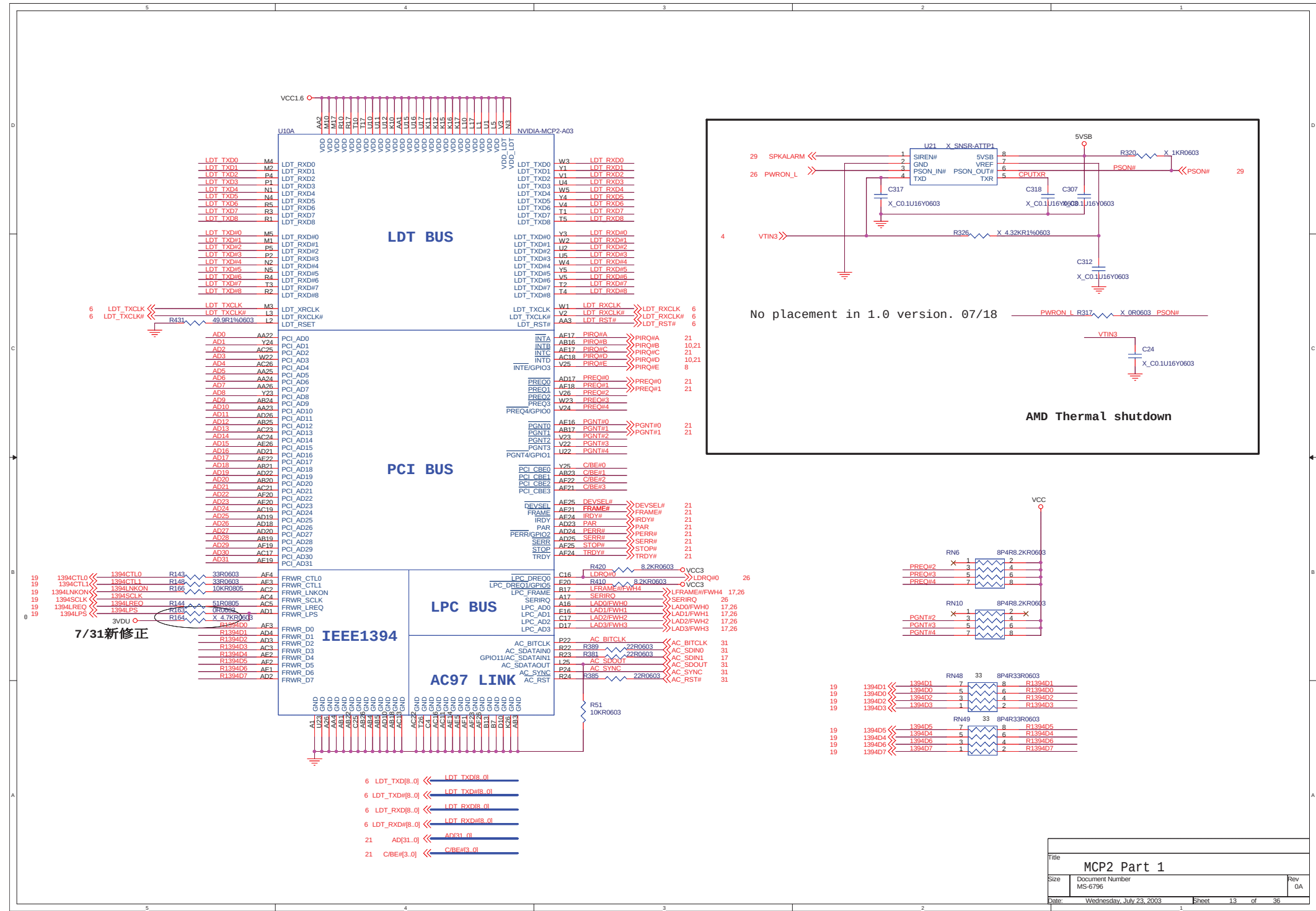
CSA#0 << CSA#0 7.11
CSA#1 << CSA#1 7.11
CSB#0 << CSB#0 7.11
CSB#1 << CSB#1 7.11

7.11 RASA# << RASA#
7.11 CSA# << CSA#
7.11 WEA# << WEA#
7.11 CKEA << CKEA
7.11 RASB# << RASB#
7.11 CASB# << CASB#
7.11 WEB# << WEB#
7.11 CKEB << CKEB

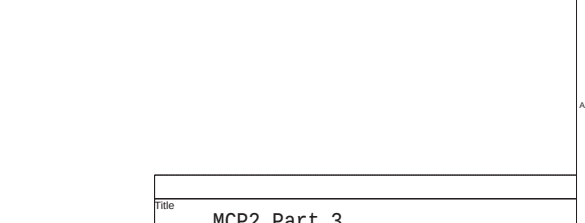
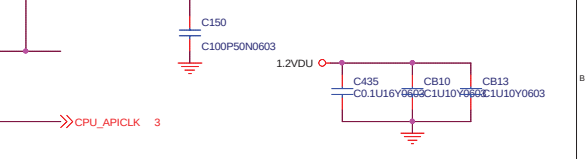
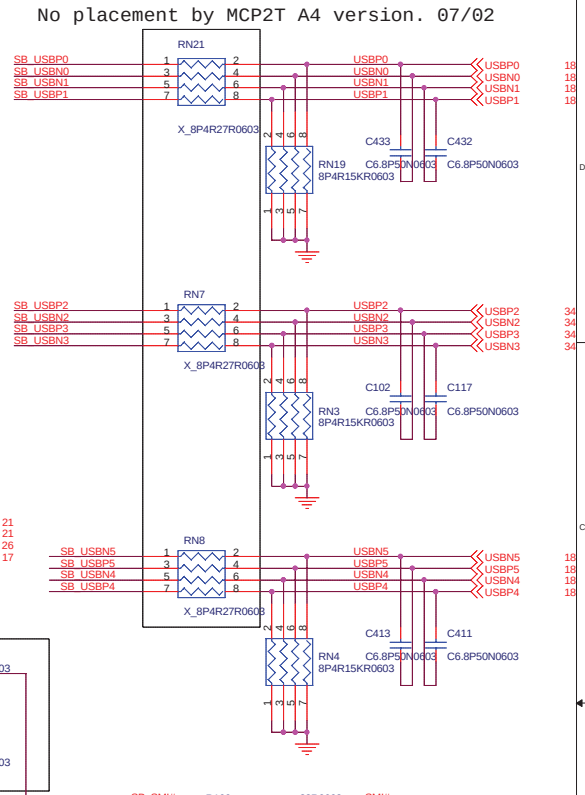
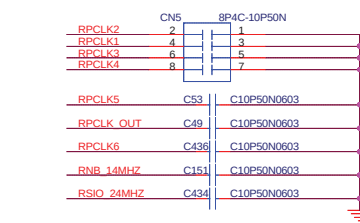
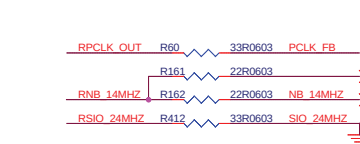
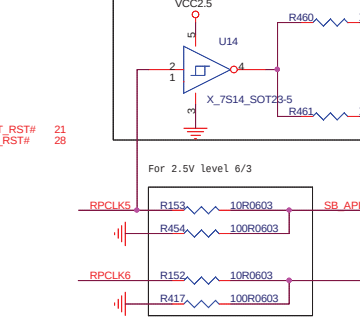
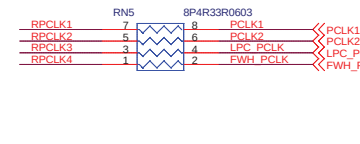
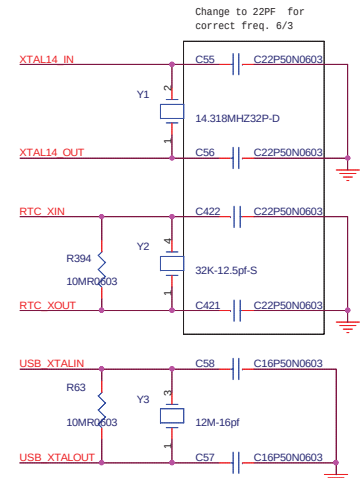
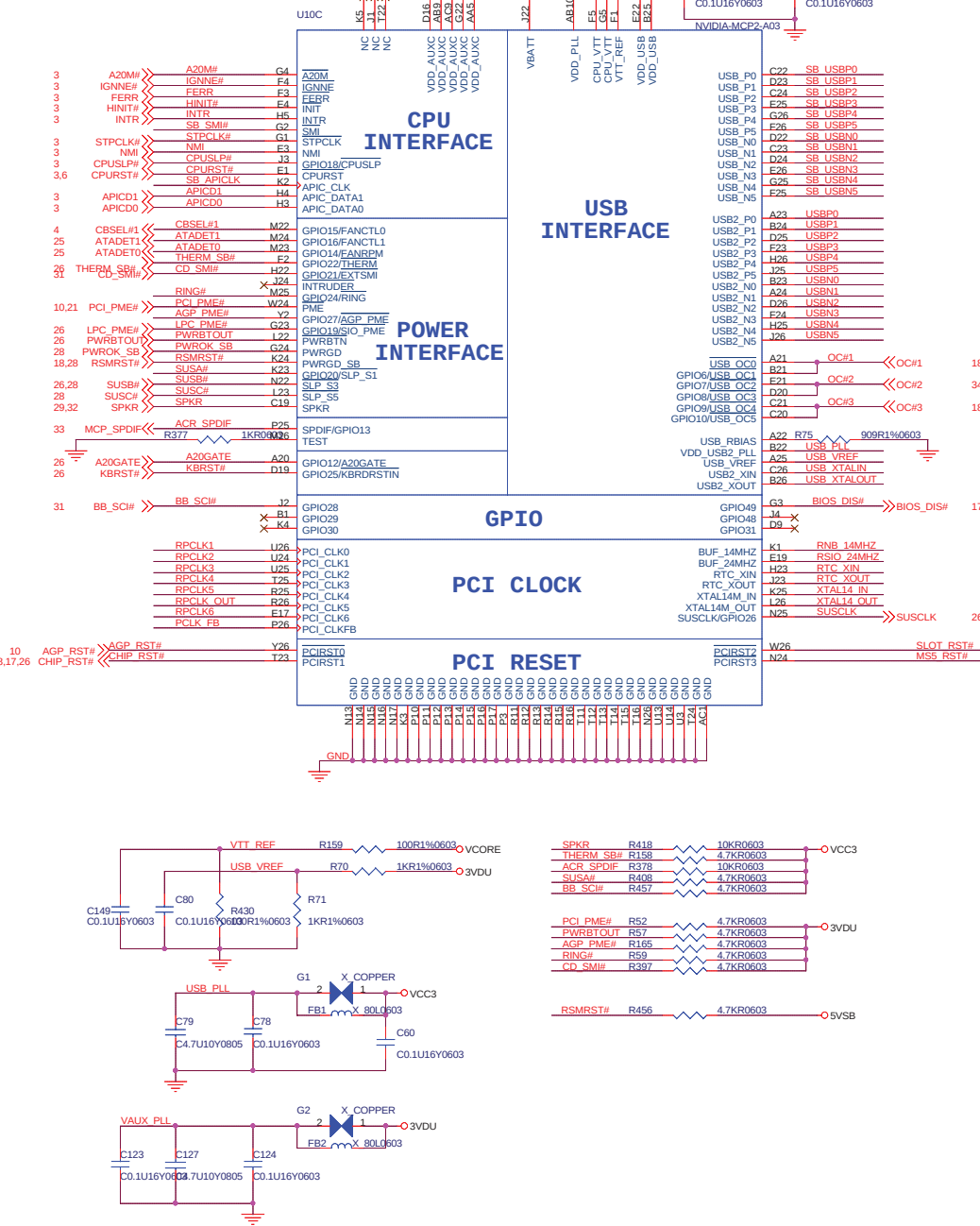


7.11 MDA[63..0] << MDA[63..0]
7.11 DQMA[7..0] << DQMA[7..0]
7.11 DQSA[7..0] << DQSA[7..0]
7.11 MDB[63..0] << MDB[63..0]
7.11 DQMB[7..0] << DQMB[7..0]
8.11 MAA[13..0] << MAA[13..0]
8.11 MAB[13..0] << MAB[13..0]
7.11 DQSB[7..0] << DQSB[7..0]





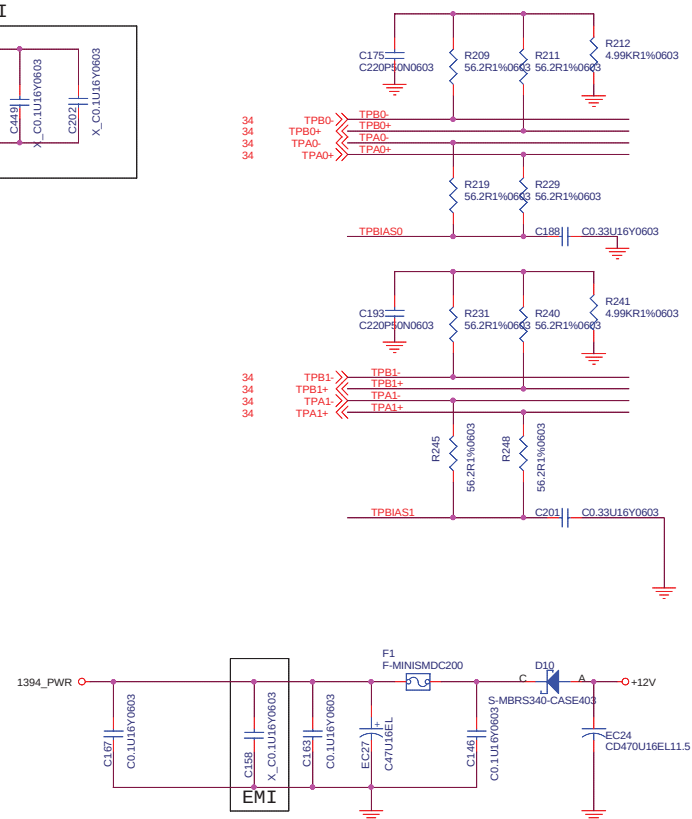
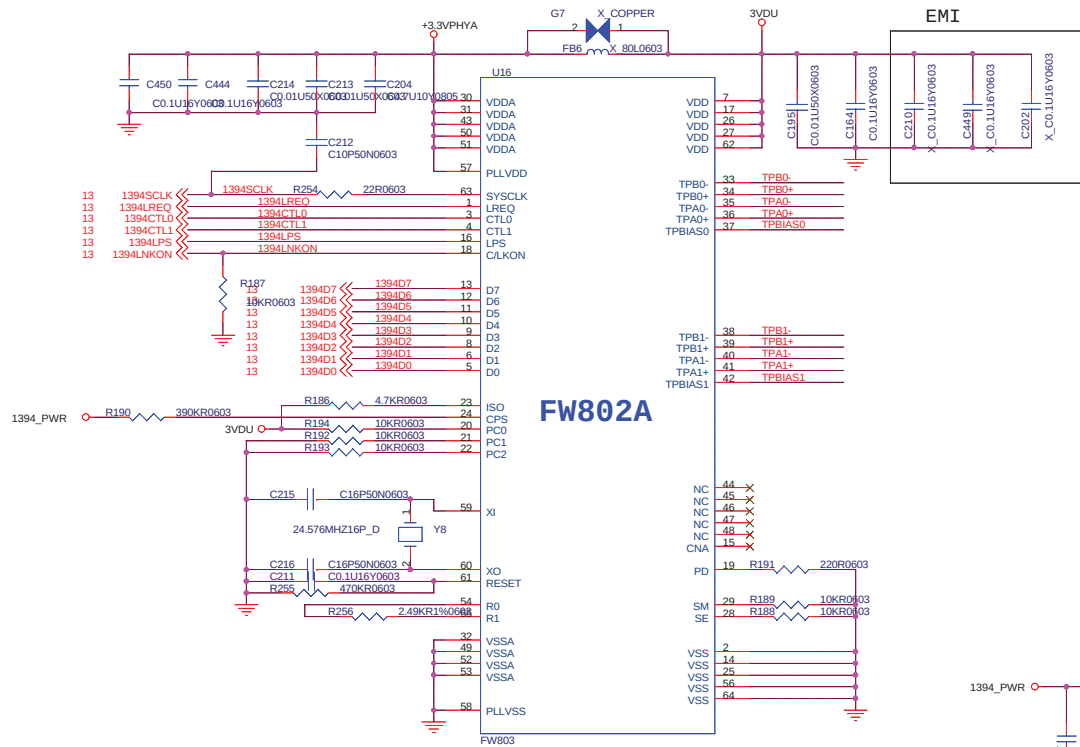
EXTSMI# change CD_SMI#
the BIOS code need to change
the sleep button function.



Title		
MCP2 Part 3		
Size	Document Number	Rev
MS-6796		0A
Date	Wednesday, July 23, 2003	Sheet 15 of 36

Title			
<Title>			
Size B	Document Number <Doc>		Rev <RevCode>
Date:	Tuesday, July 22, 2003	Sheet 16 of 36	

[illegible]



5

4

3

2

1

D

D

C

C

B

B

A

A

5

4

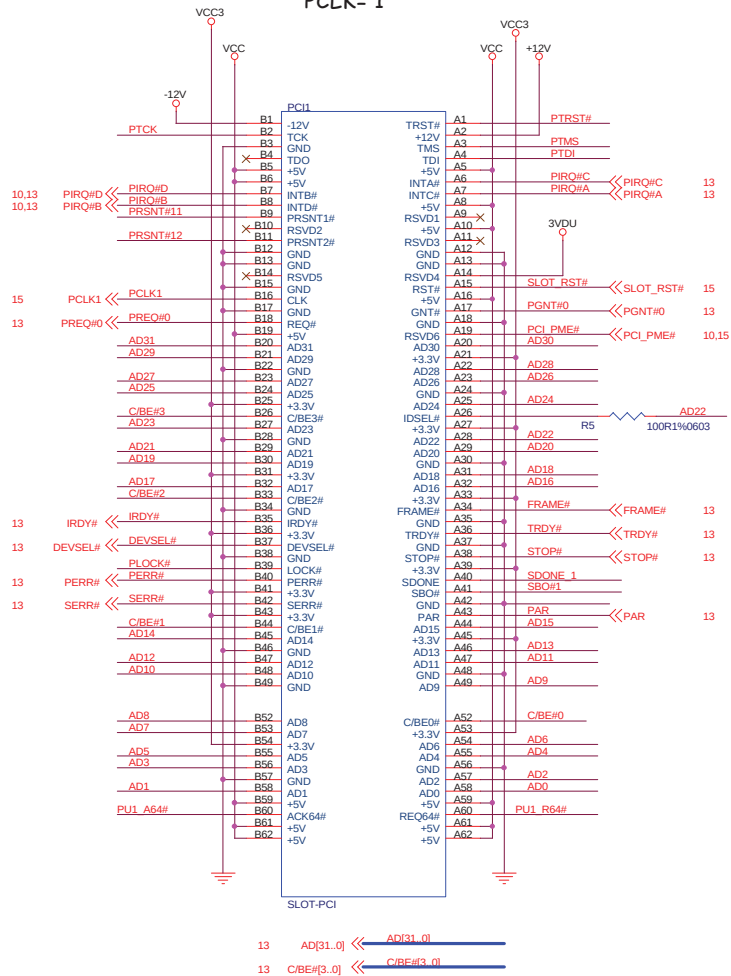
3

2

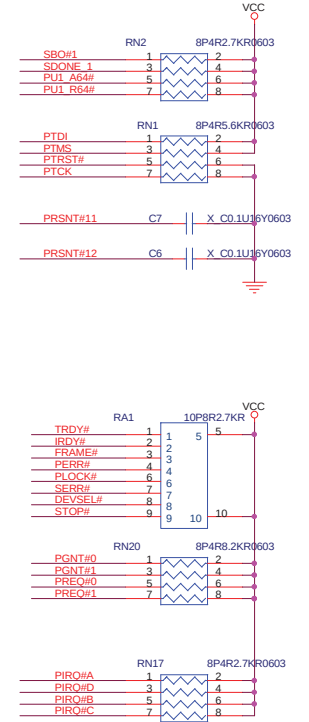
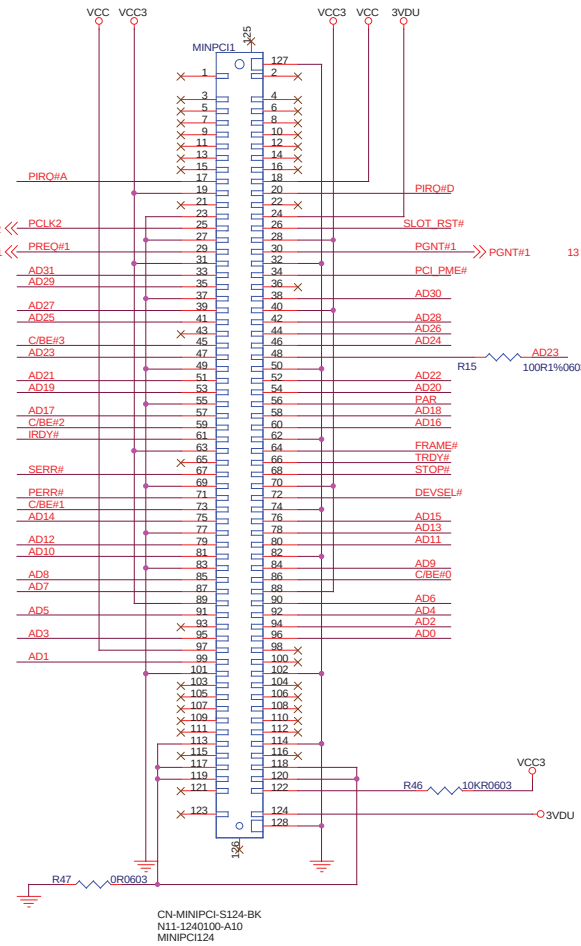
1

				A
Title <Title>				
Size A	Document Number <Doc>			Rev <RevCode>
Date:	Tuesday, July 22, 2003		Sheet 20 of 36	

IDSEL= AD22
INT# = A
PGNT# = 0
PREQ# = 0
PCLK = 1



IDSEL= AD23
INT# = B
PGNT# = 1
PREQ# = 1
PCLK = 2



5

4

3

2

1

D

D

C

C

B

B

A

A

5

4

3

2

1

				A
Title <Title>				
Size A	Document Number <Doc>		Rev <RevCode>	
Date: Tuesday, July 22, 2003 Sheet 22 of 36				

5

4

3

2

1

D

C

C

C

B

11

A

A

5

4

3

2

1

				A
Title <Title>				
Size A	Document Number <Doc>			Rev <RevCode>
Date:	Tuesday, July 22, 2003		Sheet 23 of 36	

5

4

3

2

1

D

5

C

C

B

11

A

A

5

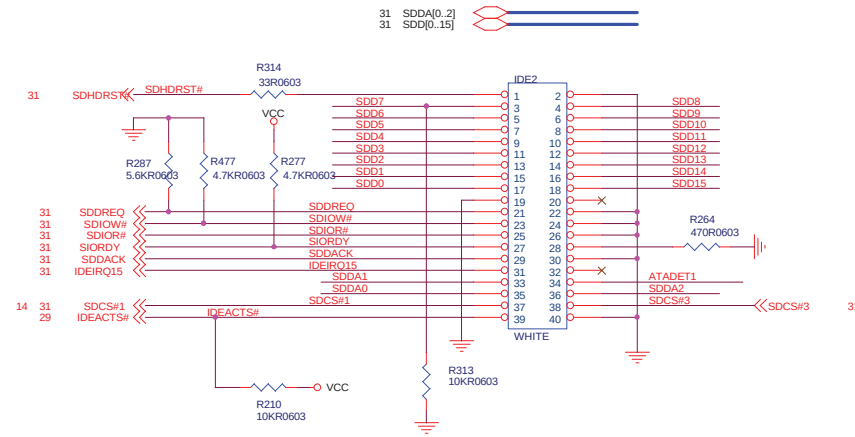
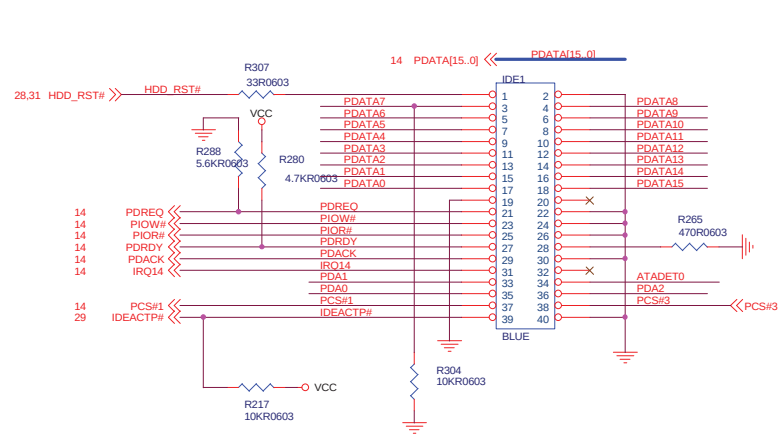
4

3

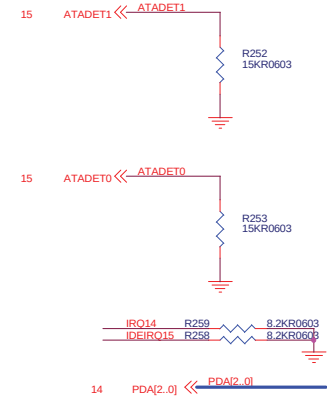
2

1

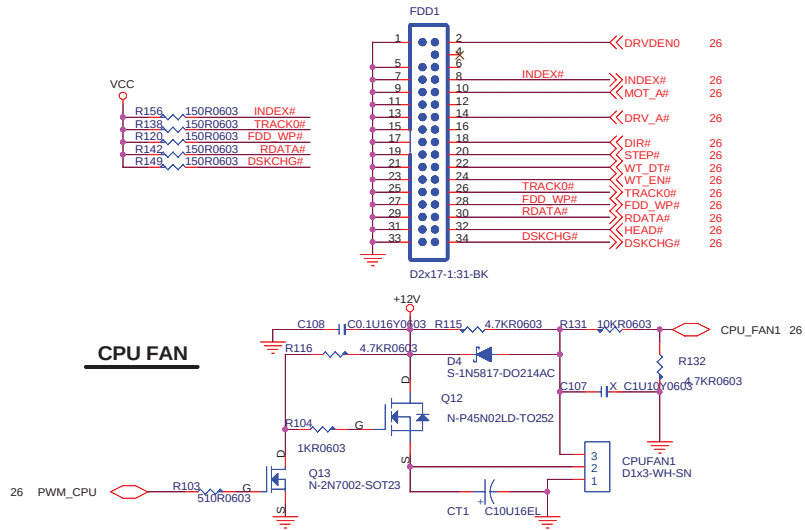
				A
Title <Title>				
Size A	Document Number <Doc>			Rev <RevCode>
Date:	Tuesday, July 22, 2003		Sheet 24 of 36	



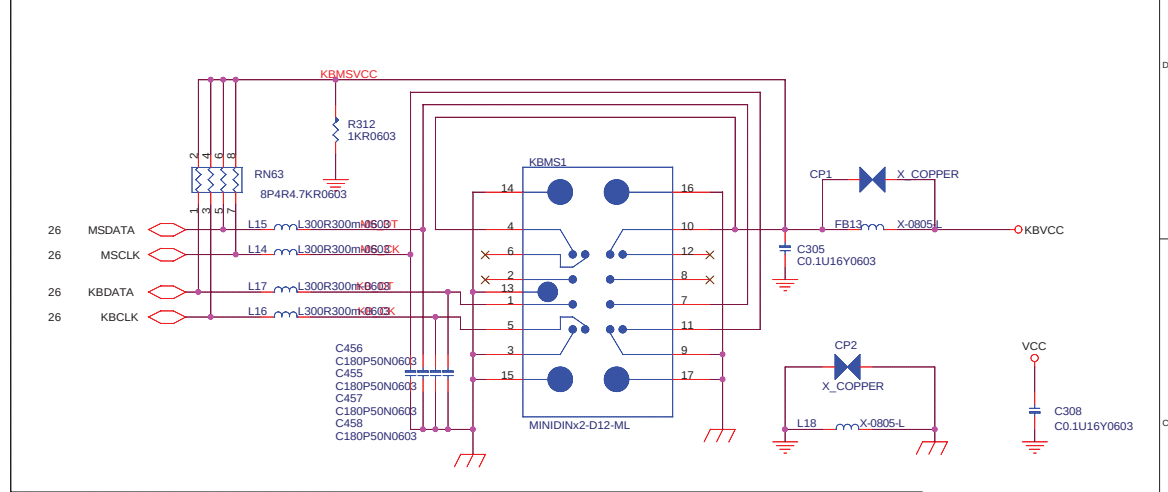
W83627 KBM CONNECTOR



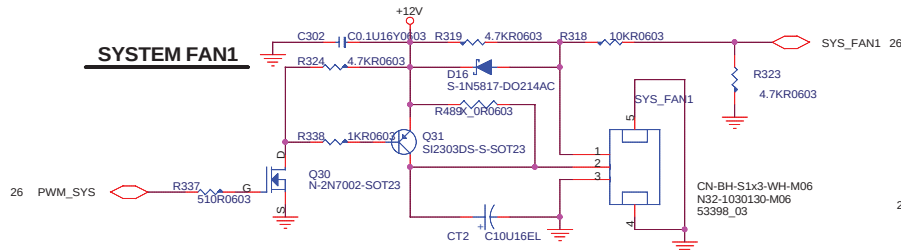
FLOPPY CONNECTOR



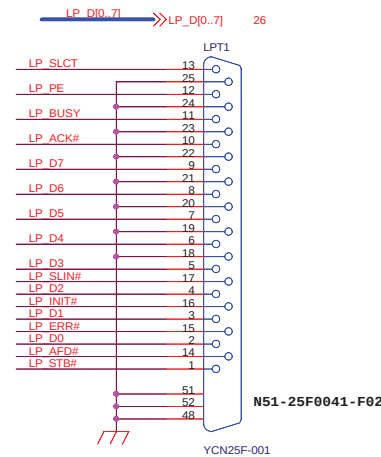
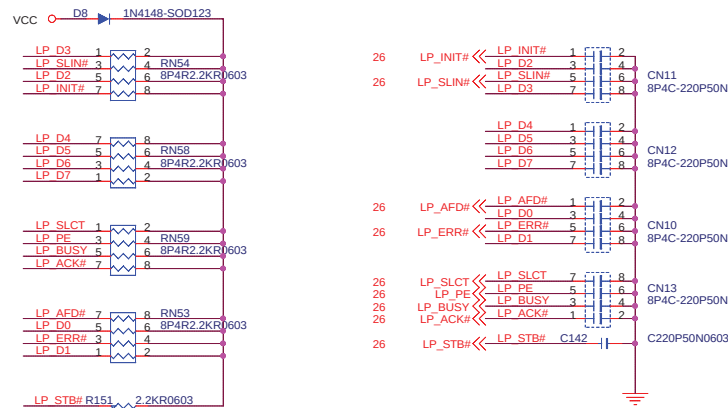
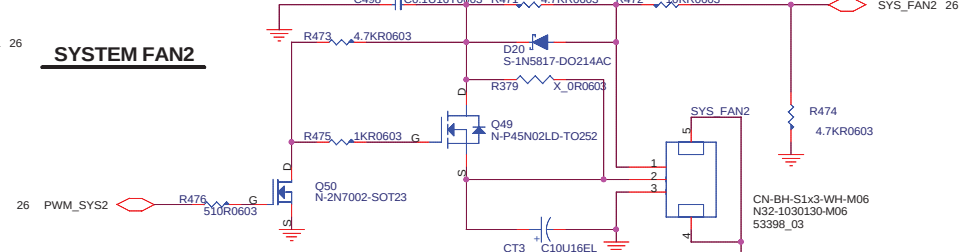
PS2 KEYBOARD & MOUSE CONNECTOR



SYSTEM FAN1

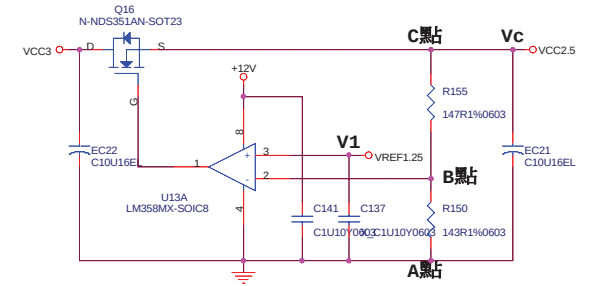
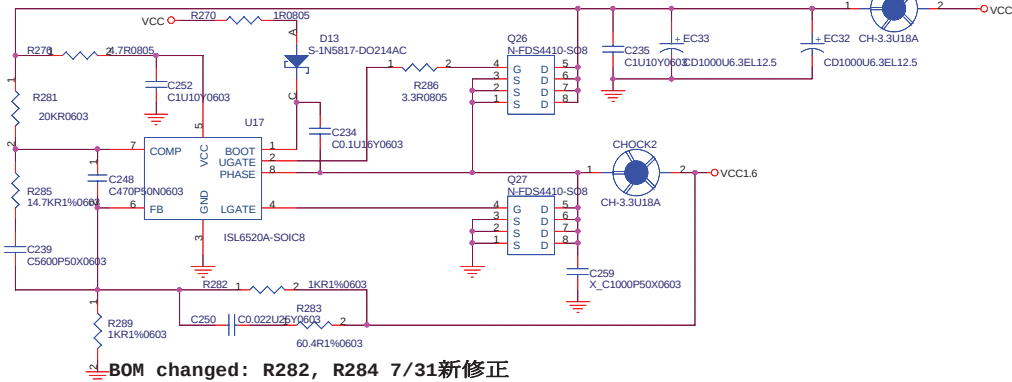


SYSTEM FAN2



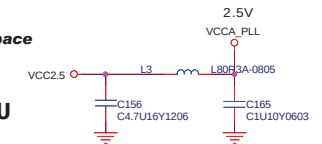
Title			<Title>
Size	Document Number	Rev	<RevCode>
Date:	Wednesday, July 23, 2003	Sheet	27 of 36

Chipset Core Power is 1.2V/6A



25 mils Trace / 12 mils Space

Closed to CPU



運算放大器分析

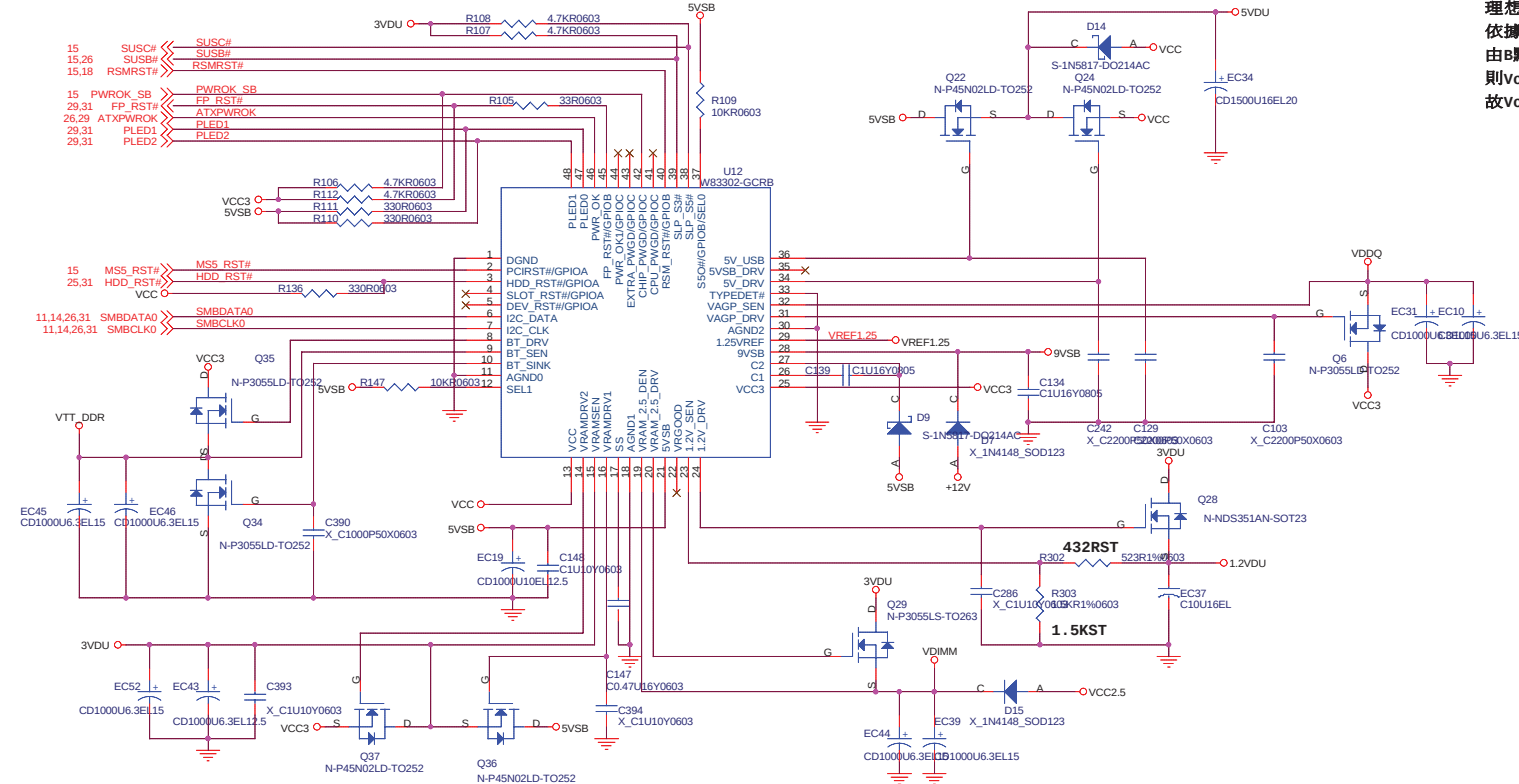
工作原理:藉由B點的回授,運算放大器可決定MOSFET的導通量。
理想運算放大器中,跨在+, -兩端雙電壓為0V。

依據KVL, $-V_{ba} + V_{bc} + V_c = 0$ 或 $V_{bc} = V_{ba} - V_c = V_1 - V_c$

由B點的KCL可得: $(V_1/R55) + [(V_1 - V_c)/R48] = 0$

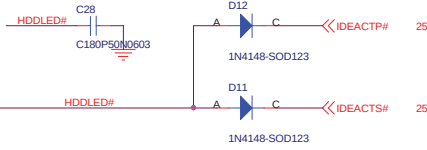
則 $V_c = uV_1$, $u = 1 + (R48/R55)$

故 $V_c = (1 + R48/R55) * V_1$

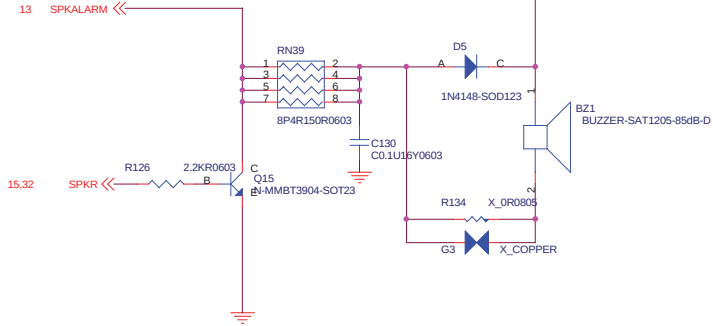
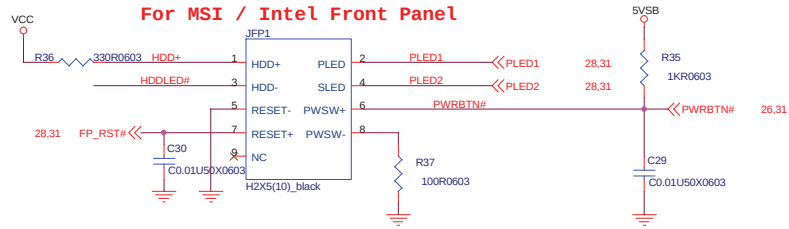


Title		
MS5 ACPI Controller		
Size	Document Number	Rev
MS-6796		0A
Date	Wednesday, July 23, 2003	Sheet 28 of 36

ESD Protect

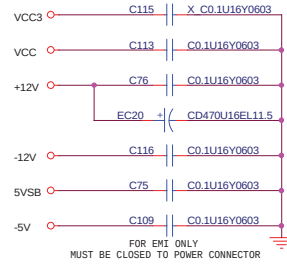


For MSI / Intel Front Panel

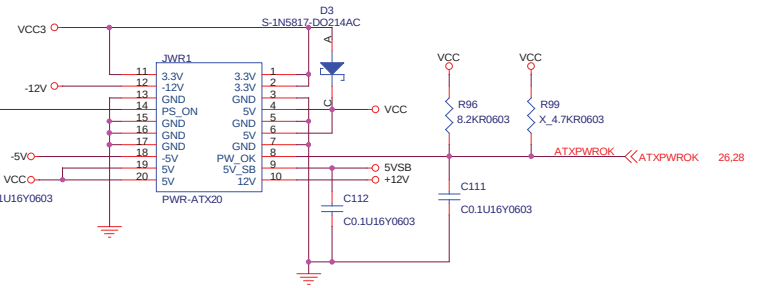


AMD Thermal shutdown

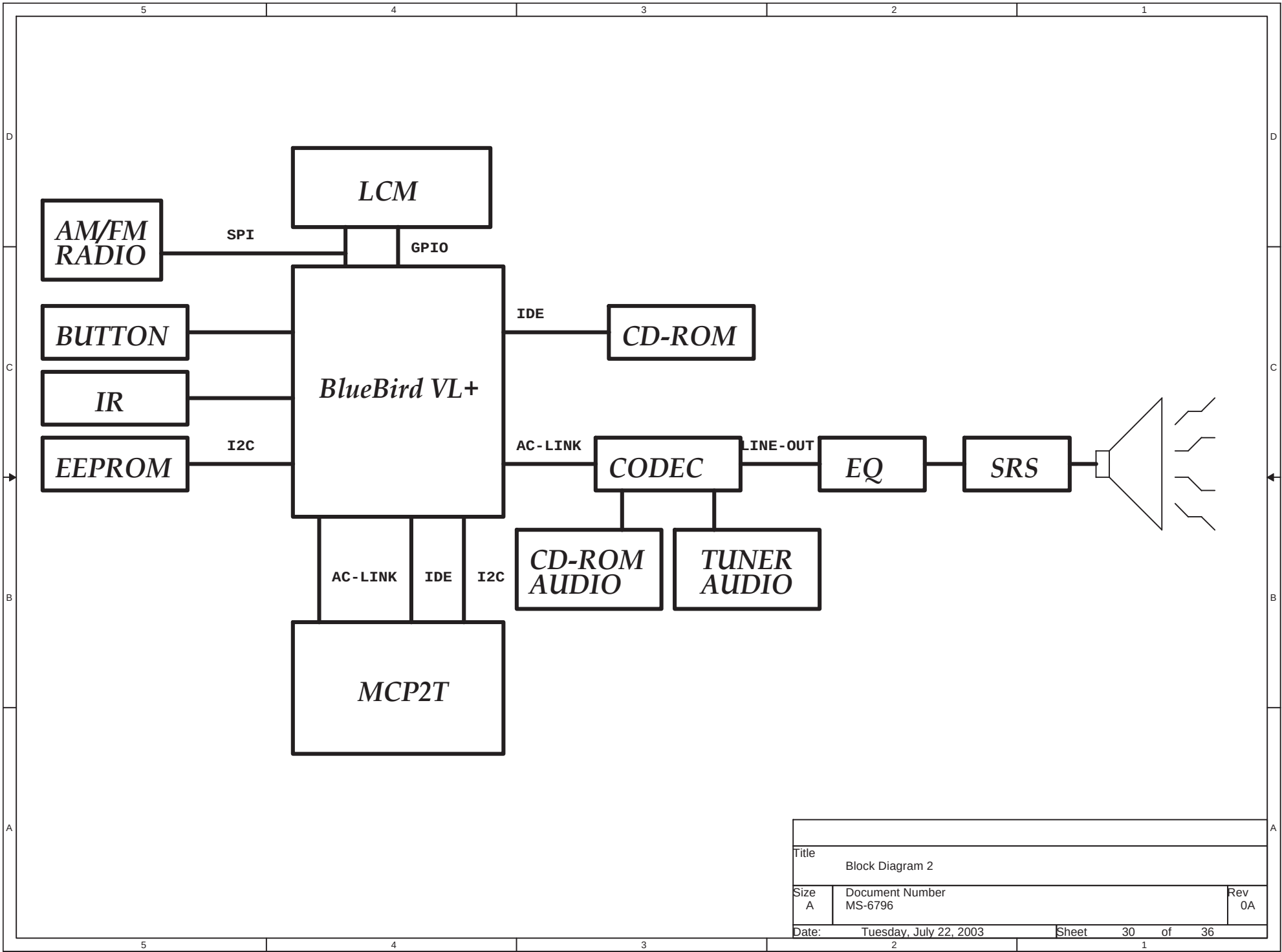
13 PSON#



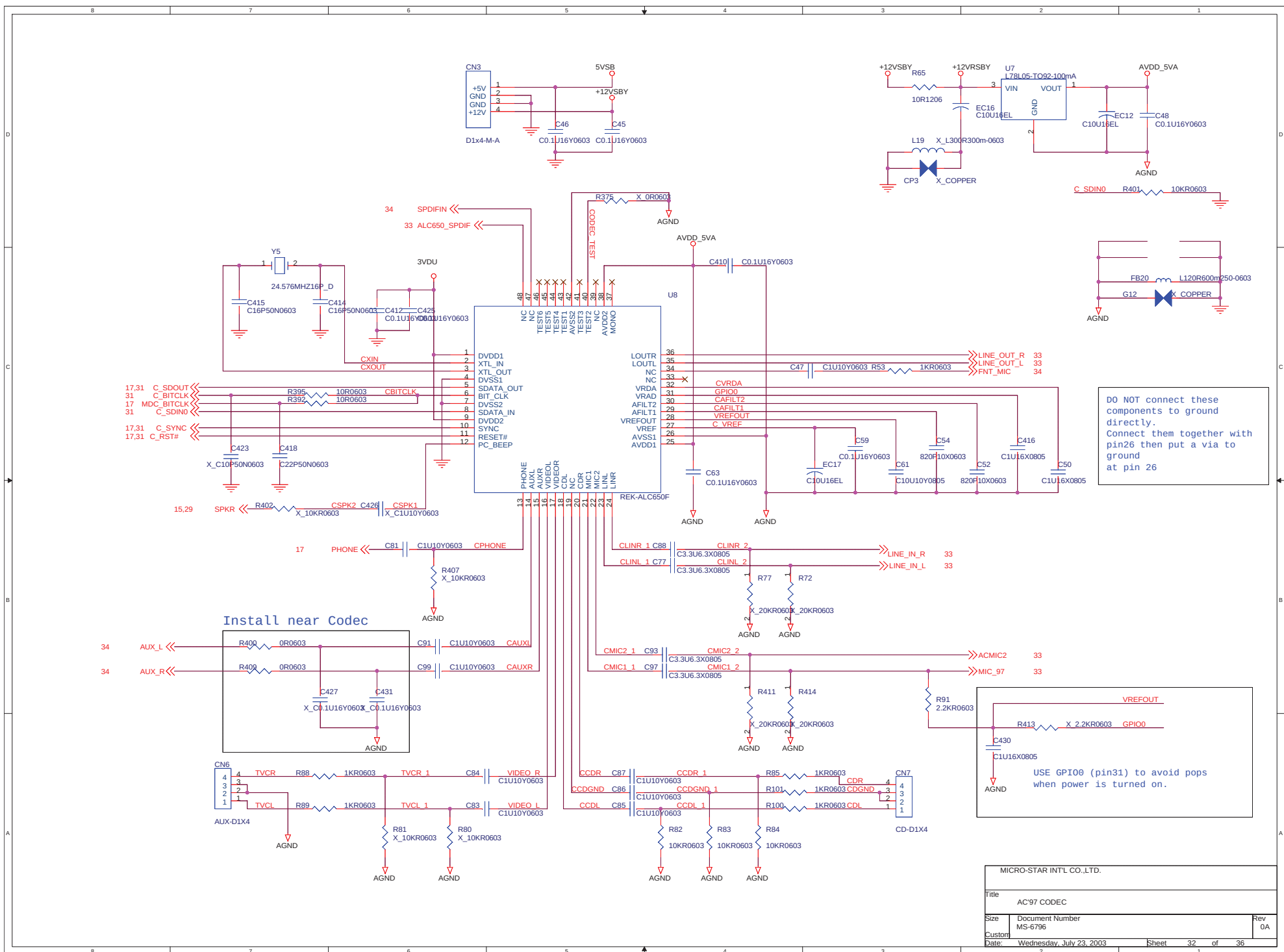
AMD Thermal shutdown

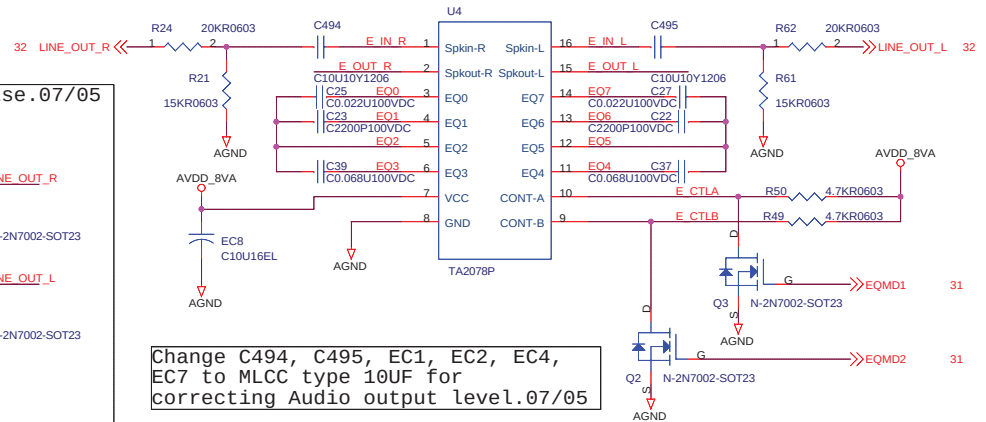
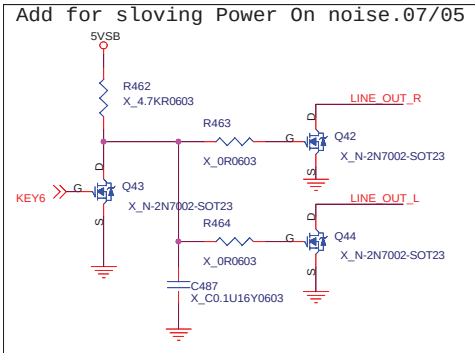
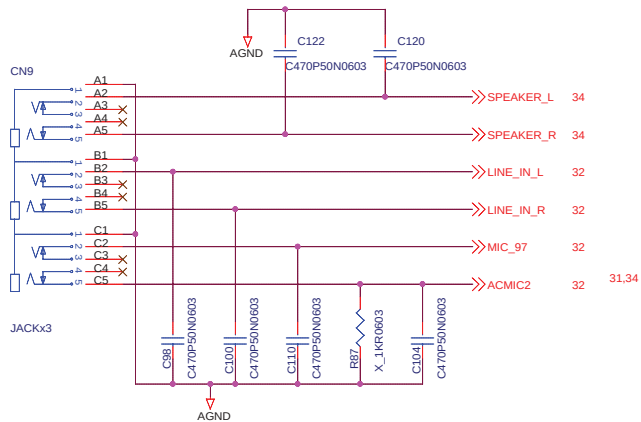


Title			
Front Panel & ATX Power			
Size	Document Number	Rev	
	MS-6796	0A	
Date	Wednesday, July 23, 2003	Sheet	29 of 36

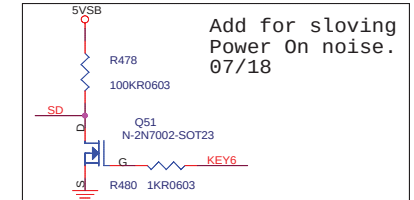
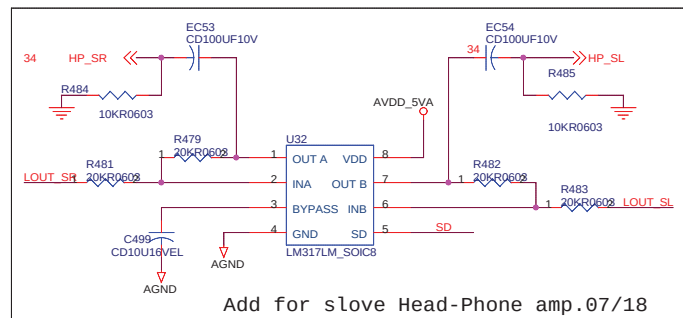
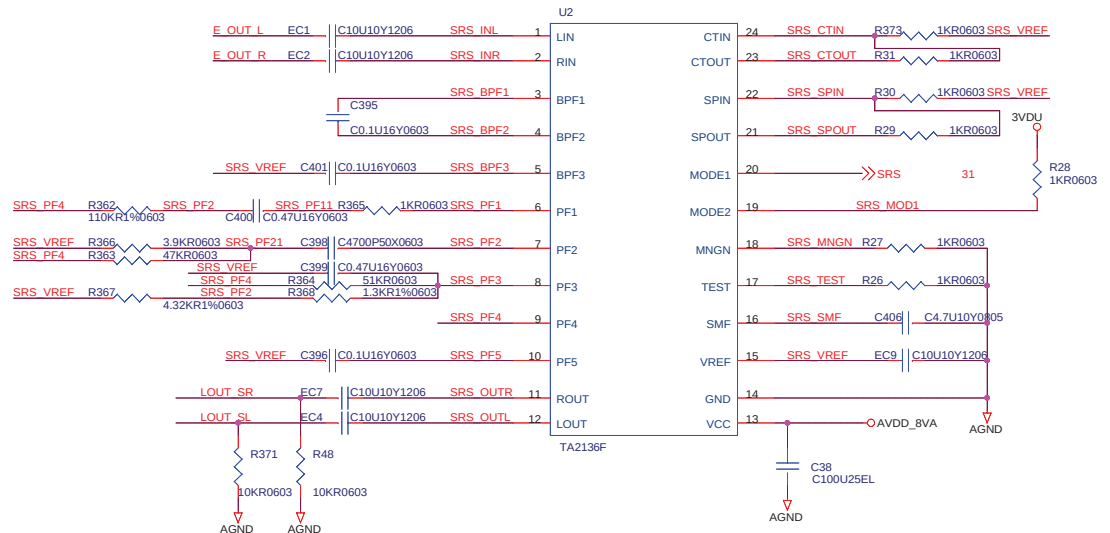
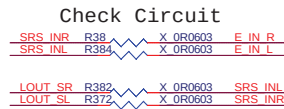
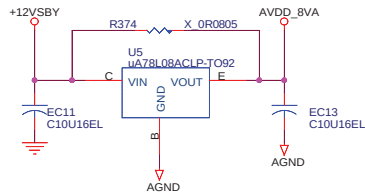


Title		
Block Diagram 2		
Size	Document Number	Rev
A	MS-6796	0A
Date:	Tuesday, July 22, 2003	Sheet 30 of 36

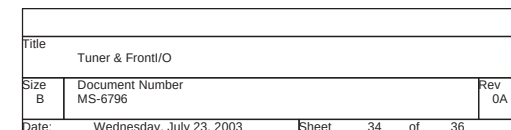




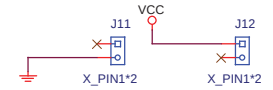
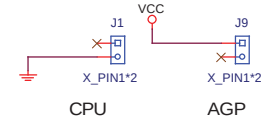
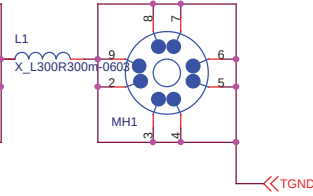
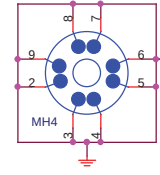
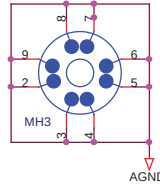
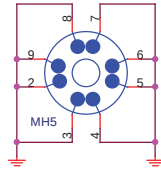
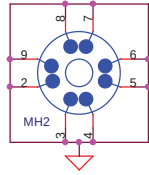
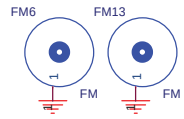
Change C494, C495, EC1, EC2, EC4, EC7 to MLCC type 10UF for correcting Audio output level.07/05



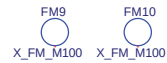
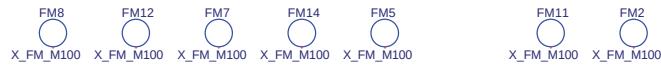
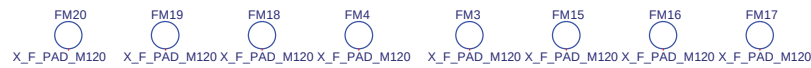
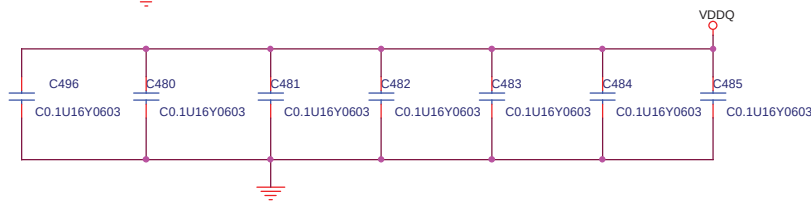
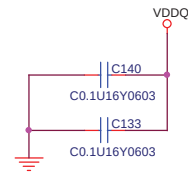
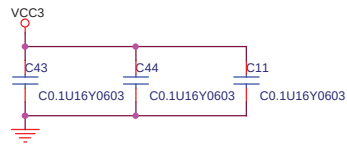
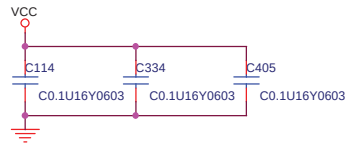
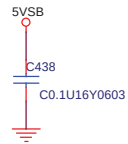
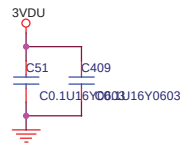
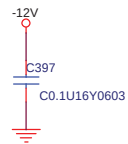
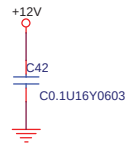
MICRO-STAR INT'L CO.,LTD.			
Title AUDIO IC & CONNECTOR			
Size Custom	Document Number MS-6796	Rev 0A	
Date: Wednesday, July 23, 2003	Sheet 33	of 36	



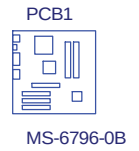
PCB OTHER COMPONENT



System Decoupling Capacitors



High Freq. return current decoupling



MICRO-STAR INT'L CO.,LTD.		
Title DECOUPLING CAPACITOR		
Size B	Document Number MS-6796	Rev 0A
Date: Wednesday, July 23, 2003	Sheet 35 of 36	1

