

MS-6716 Ver : 100

VIA KT400 + VT8235 Chipset

CPU:

AMD Athlon XP / Athlon / Duron Socket 462

System Chipset:

VIA VT8377 (KT400) + VT8235

On Board Chipset:

LPC Super I/O -- W83697HF

Lan : VIA PHY VT6103

1394 : VIA 1394

Expansion Slots:

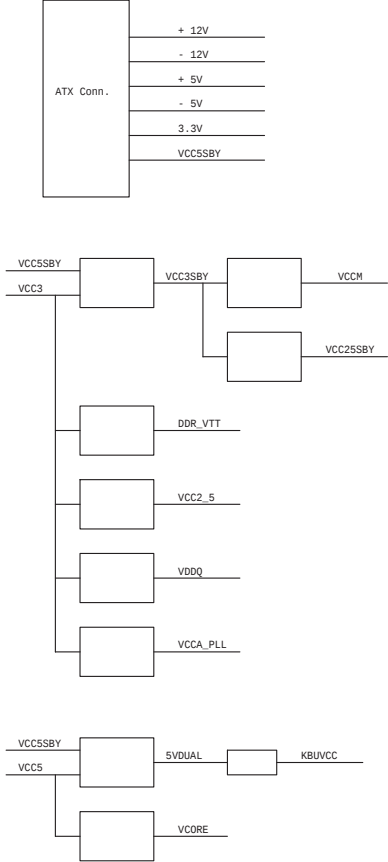
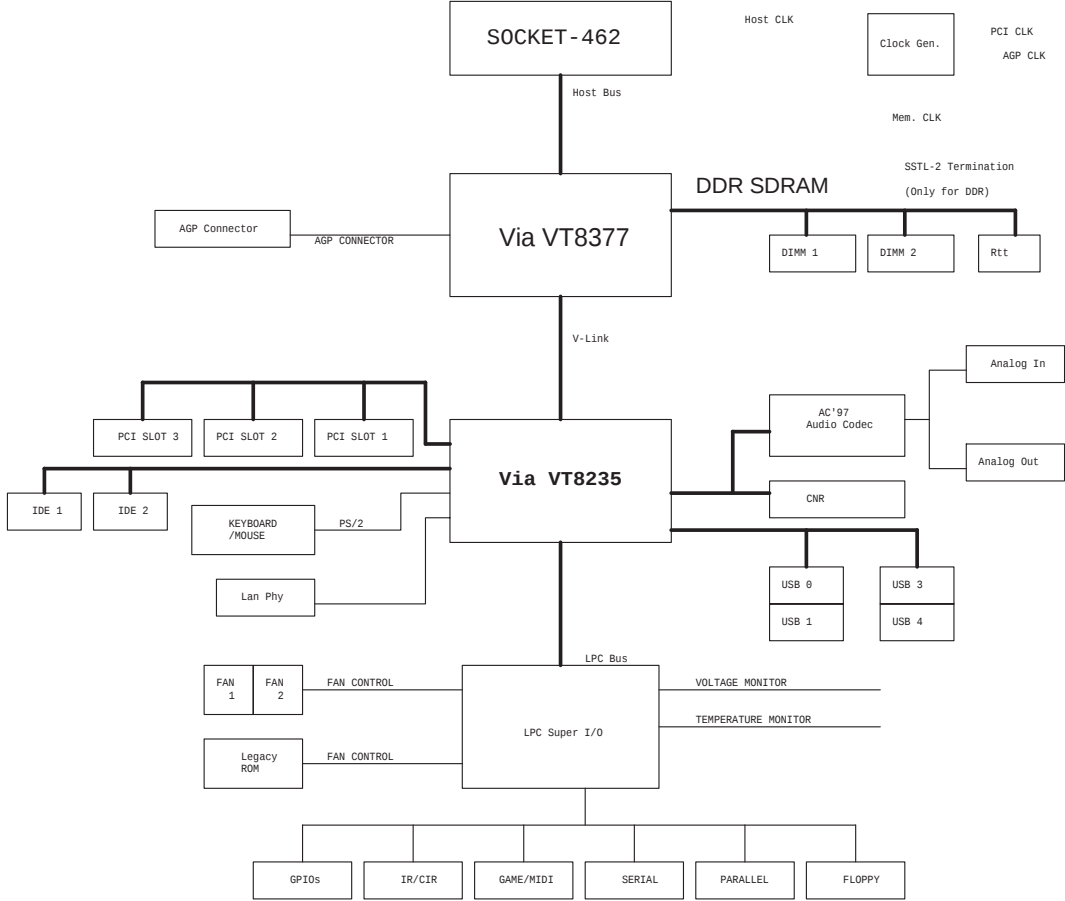
AGP 3.0 Slot * 1

DDR Slot * 2

PCI 2.2 Slot * 3

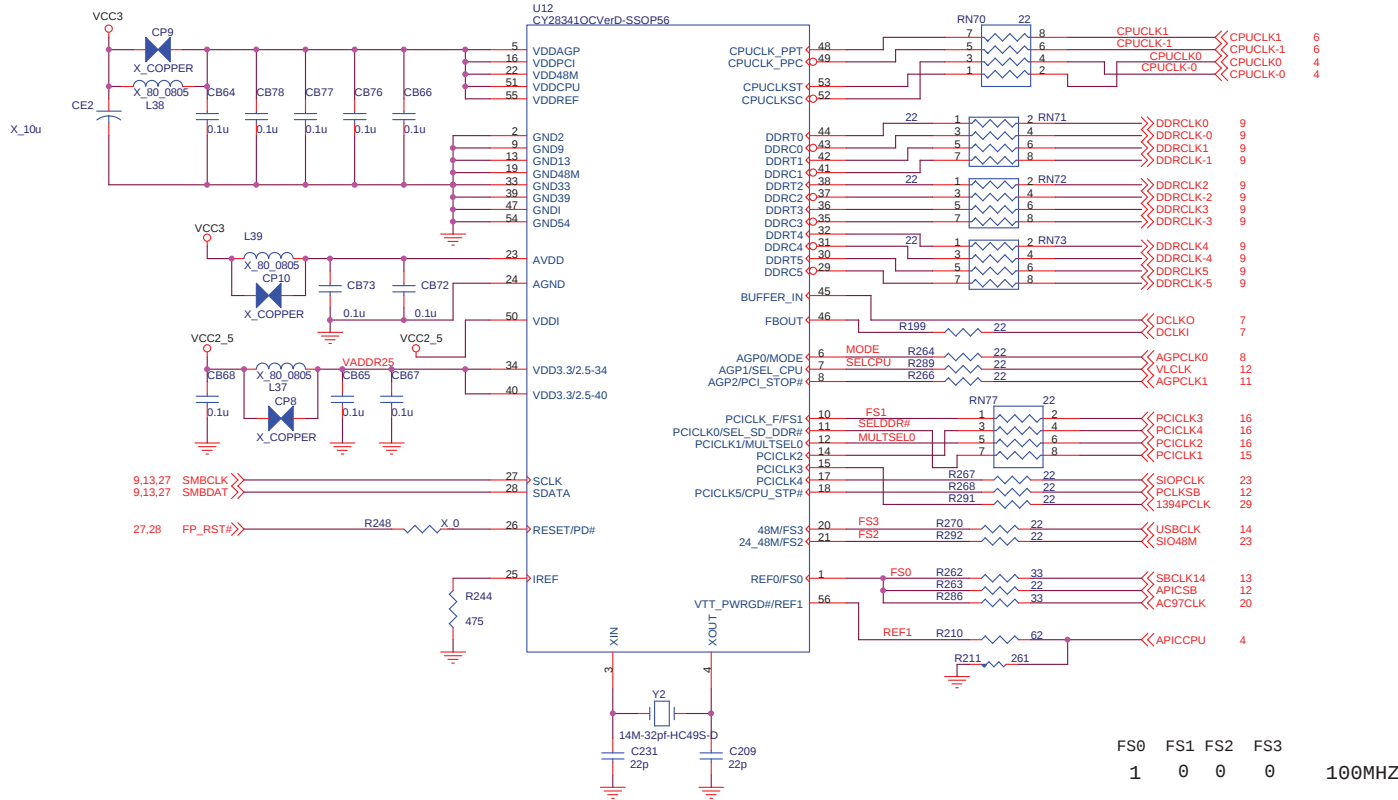
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System Block Diagram



Main Clock Generator

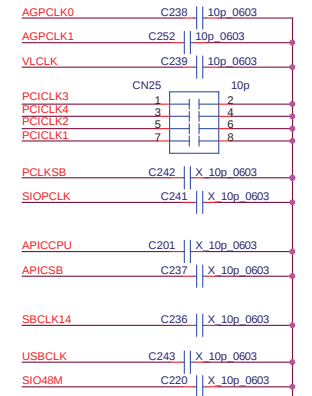
Damping Resistors
Place near to the
Clock Outputs



FS0	FS1	FS2	FS3	
1	0	0	0	100MHZ
1	1	0	0	133MHZ
1	1	0	1	166MHZ

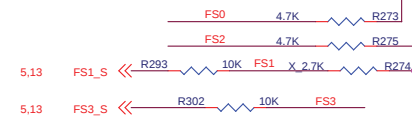
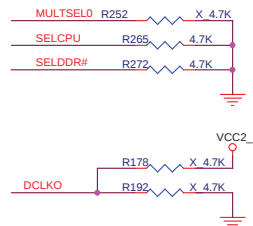
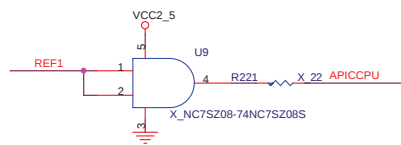
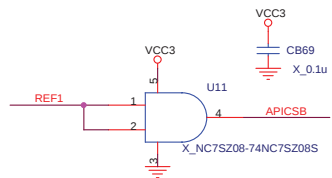
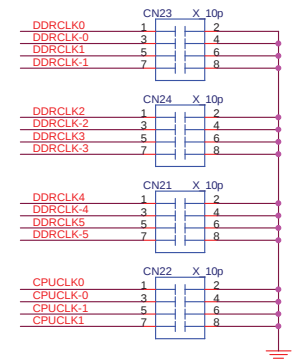
By-Pass Capacitors

Place near to the Clock Outputs

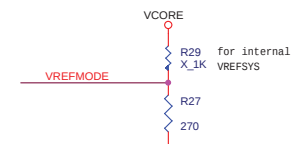
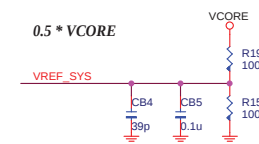
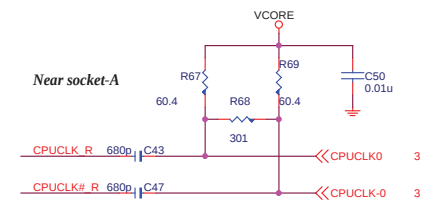
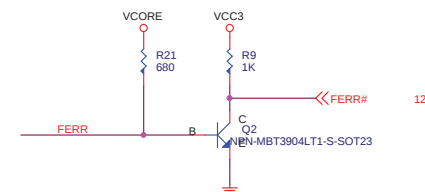
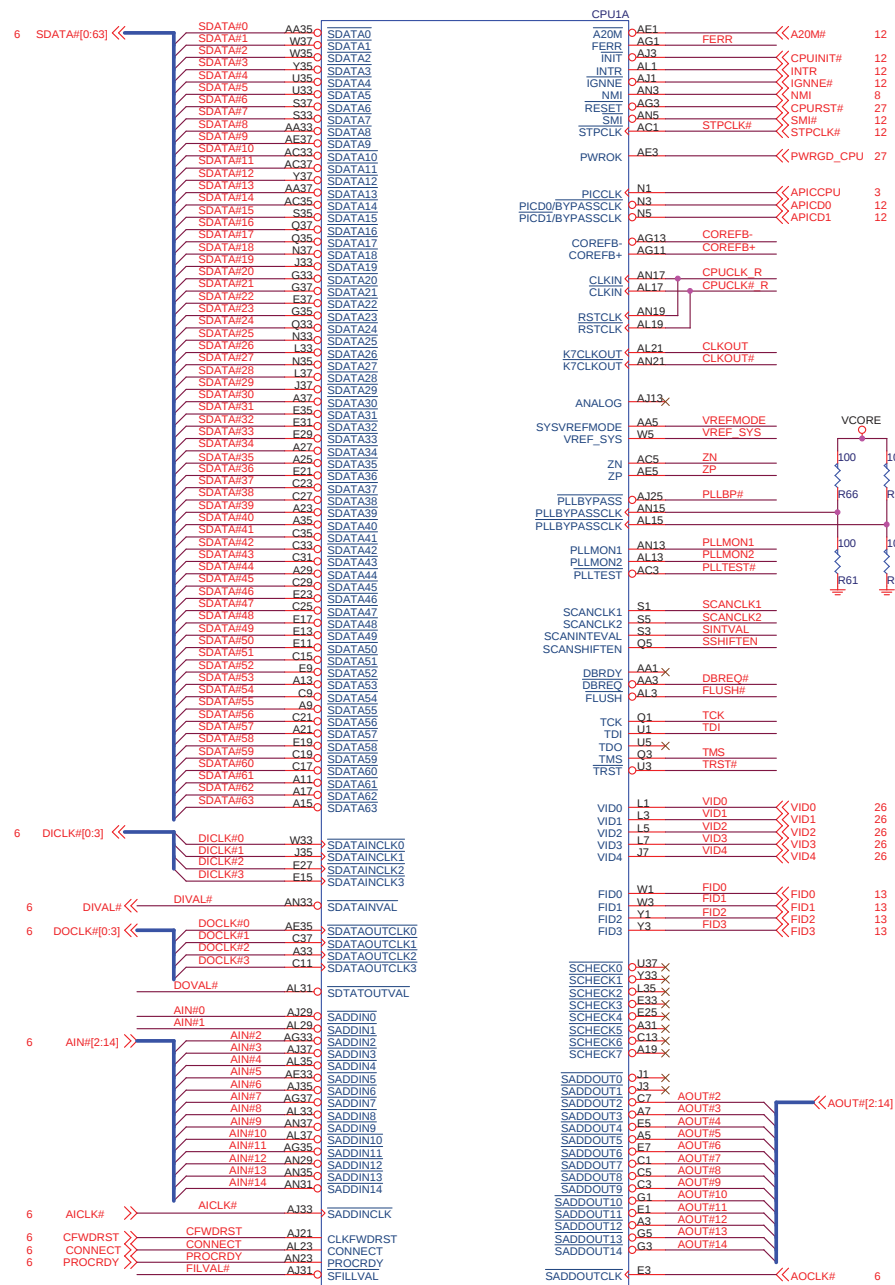


By-Pass Capacitors

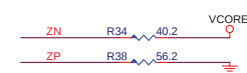
Place near to the Clock Buffer



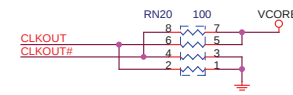
MICRO-STAR INT'L CO.,LTD.



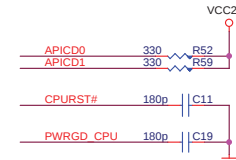
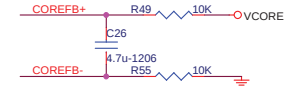
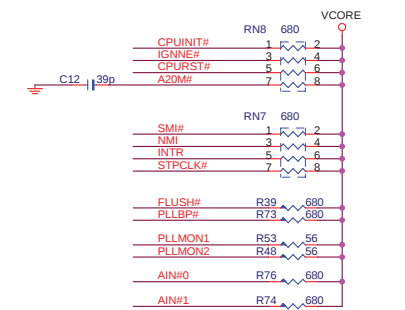
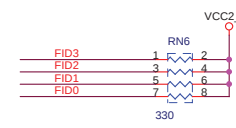
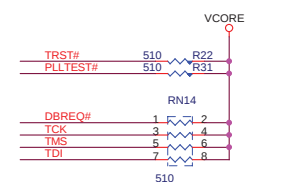
VREFMODE=Low=No voltage scaling

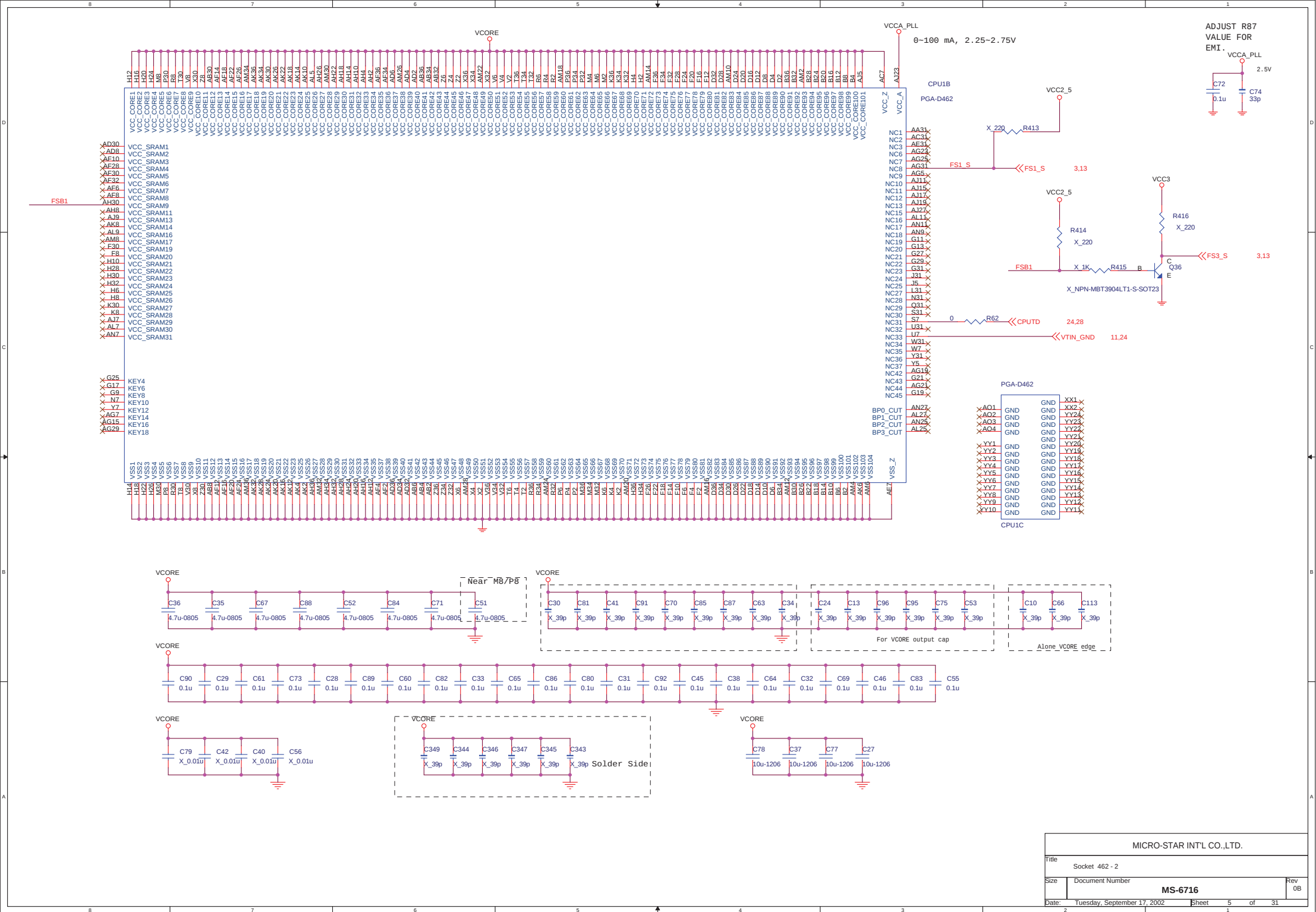


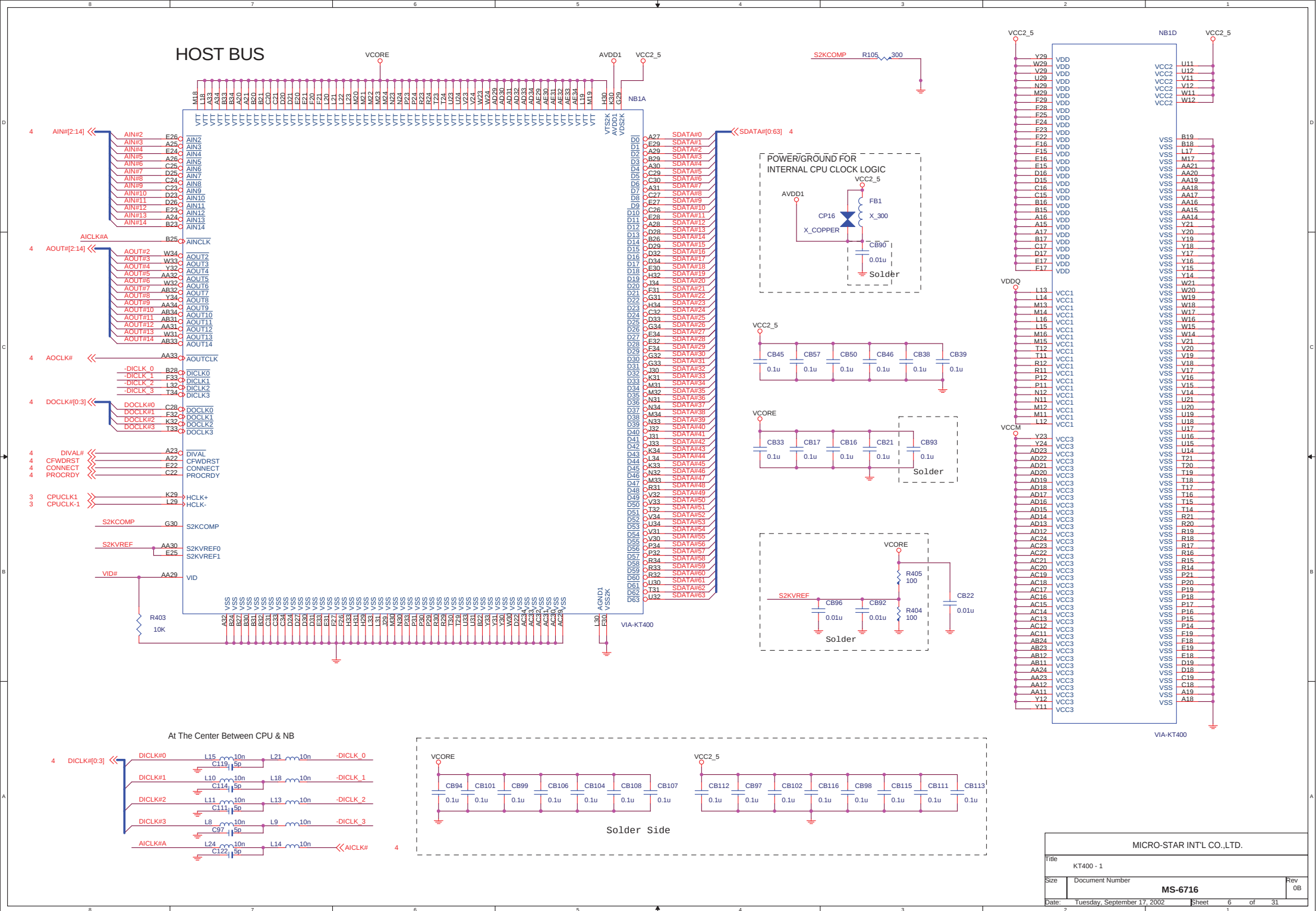
match the transmission line
Push-pull compensation circuit



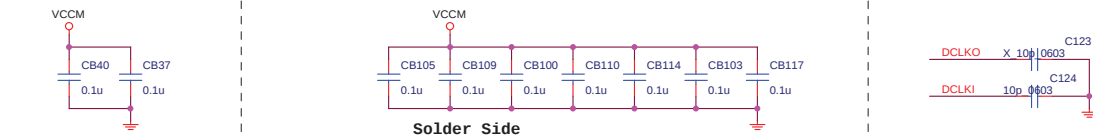
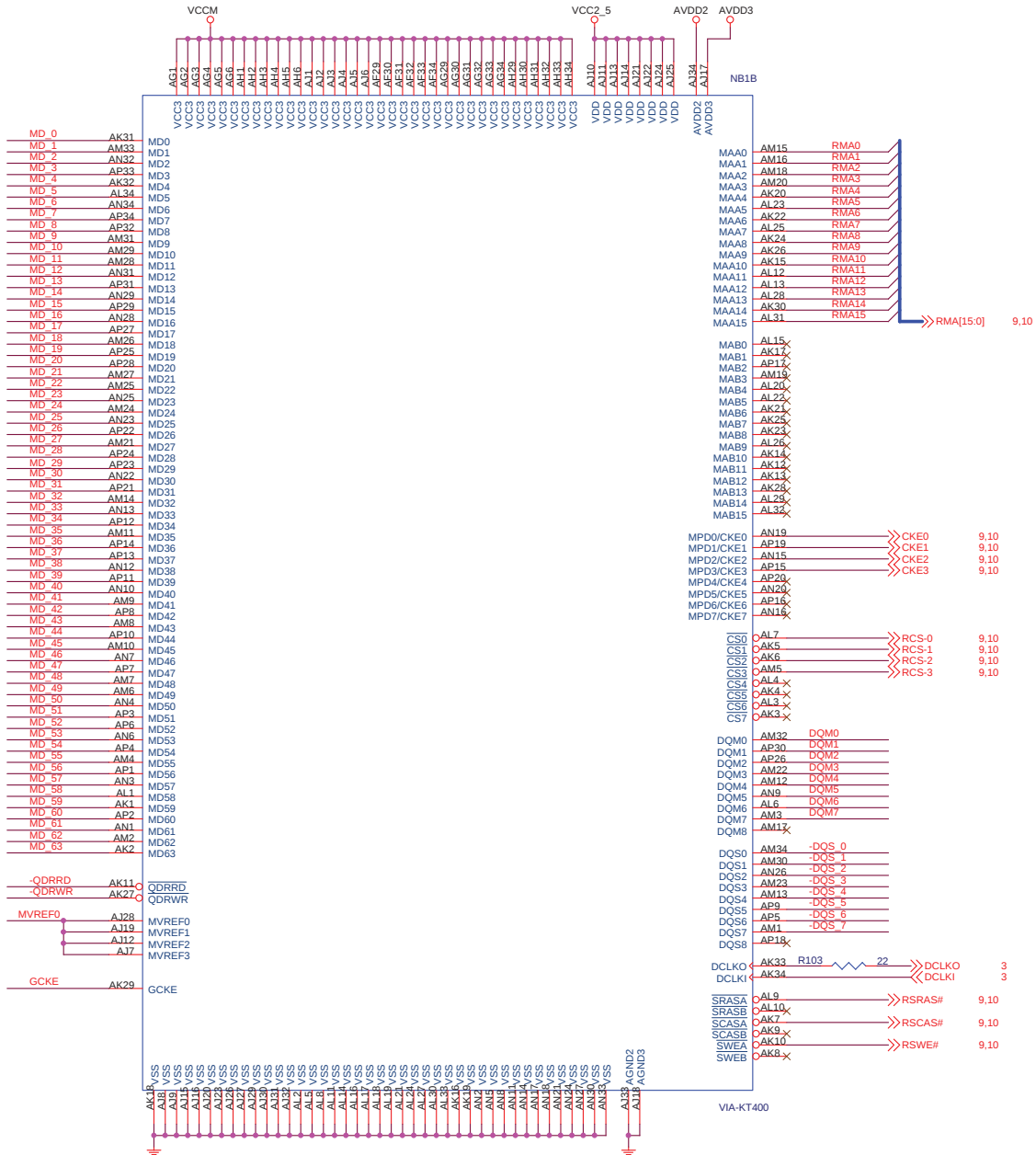
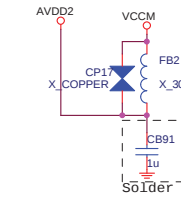
* Trace lengths of CLKOUT and -CLKOUT are between 2" and 3"



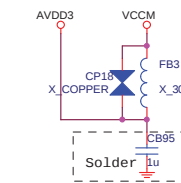




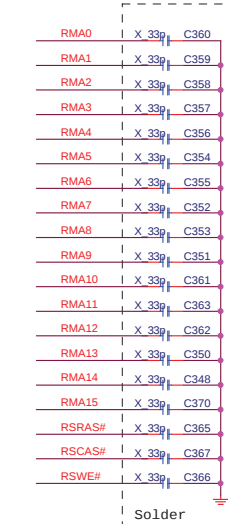
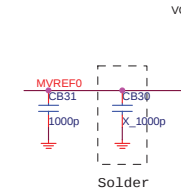
MEMORY

POWER FOR DRAM CLCOK
DESKEW CIRCUIT

POWER FOR DDR DLL CIRCUIT

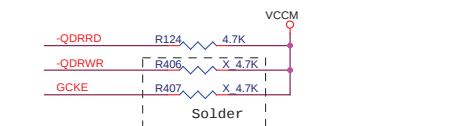
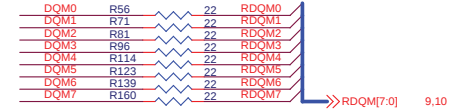
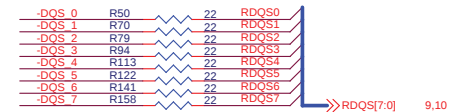
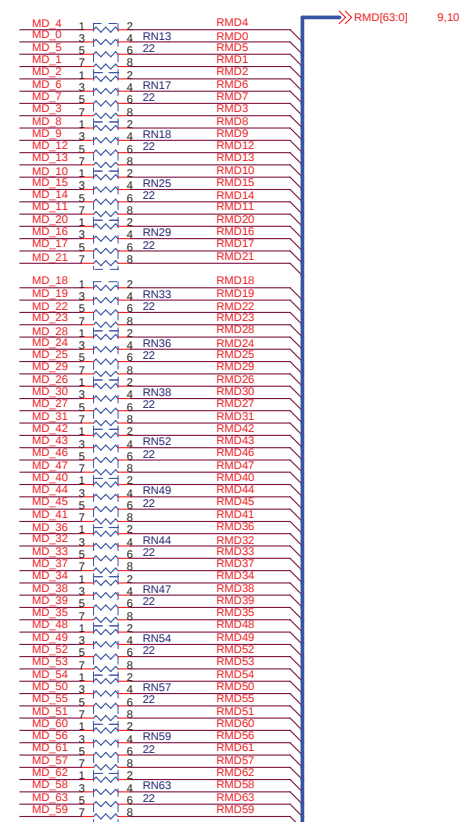


SSTL_2 RECEIVER VREF FOR DDR



| Solder

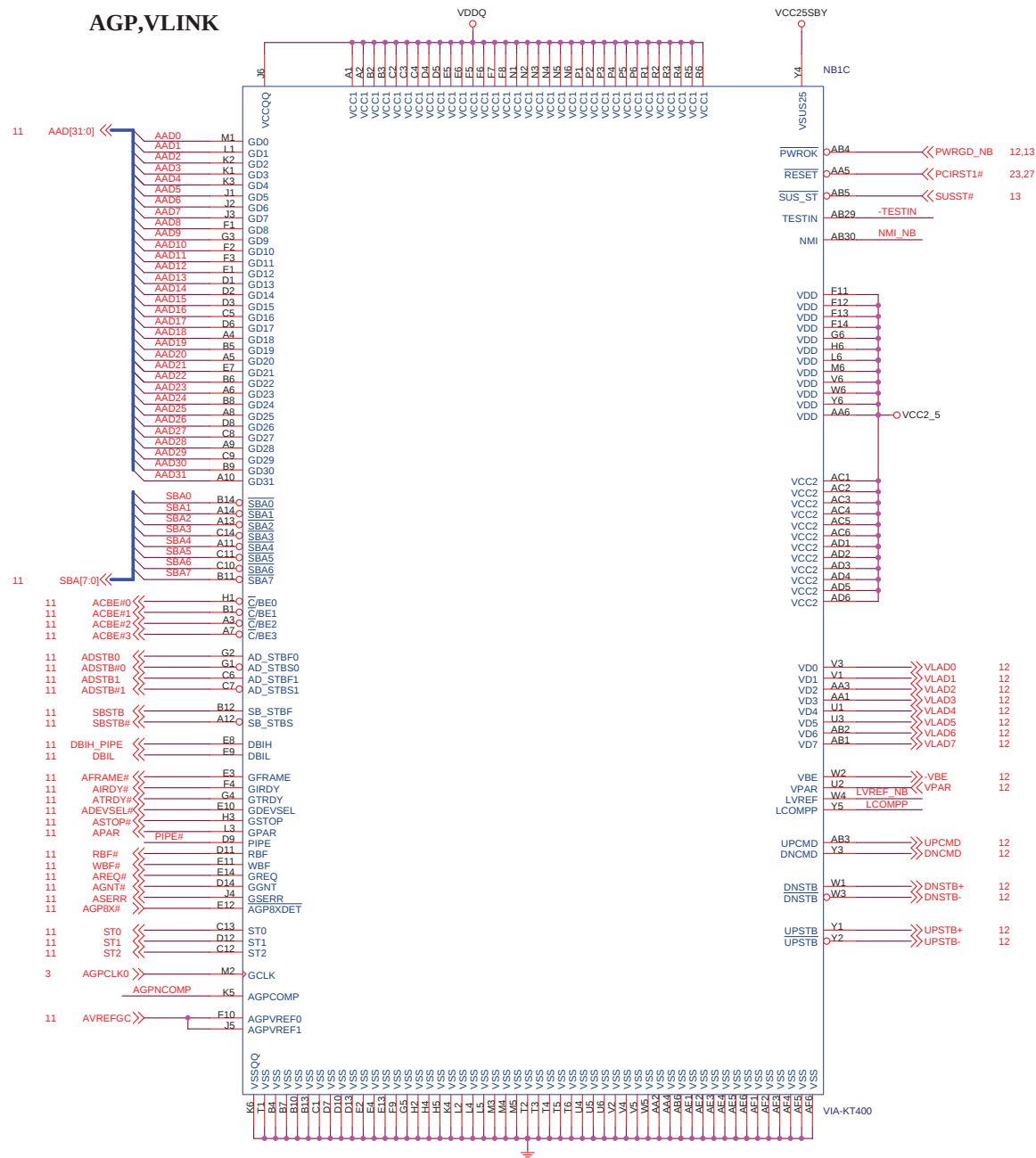
Place these damping resistors close to DIMM1



MICRO-STAR INT'L CO.,LTD.

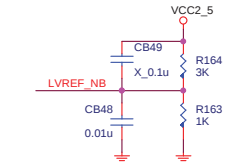
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KT400 - 2			
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AGP,VLINK

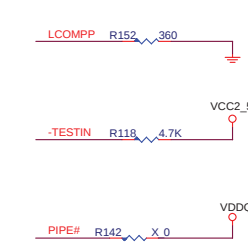


VLINK

1. VLINK REFERENCE VOLTAGE - 0.9V



2. VLINK P-CHANNEL COMPENSATION



AGP

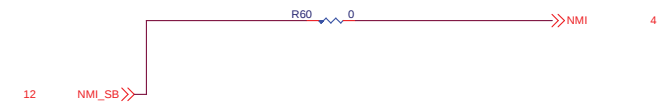
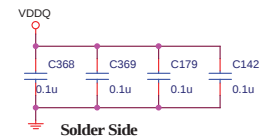
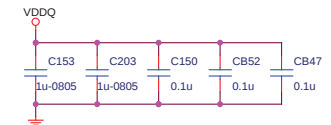
1. AGPVREF (PROVIDED BY AGP 4X CARD OR BY SYSTEM FOR OTHERS)

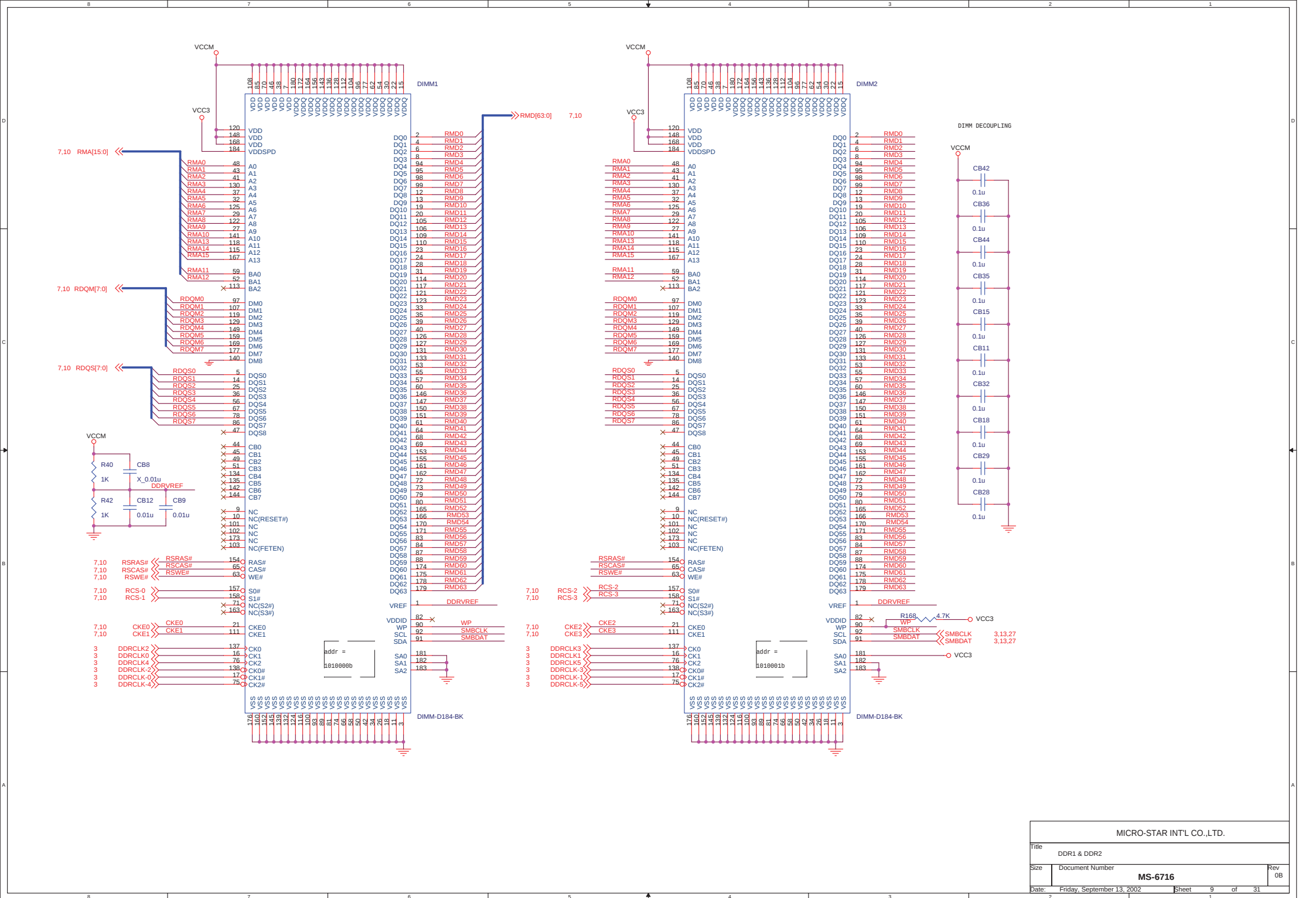


2. AGPNCOMP : AGP N-CHANNEL COMPENSATION



3. AGP POWER

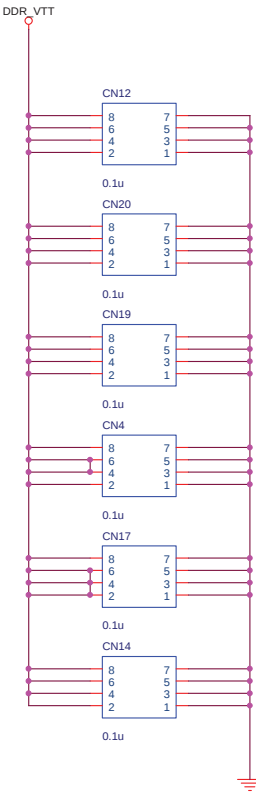
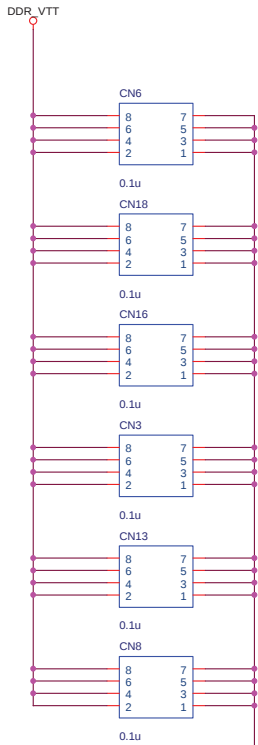
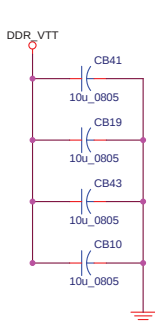
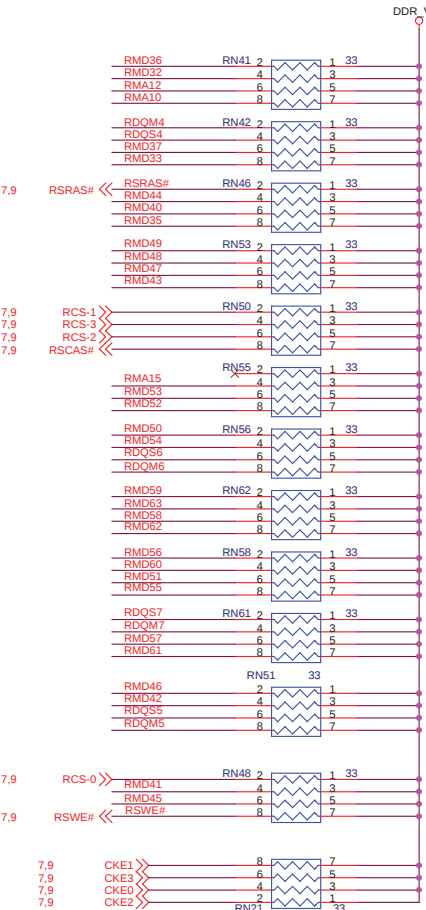
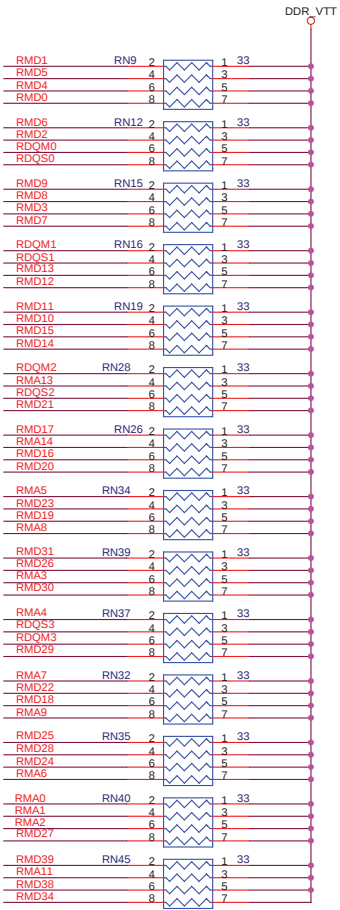


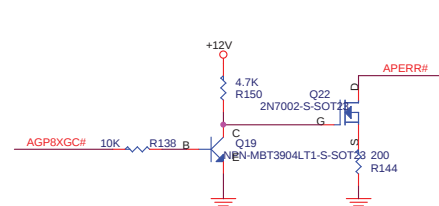
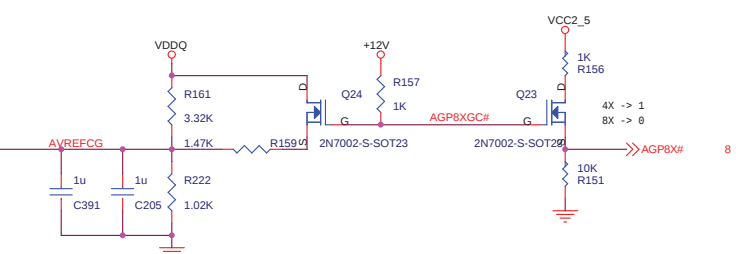
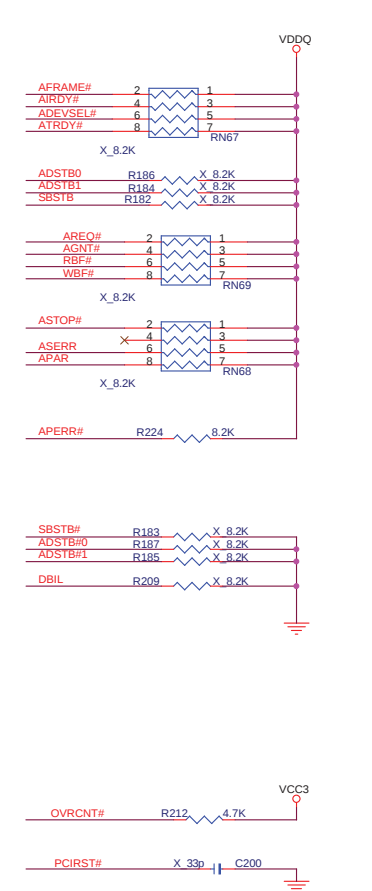
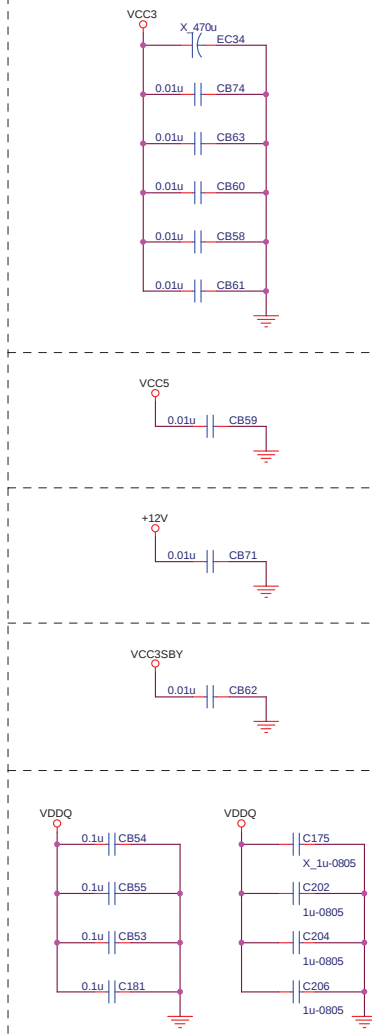


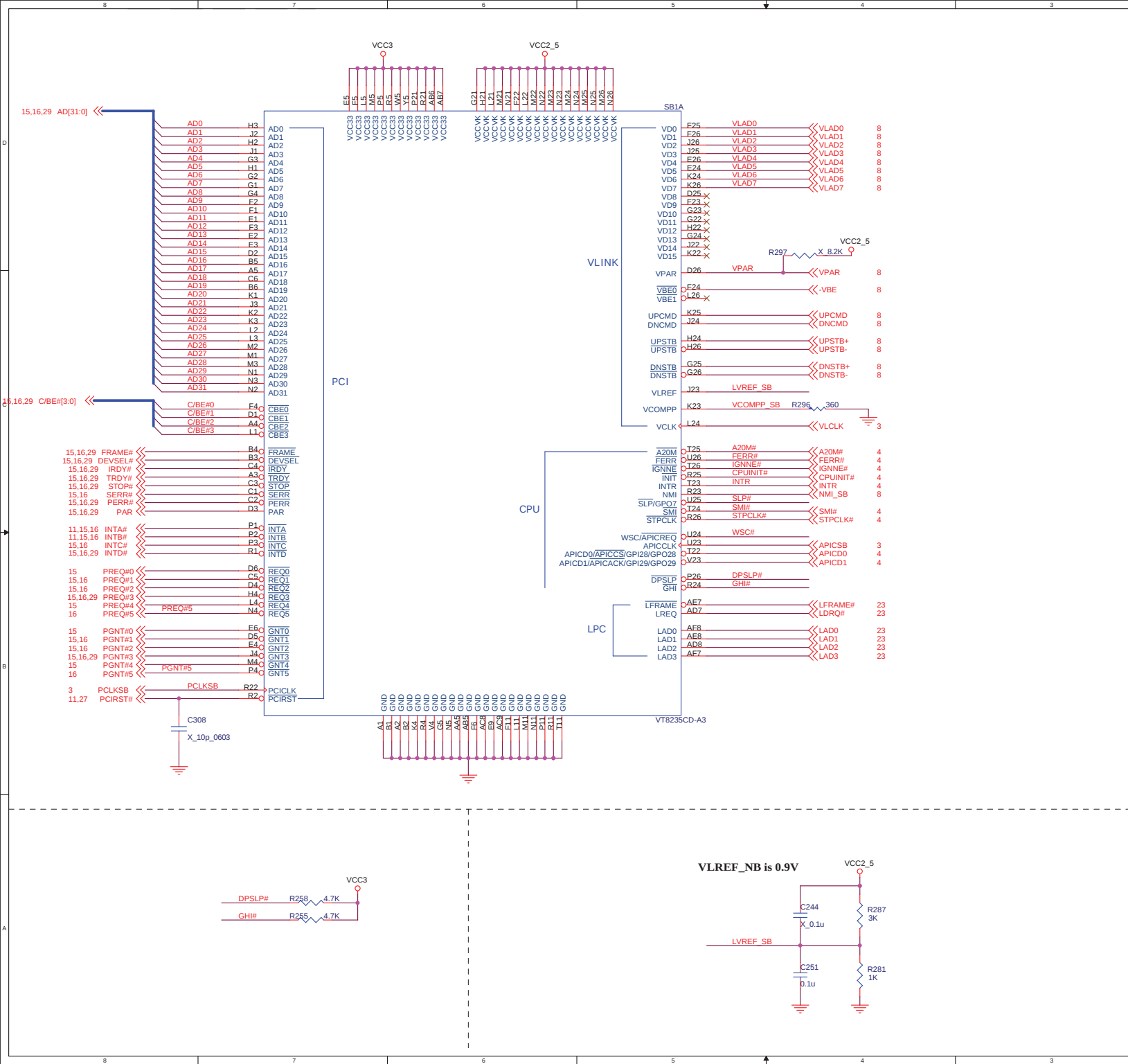
SSTL-2 Termination Resistors



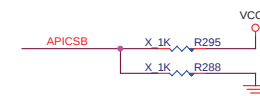
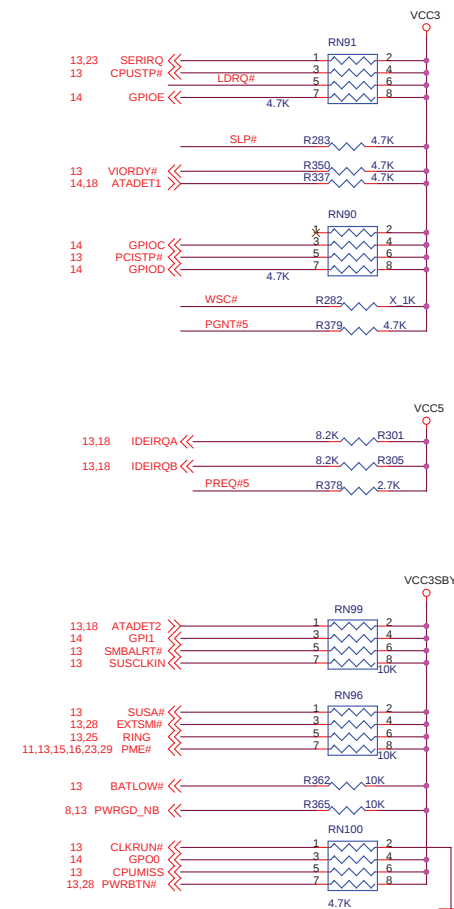
	SDR		DDR		
MD/DQM(/DQS)	LV-CMOS	8/10/-	SSTL-2	10	47
MA/Control	LV-CMOS	8	SSTL-2	10	47
CS	LV-CMOS	8	SSTL-2	10	47
CKE	DD 3.3V		DD 2.5V		



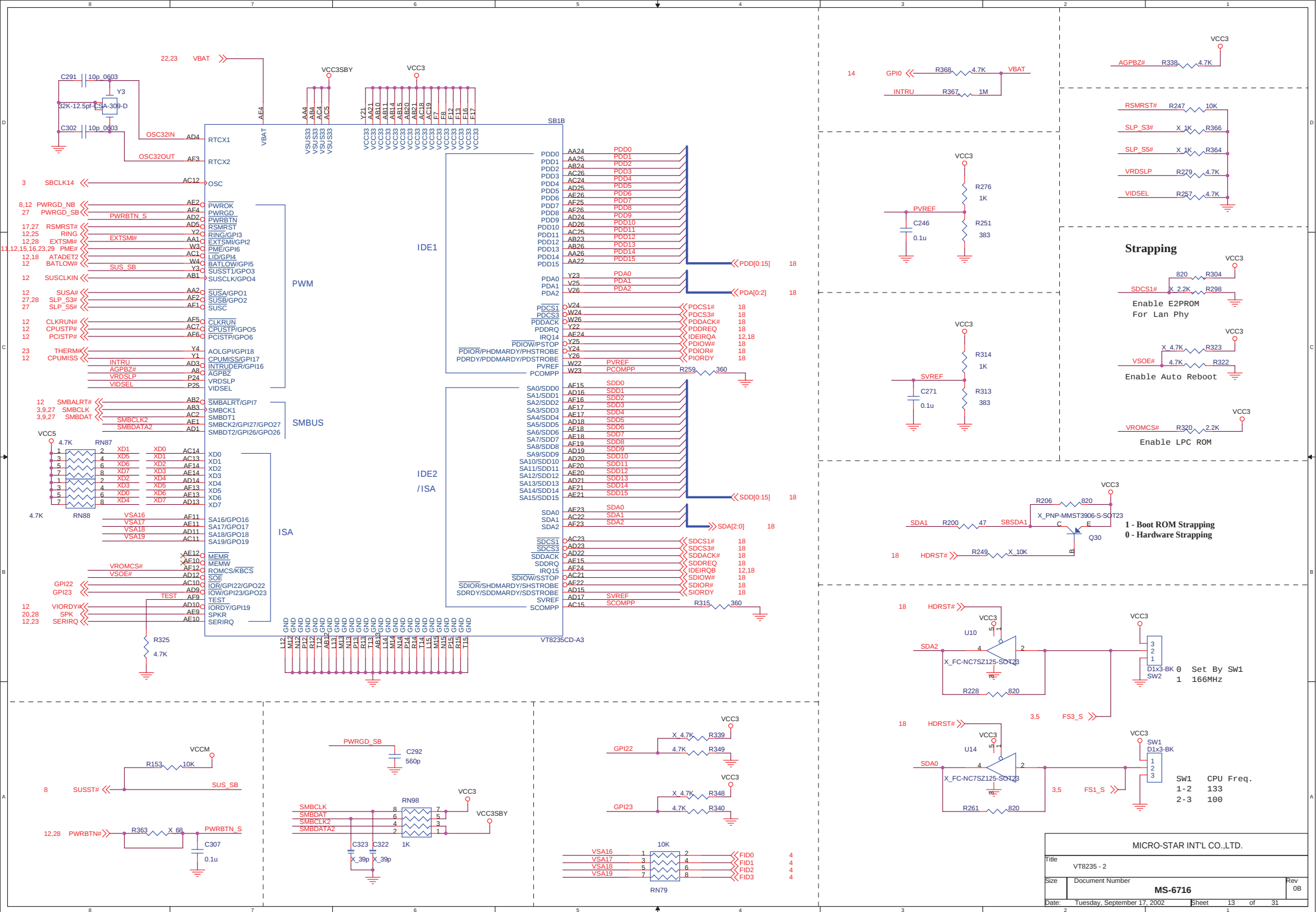




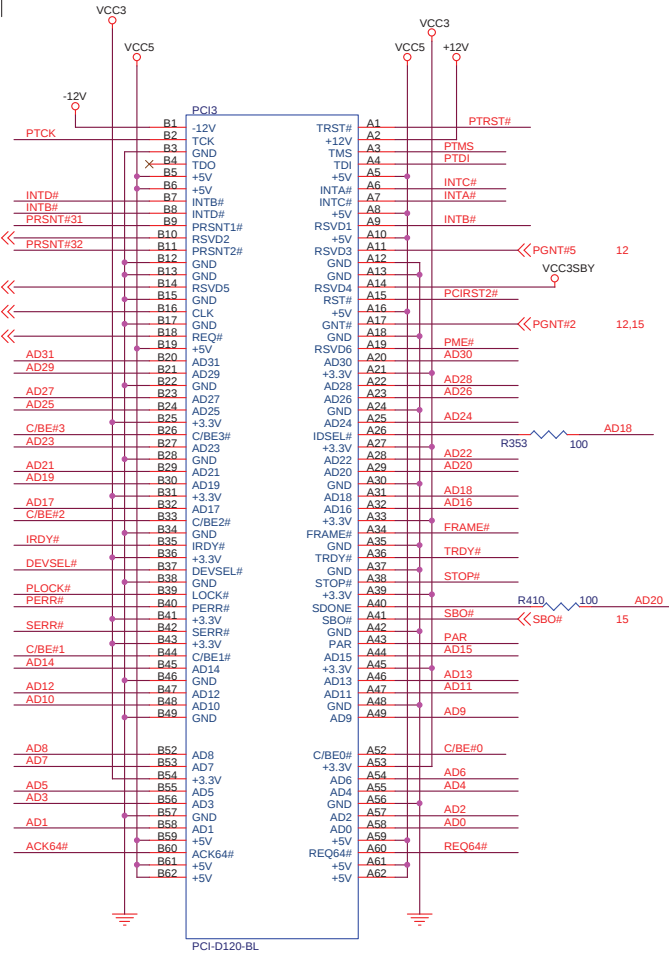
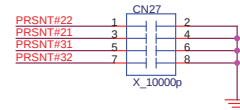
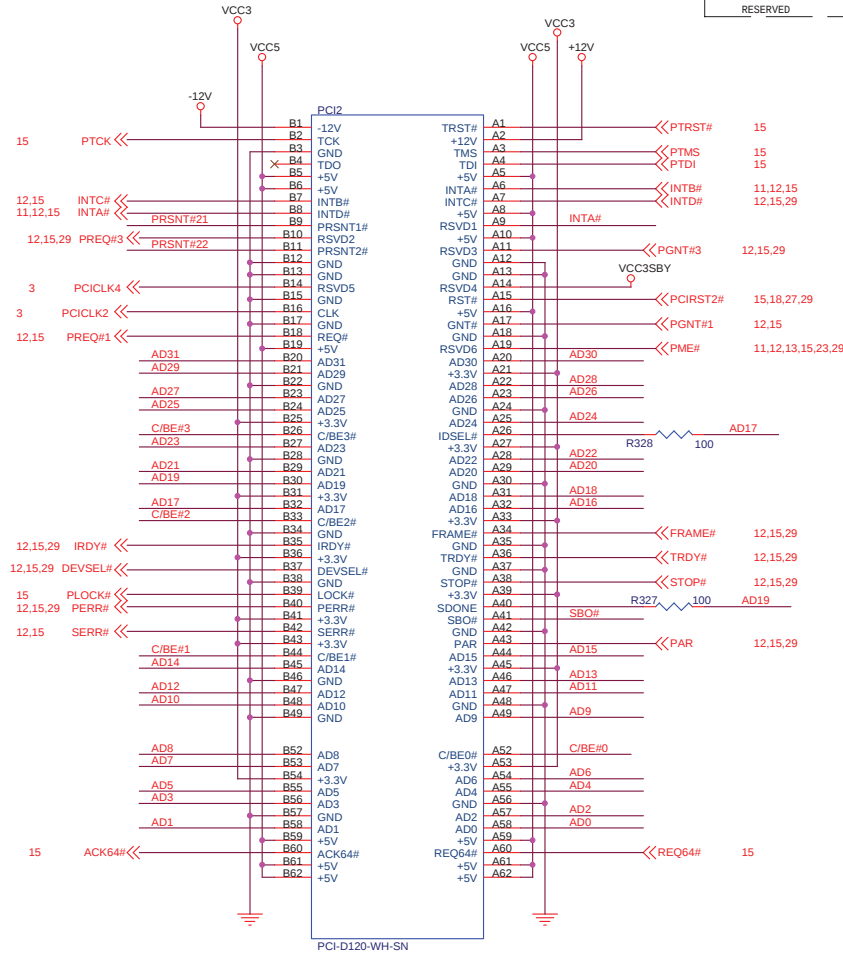
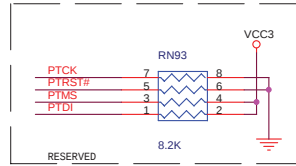
SB Pull up resistors



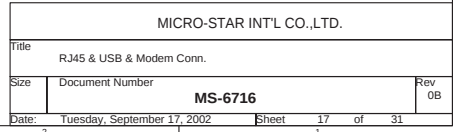
MICRO-STAR INT'L CO.,LTD.			
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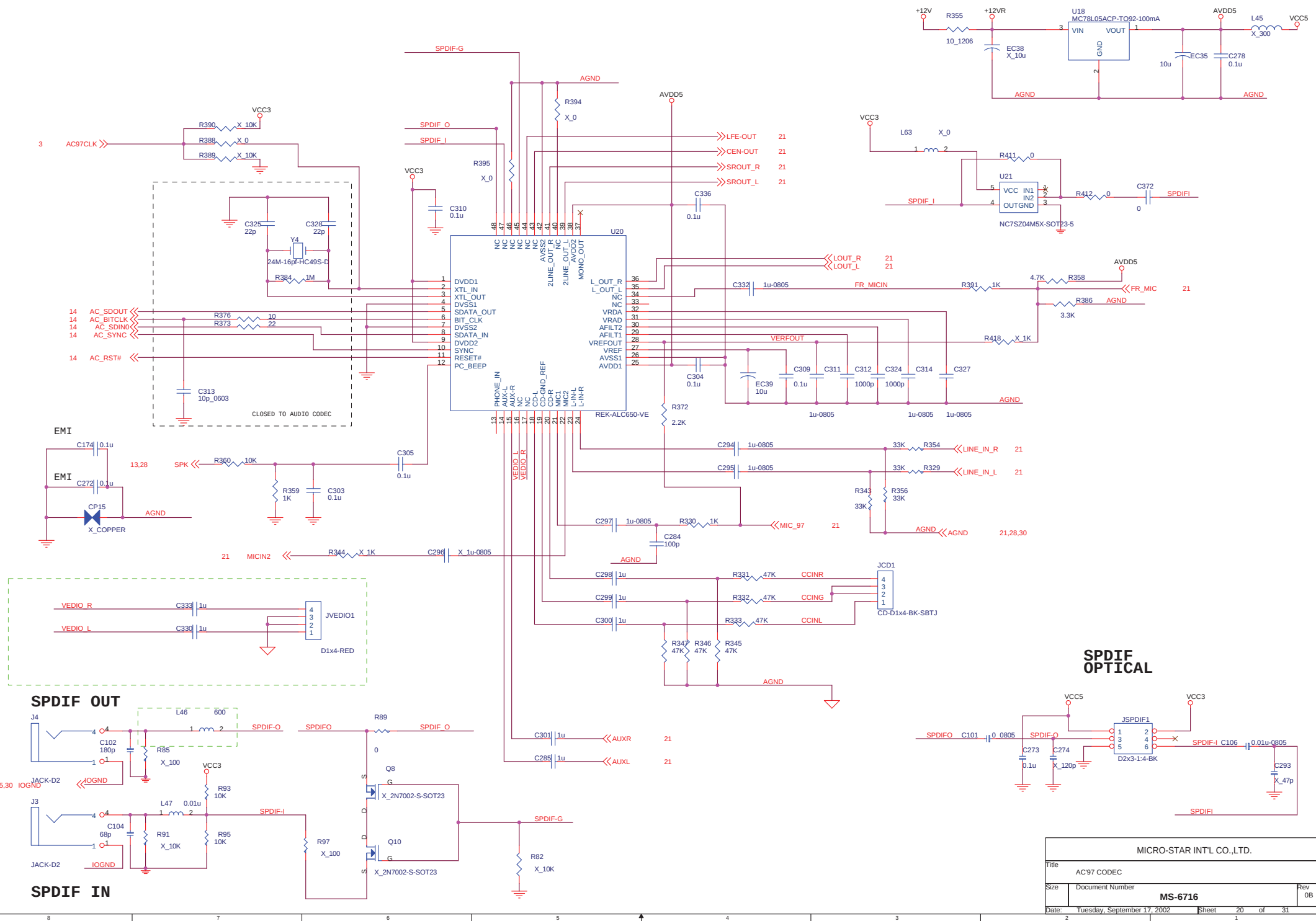


12,15,29 AD[31:0] <<
12,15,29 C/BE#[3:0] <<

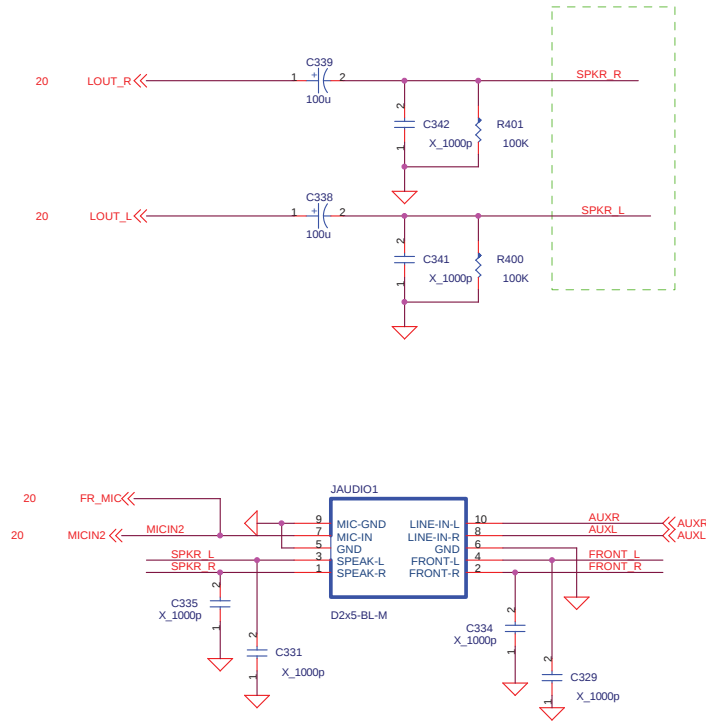


7/17 modify





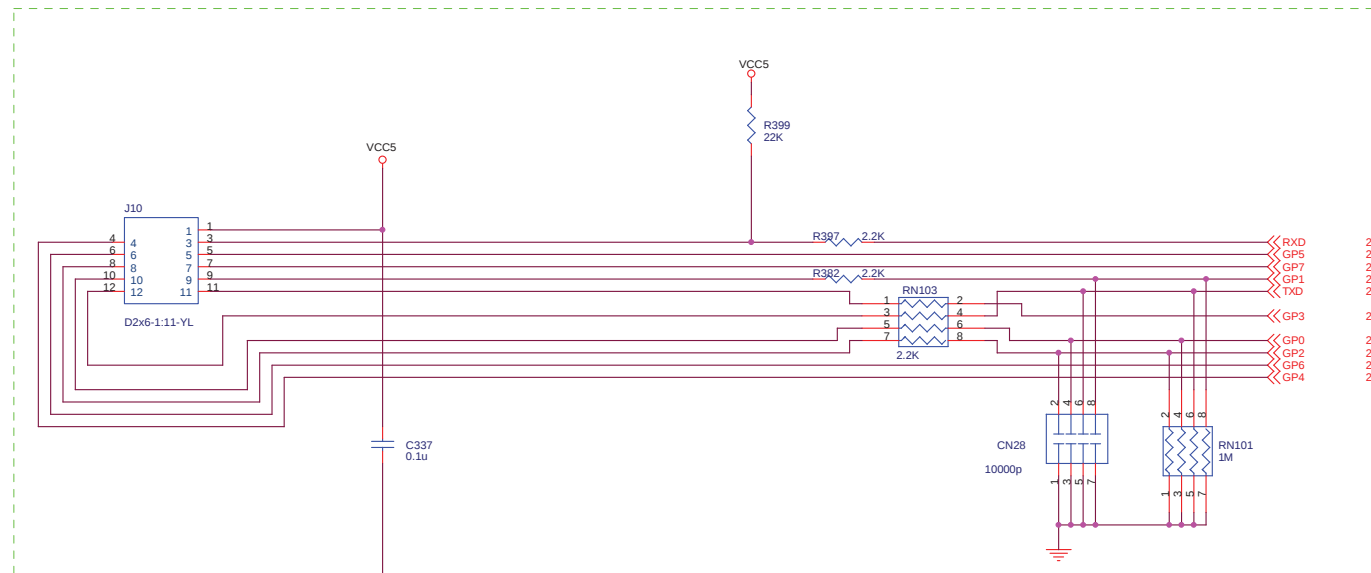
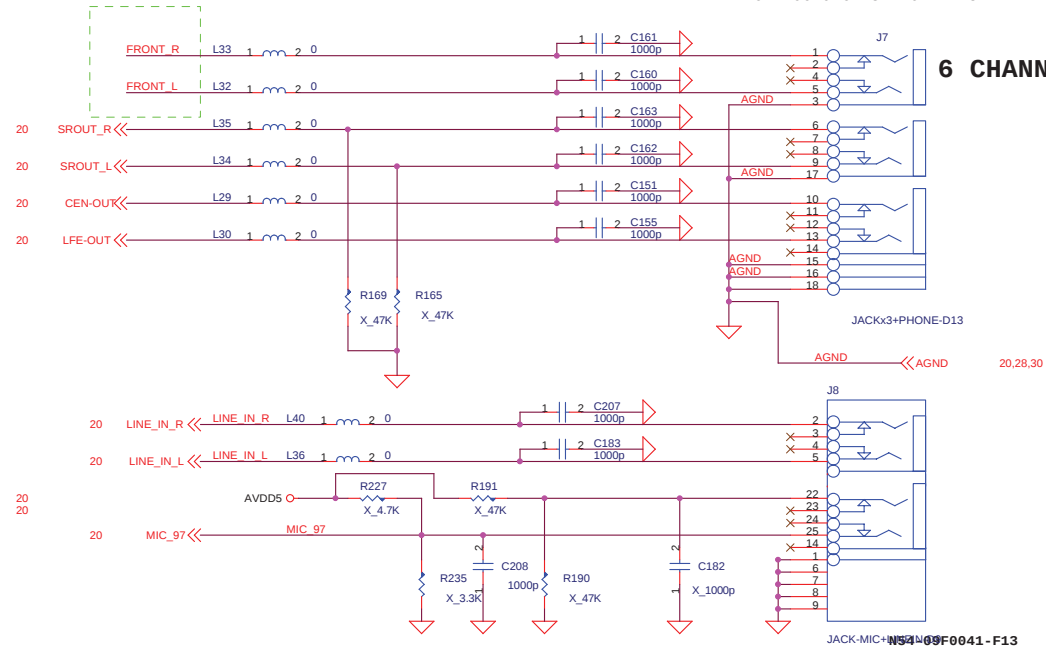
SPEAKER OUT CIRCUIT



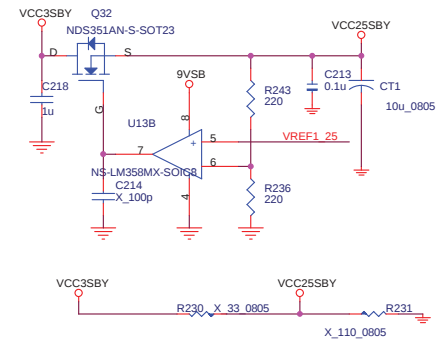
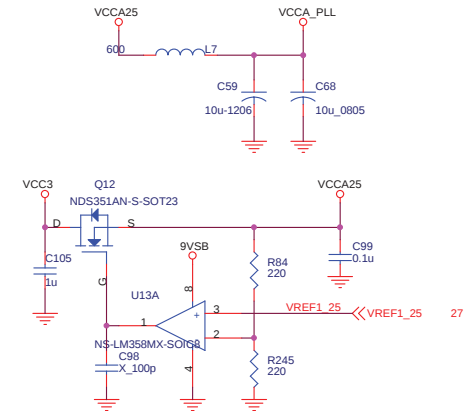
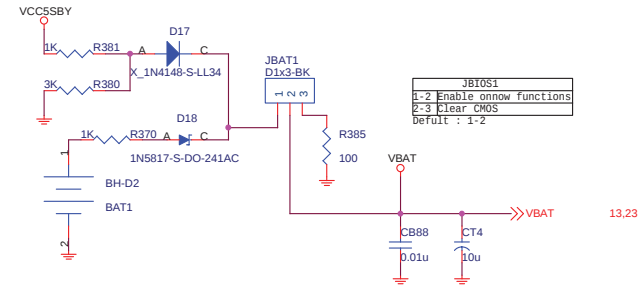
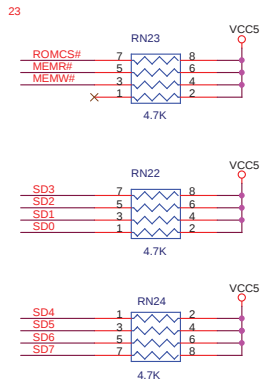
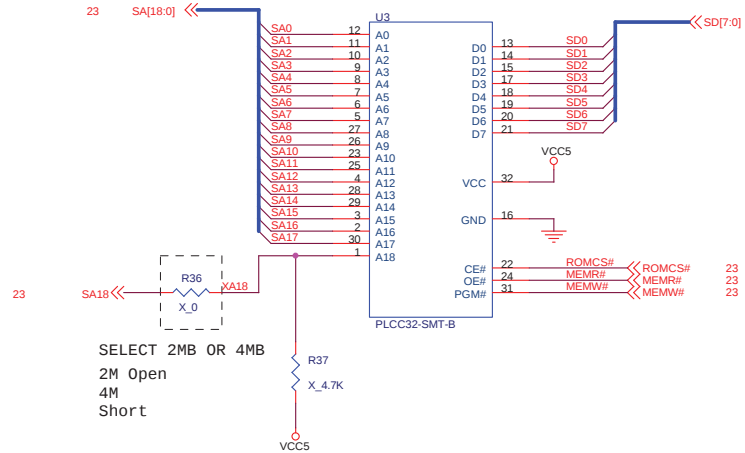
Audio Connector

N54-13F0011-K06
CN-IOC-JACKx3+PHONE-D13

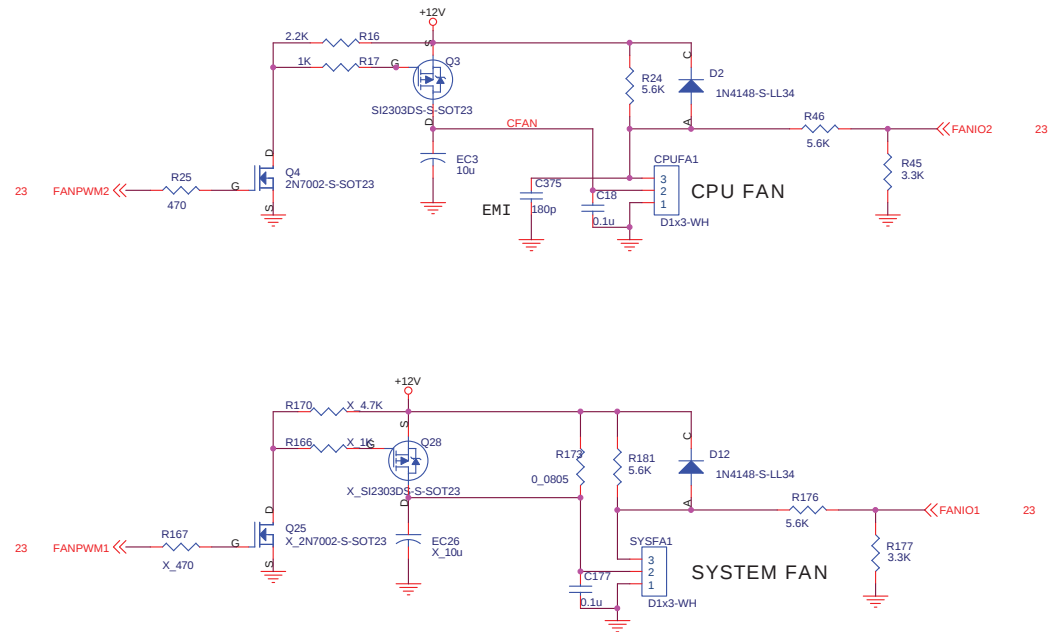
6 CHANNEL



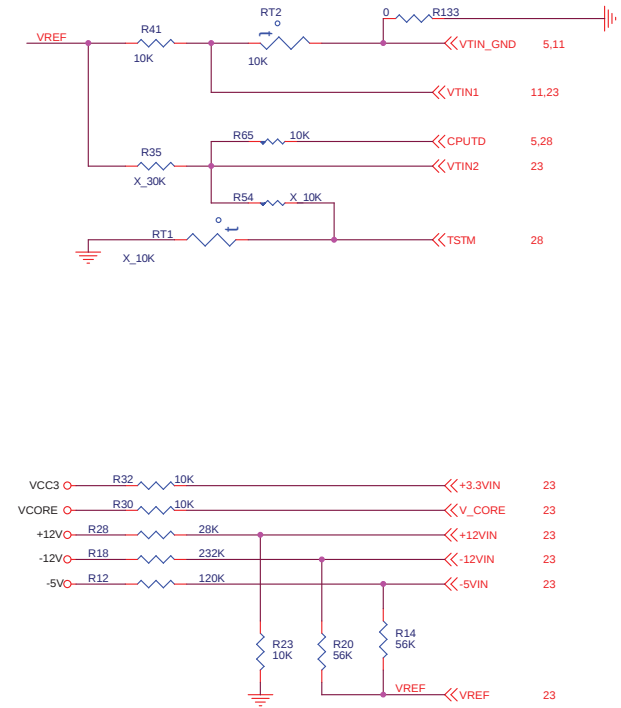
MICRO-STAR INT'L CO.,LTD.			
Title		AUDIO CONNECTOR	
Size	Document Number	MS-6716	
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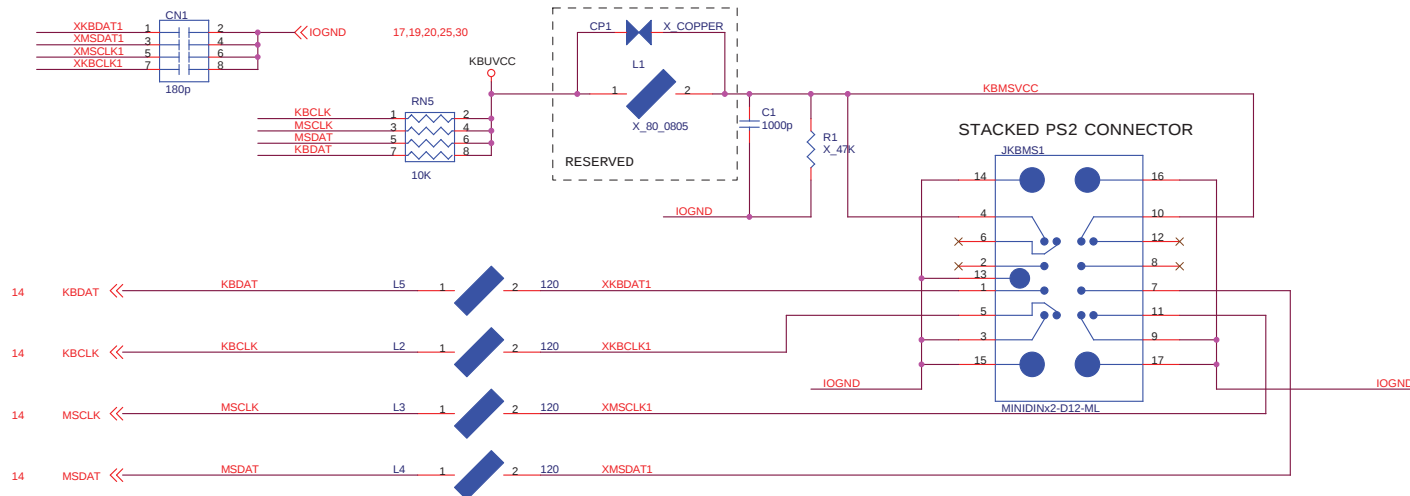
Fan



Hardware Monitor



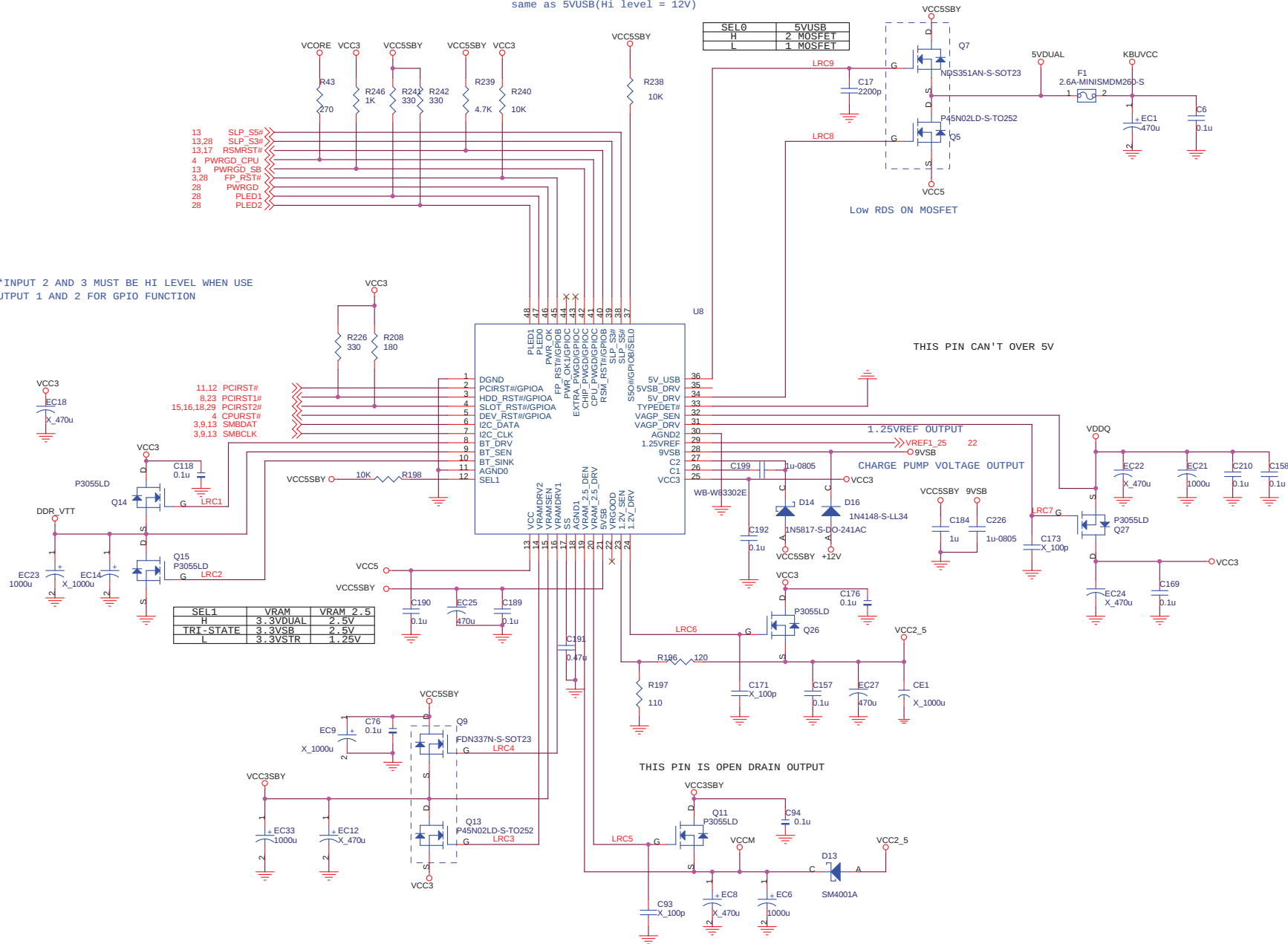
KEYBOARD/MOUSE PORTS



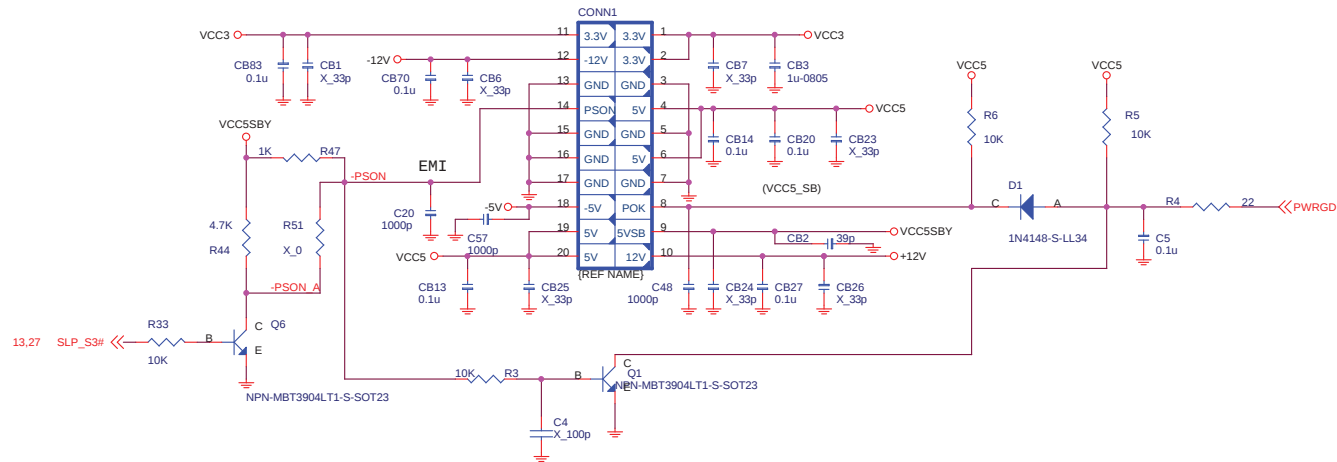
**S50# pin function(Hi level = 5V)
same as 5VUSB(Hi level = 12V)

SEL0	5VUSB
H	2 MOSFET
L	1 MOSFET

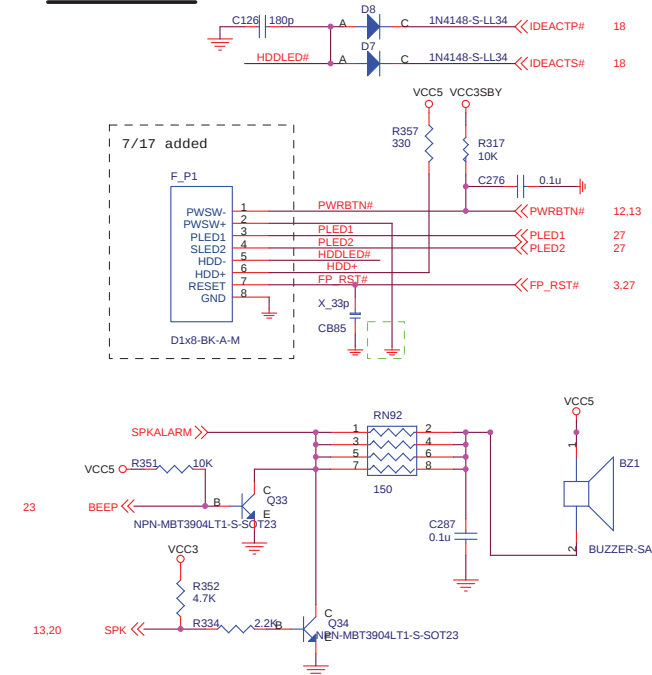
**INPUT 2 AND 3 MUST BE HI LEVEL WHEN USE
OUTPUT 1 AND 2 FOR GPIO FUNCTION



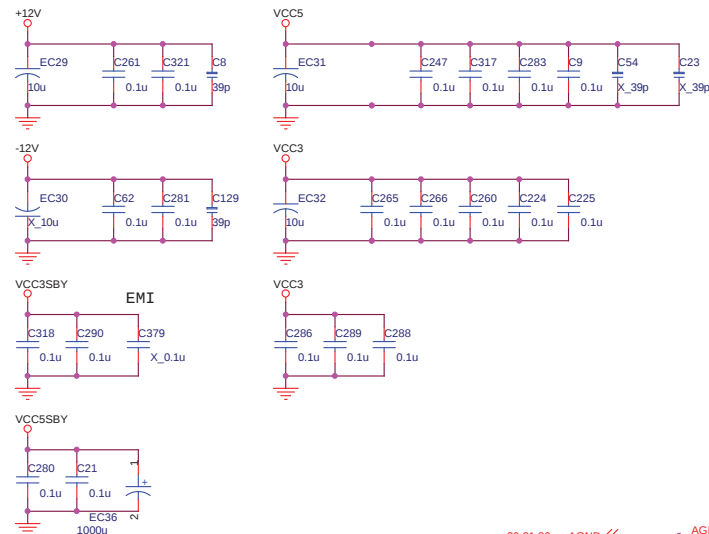
ATX CONNECTOR



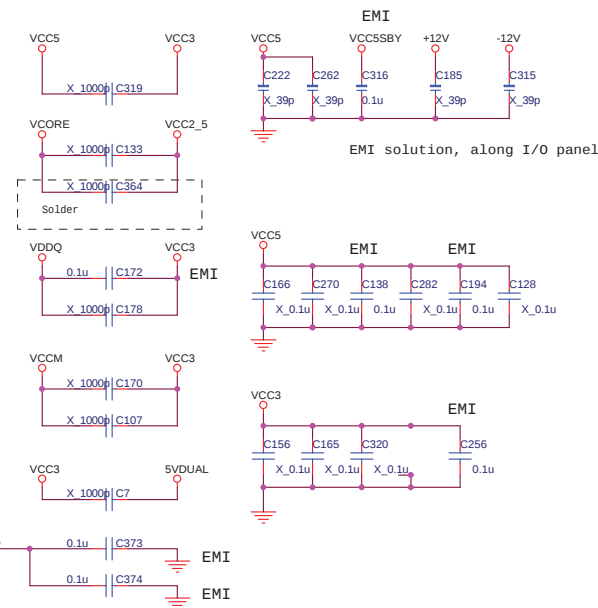
FRONT PANEL-M



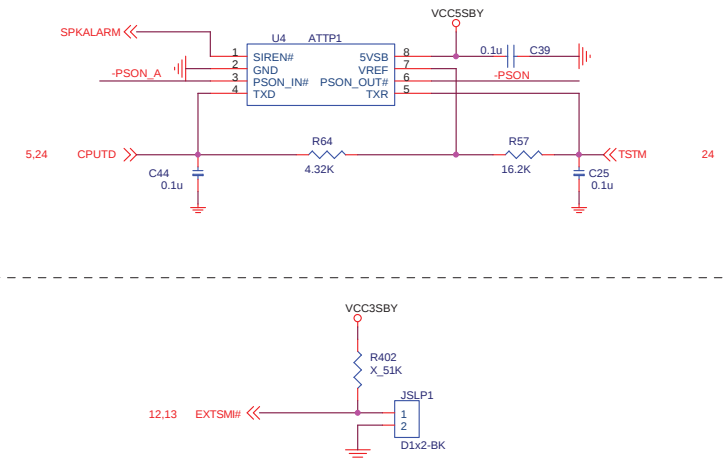
System Decoupling Capacitors



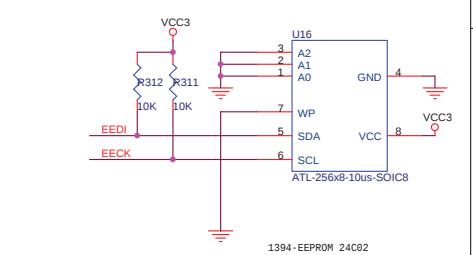
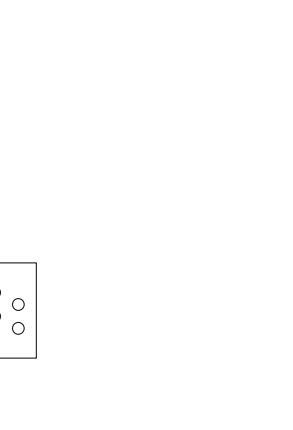
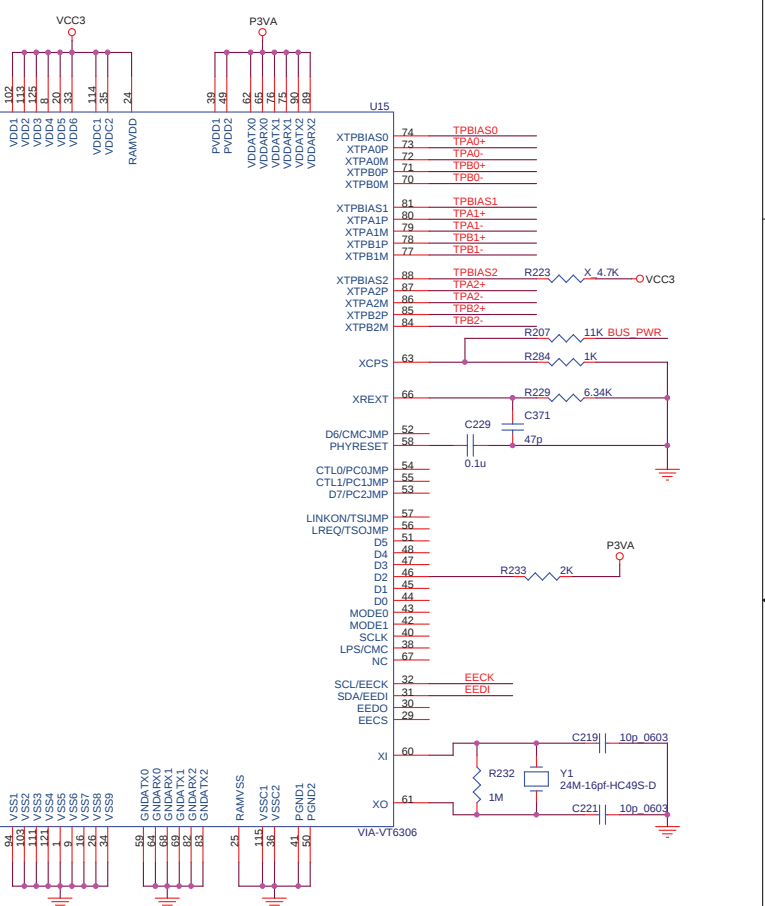
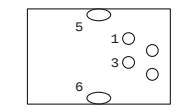
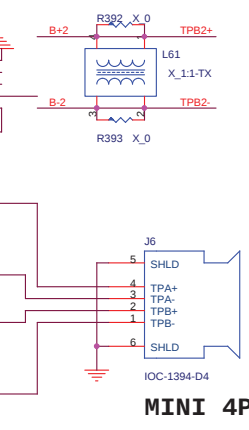
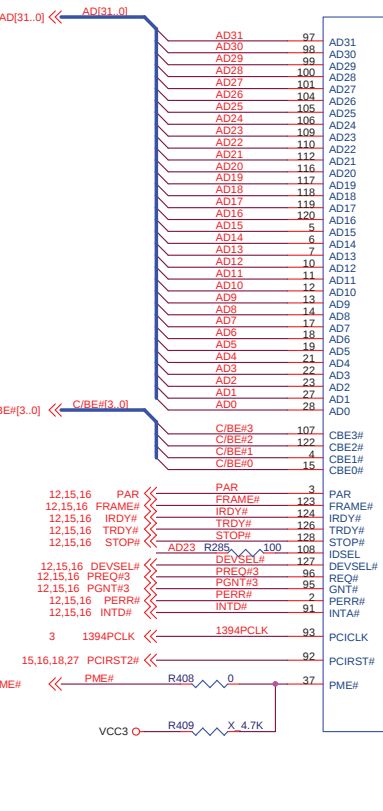
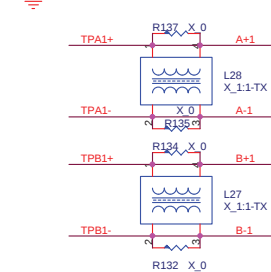
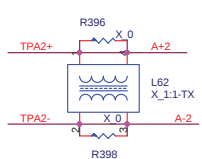
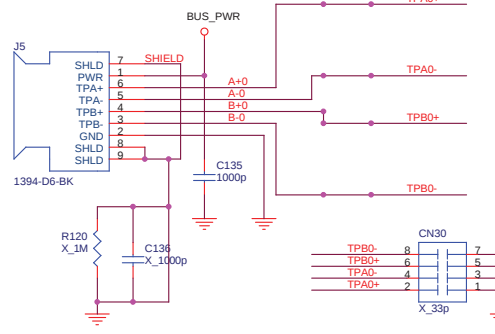
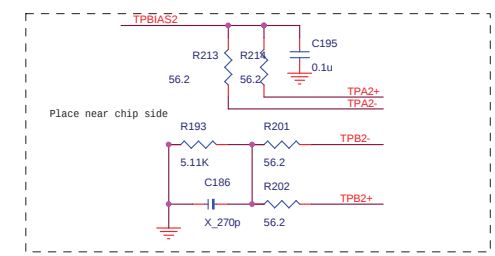
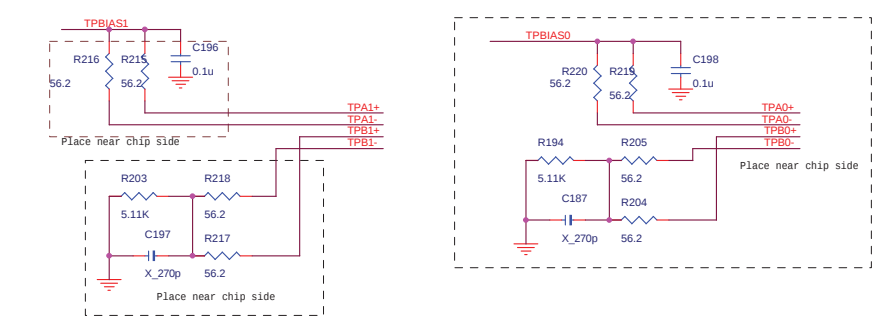
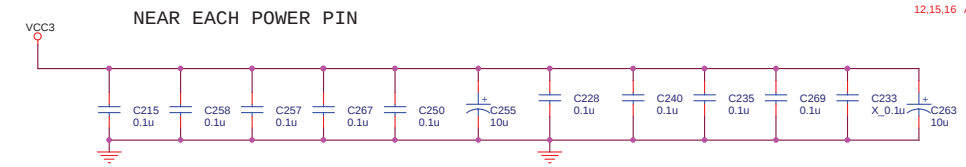
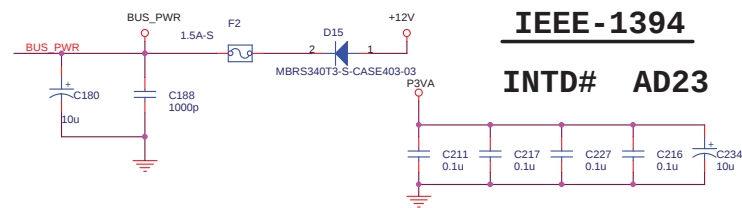
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Therm Protection

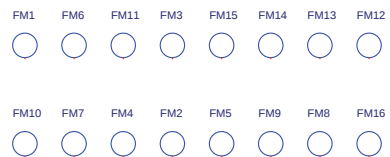
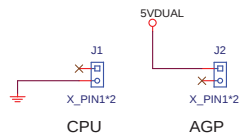
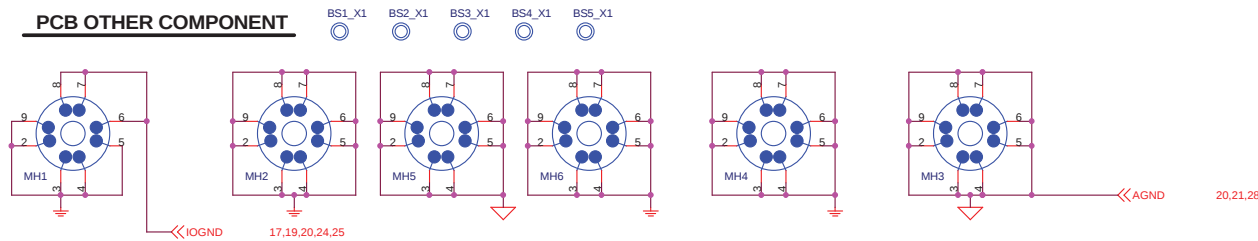


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