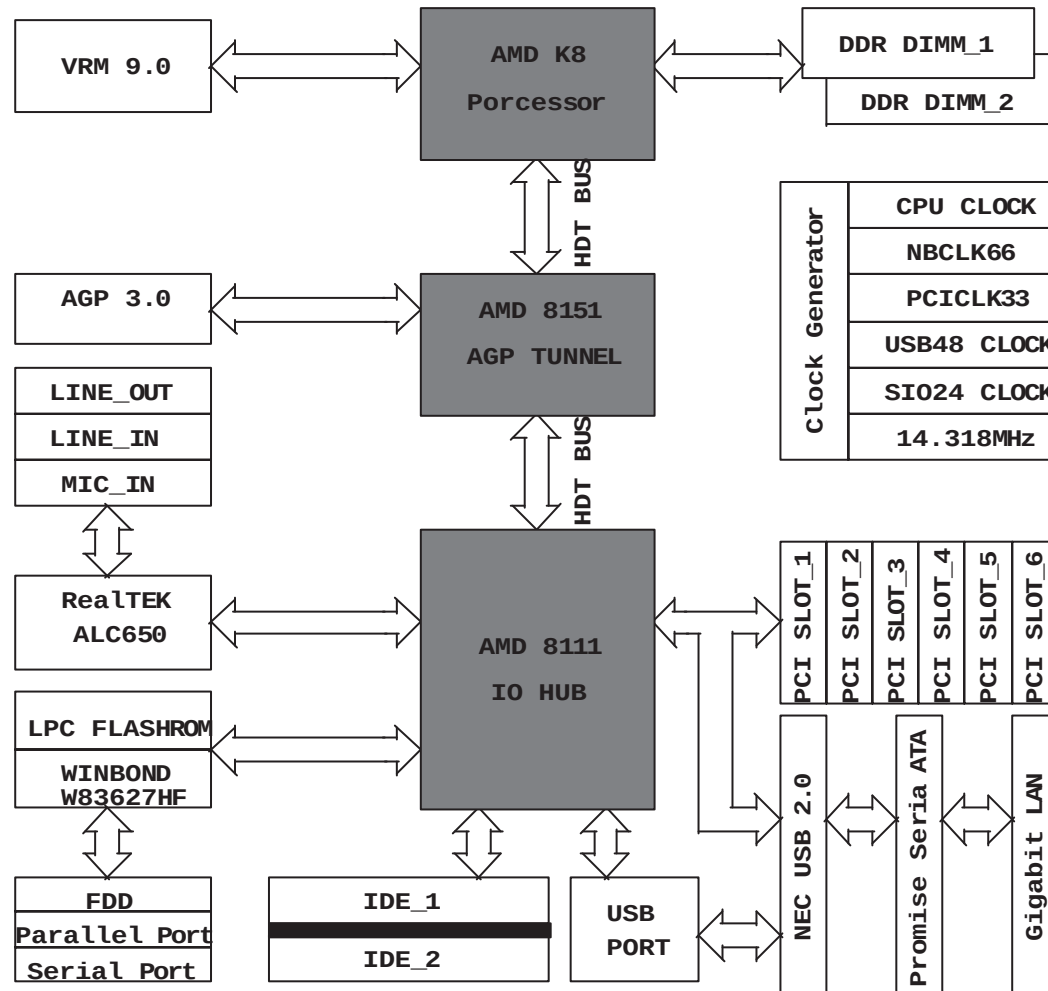




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- 01-Block Diagram
- 02-Power Div & Specification
- 03-AMD K8 Core power
- 04-Clock Generator
- 05-Hyper Transport Link_0 Part_1
- 06-Hyper Transport Link_0 Part_2
- 07-Memory damping & Keyboard/Mouse
- 08-DDR DIMM_1 & DIMM_2
- 09-DDR DIMM Terminator
- 10-Hyper Transport Link_1 Part_1
- 11-Hyper Transport Link_1 Part_2 AGP SLOT
- 12-Hyper Transport IO_HUB Part_1
- 13-Hyper Transport IO_HUB Part_2
- 14-Hyper Transport IO_HUB Part_3 USB Port
- 15-LPC Flashrom & MS_1
- 16-IDE Connector
- 17-PCI Slot_1 & 2
- 18-PCI Slot_3 & 4
- 19-PCI Slot_5 & 6
- 20-RealTEK ALC650 AC97 Codec
- 21-NEC USB20 Controller
- 22-Gigabit LAN Controller
- 24-Serial ATA Controller
- 25-W83627HF SIO & H/W Monitor
- 26-Parallel & Serial Port
- 27-System Power Part_1
- 28-System Power Part_2
- 29-ATX Power & Front Panel

Specification description:

Processor: AMD K8

System chipset: AMD 8151 + AMD 8111

System power: Inter_sil ISL6569CB

Clock generator: Cypress CY28311

Super IO: Winbond W83627HF

Flash Rom: LPC interface

On board AGP slot: 1

On board PCI slot: 6

On board Gigabit LAN: 1

On board USB_20: 1

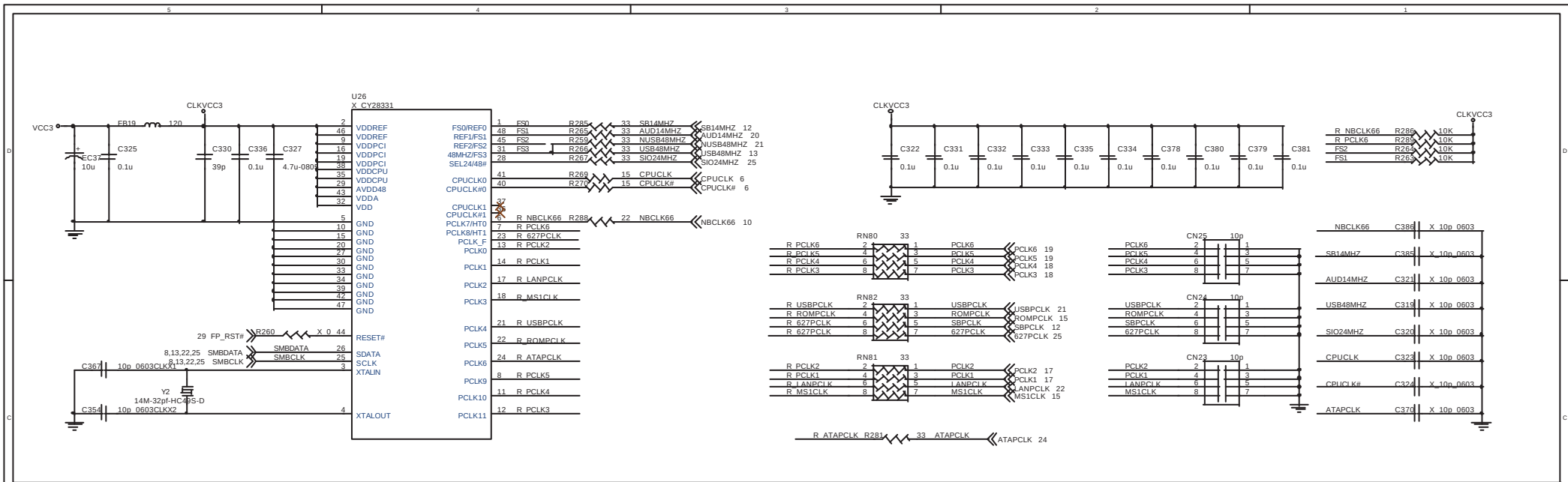
On board serial ATA & Raid: 1

System Audio: RealTEK ALC650 AC97 Codec

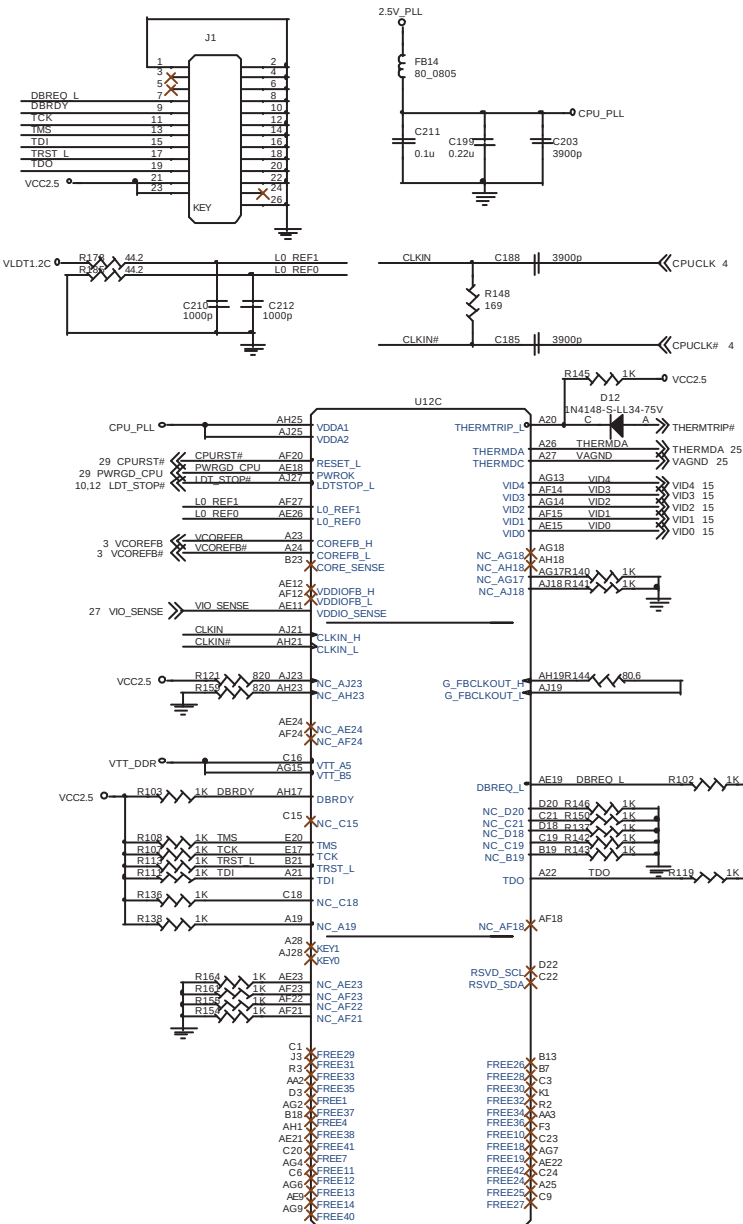
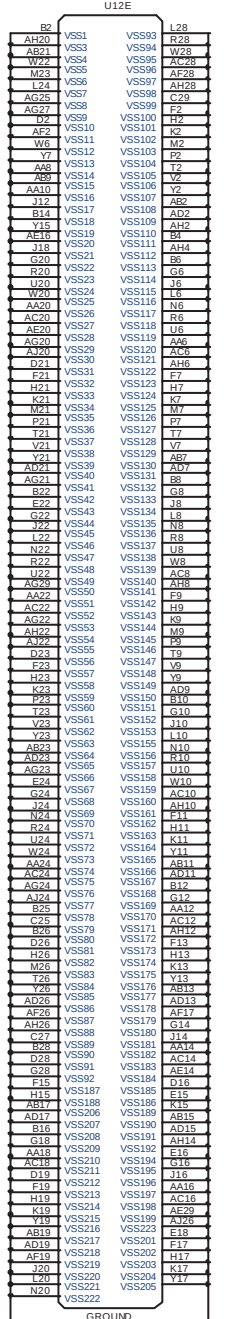
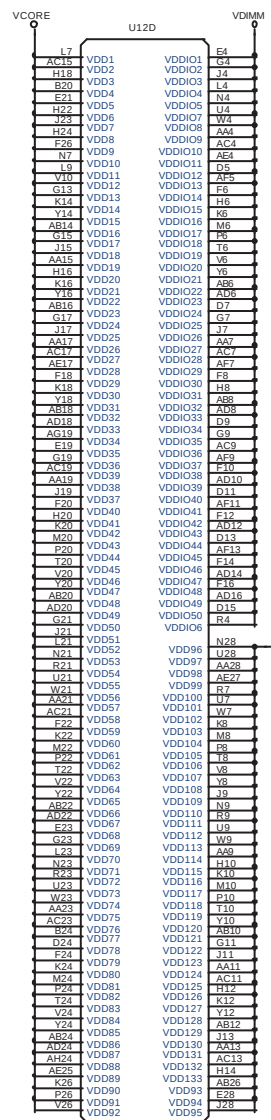
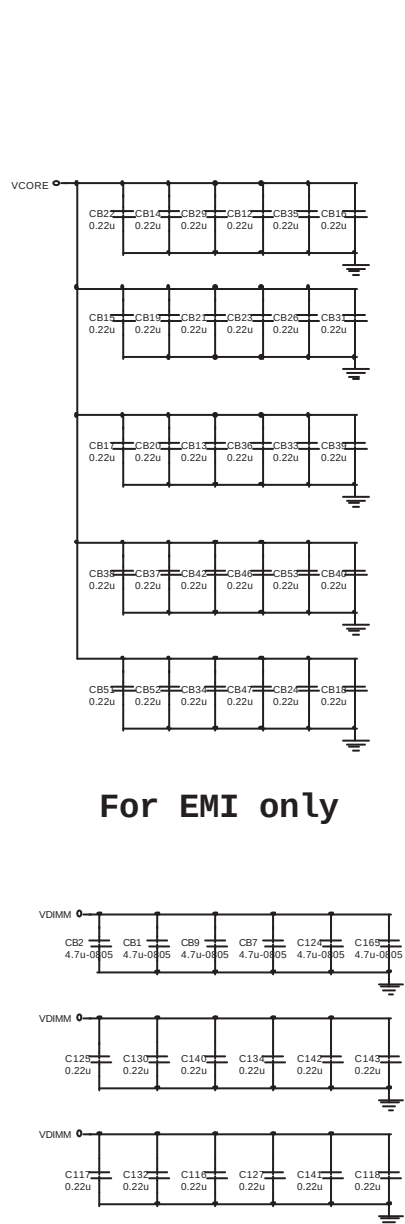
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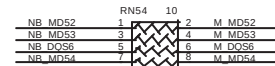
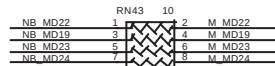
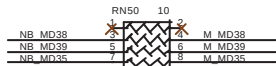
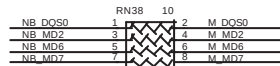
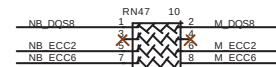
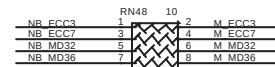
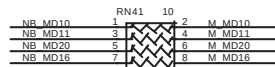
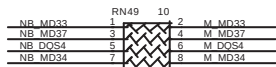
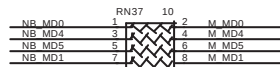
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PCI slot_4	AD19 PIRQ#D	PIRQ#A	PIRQ#B	PIRQ#C
PCI slot_5	AD20 PIRQ#A	PIRQ#B	PIRQ#C	PIRQ#D
PCI slot_6	AD21 PIRQ#B	PIRQ#C	PIRQ#D	PIRQ#A
PCI LAN	AD24 PIRQ#D			
USB_20	AD23 PIRQ#A	PIRQ#B	PIRQ#C	
Serial ATA	AD22 PIRQ#C			

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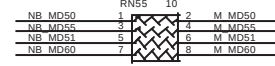
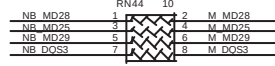
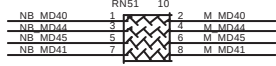
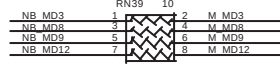




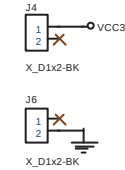
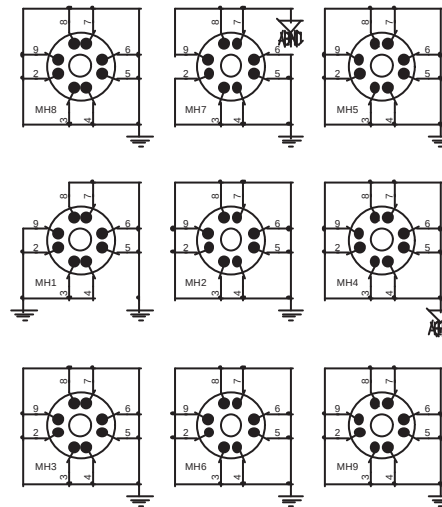
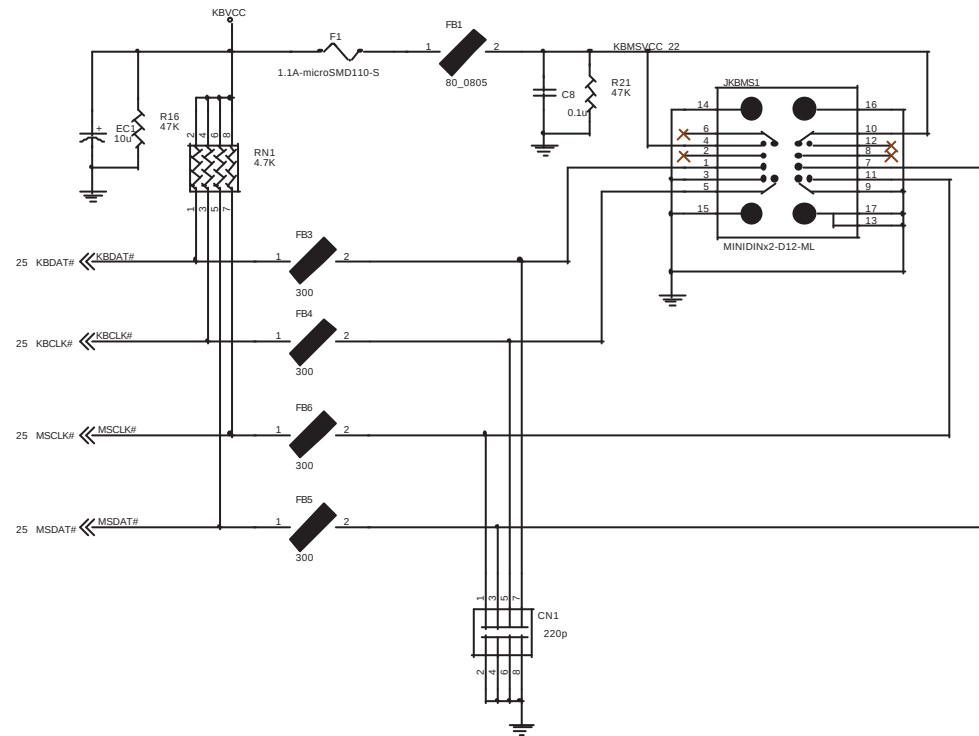
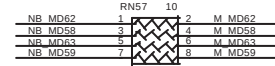
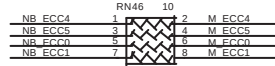
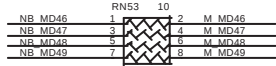
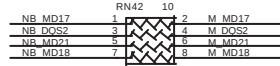
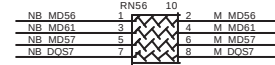
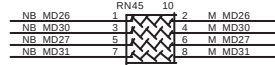
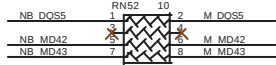
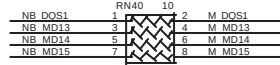


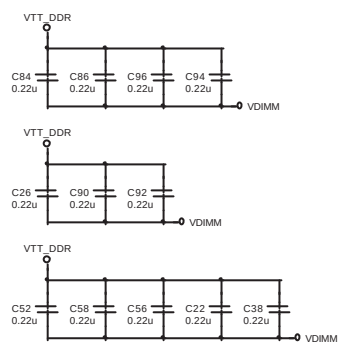
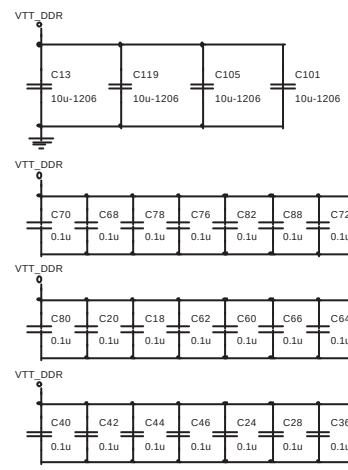
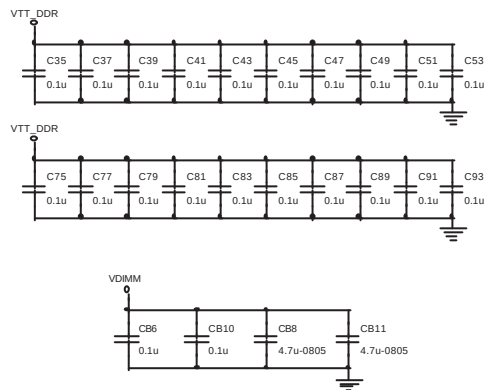
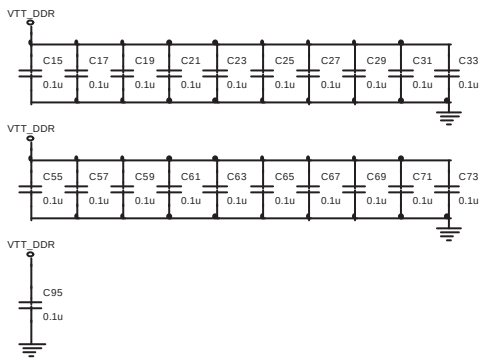
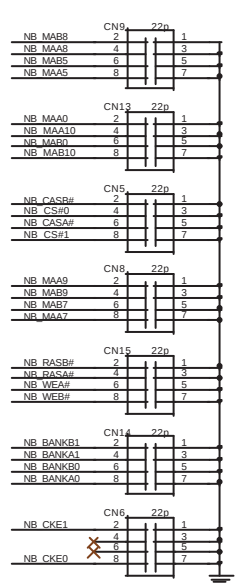
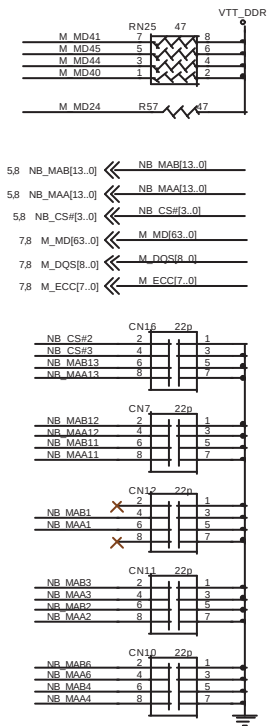
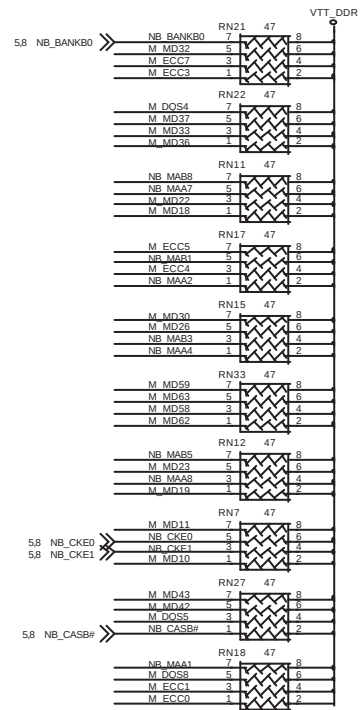
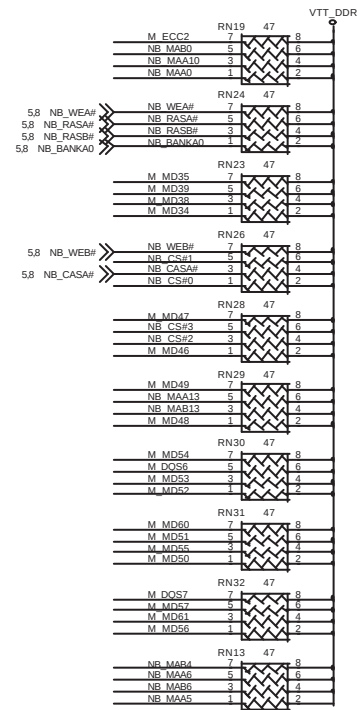
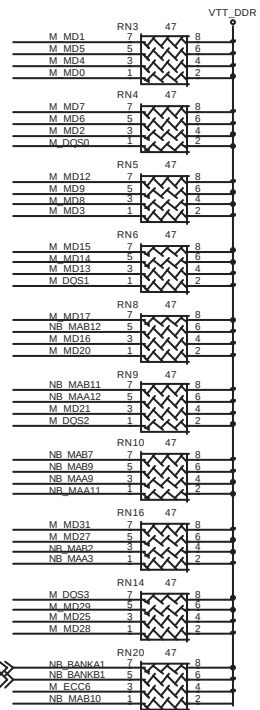


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 5 NB_DQS[8..0] <= NB_DQS[8..0]
 5 NB_ECC[7..0] <= NB_ECC[7..0]

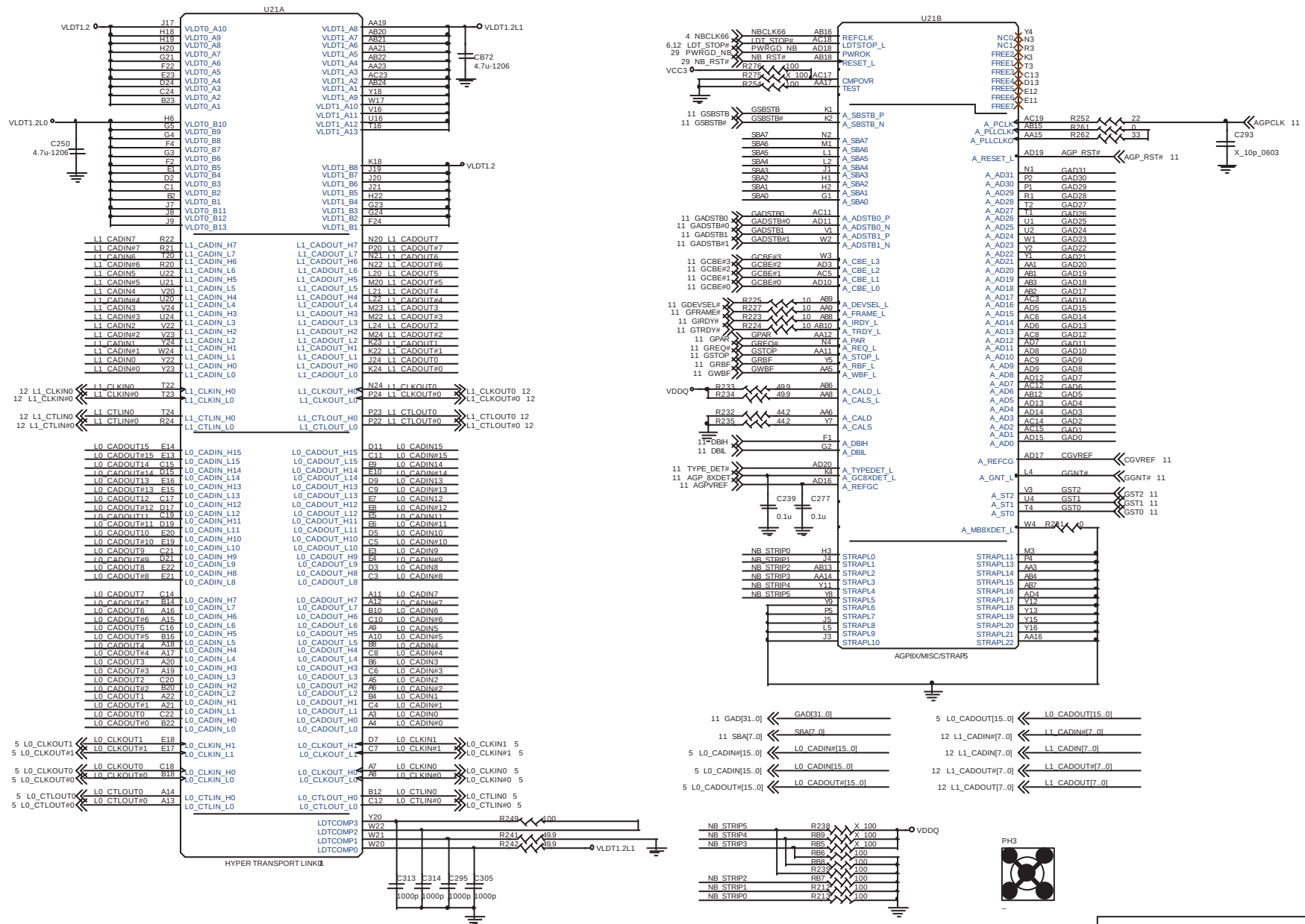


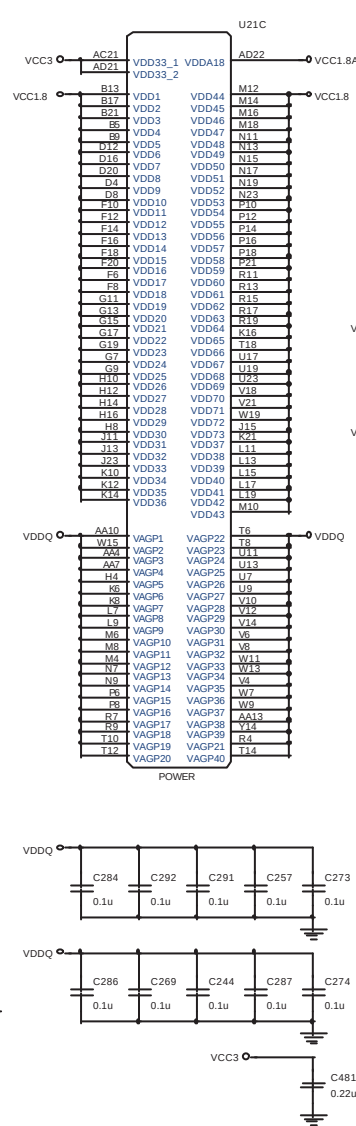
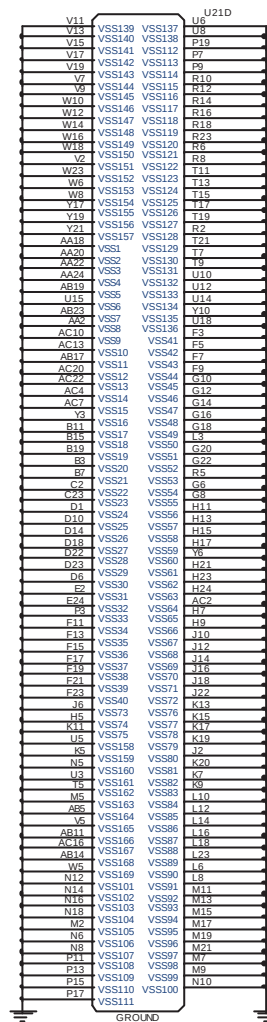
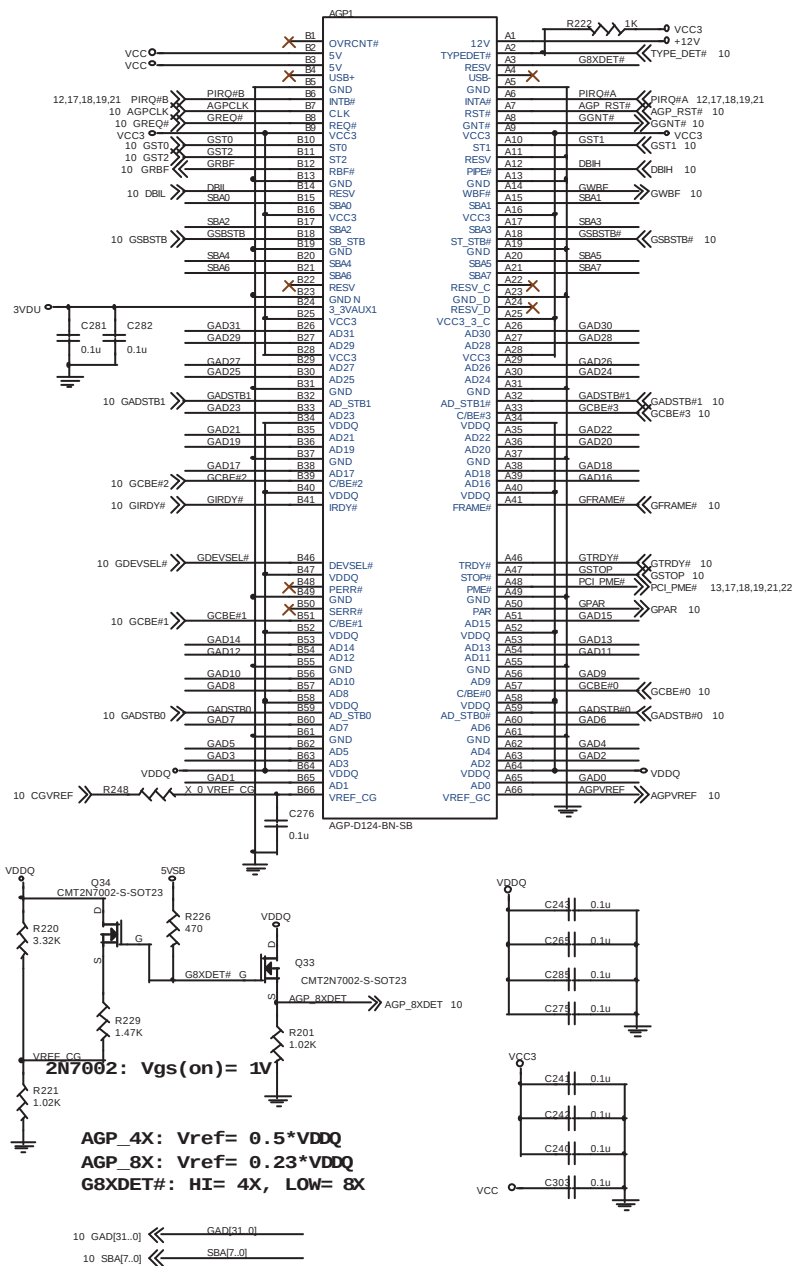
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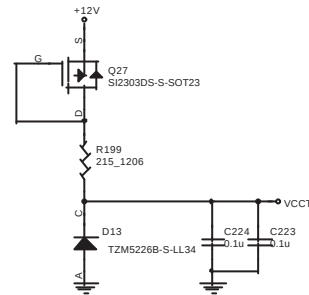
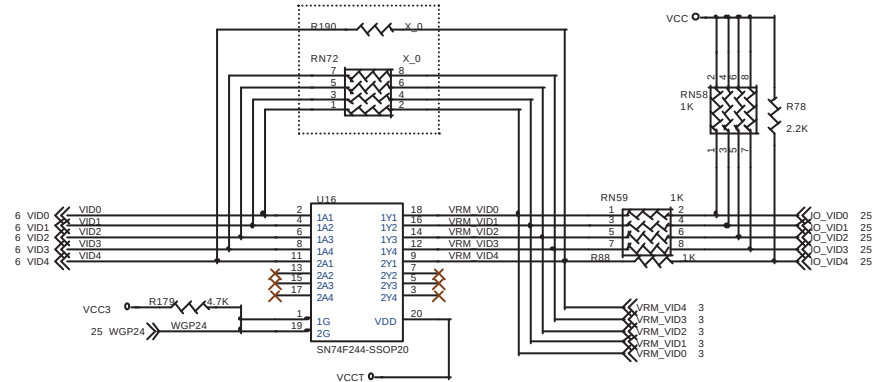
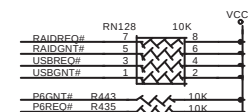
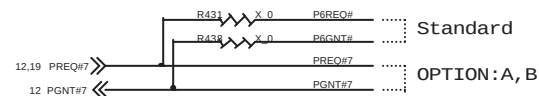
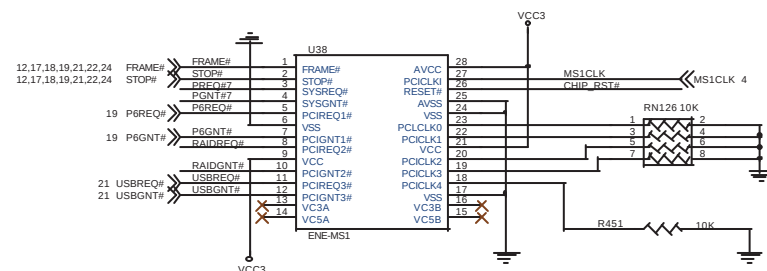
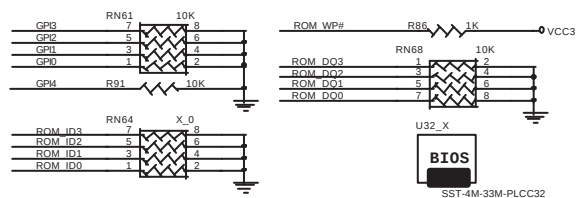
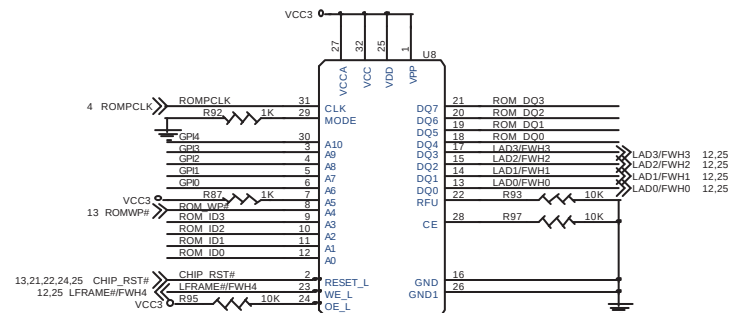




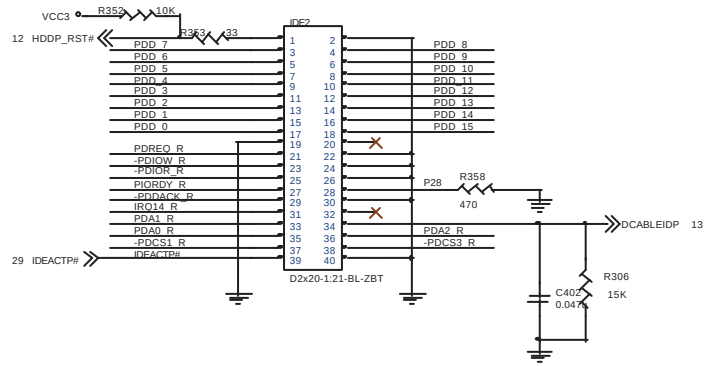
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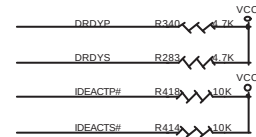
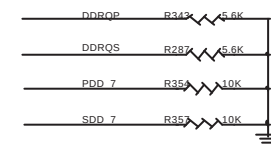
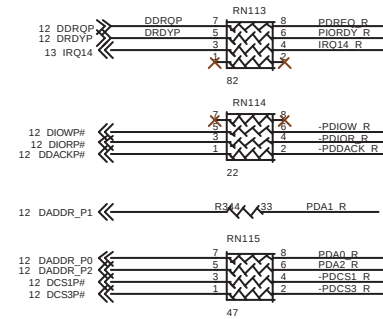
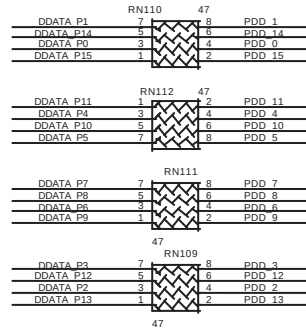




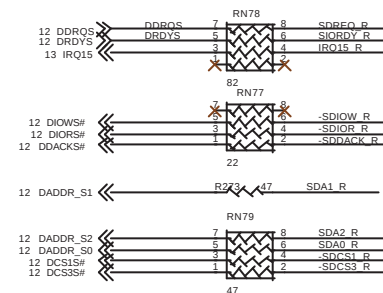
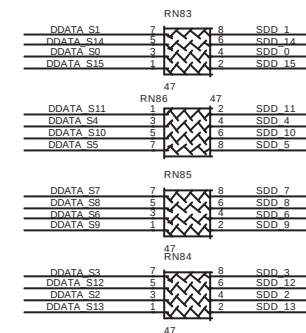
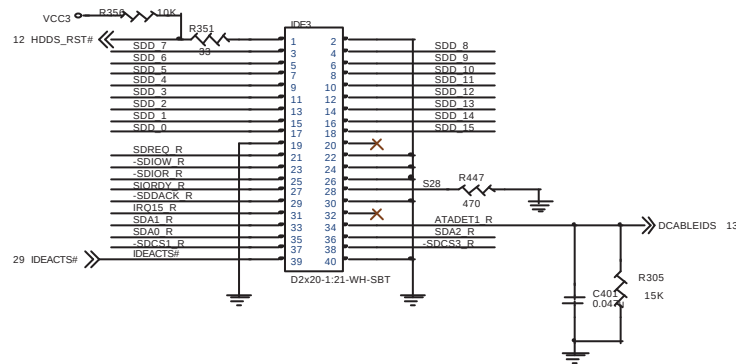
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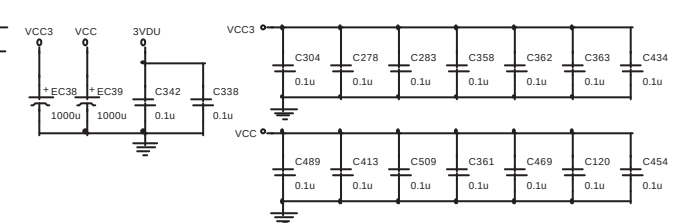
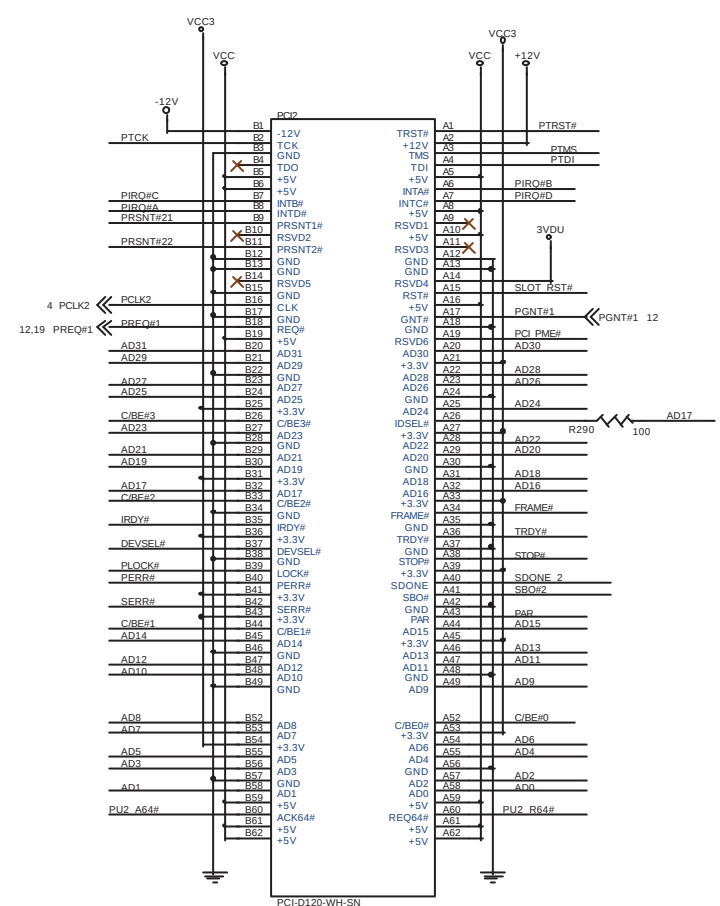
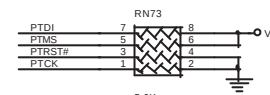
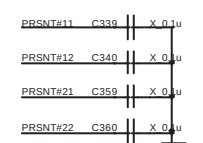
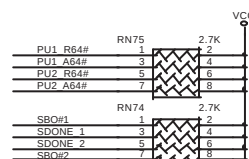
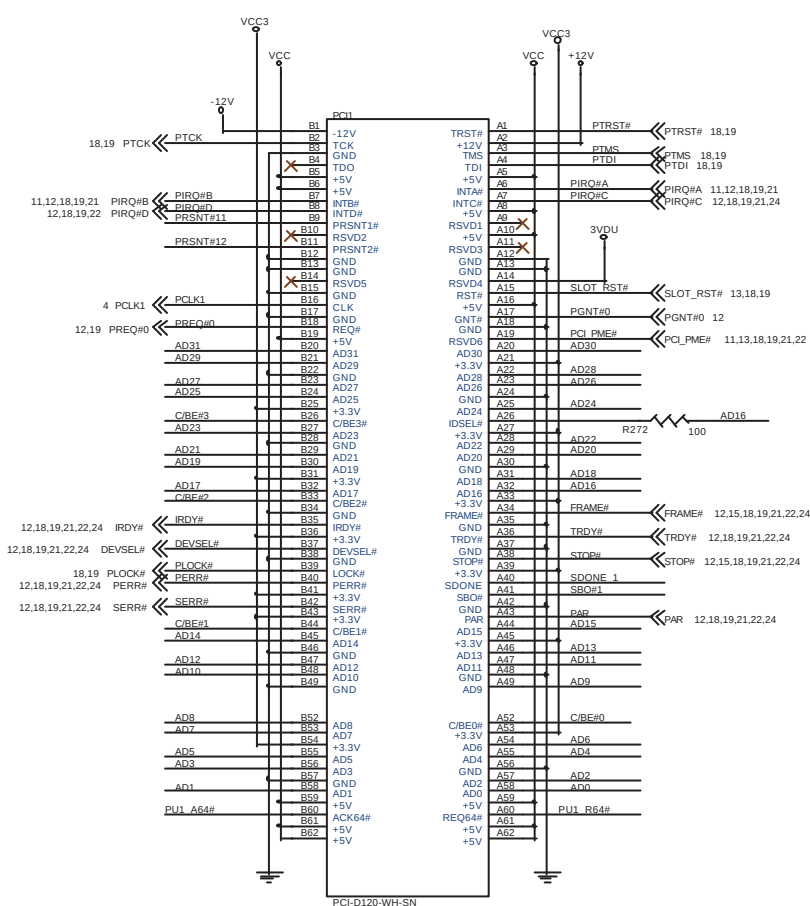


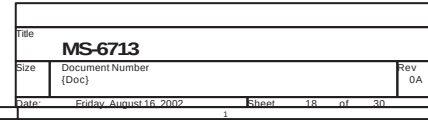
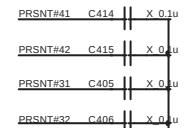
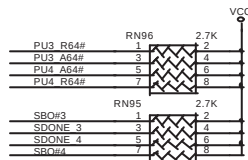
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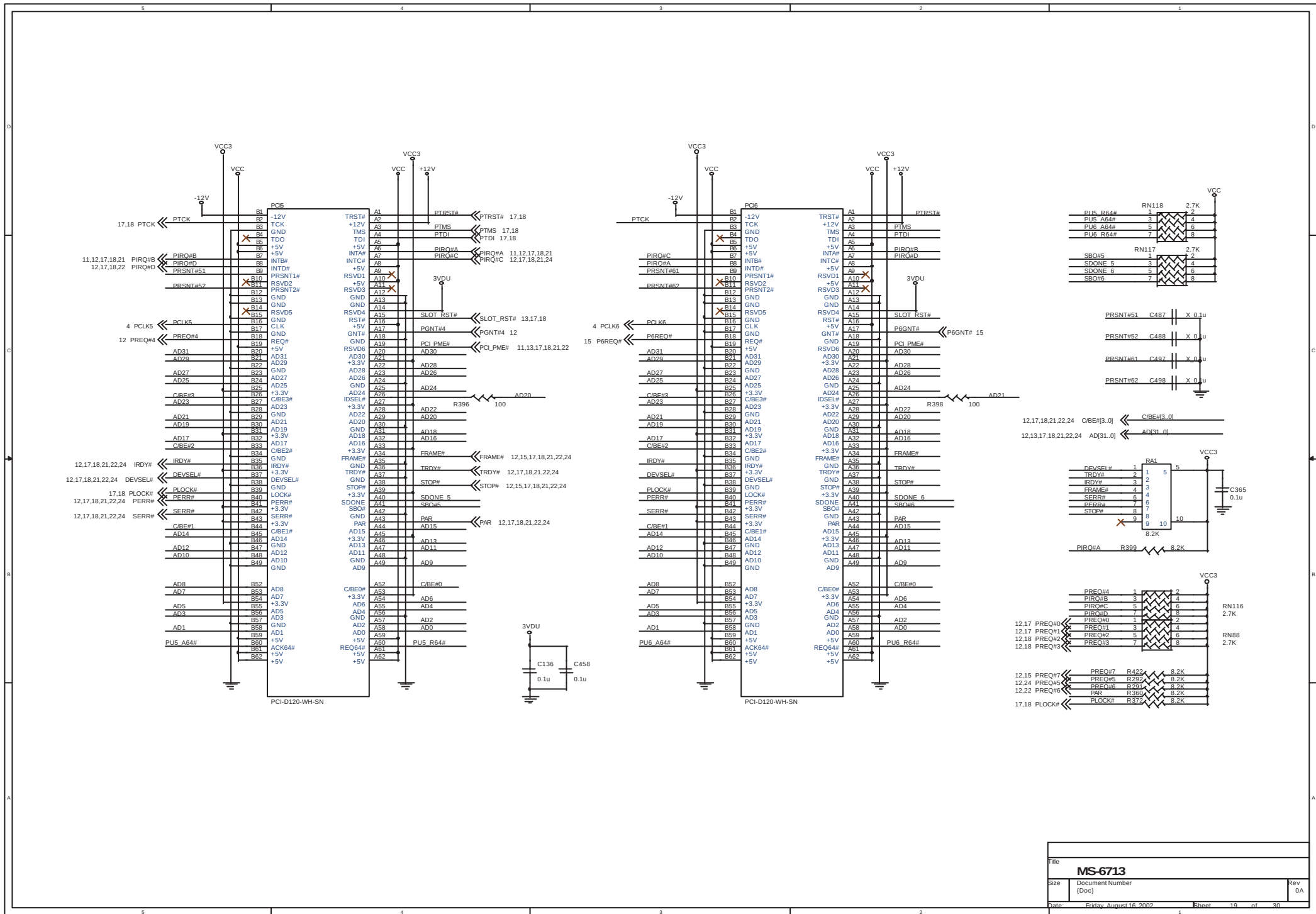


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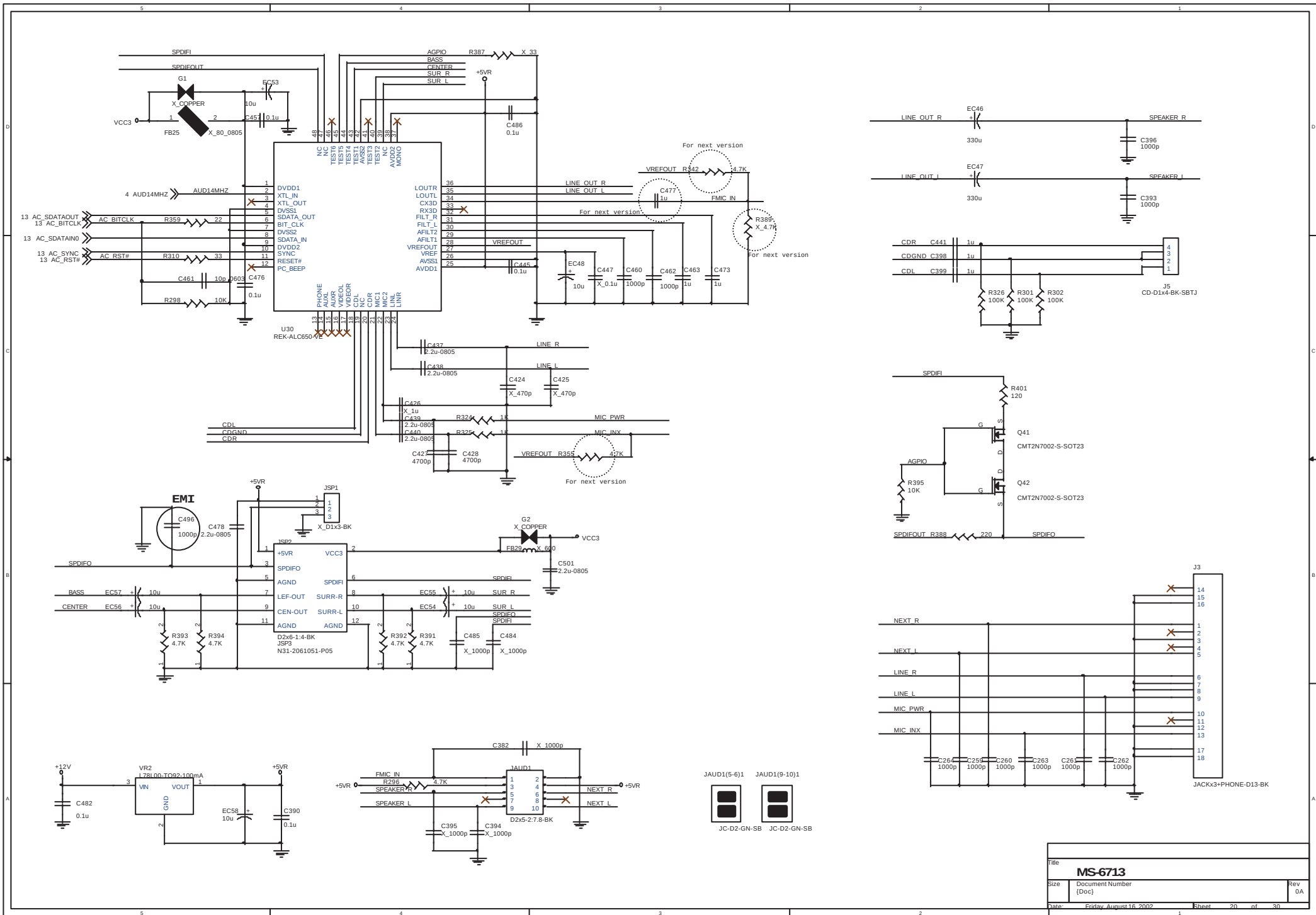






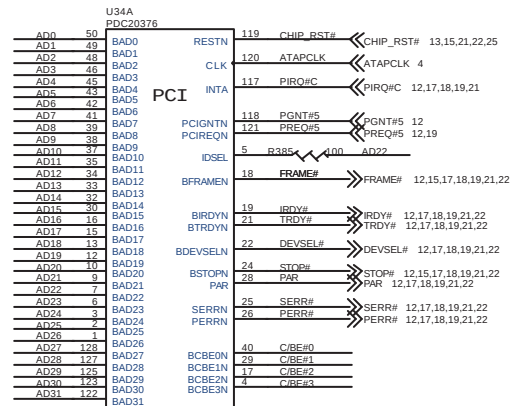


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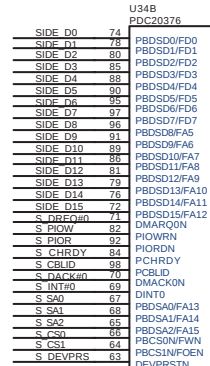
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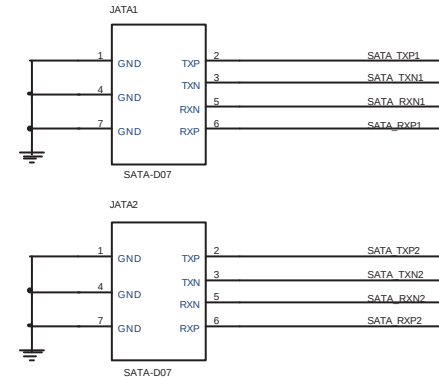
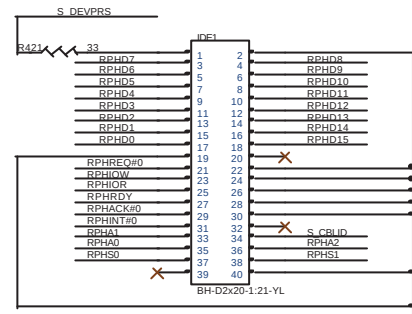
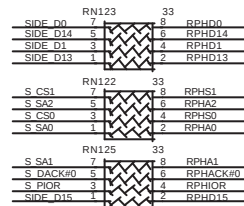
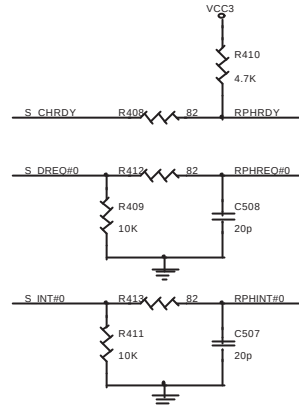
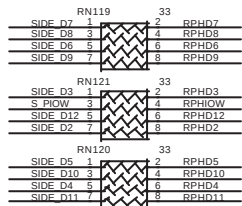
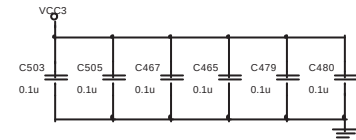
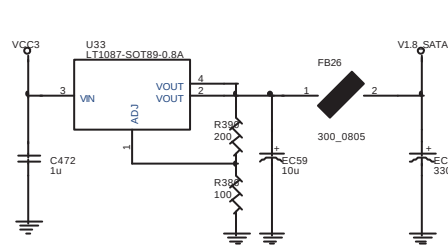
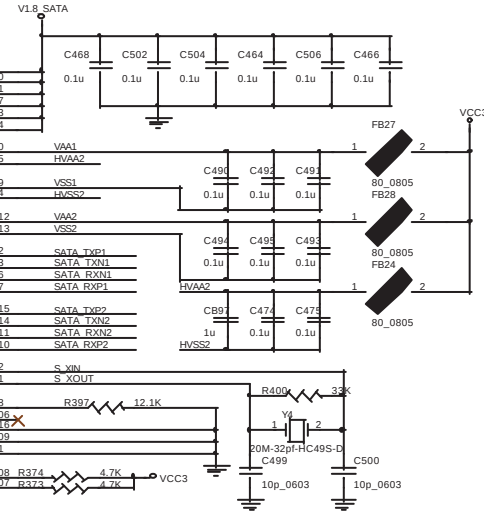
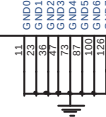


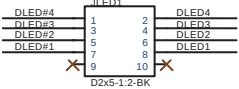
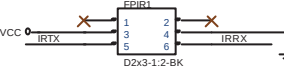
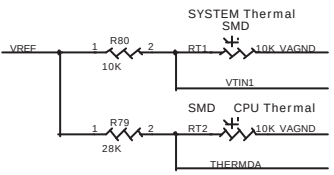
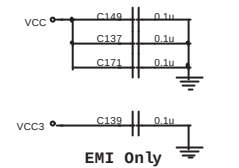
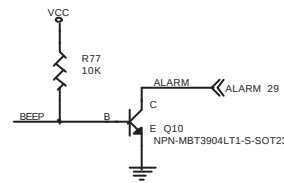
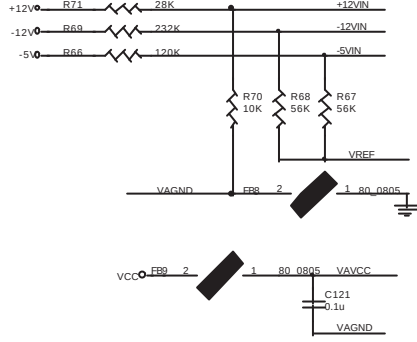
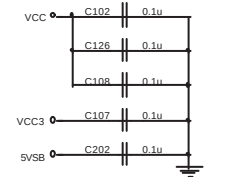
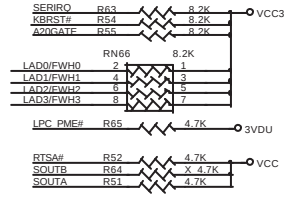
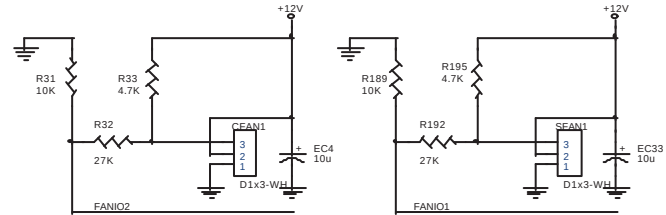
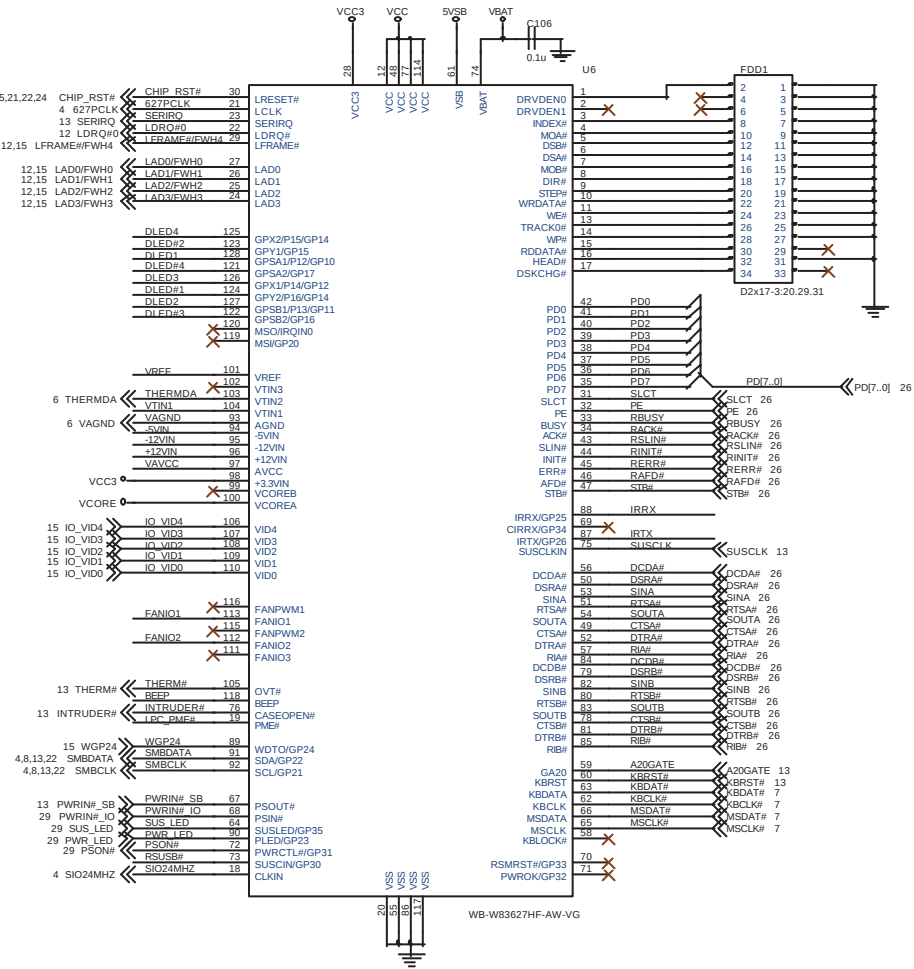
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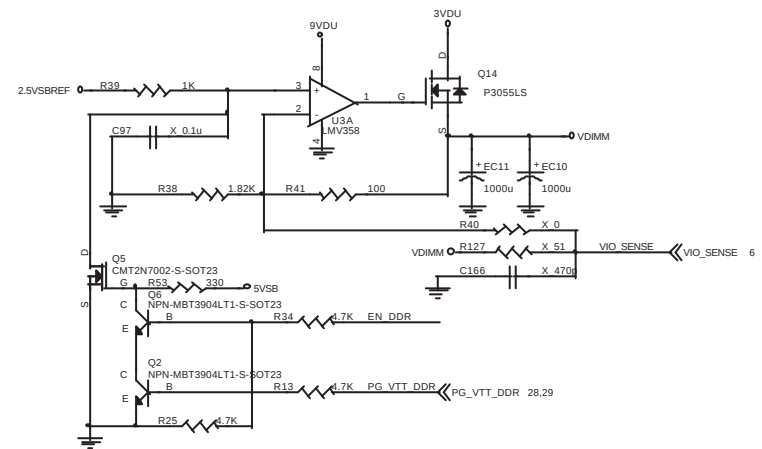
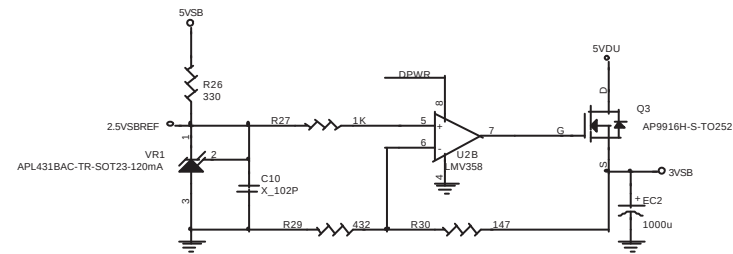
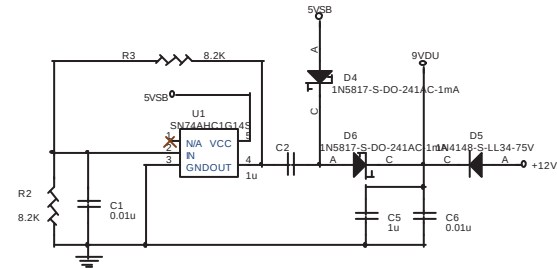
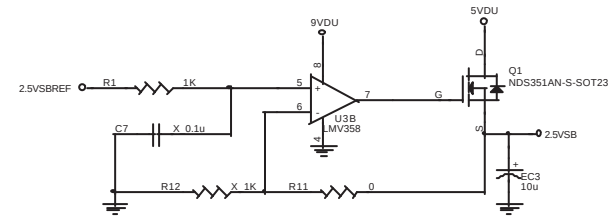
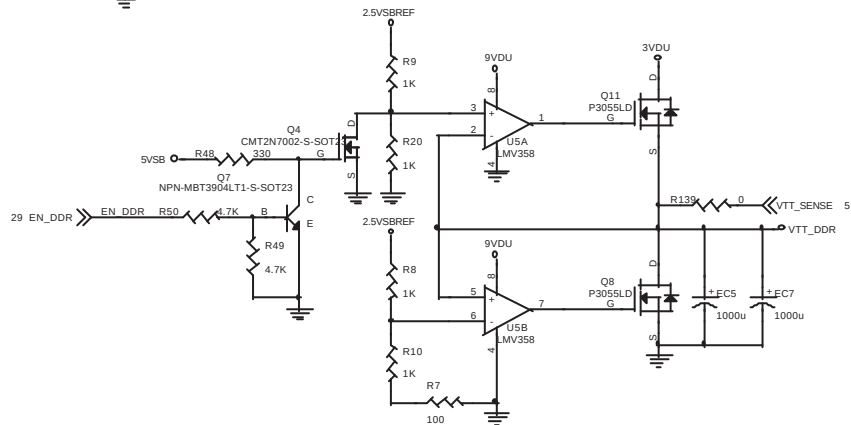
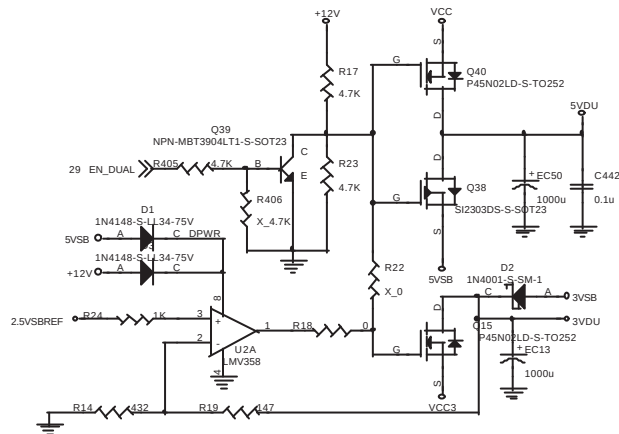
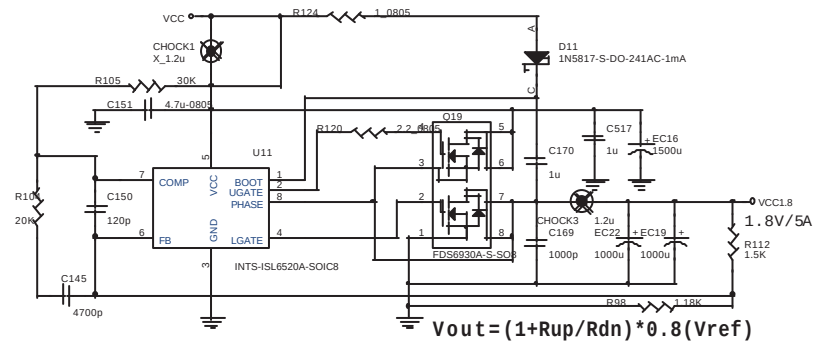


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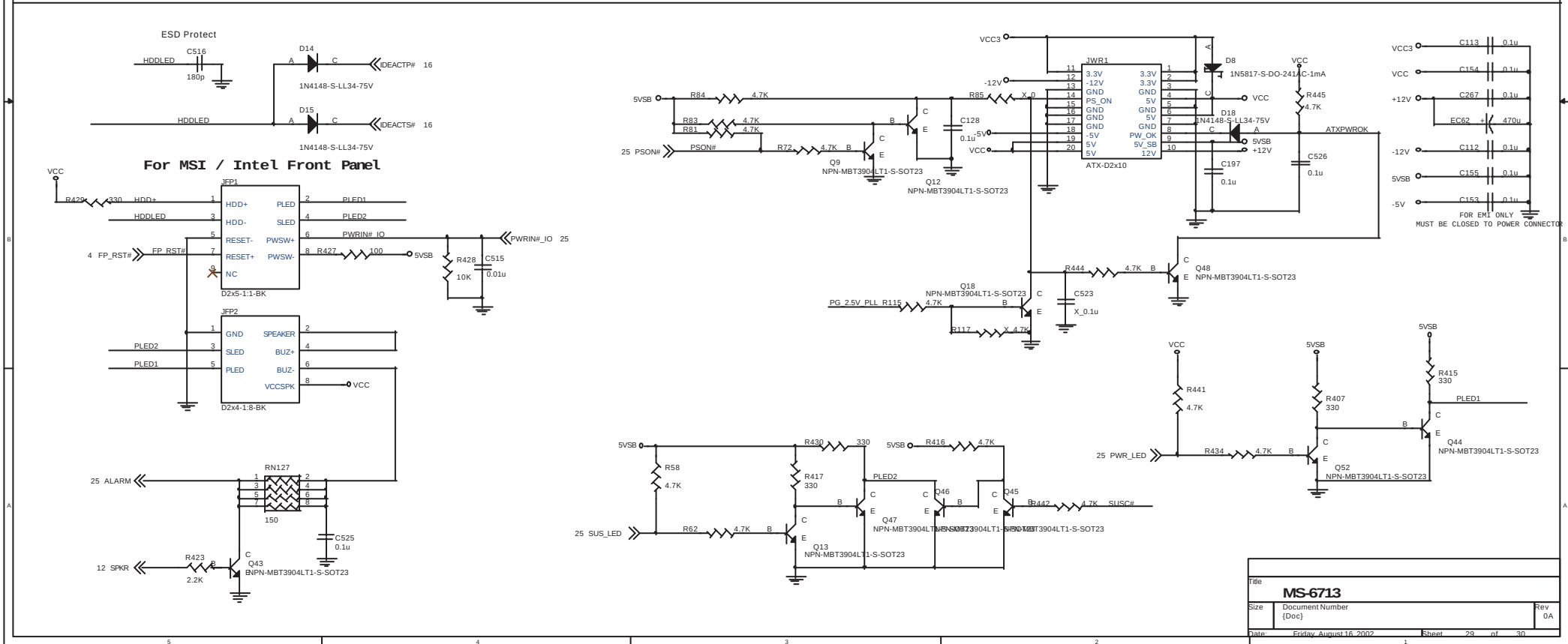
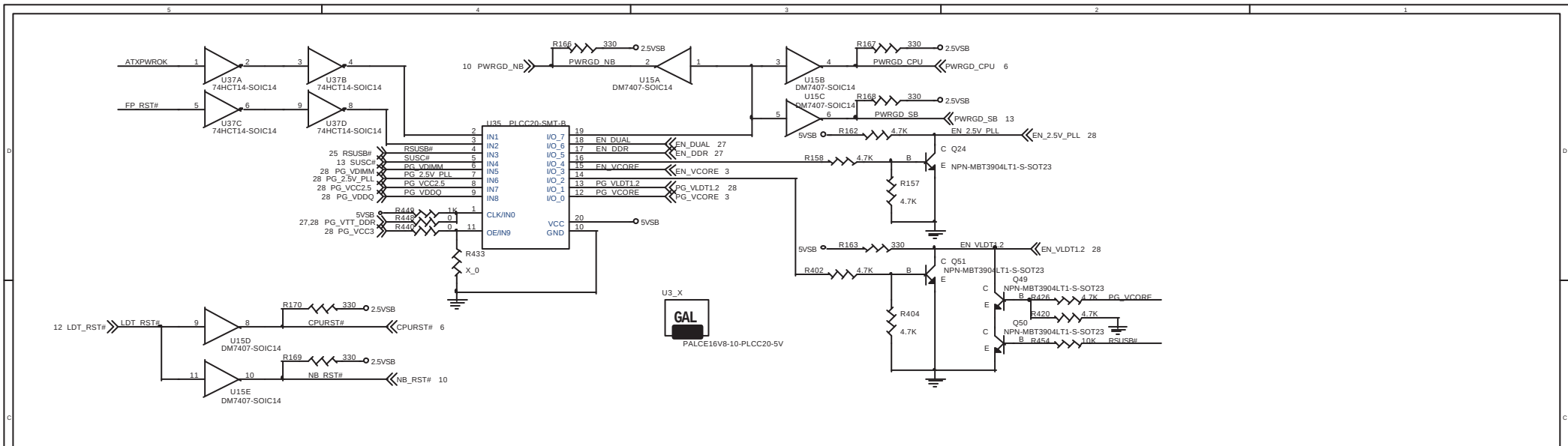




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