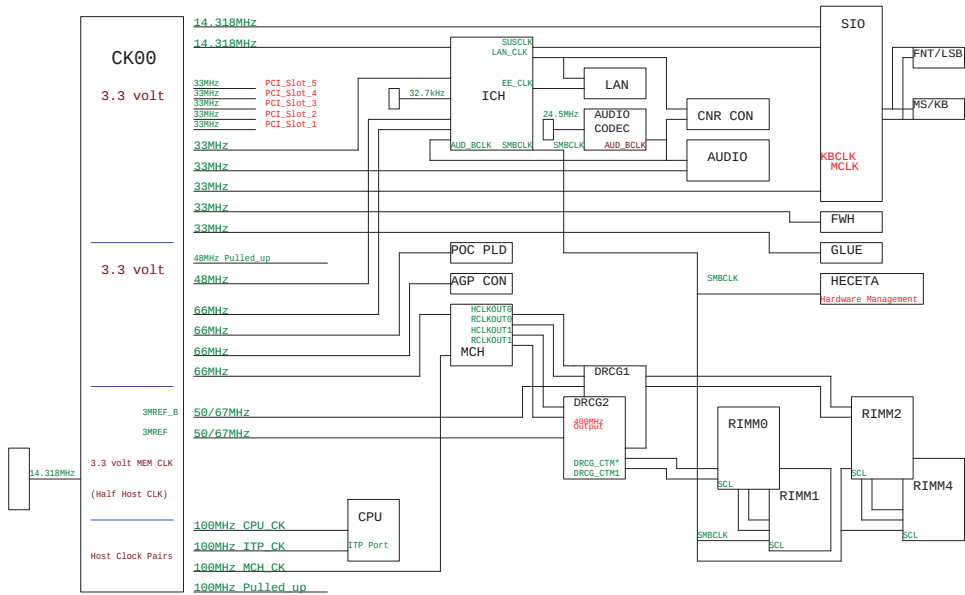


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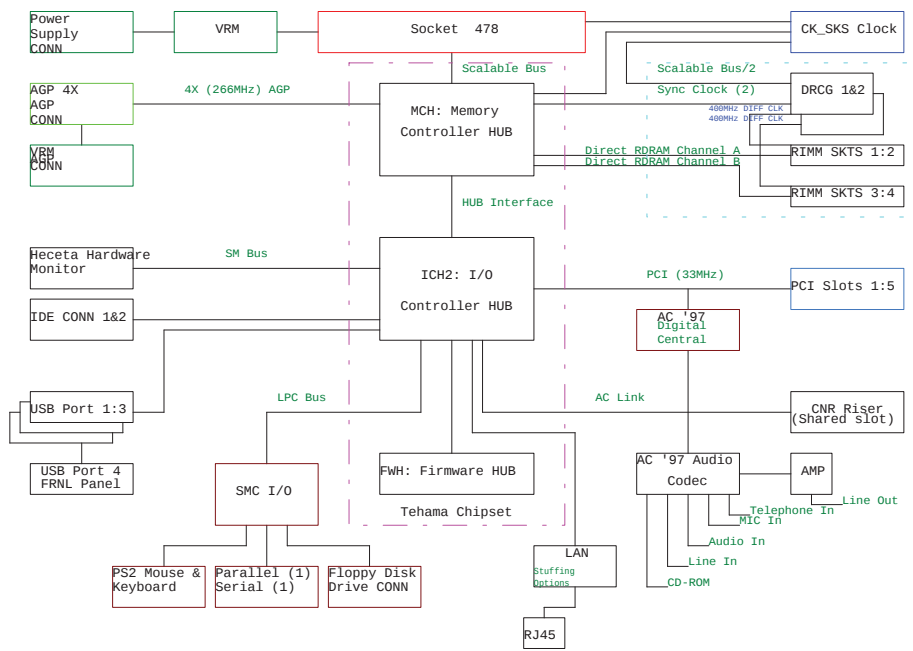
INTEL (R) 850 CHIPSET
Pentium(R) 4 PROCESSOR in 478-Pin SCHEMATICS

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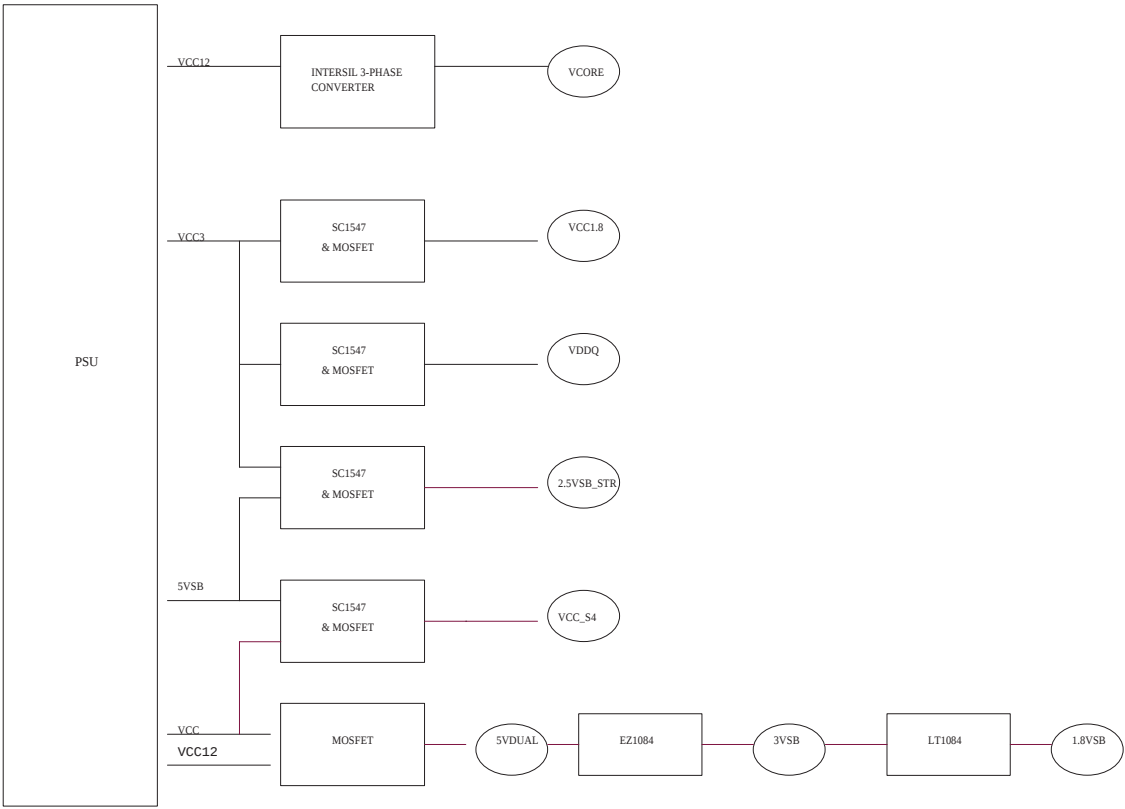
Pentium(R) 4 Mother Board			
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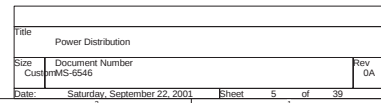
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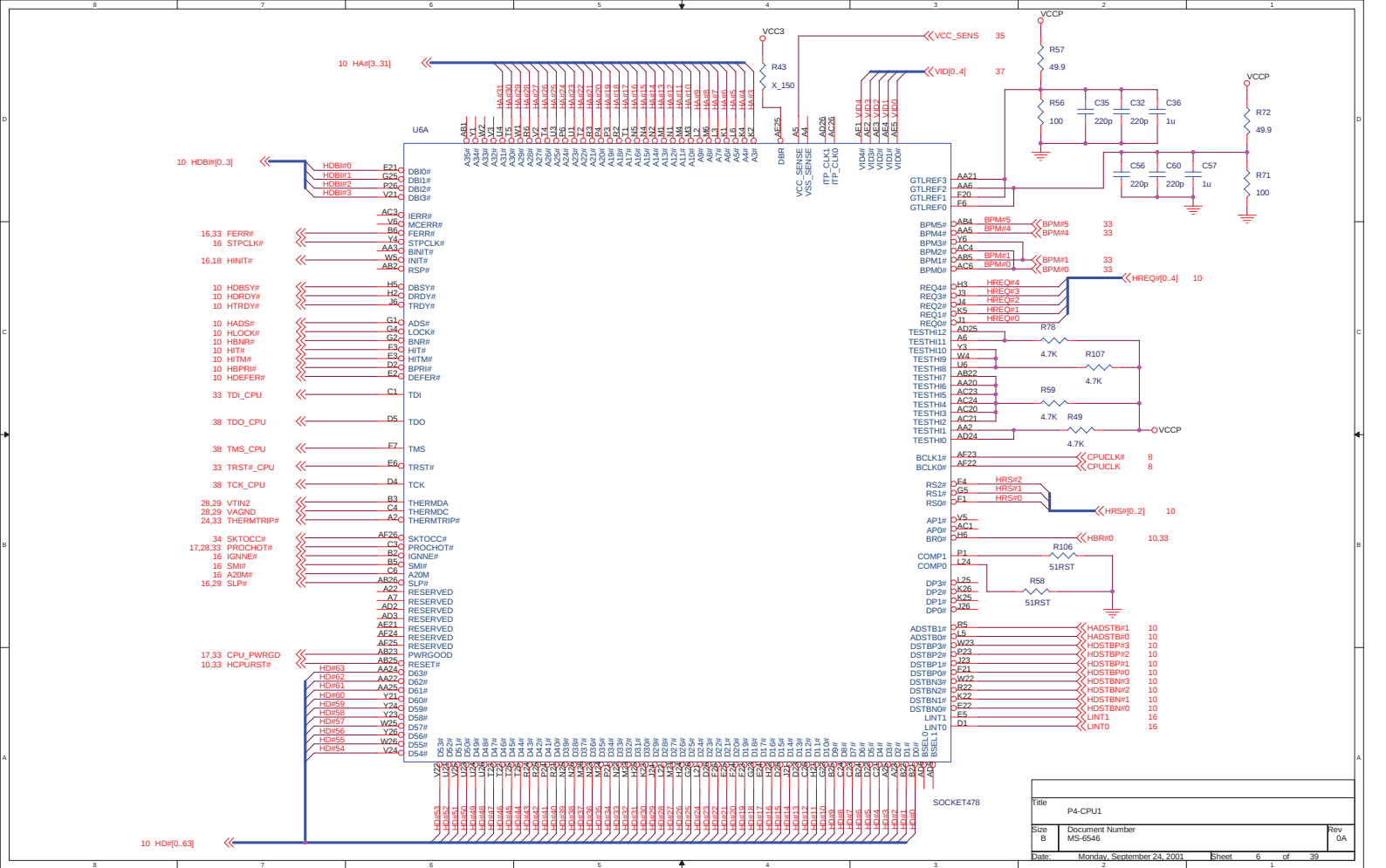


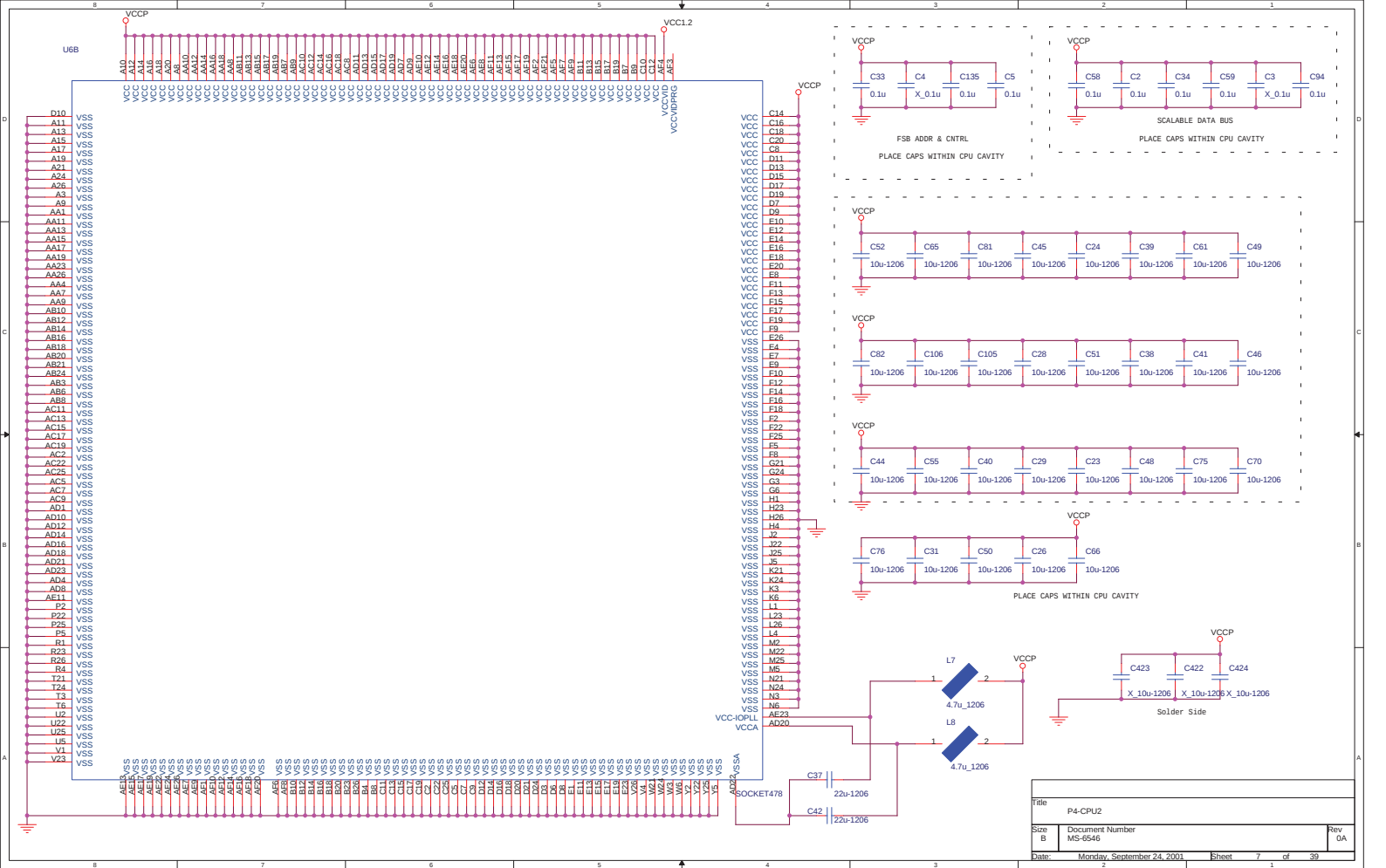
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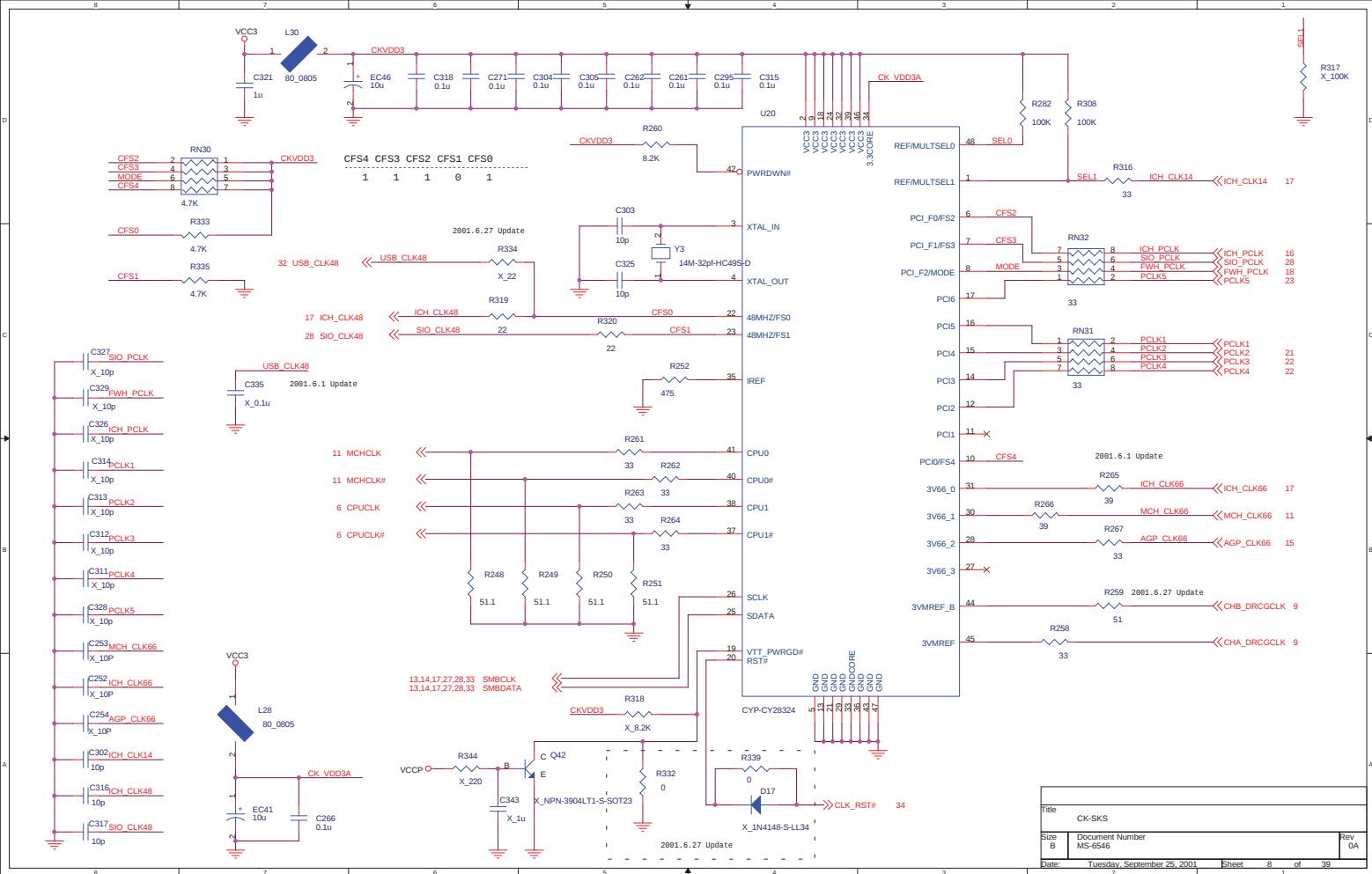


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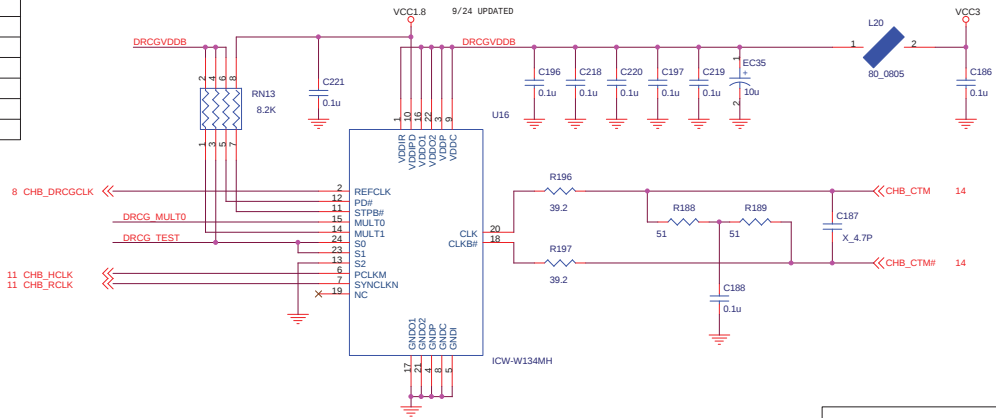
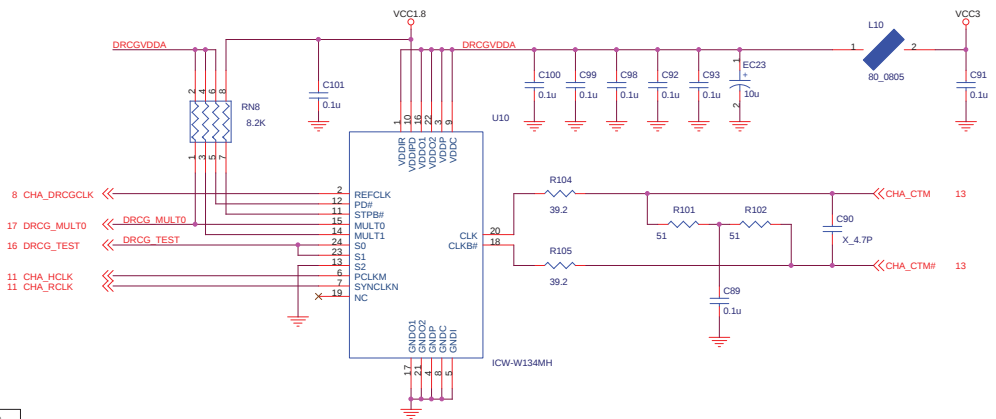




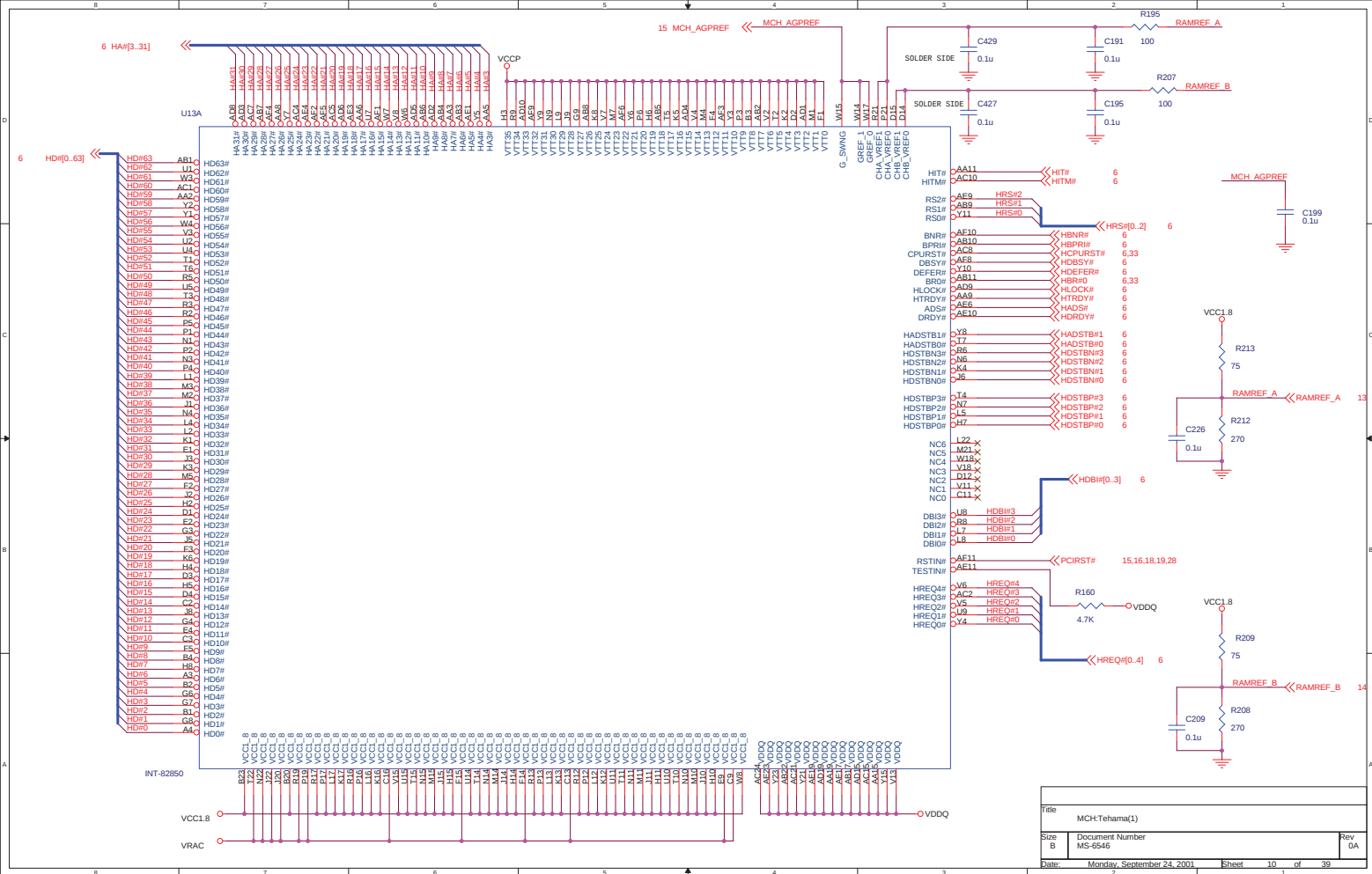


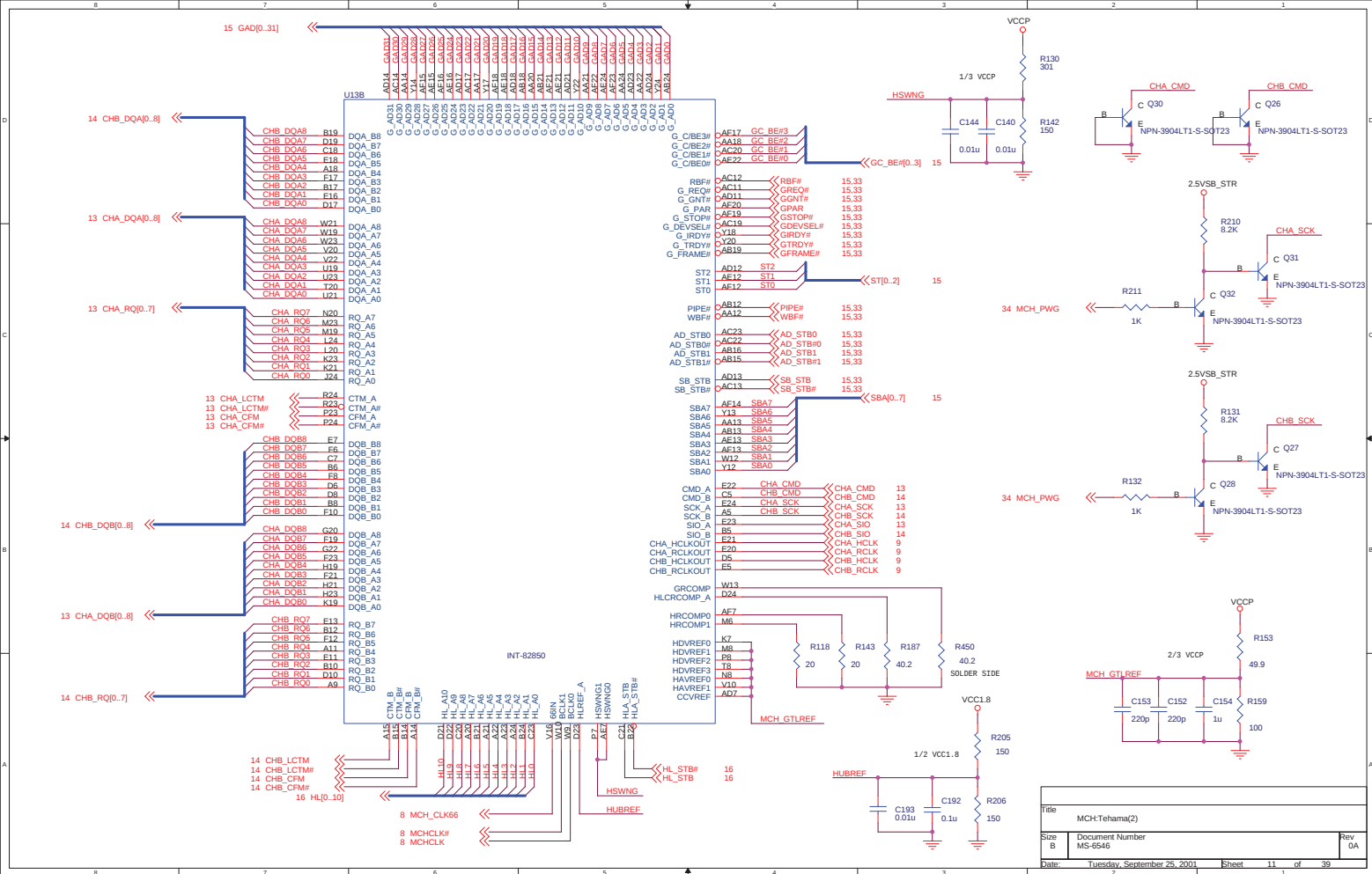


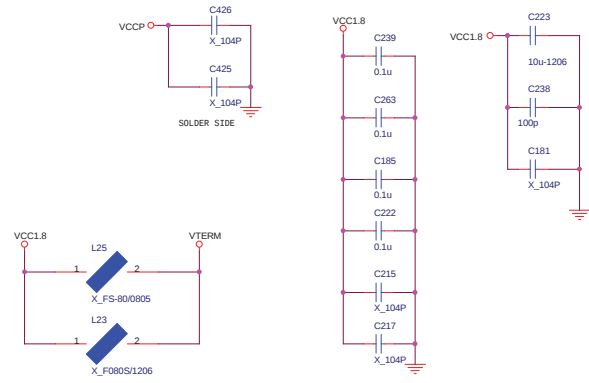
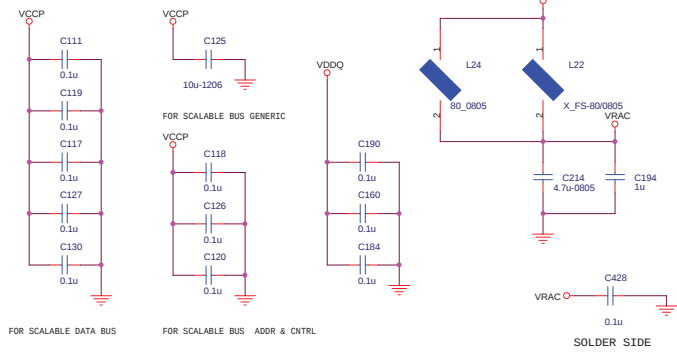
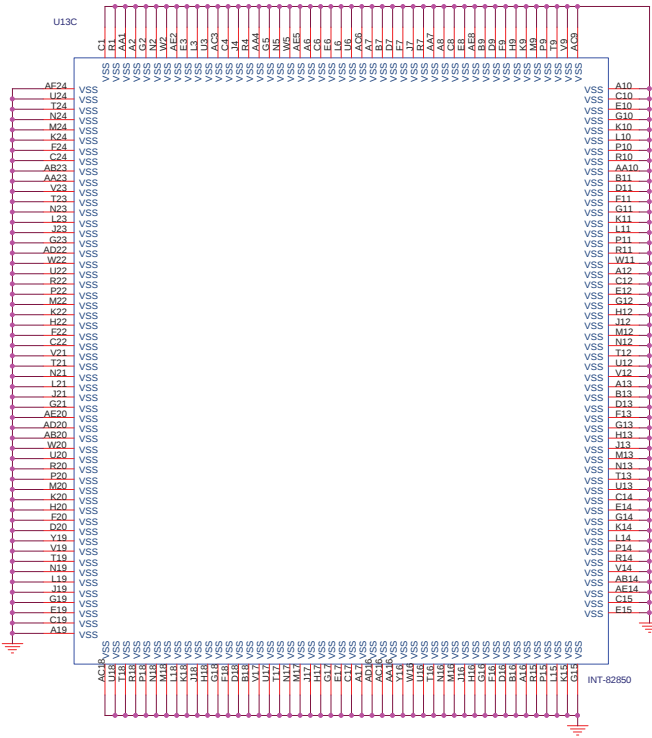
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NORMAL	0	0	0		
BYPASS	1	0	0		
TEST	1	1	0		
OUTPUT TEST	0	1	*		
RESERVED	0	0	1		
RESERVED	1	0	1		
RESERVED	1	1	1		



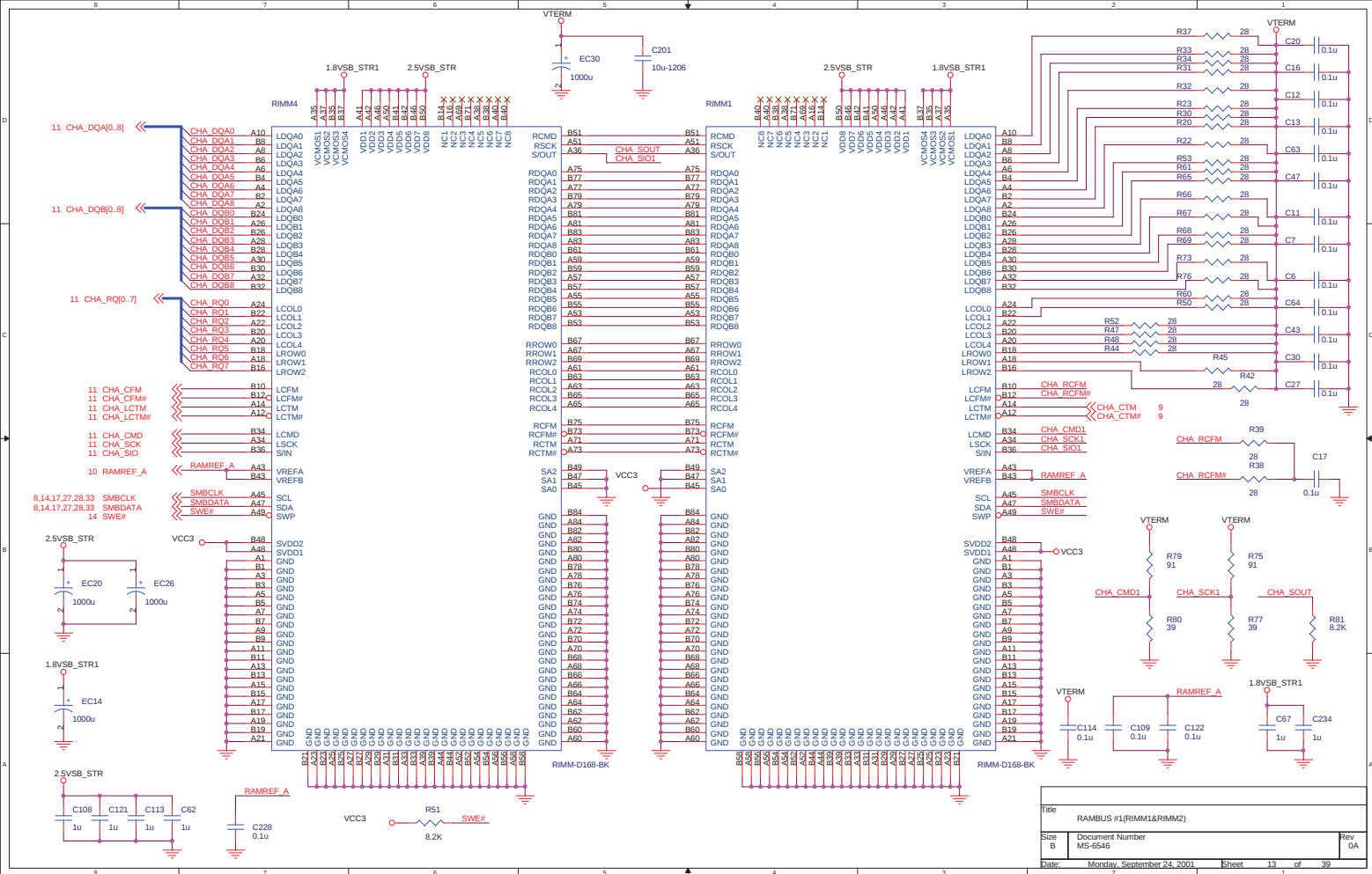
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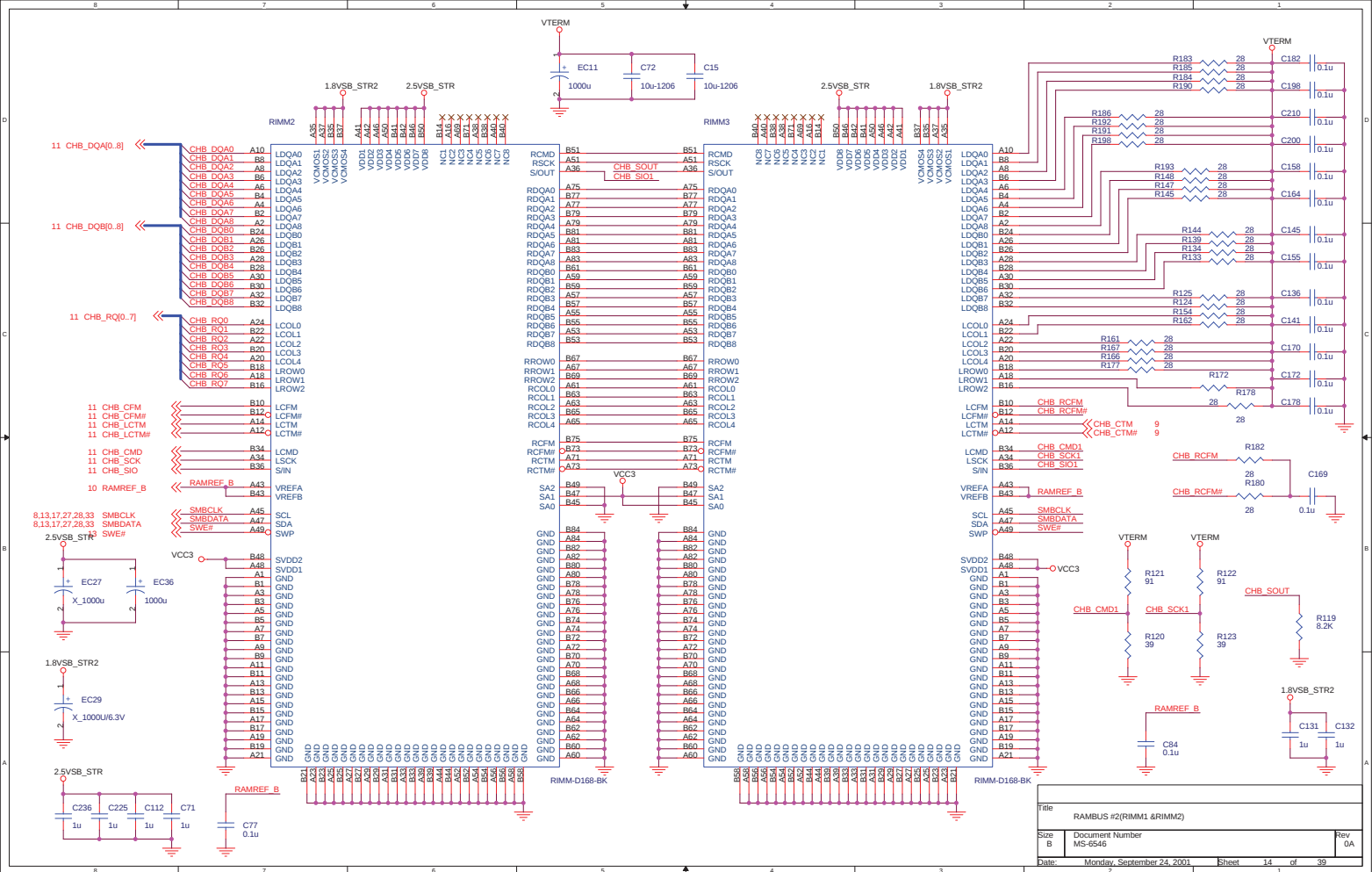


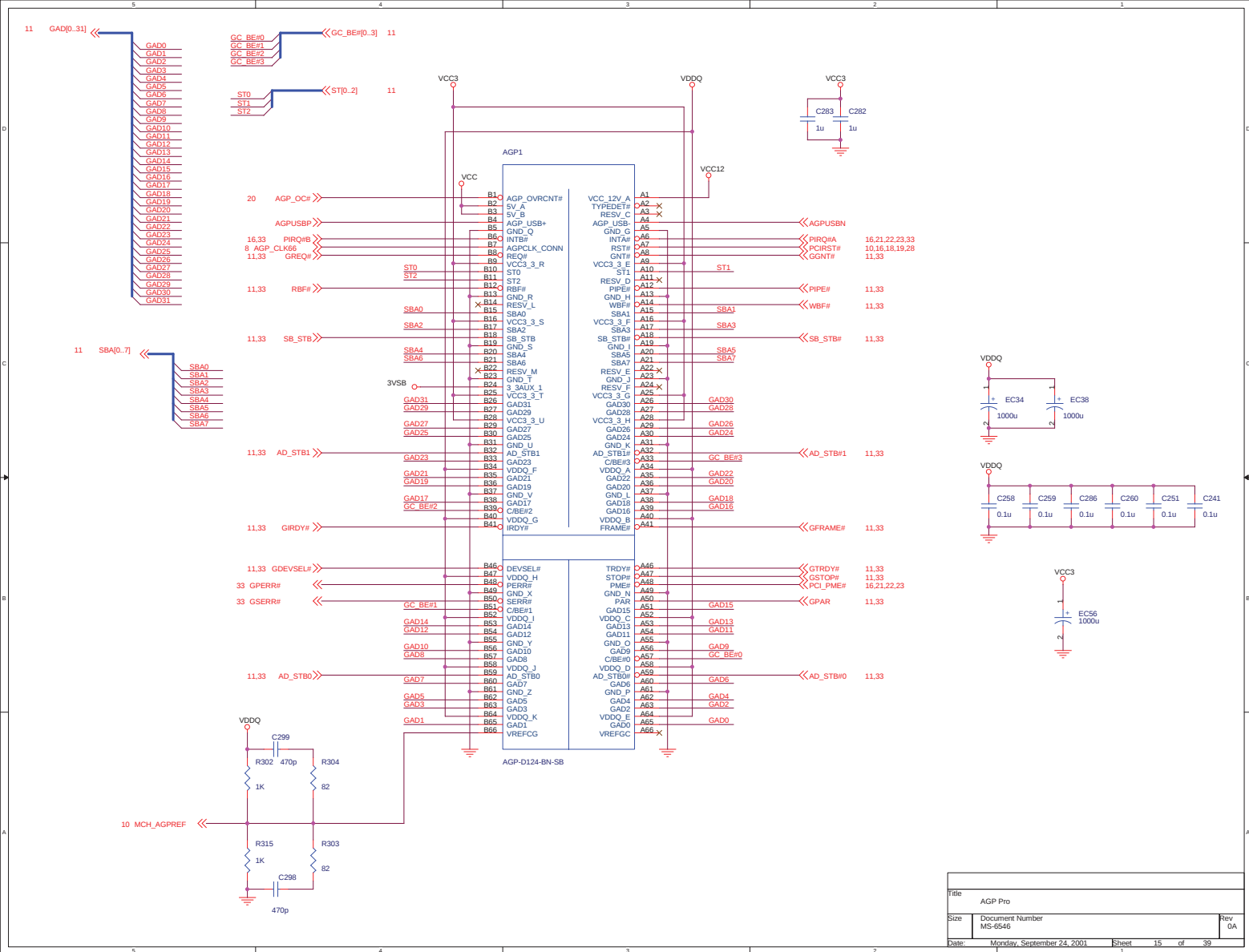


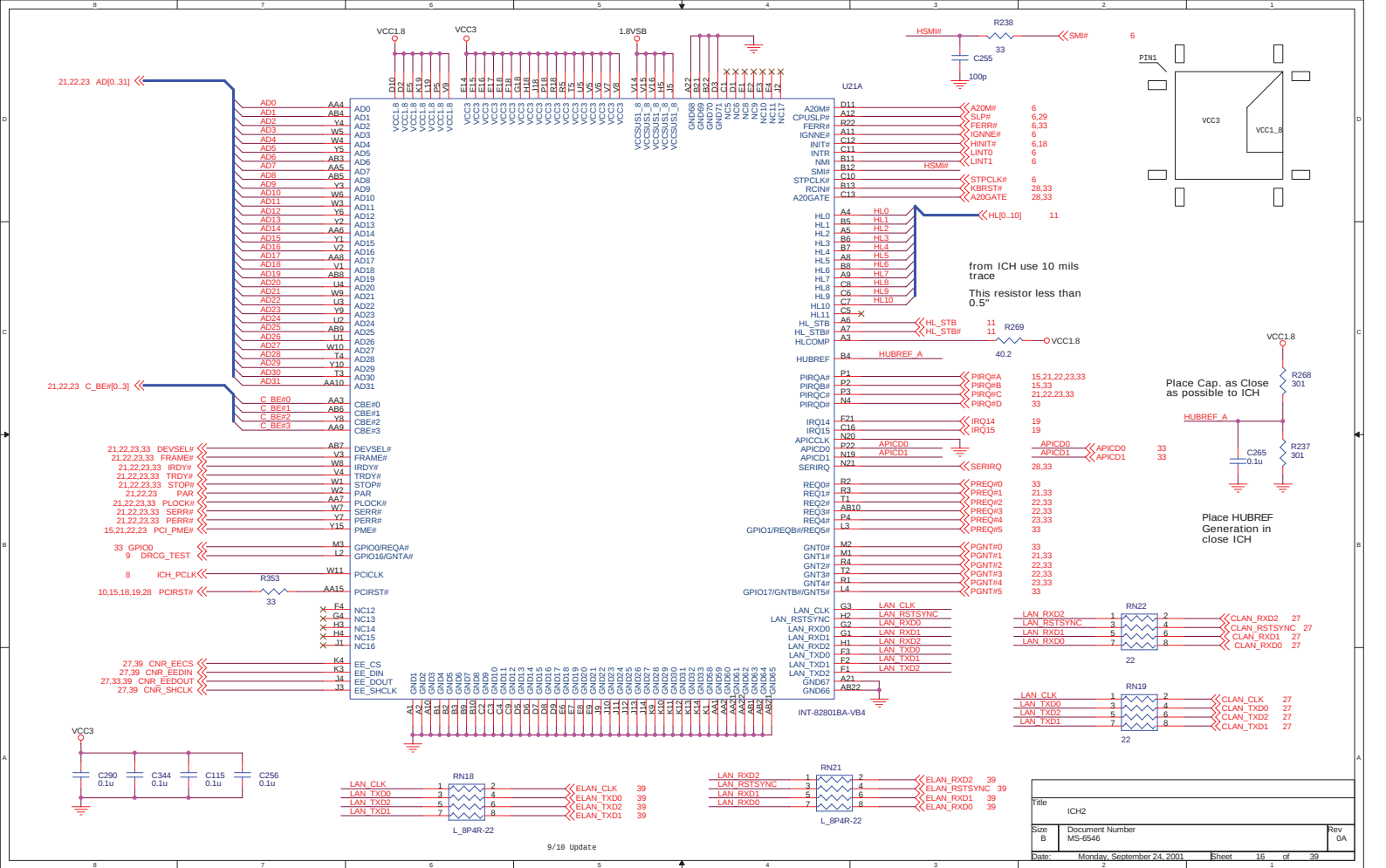


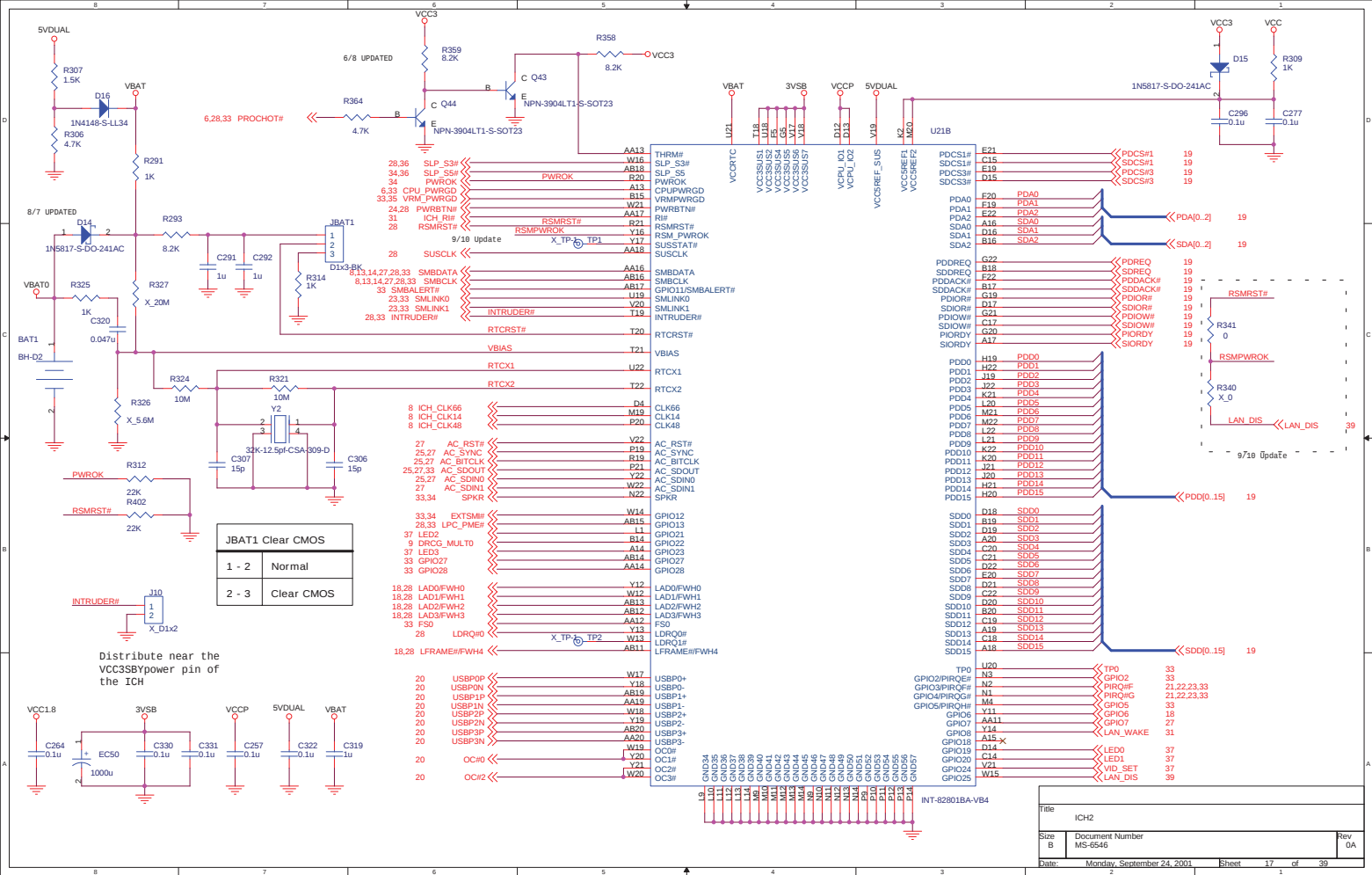
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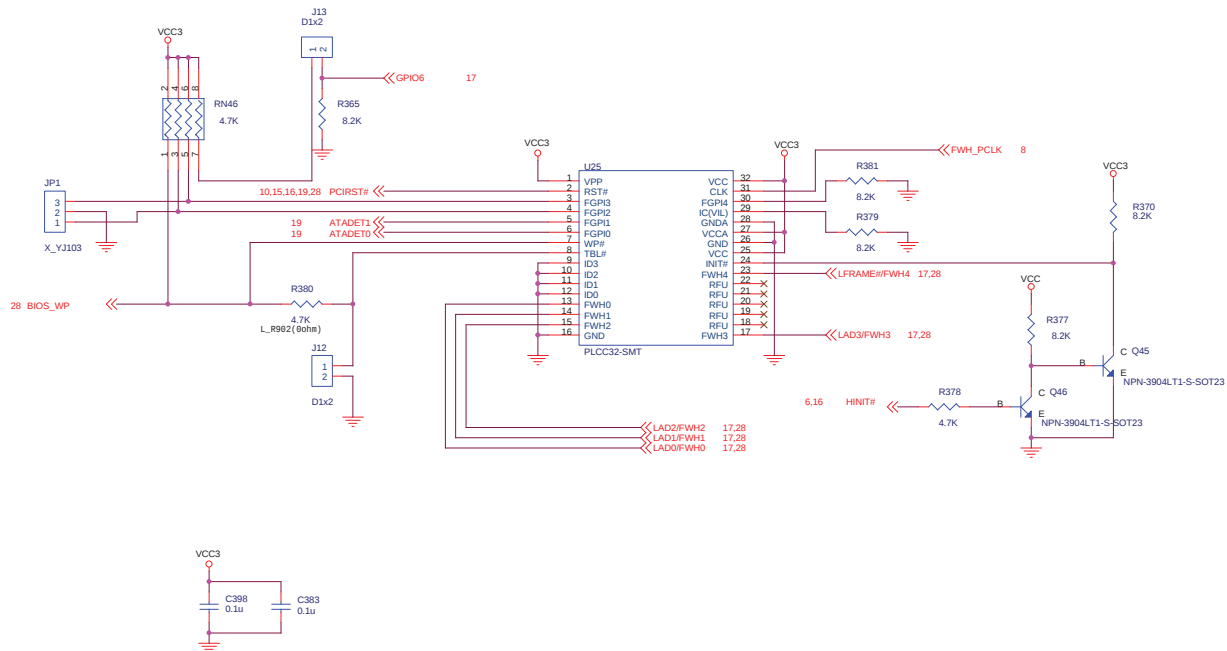






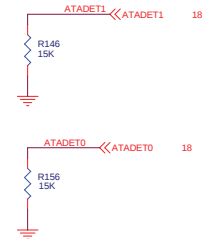
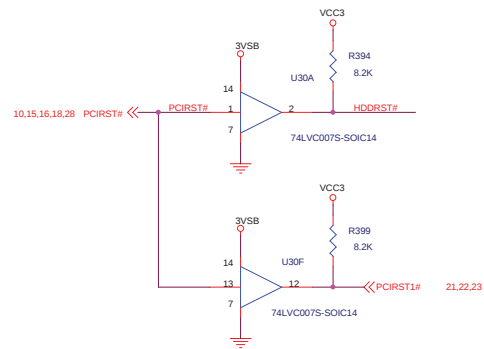
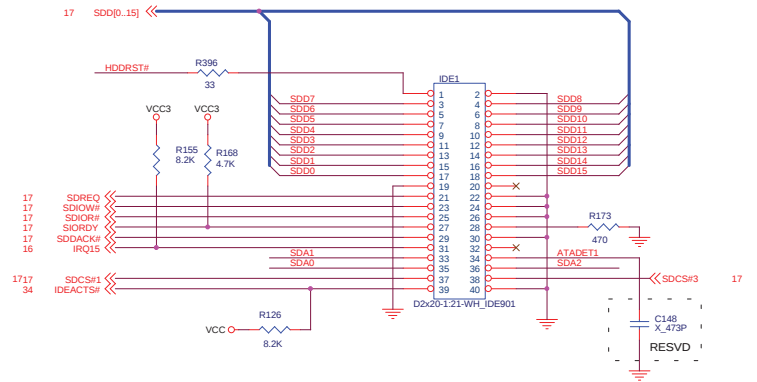


Title			ICH2
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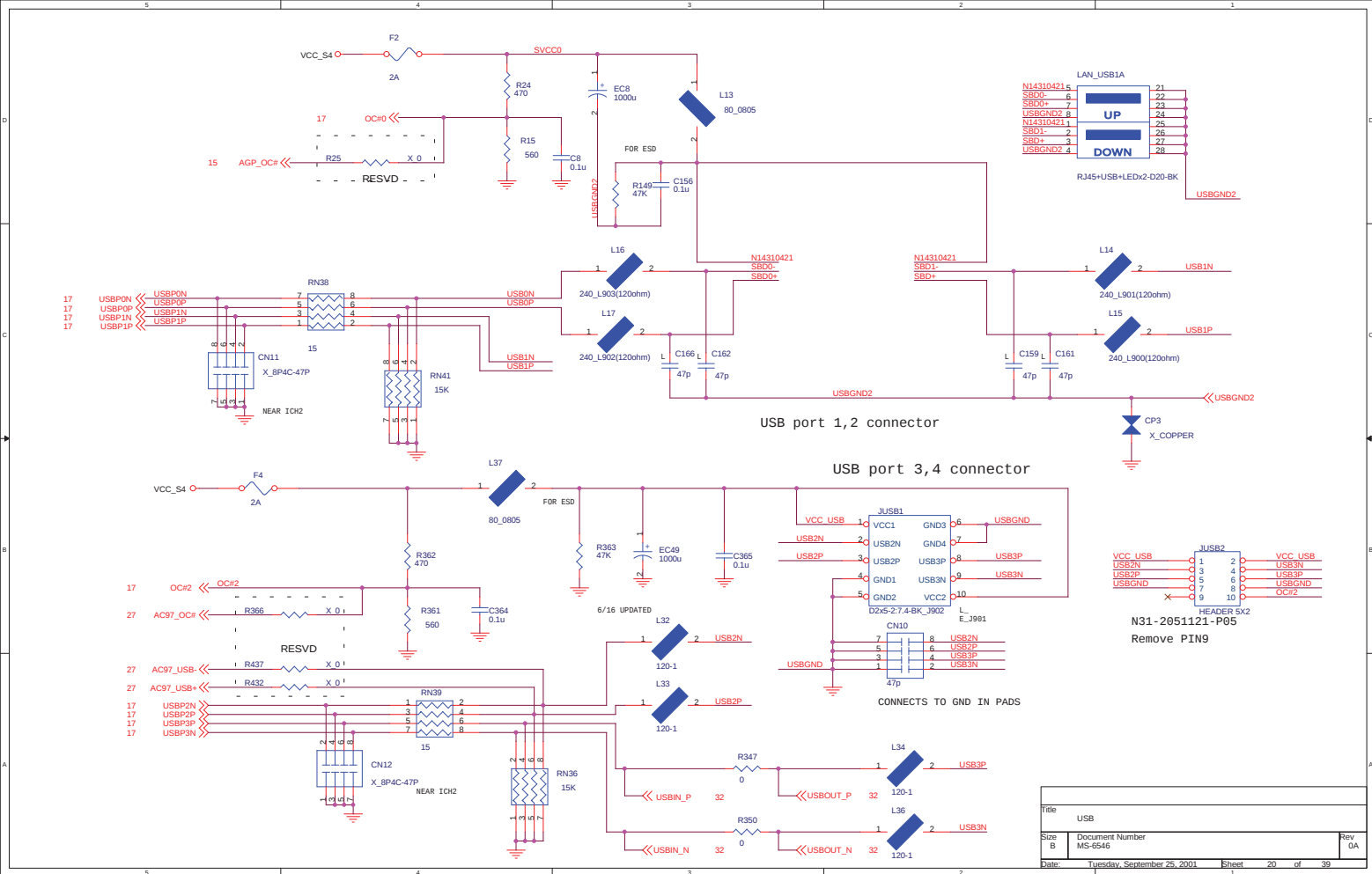


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FWH		
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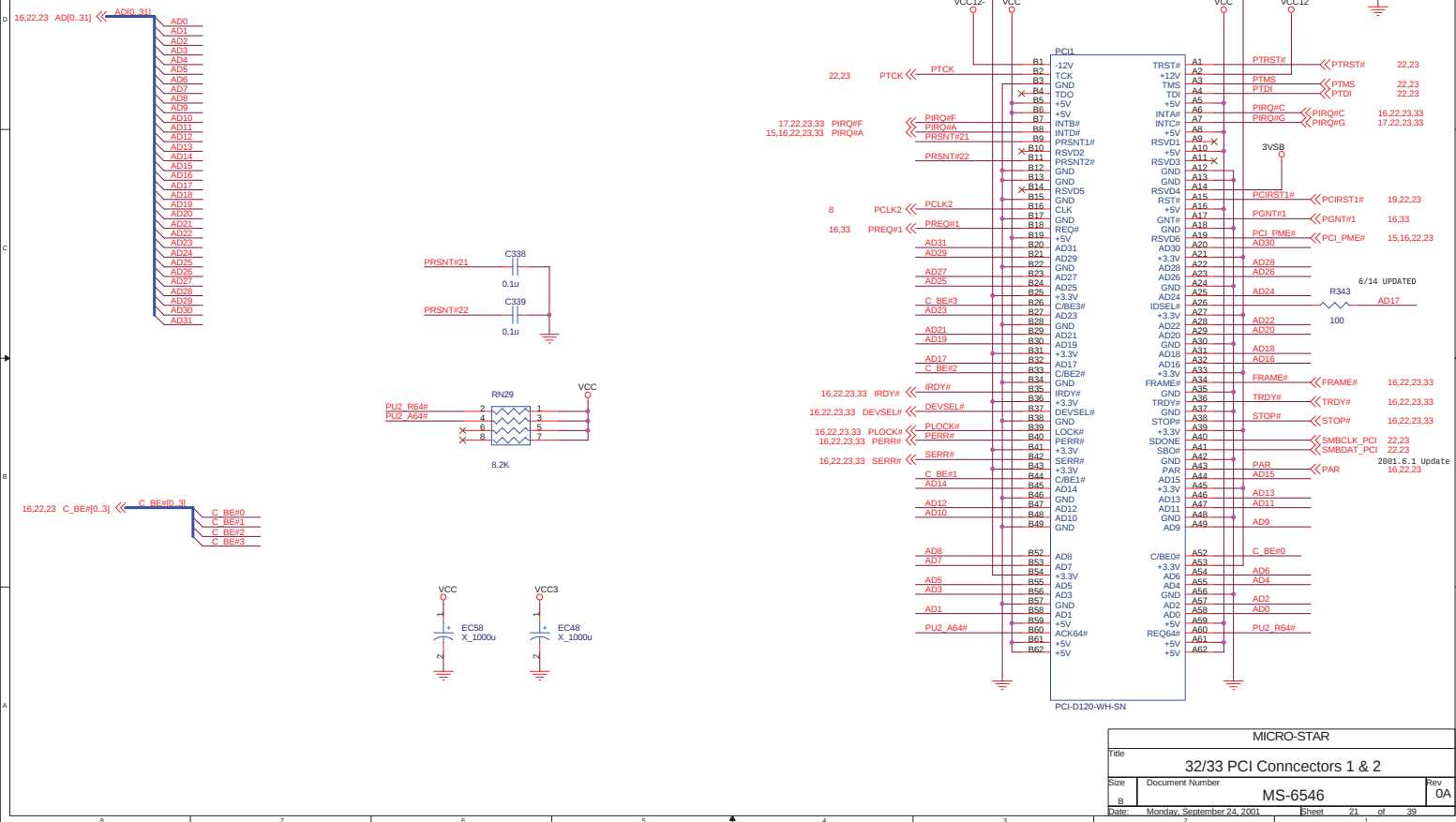
SECONDARY IDE CONN.



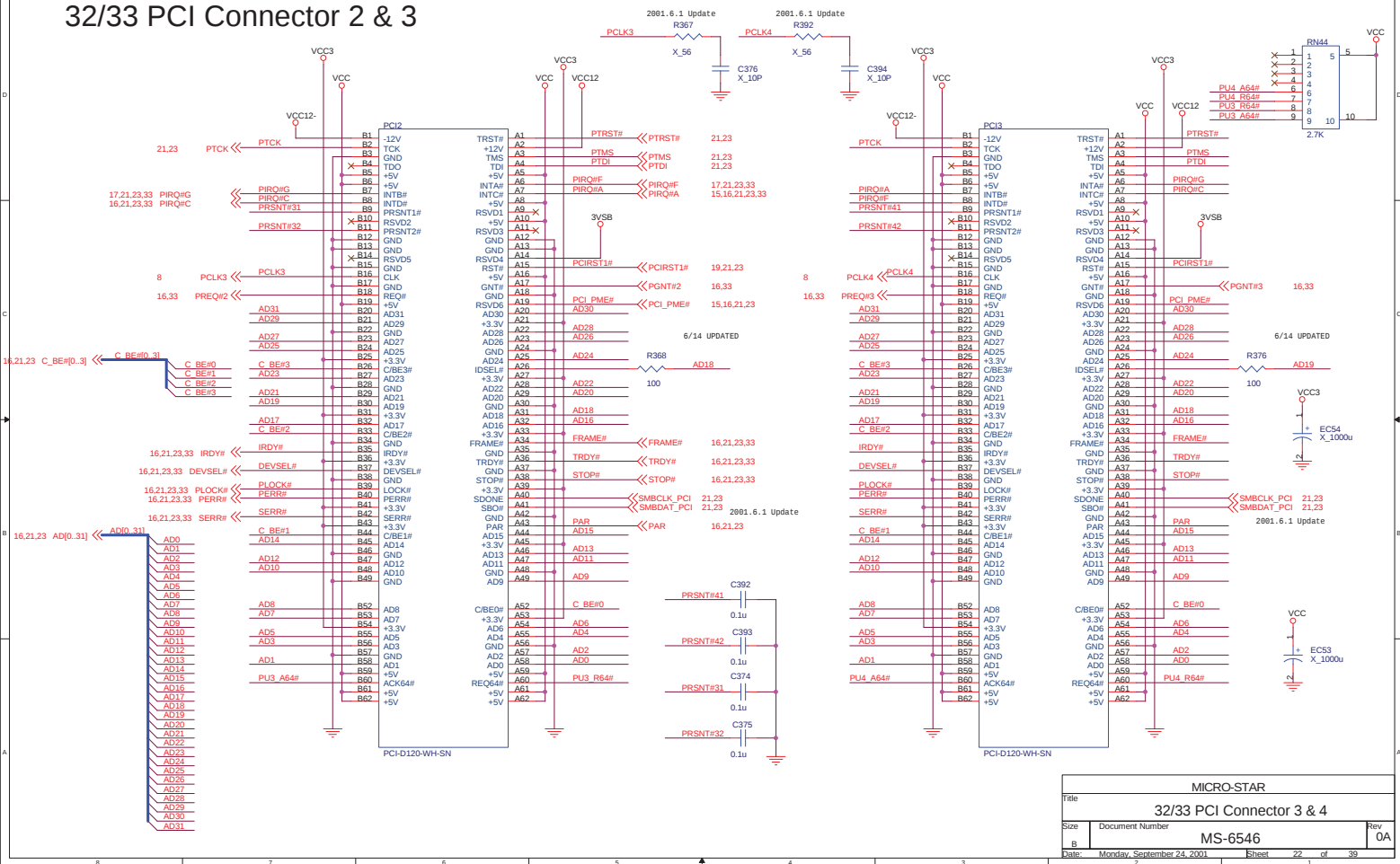
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32/33 PCI Connector 1

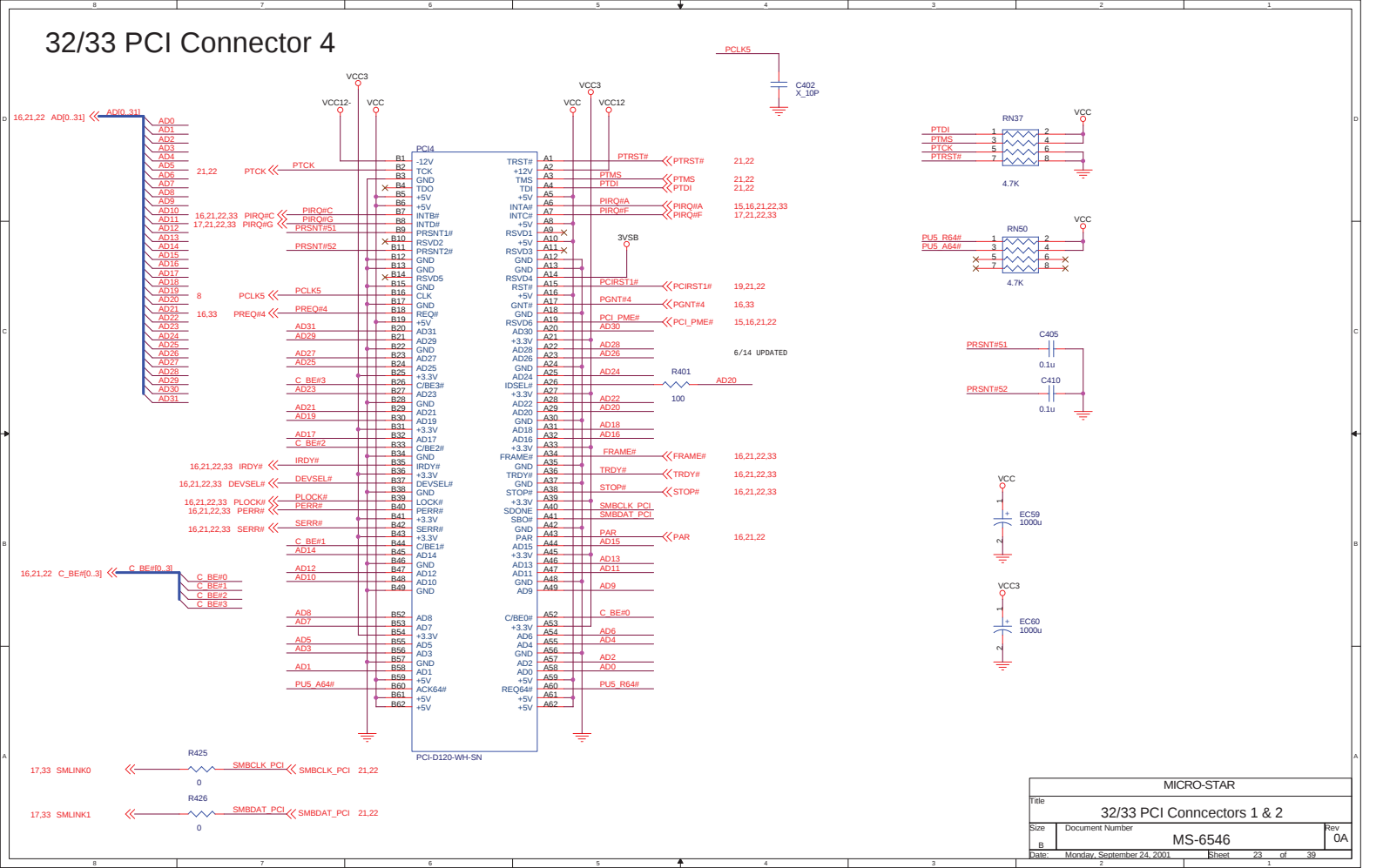


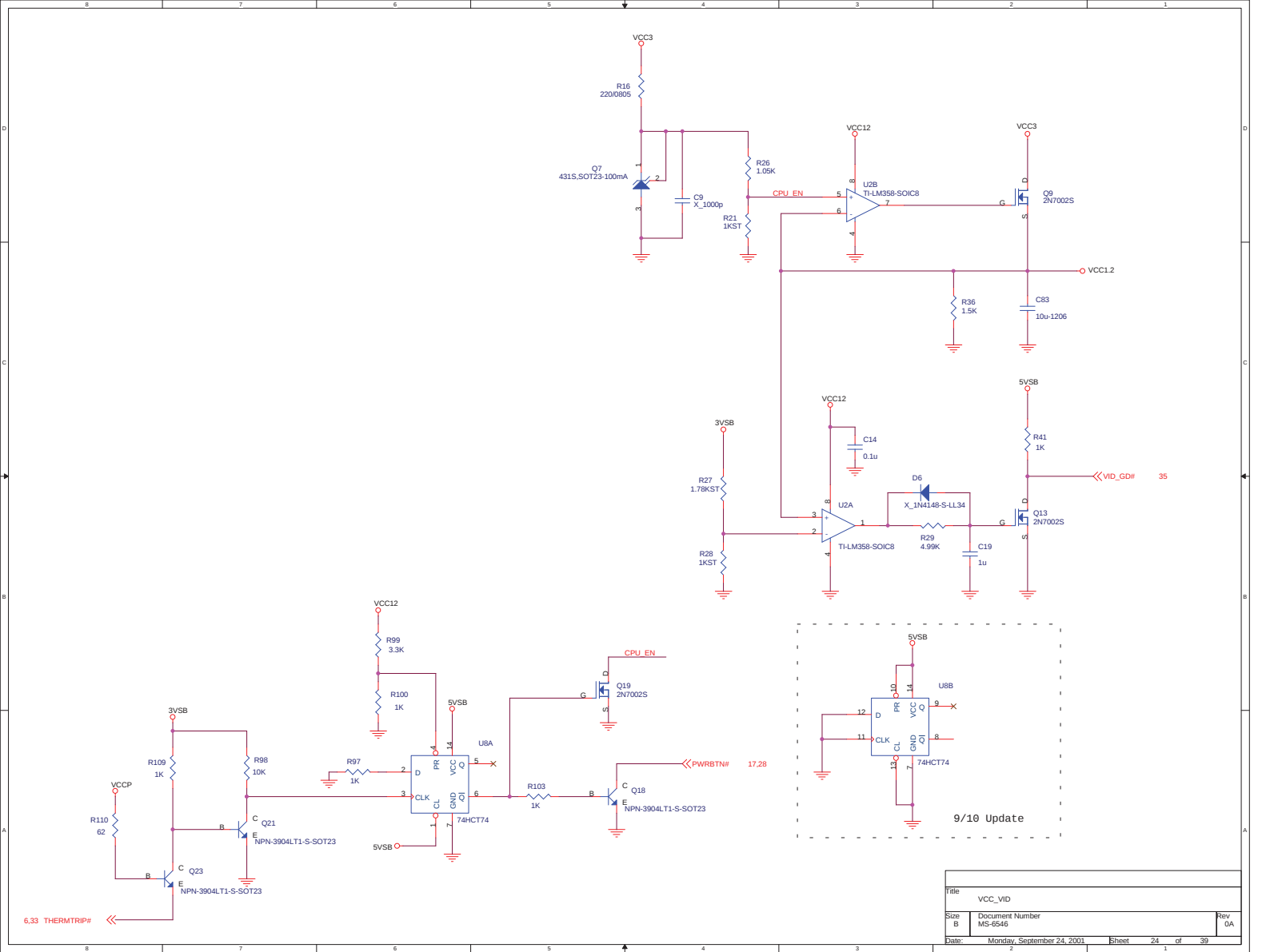
32/33 PCI Connector 2 & 3

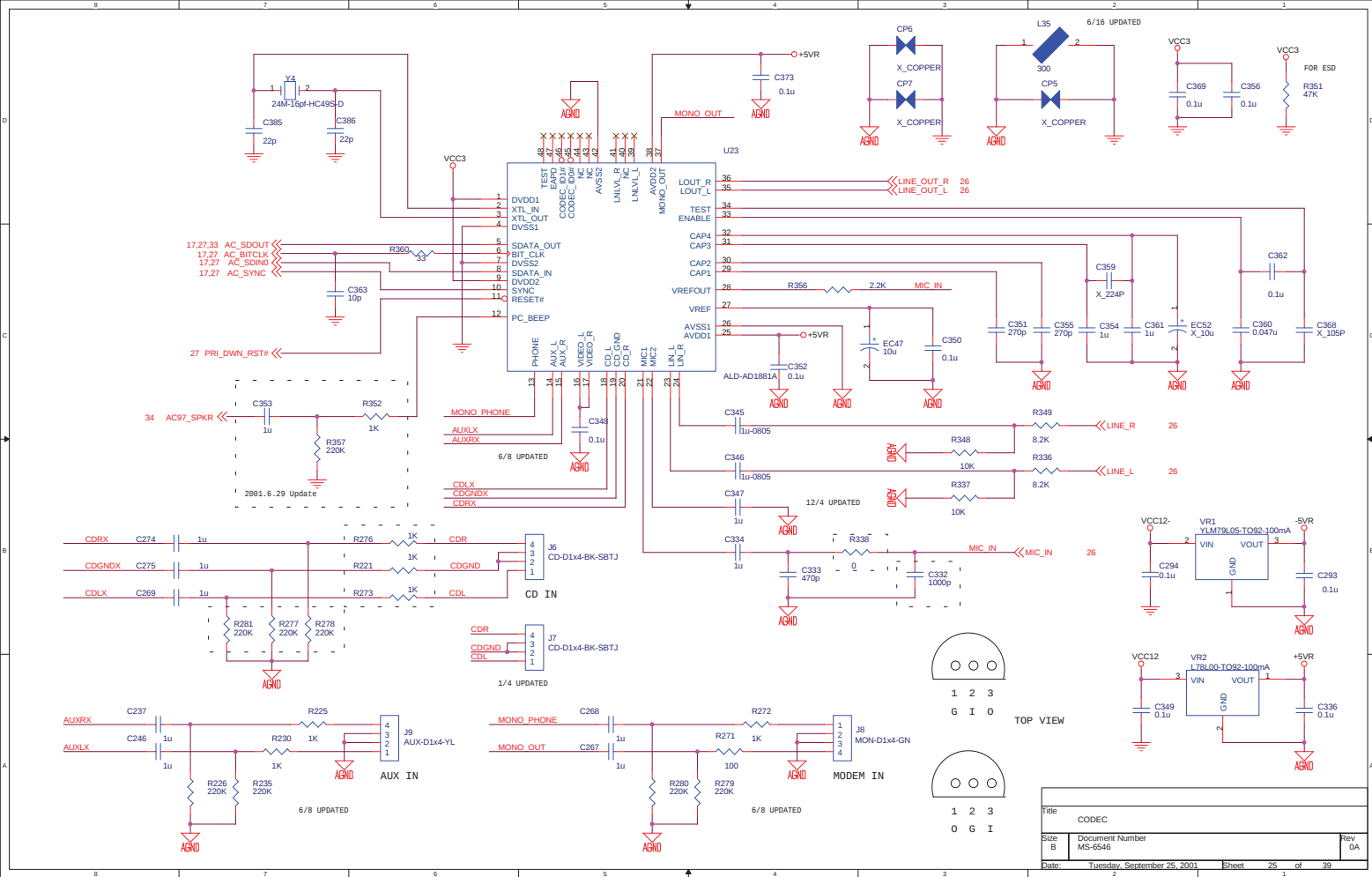


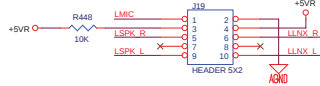
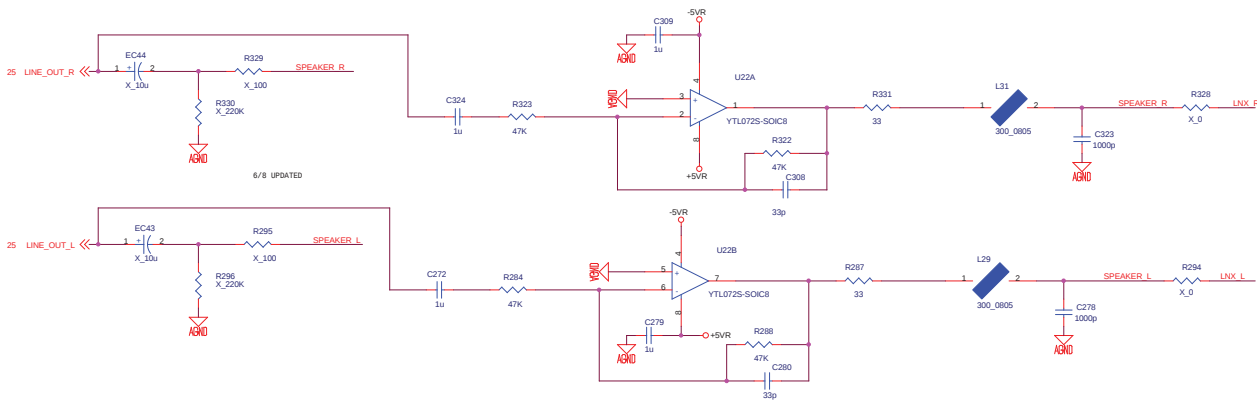
MICRO-STAR			
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32/33 PCI Connector 4

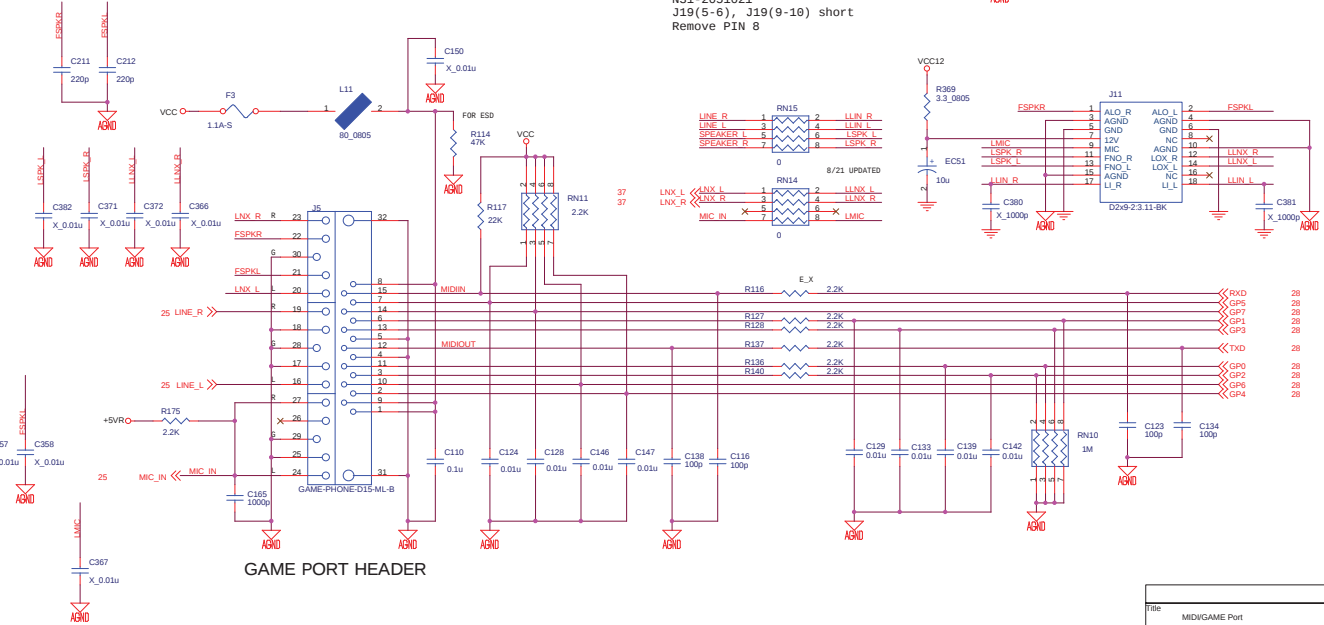
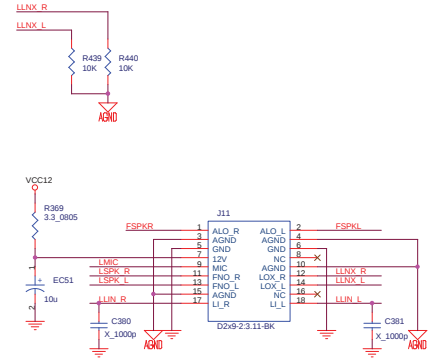




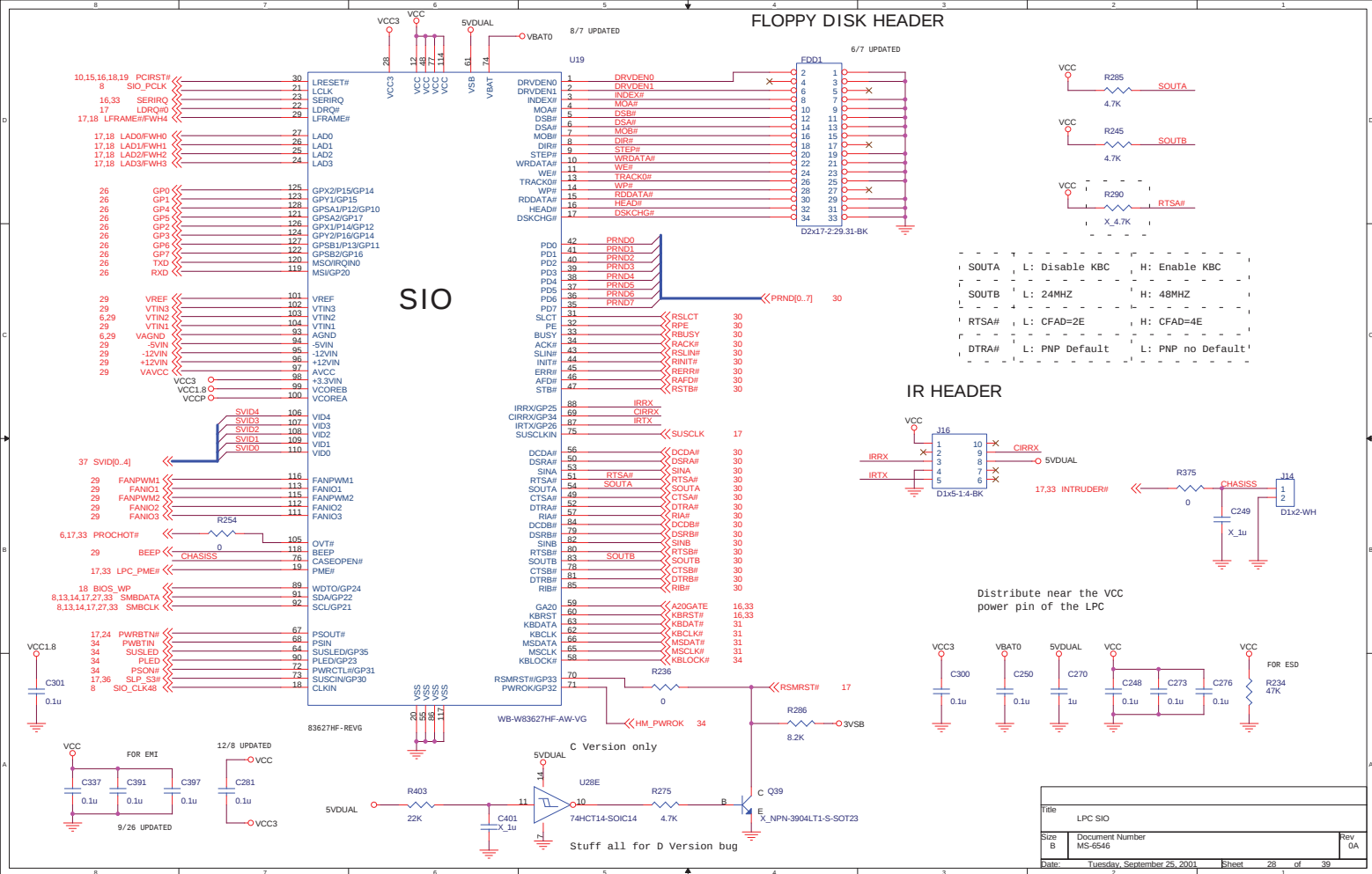




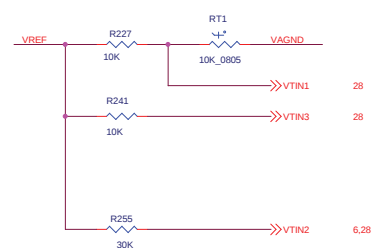
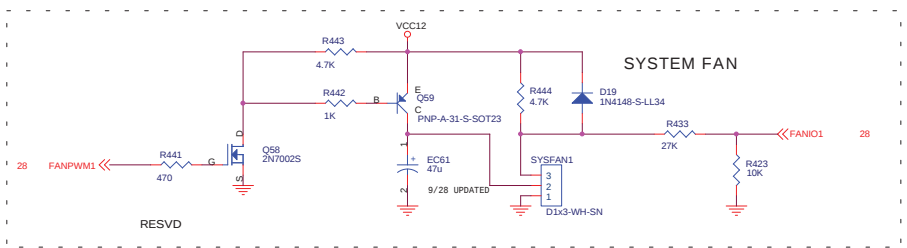
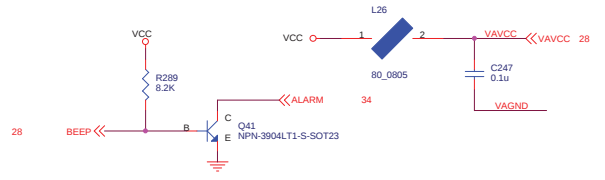
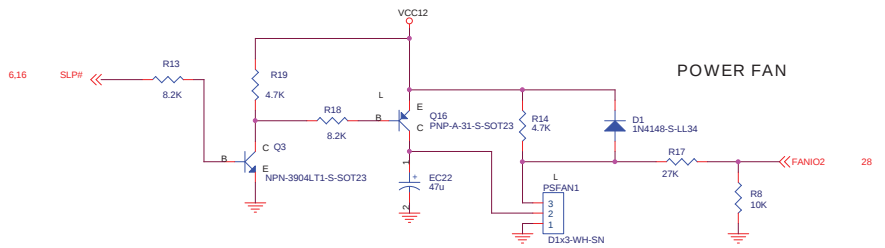
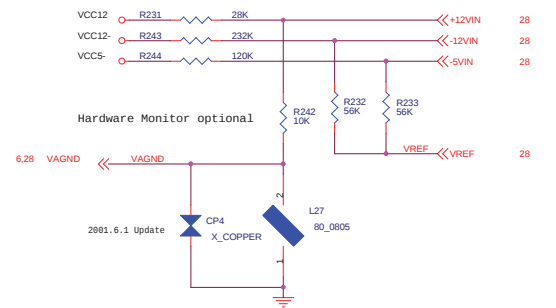
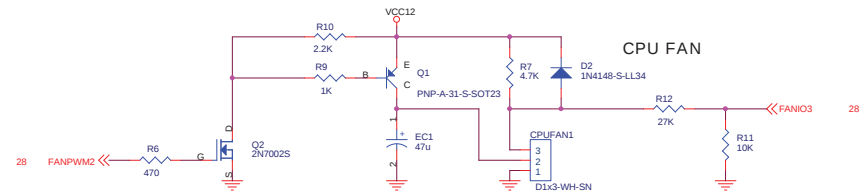
N31-2051021
J19(5-6), J19(9-10) short
Remove PIN 8



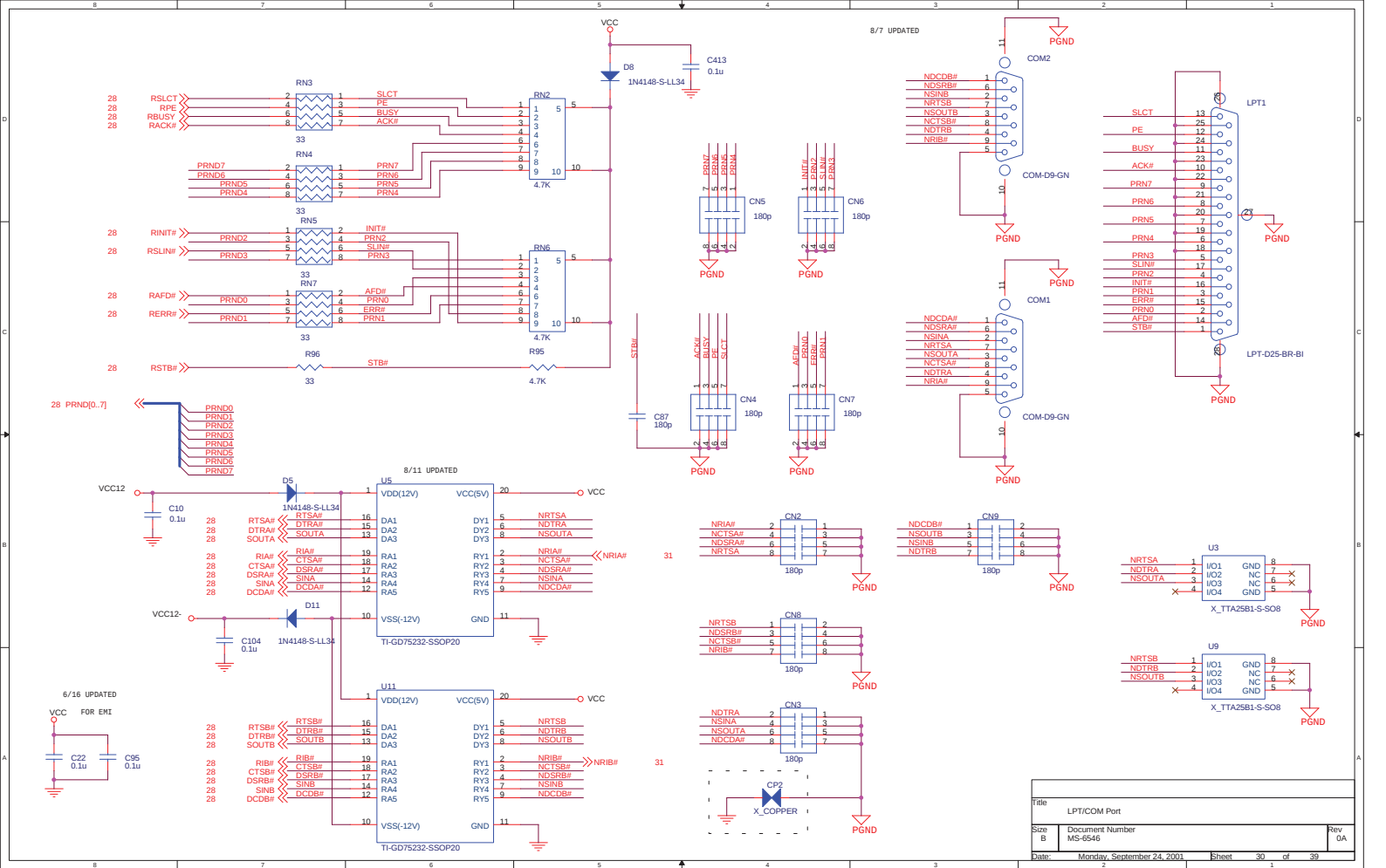
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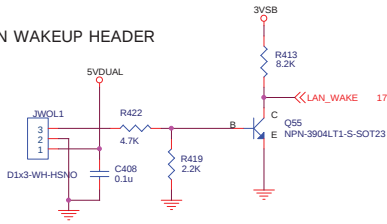
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LPC SIO			
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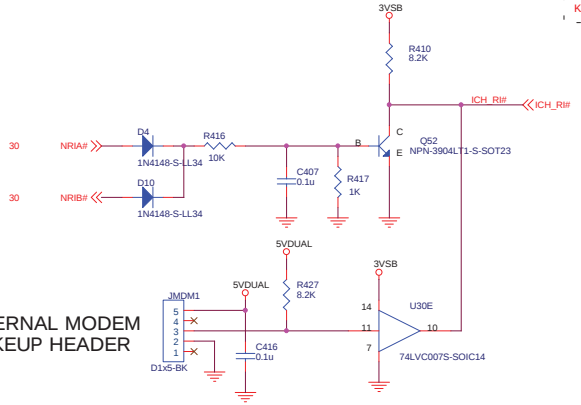
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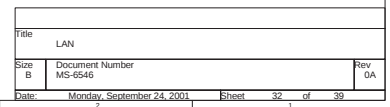
LAN WAKEUP HEADER



INTERNAL MODEM WAKEUP HEADER

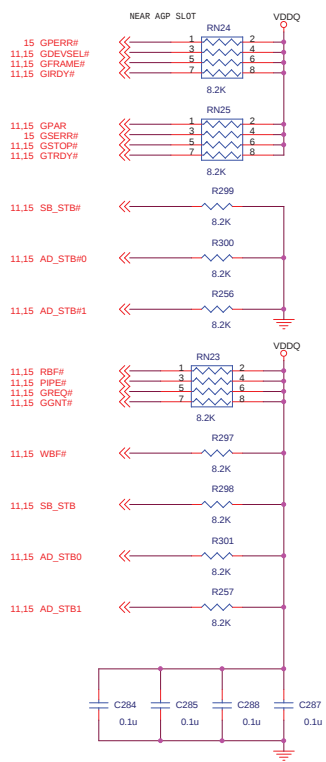


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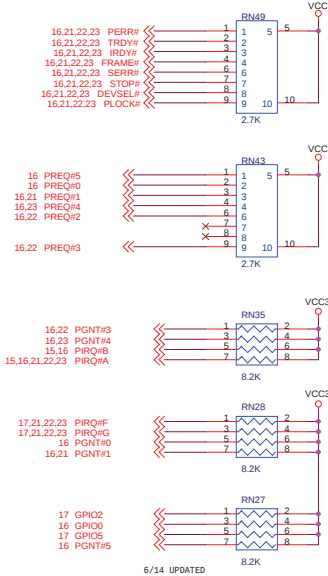


PULL-UP RESISTORS

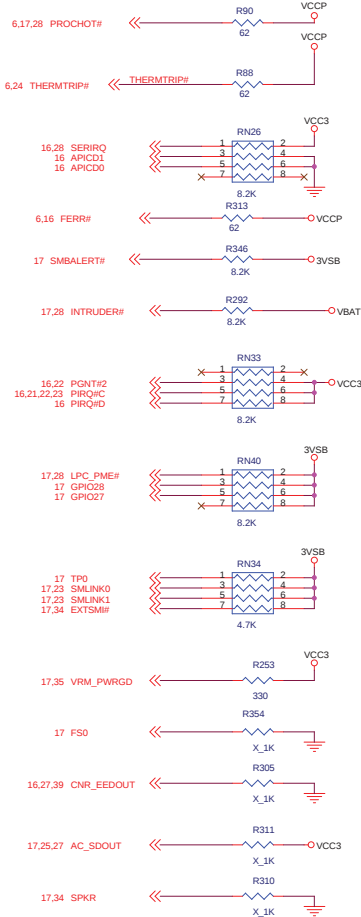
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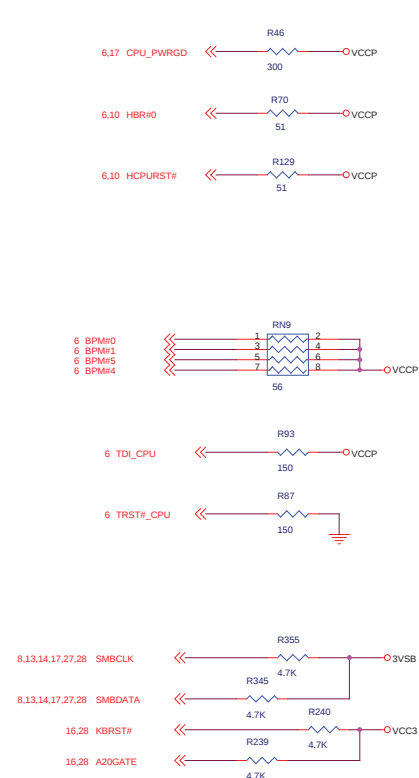
PCI BUS



ICH

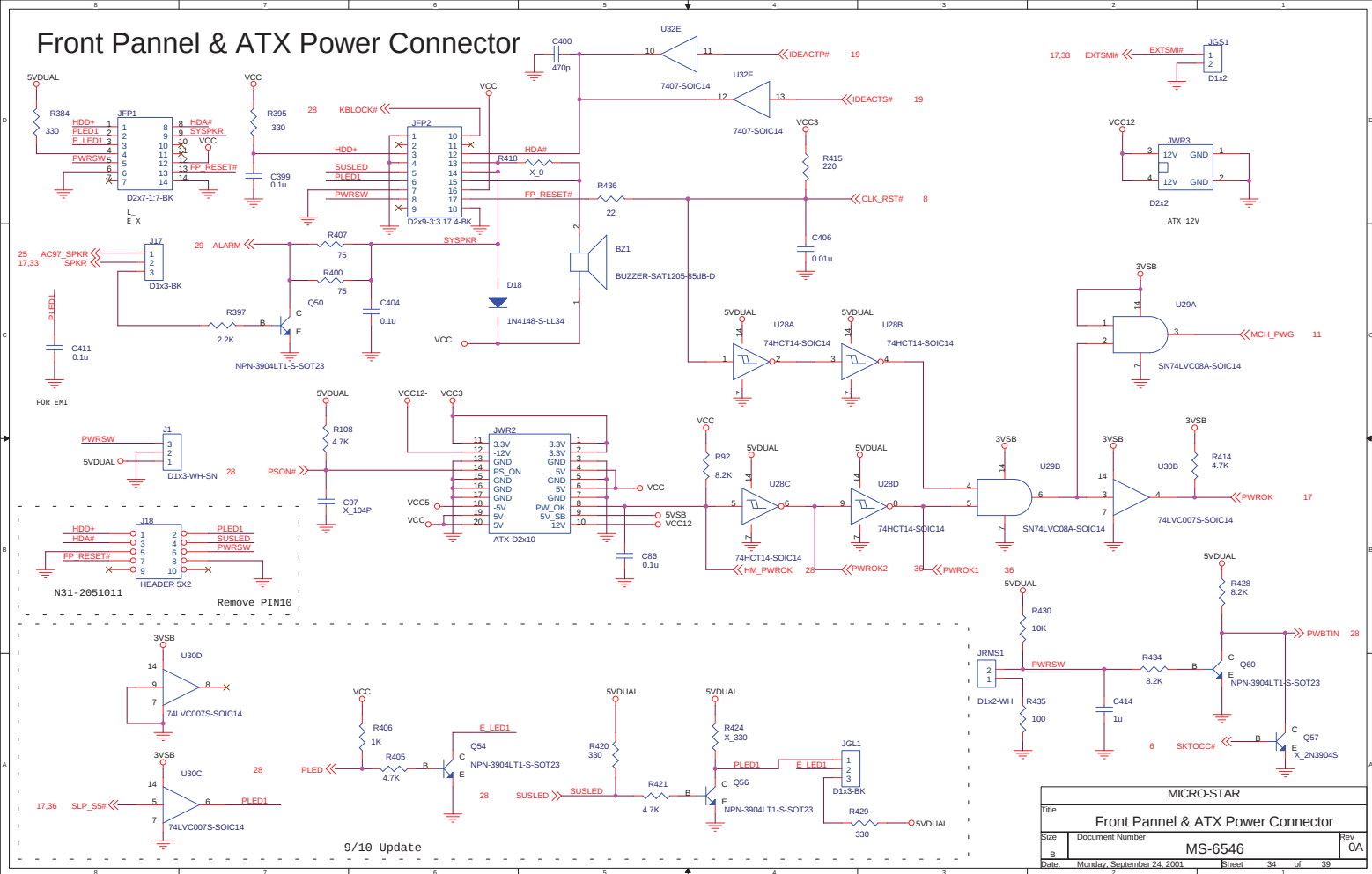


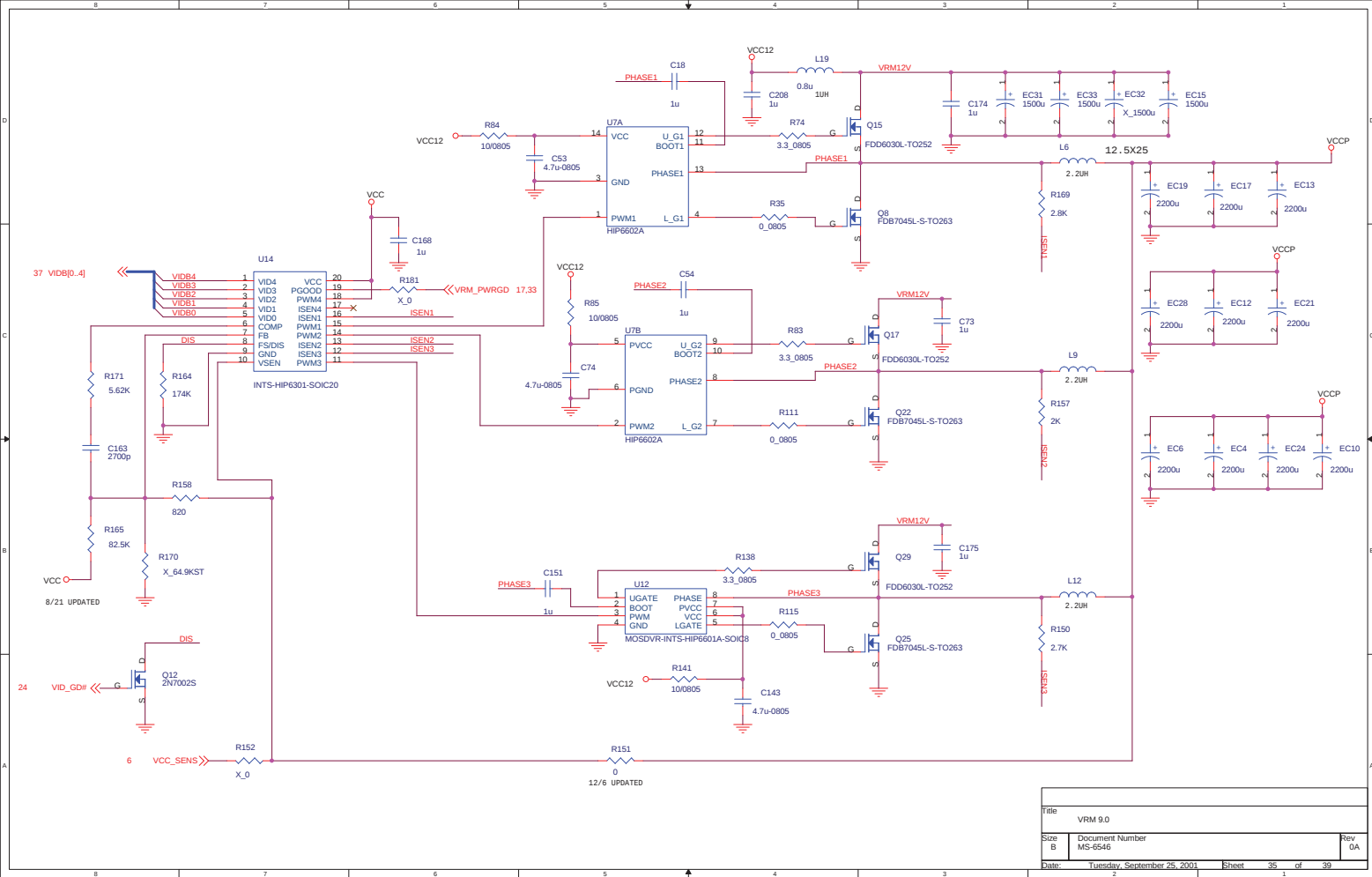
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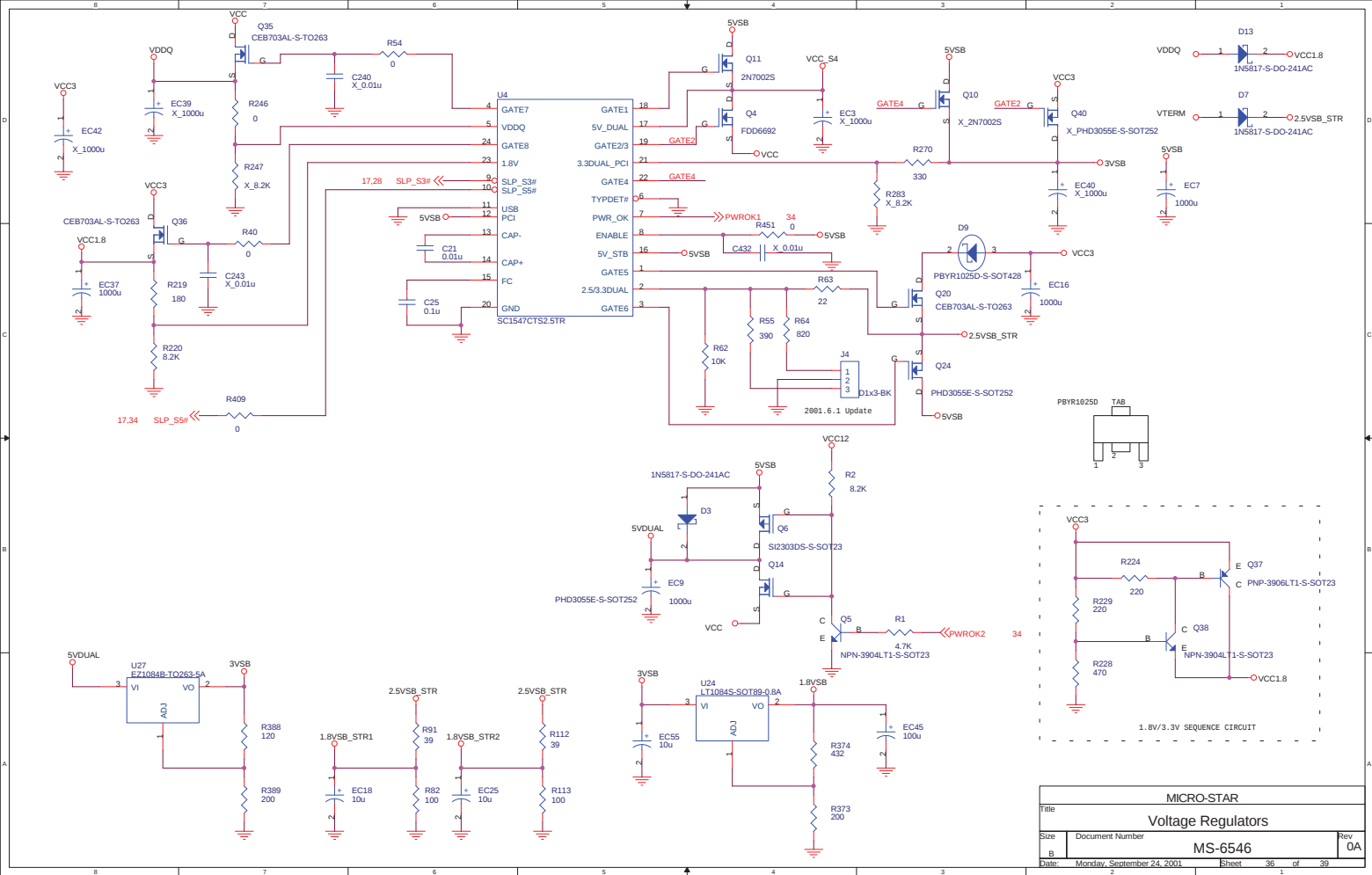
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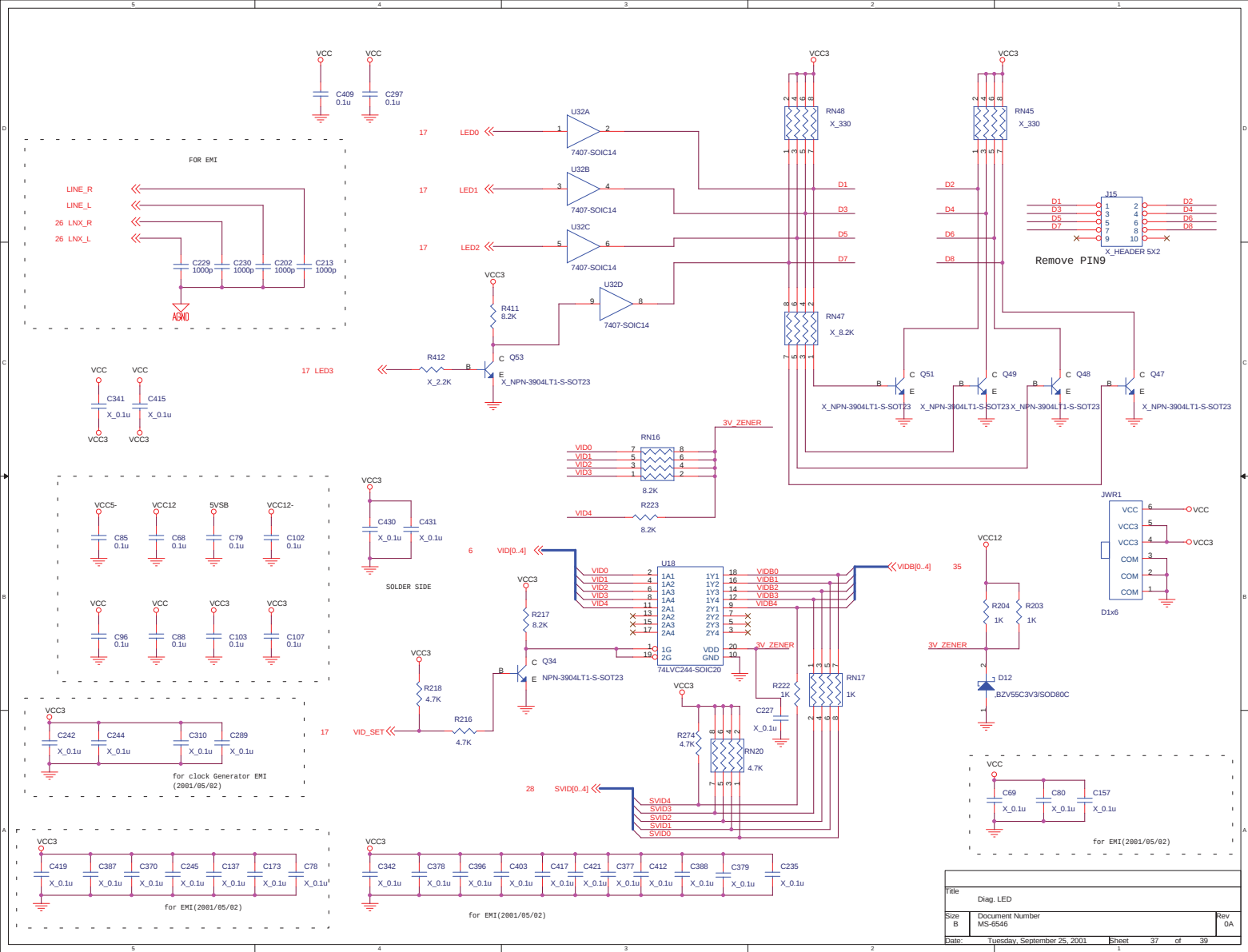
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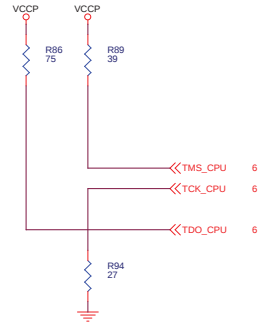
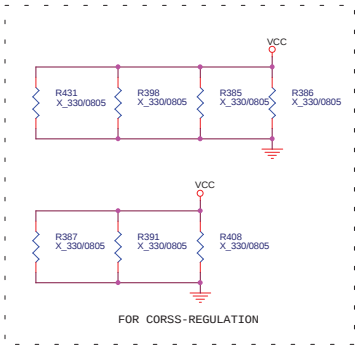
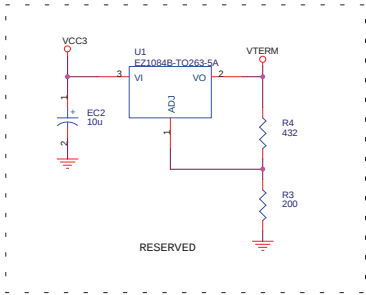




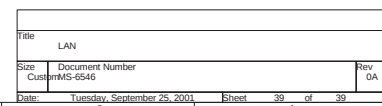
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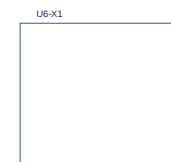
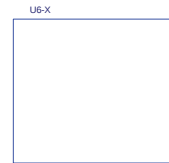
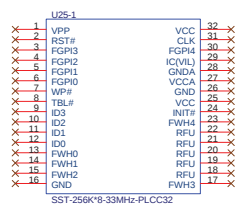
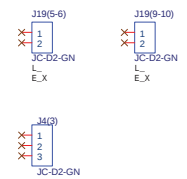
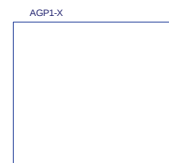
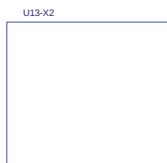
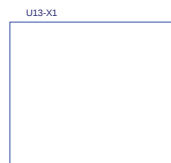
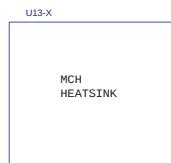
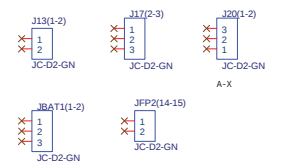
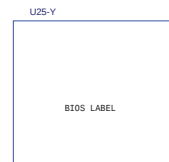
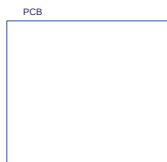
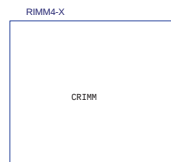
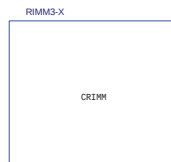




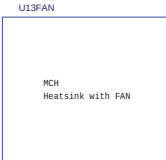
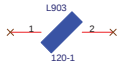
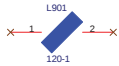
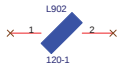
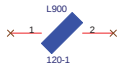
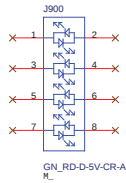
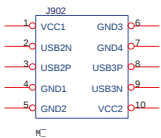
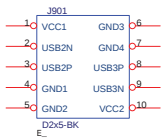


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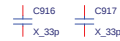
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for MS1



for R335



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