

MS-6545

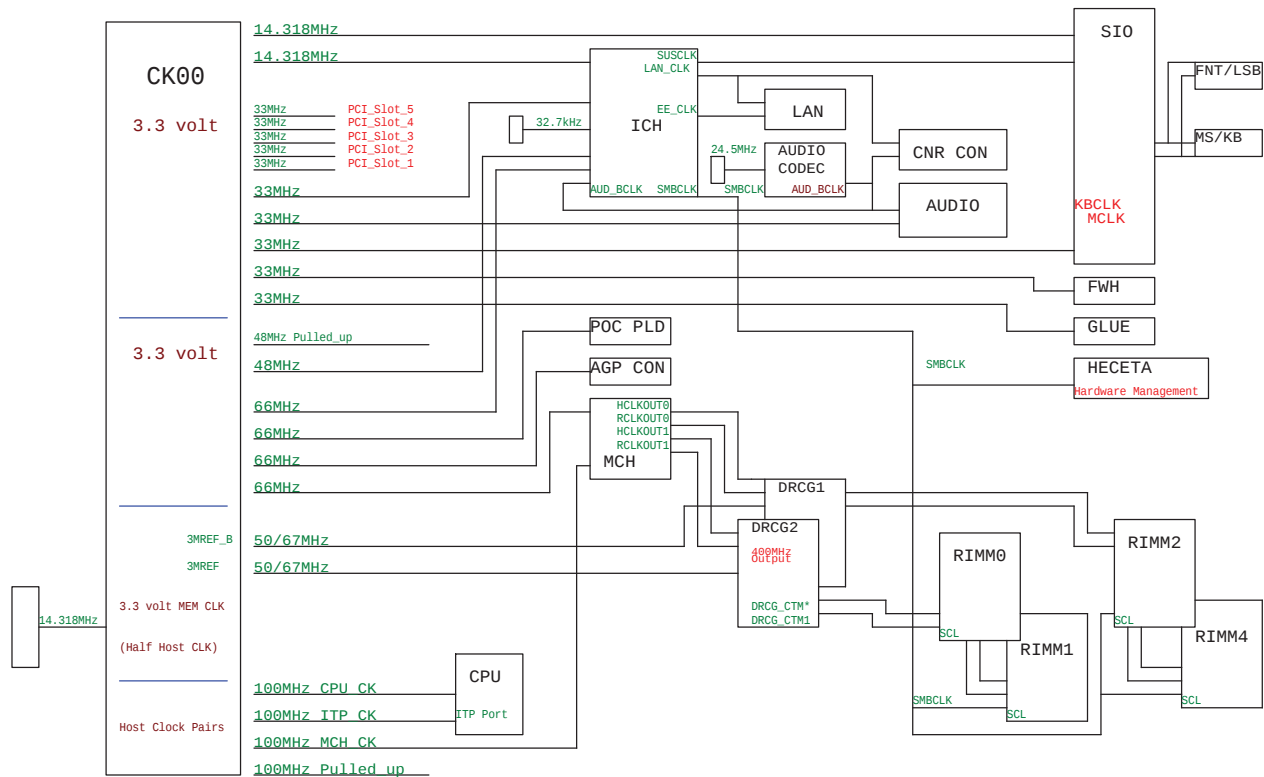
INTEL (R) 850 CHIPSET
WILLAMETTE PROCESSOR SCHEMATICS

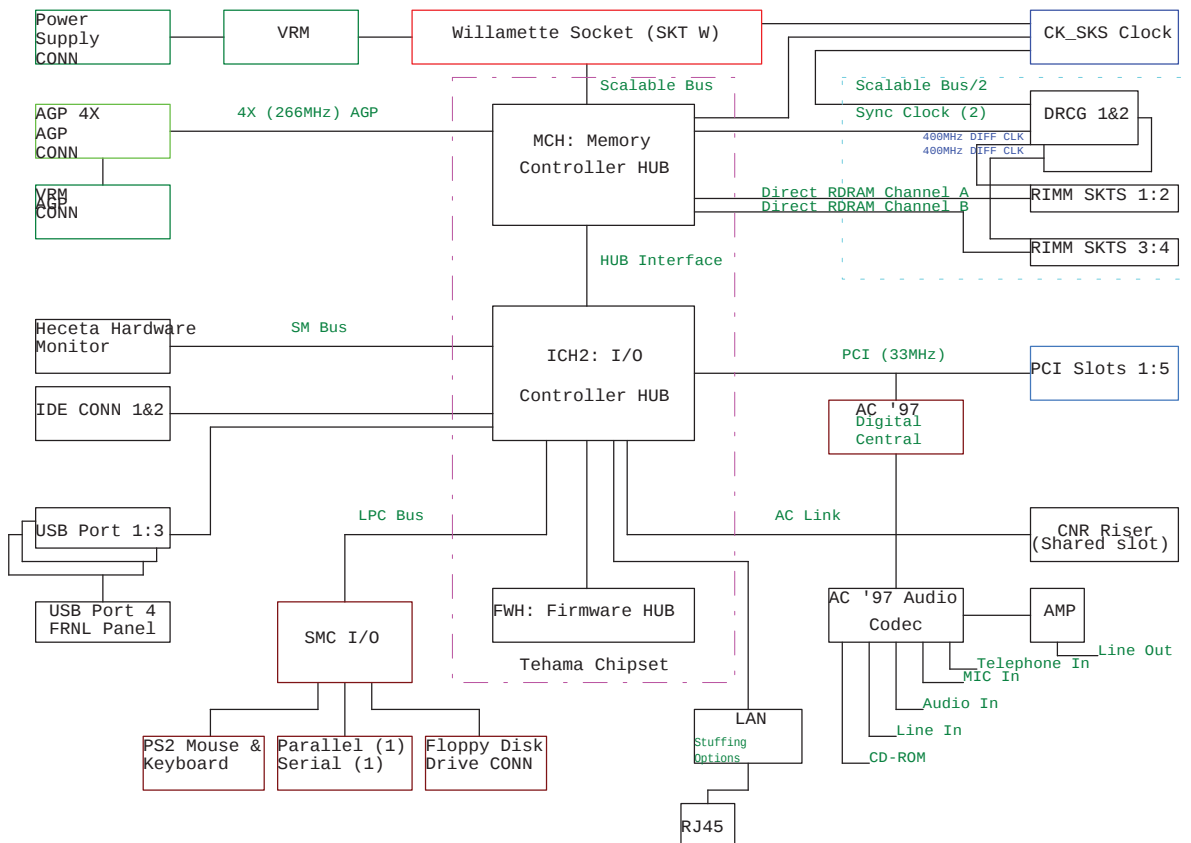
Title	Page	Title	Page
Cover Sheet	1	Audio	25,26
Block Diagram	2,3,4,5	CNR	27
Processor Sockets	6,7	LPC/Flopy Connector	28
Clock Synthesizer	8	Hardware Monitor	29
DRCG	9	Parallel Port/Serial Port	30
MCH	10,11,12	KeyBoard/Mouse Ports	31
SCK/CMD	12	SPARE	32
RIMM	13,14	Pull-up Resistors	33
AGP	15	Front Pannel	34
ICH2	16,17	VRM 9.0	35
FWH	18	Voltage Regulator	36
IDE Connectors	19	MISC	37
USB Connectors	20	ITP PORT	38
PCI Connectors	21,22,23	MANUAL PARTS	39
VID Circuit	24	OPT PARTS	40
		LAN	41
		USB 2.0	42,43

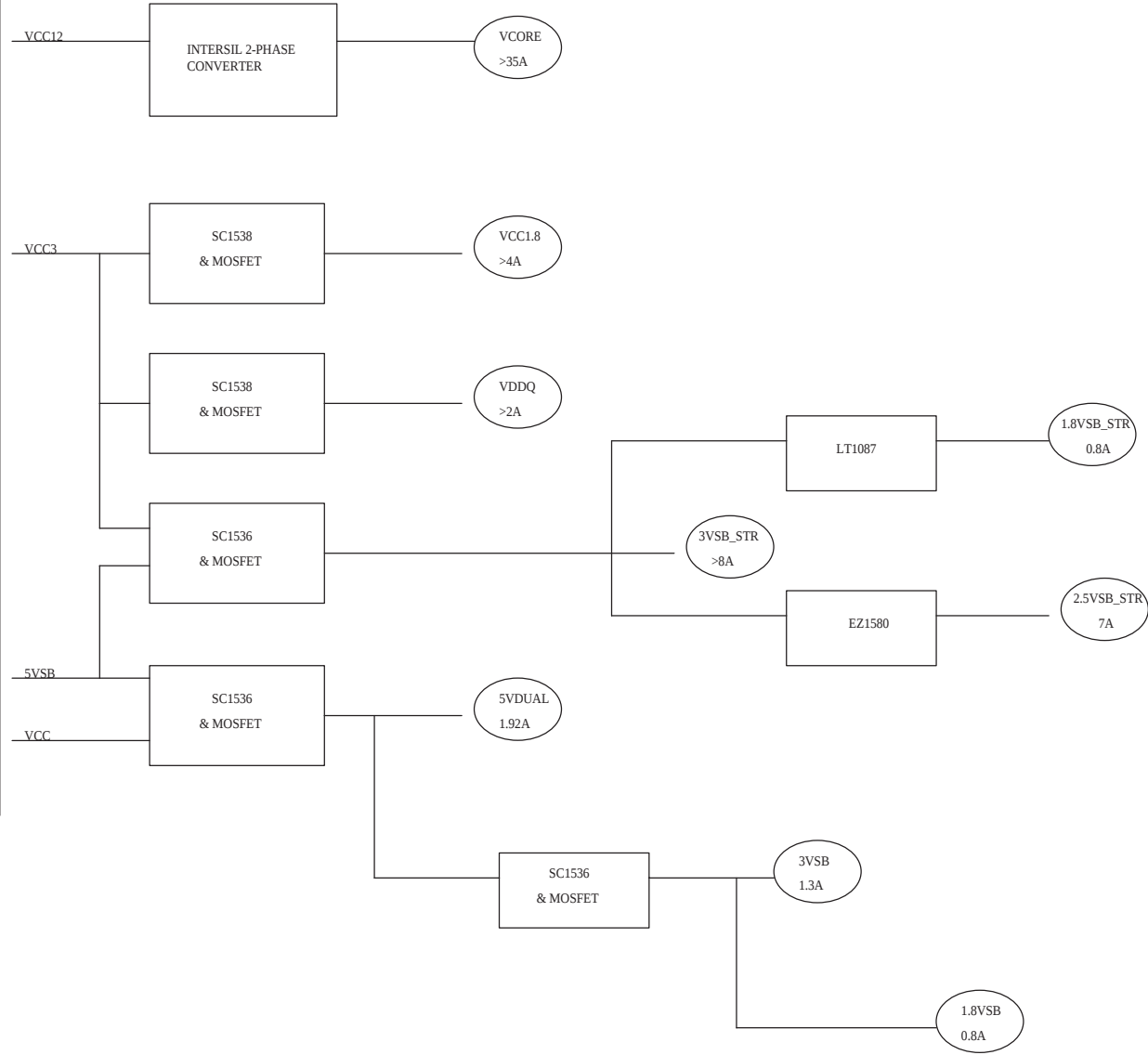
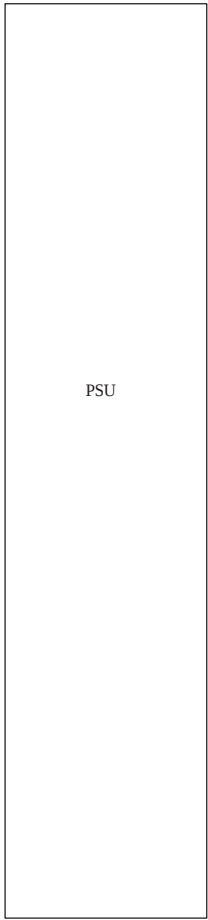
Options

X: DEFAULT NOT STUFFED

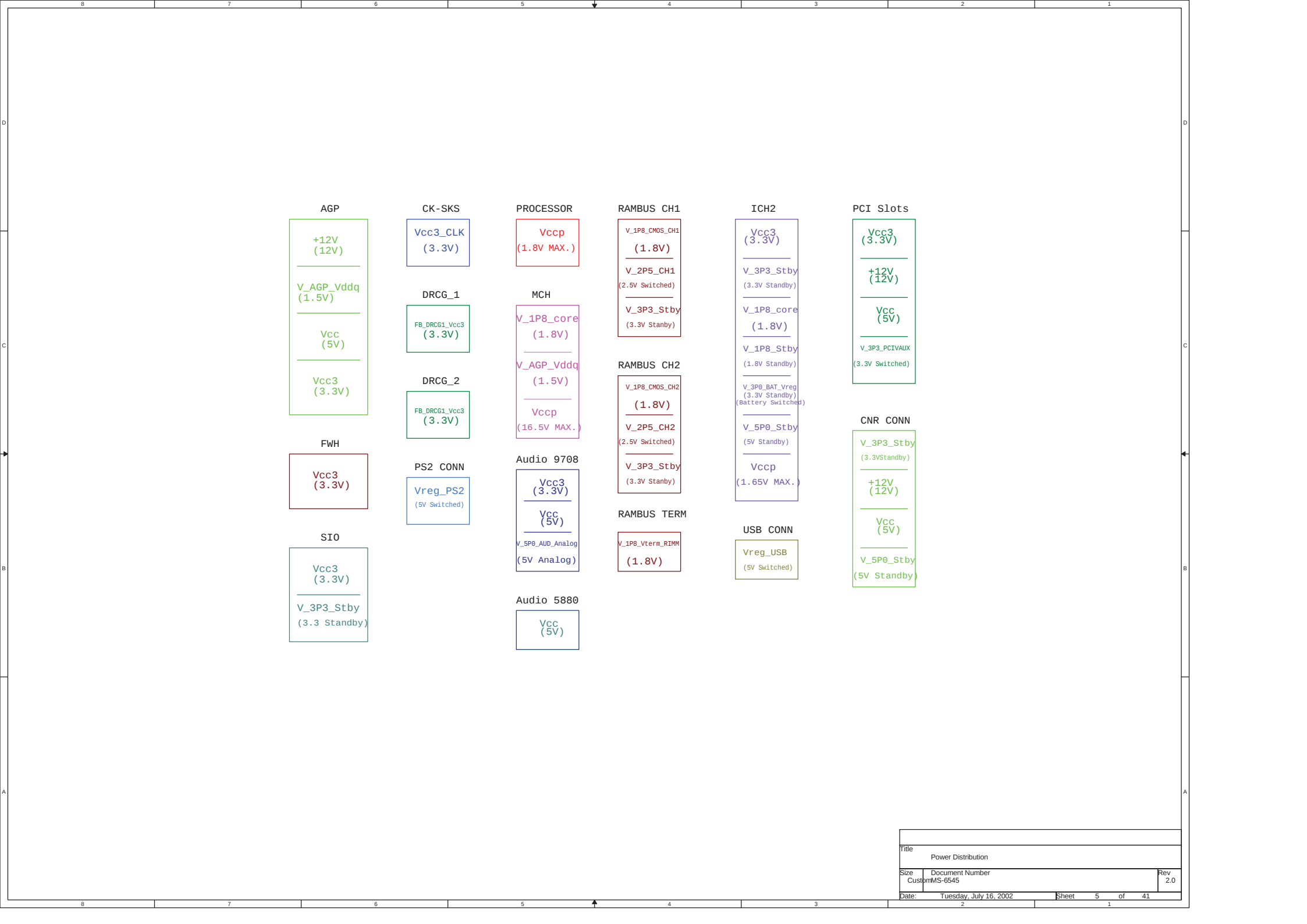
Willamette Mother Board			
Title			
COVER			
Size	Document Number		Rev
Custom	MS-6545		2.0
Date:	Tuesday, July 16, 2002	Sheet	1 of 41

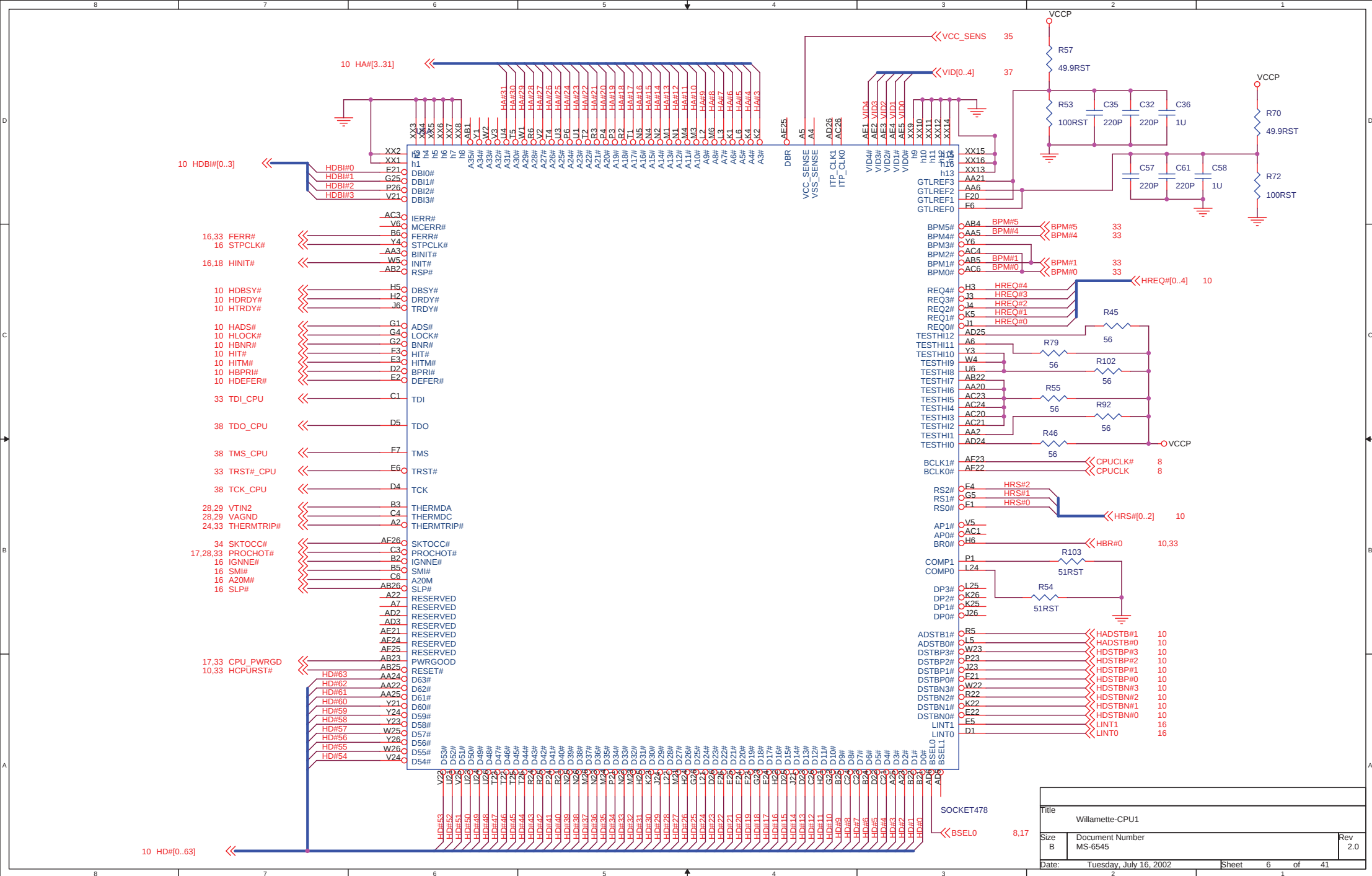


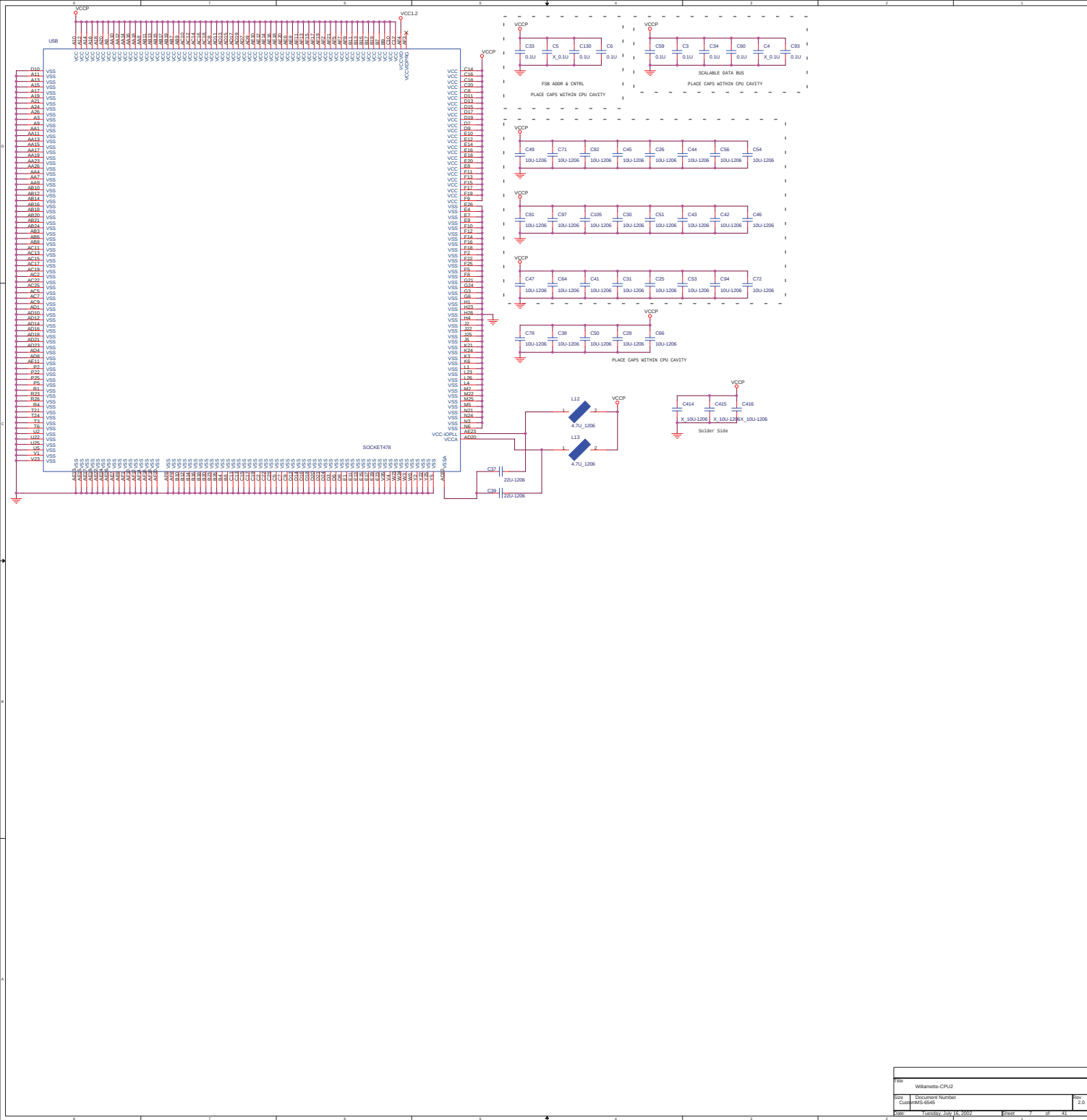


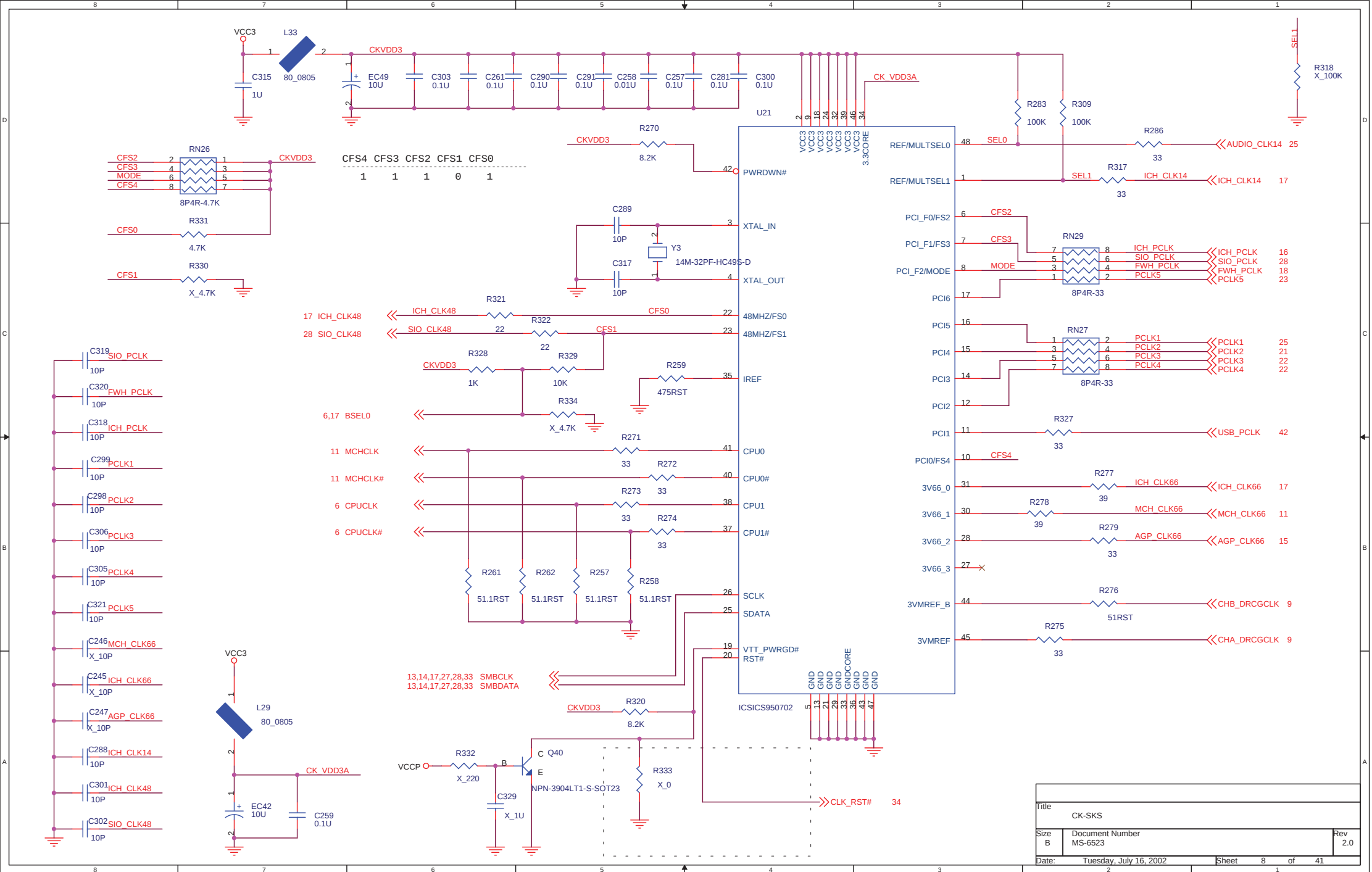


Title			
Reset/Power on Map			
Size	Document Number		Rev
Custom	MS-6545		2.0
Date:	Tuesday, July 16, 2002	Sheet	4 of 41



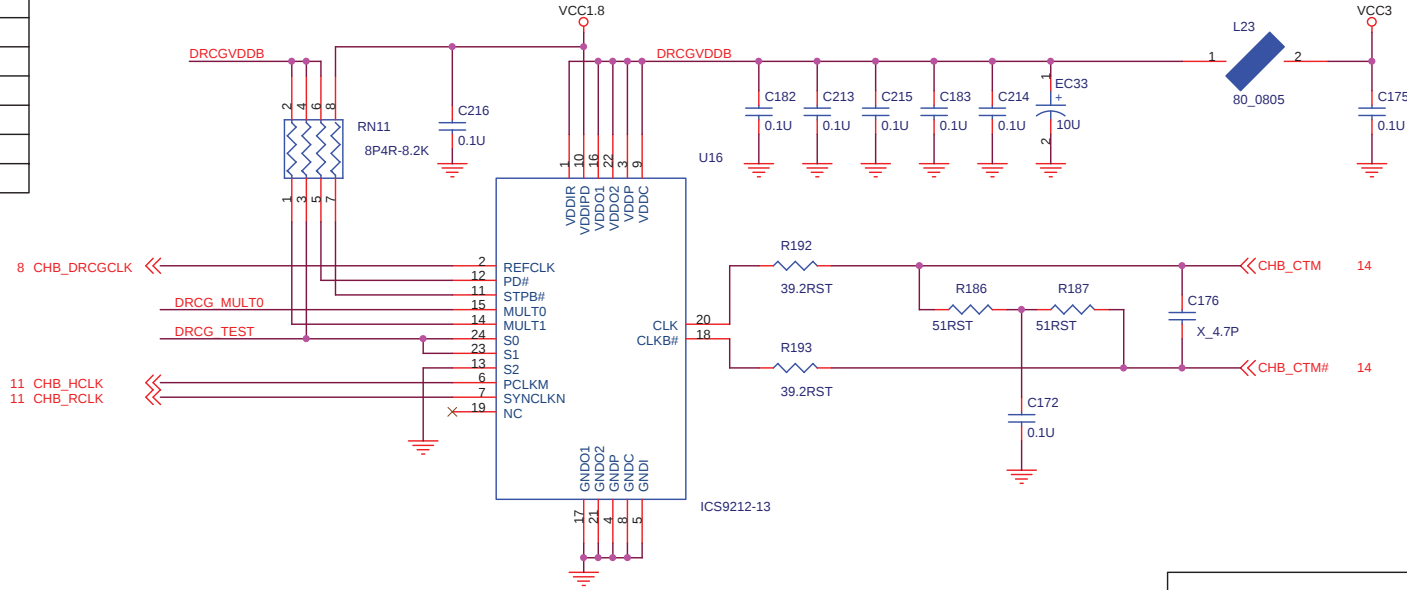
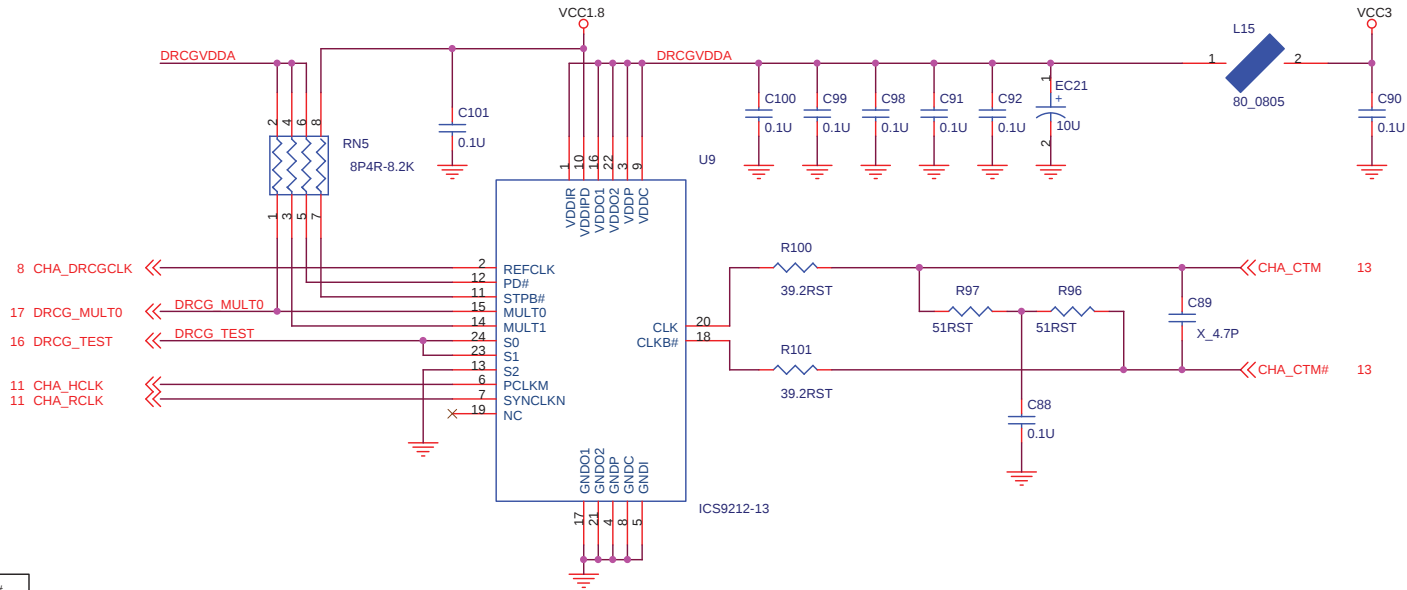




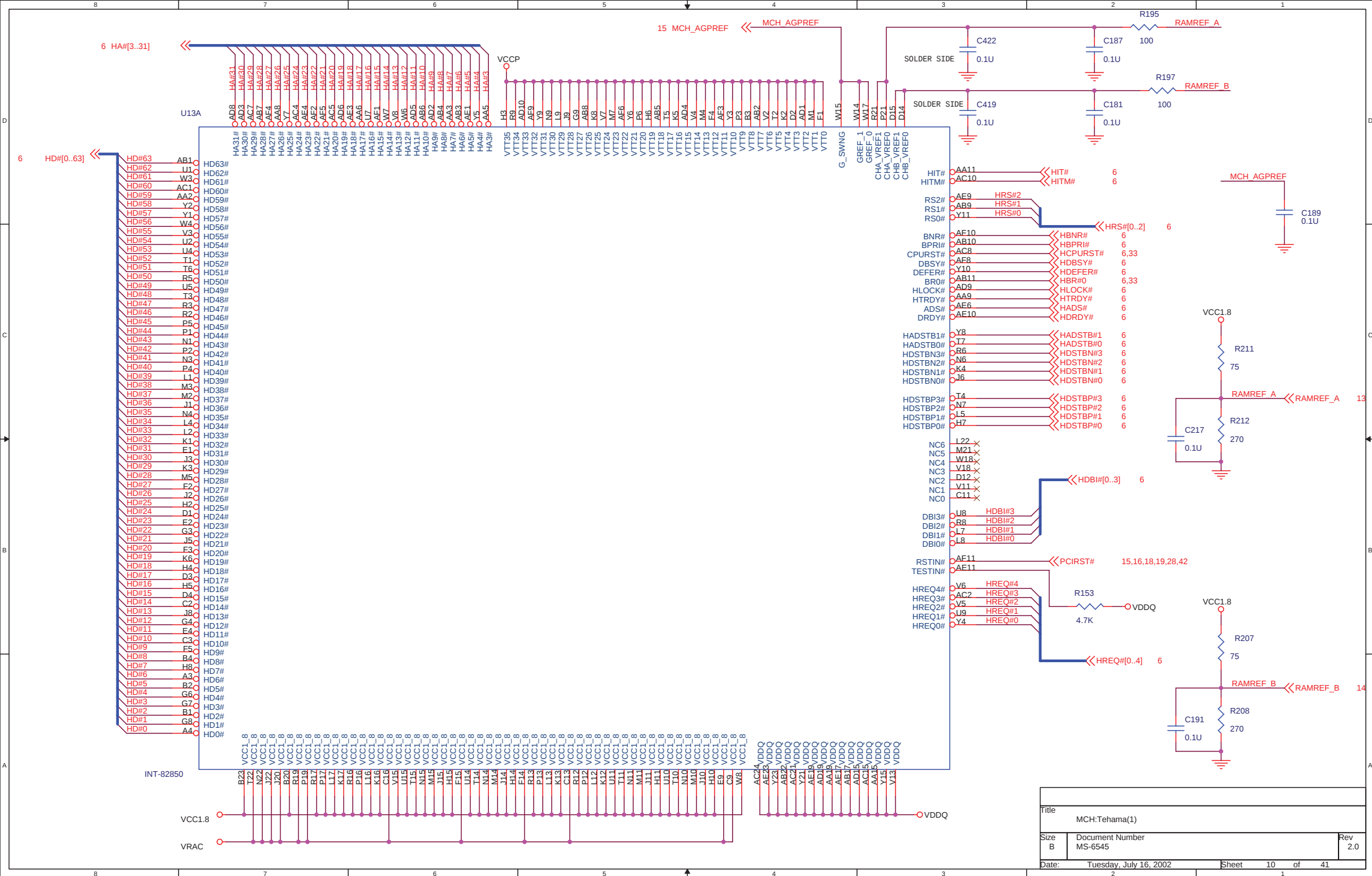


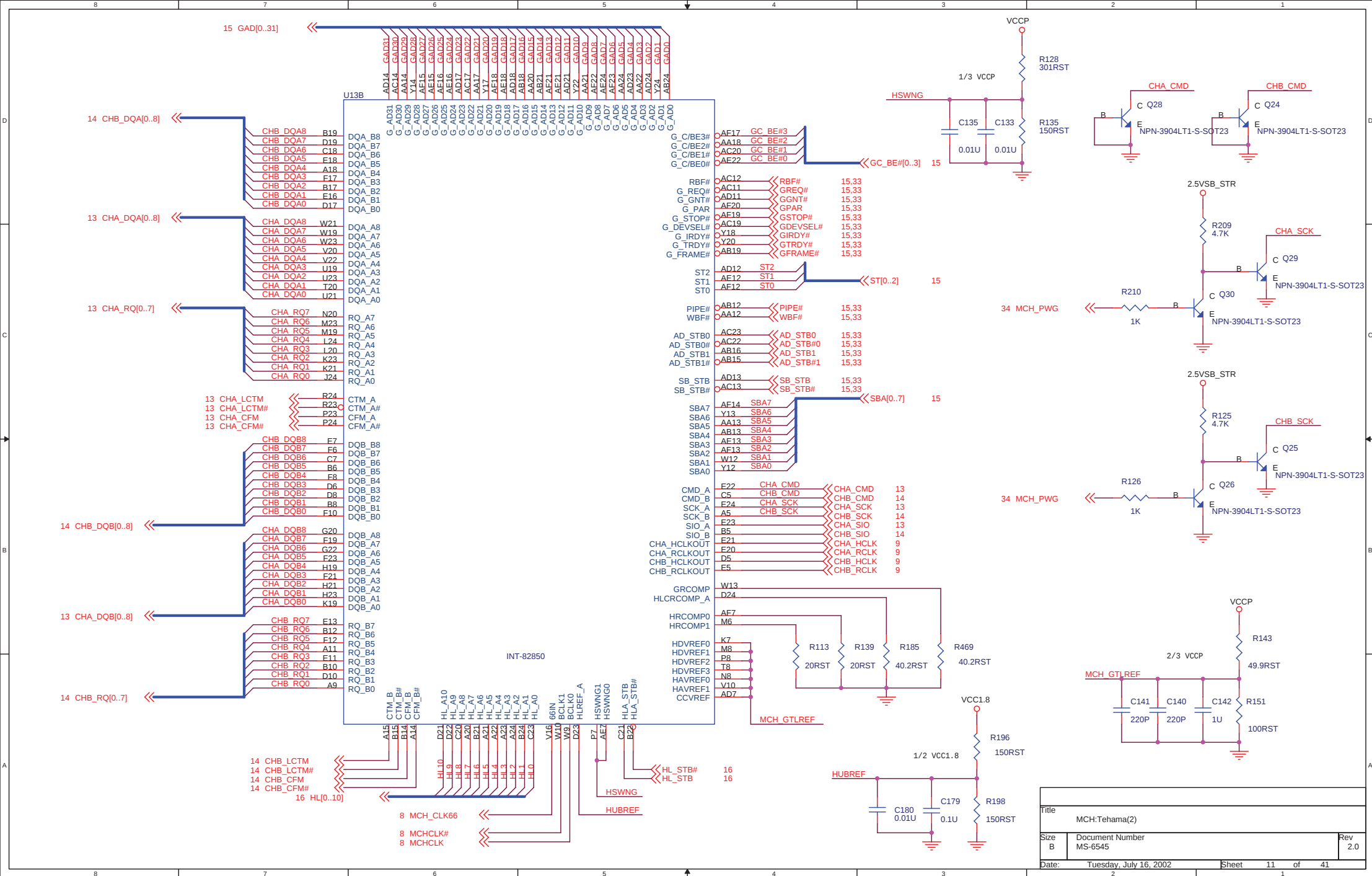
Title			
CK-SKS			
Size	Document Number		Rev
B	MS-6523		2.0
Date:	Tuesday, July 16, 2002		Sheet 8 of 41

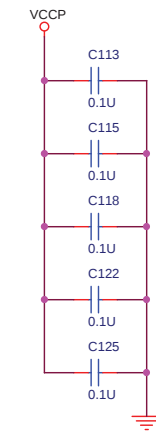
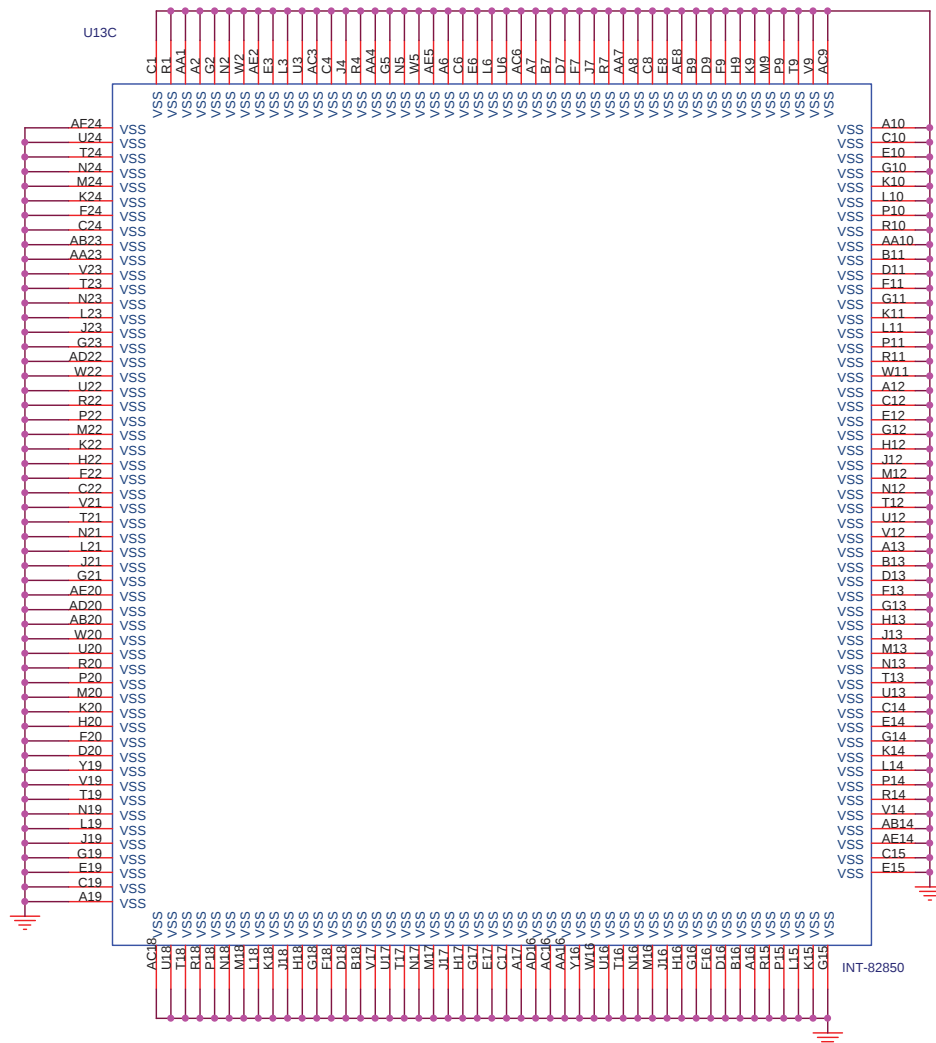
FUNCTION	S0	S1	S2	CLK	CLKB#
NORMAL	0	0	0		
BYPASS	1	0	0		
TEST	1	1	0		
OUTPUT TEST	0	1	*		
RESERVED	0	0	1		
RESERVED	1	0	1		
RESERVED	1	1	1		



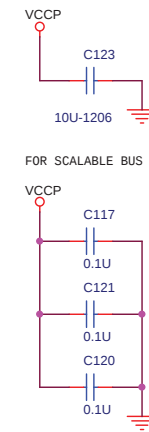
Title			DRCG
Size	Document Number	Rev	
B	MS-6545	2.0	
Date:	Tuesday, July 16, 2002	Sheet	9 of 41





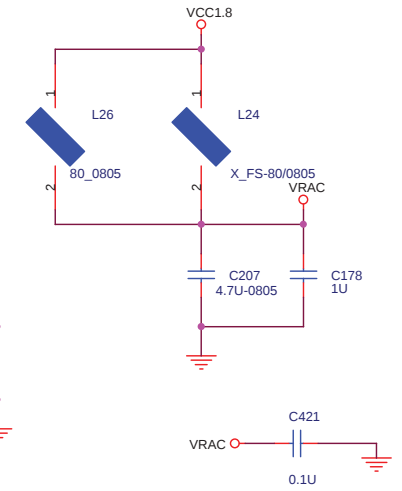
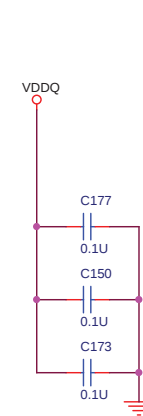


FOR SCALABLE DATA BUS

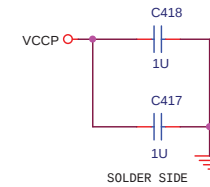


FOR SCALABLE BUS GENERIC

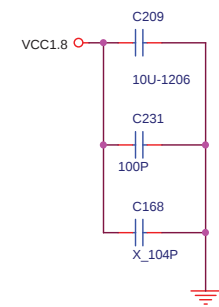
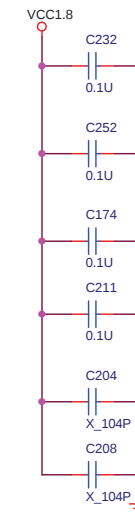
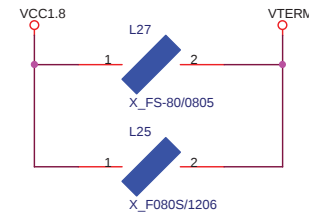
FOR SCALABLE BUS ADDR & CNTRL



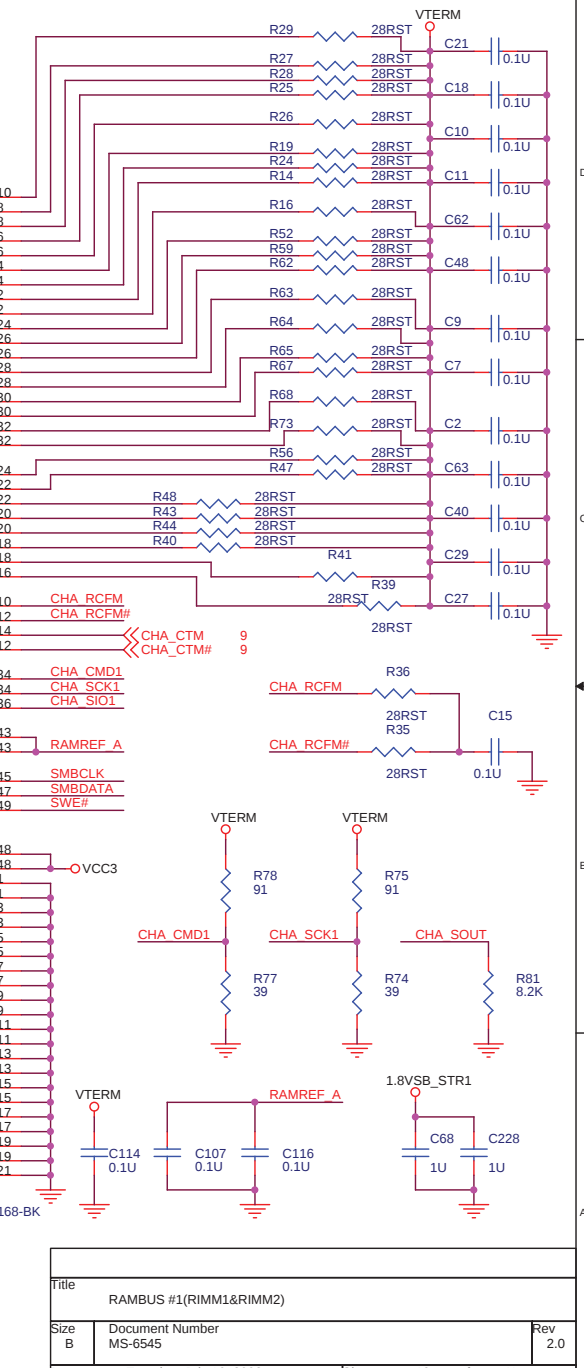
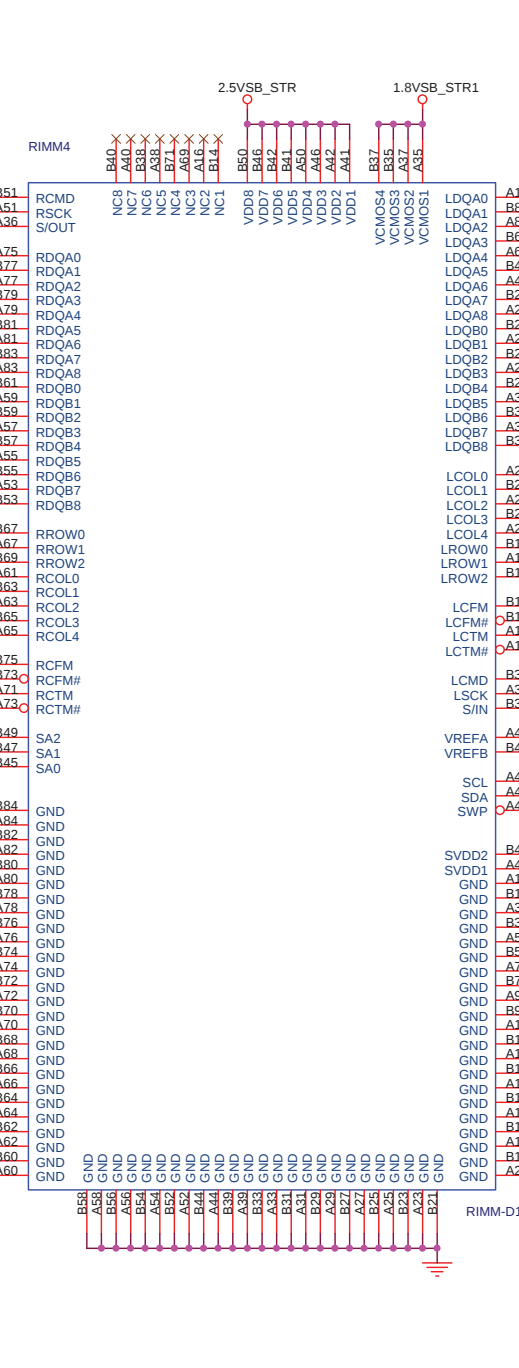
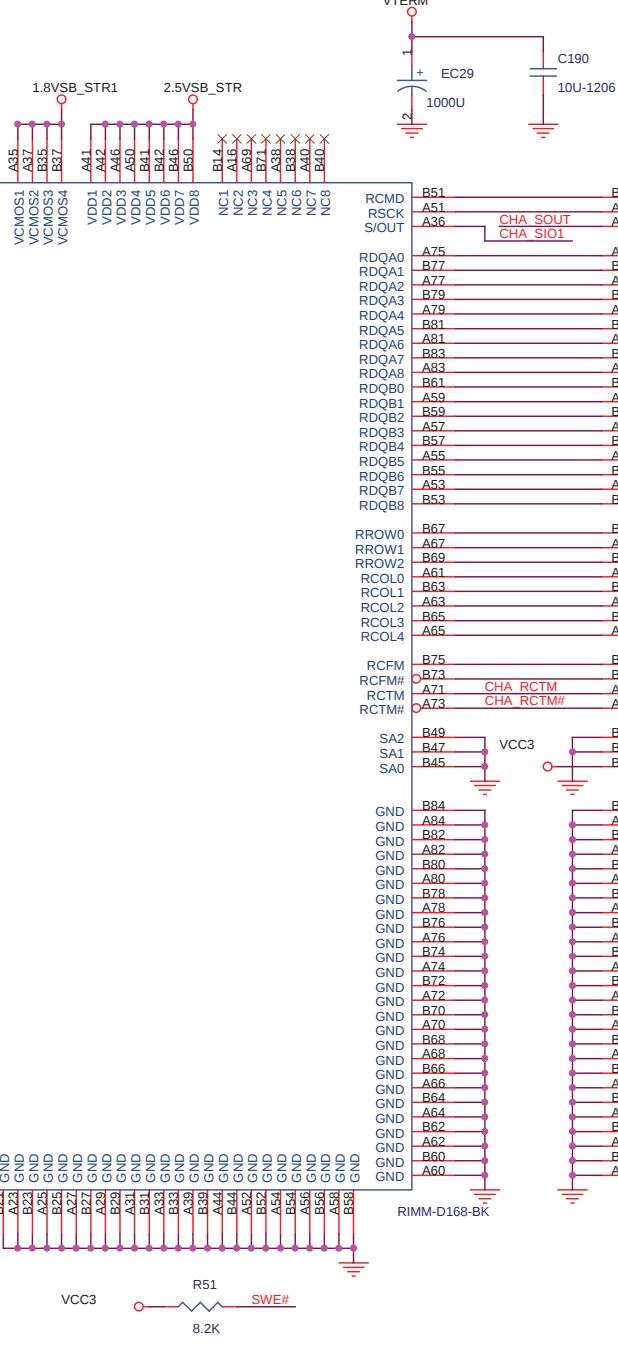
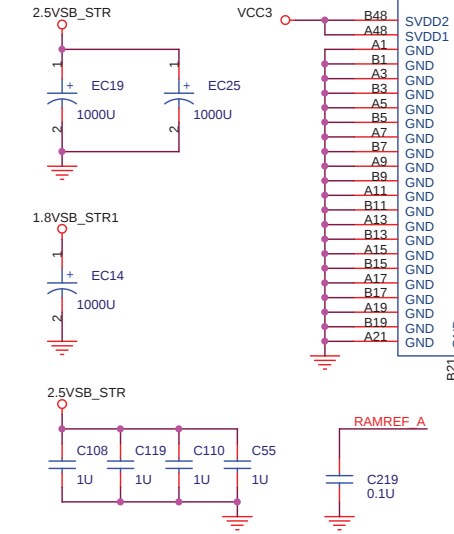
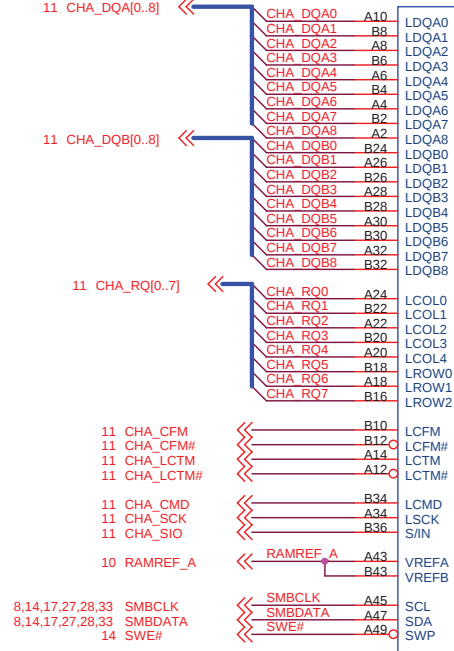
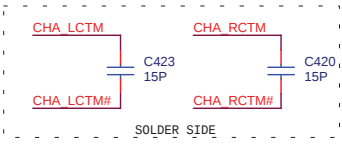
SOLDER SIDE



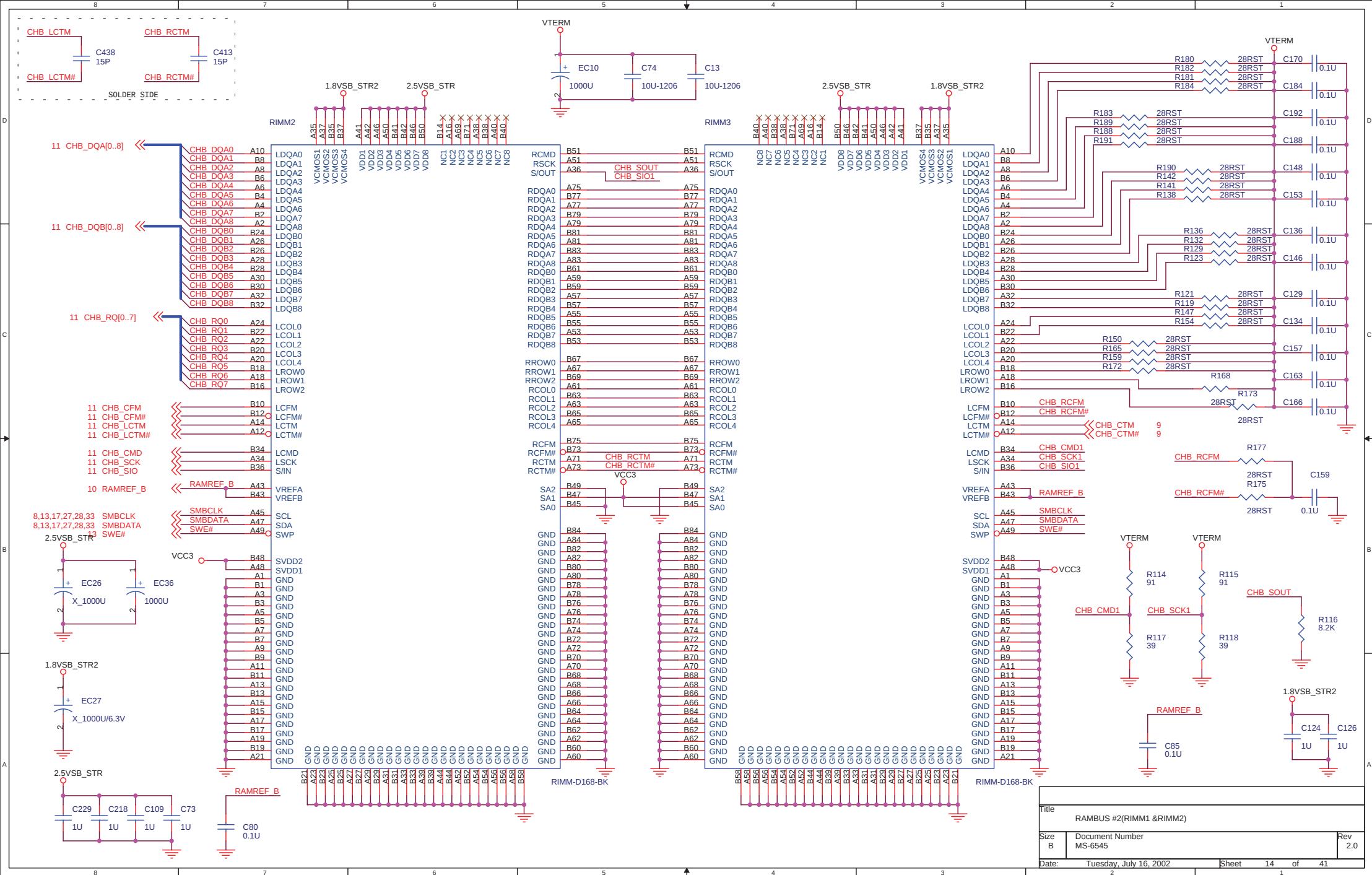
SOLDER SIDE

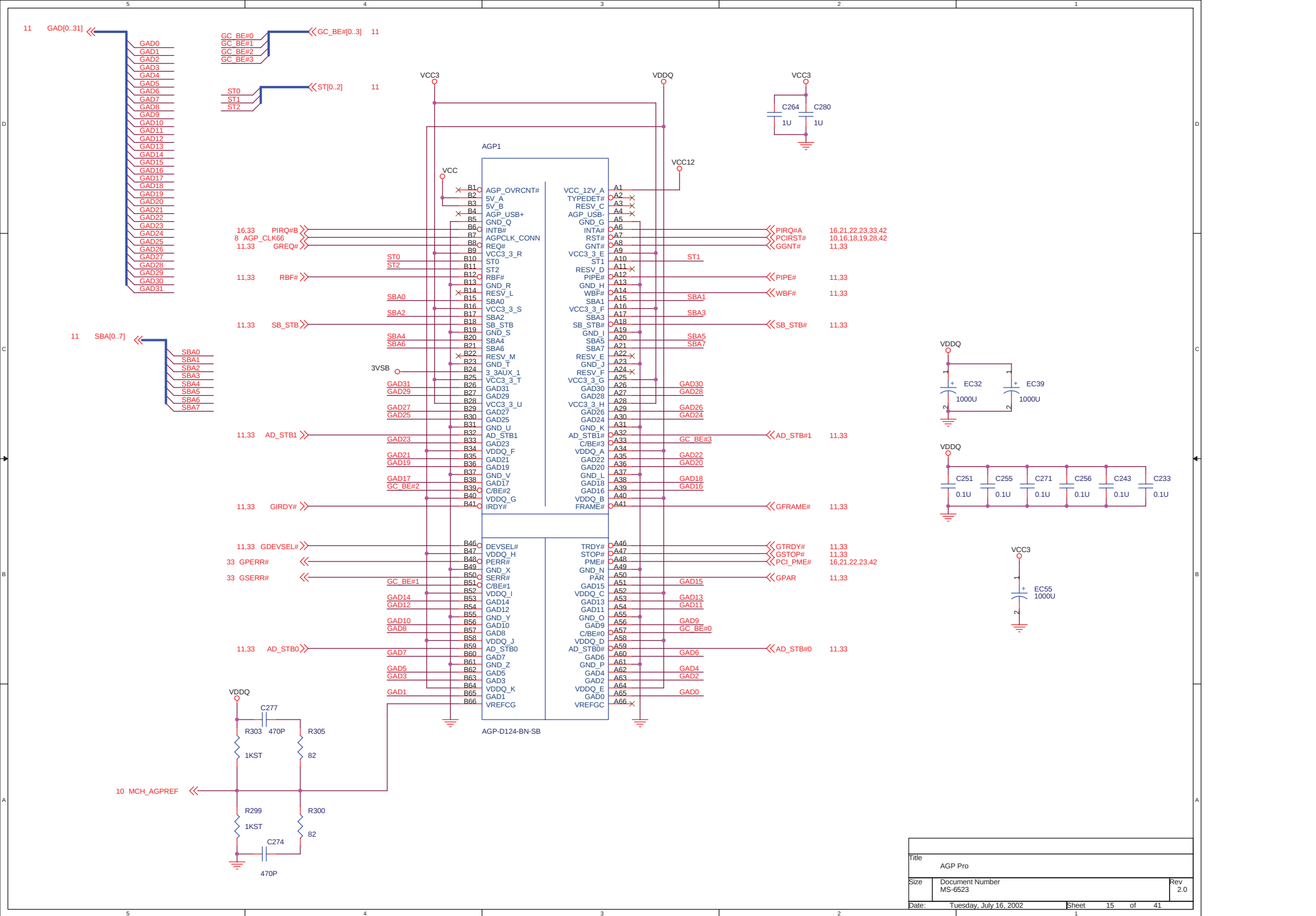


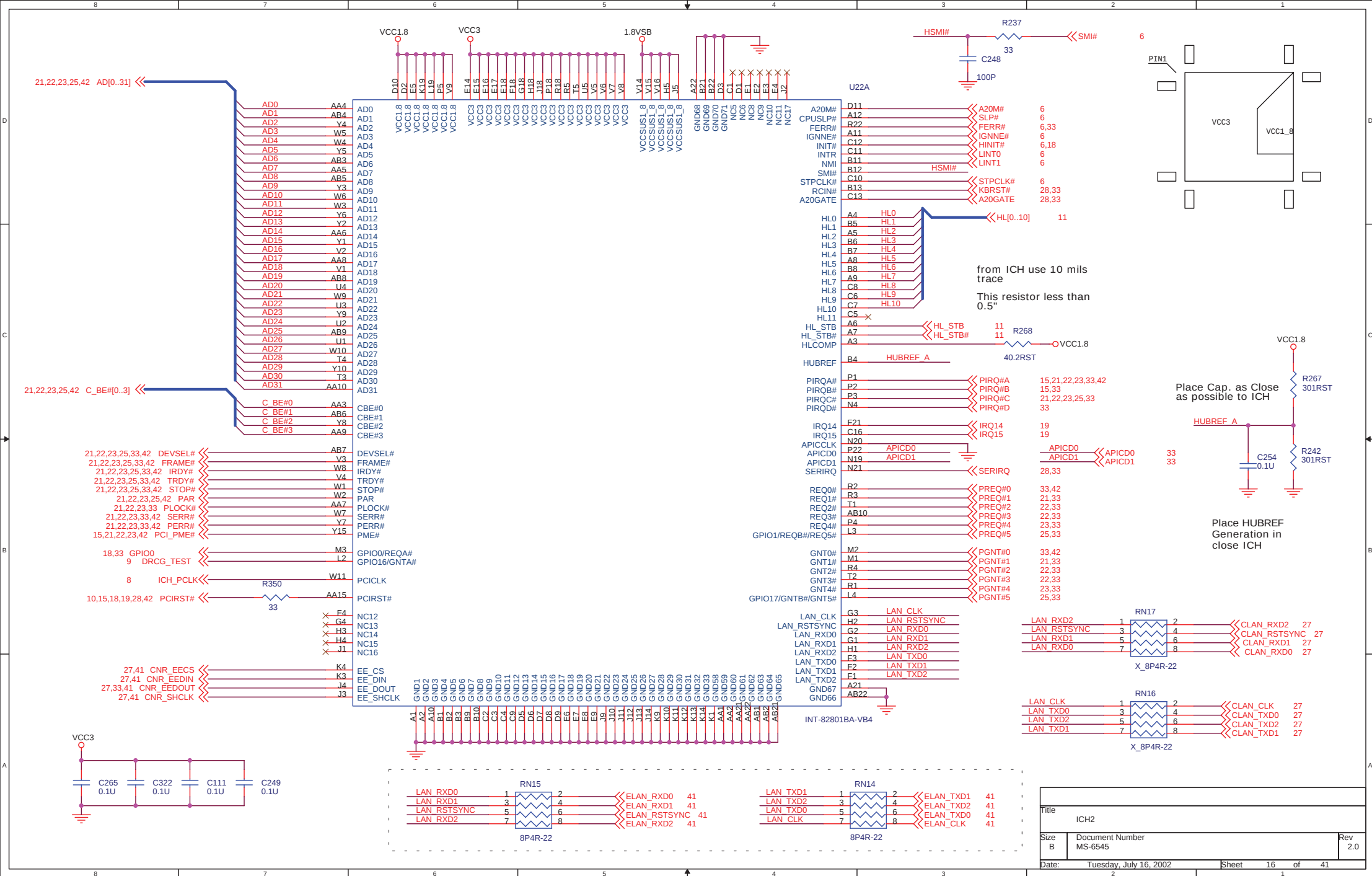
Title				
MCH:Tehame(3)				
Size B	Document Number MS-6545			Rev 2.0
Date:	Tuesday, July 16, 2002	Sheet	12 of 41	

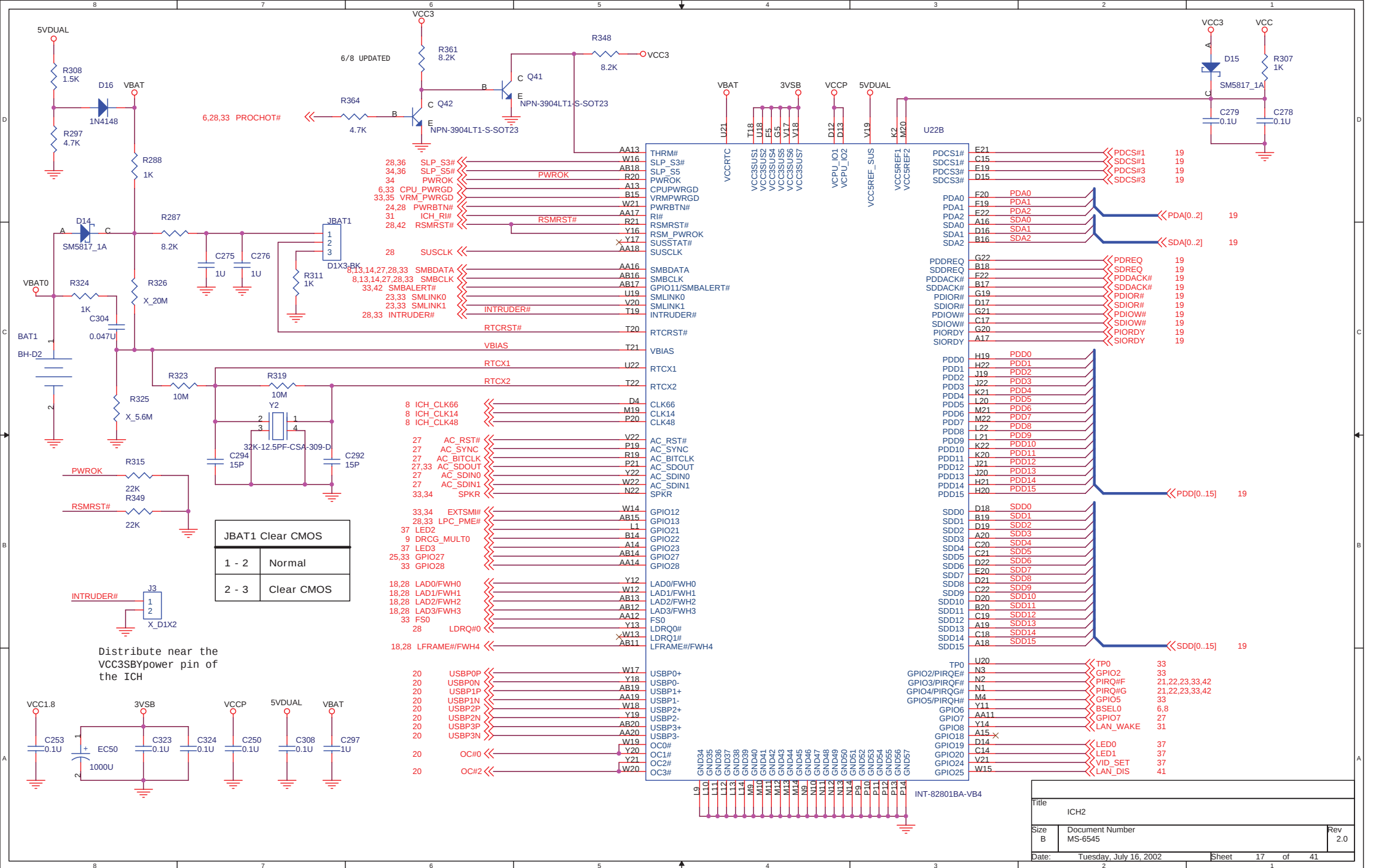


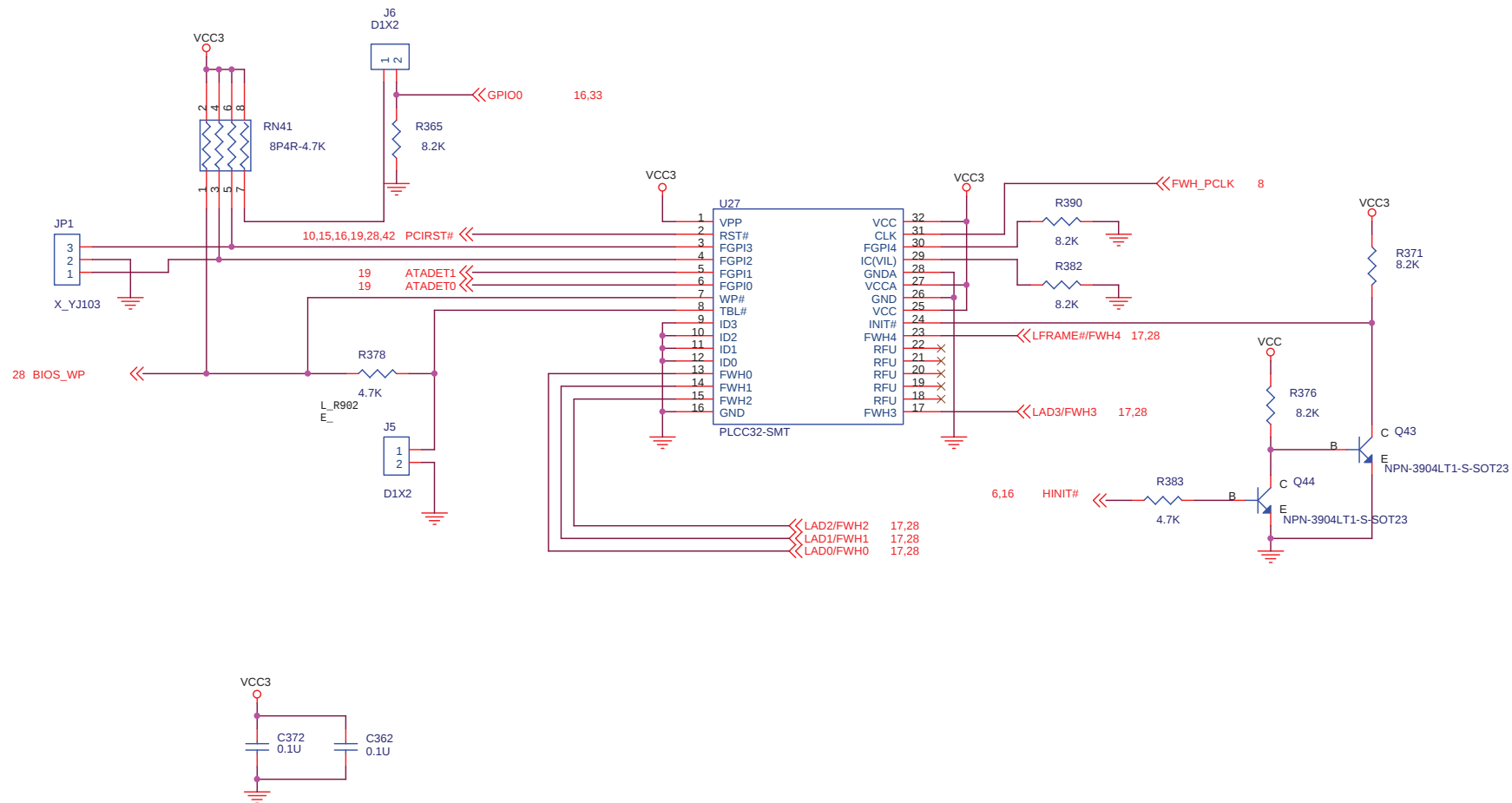
Title		
RAMBUS #1(RIMM1&RIMM2)		
Size	Document Number	Rev
B	MS-6545	2.0
Date:	Tuesday, July 16, 2002	Sheet 13 of 41





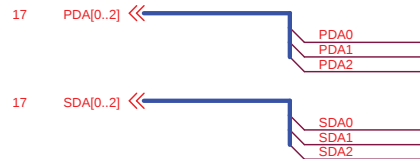
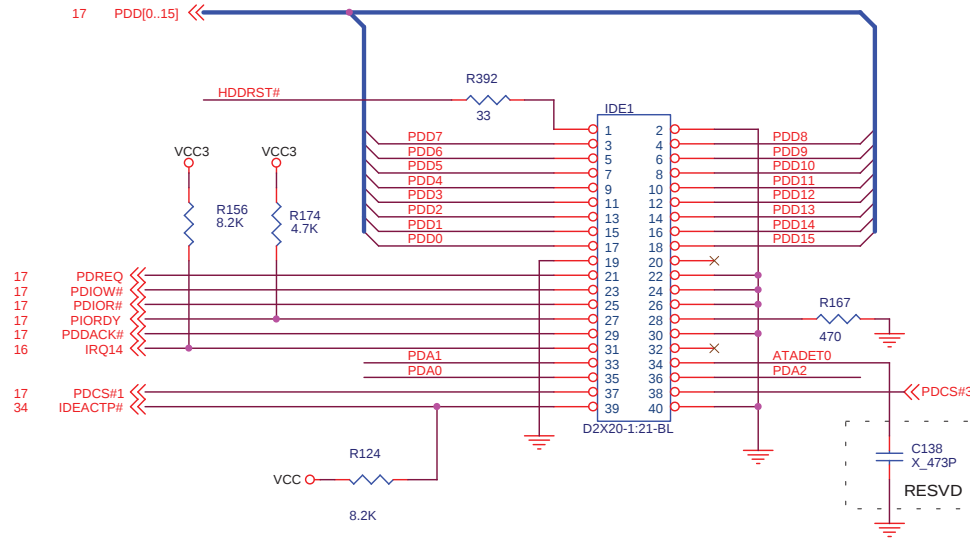




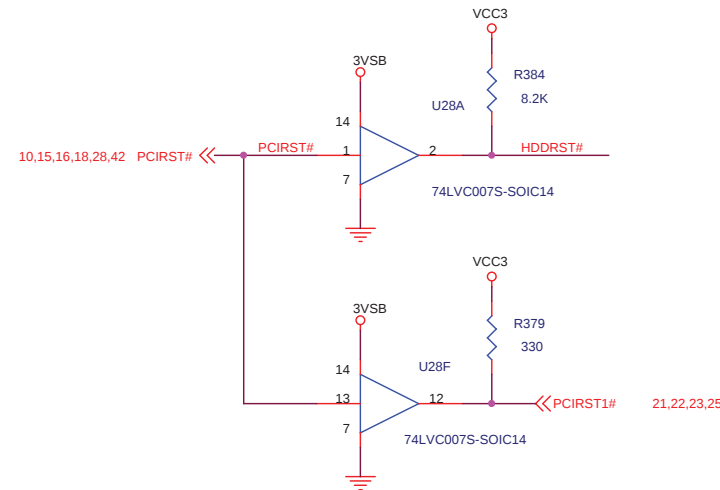
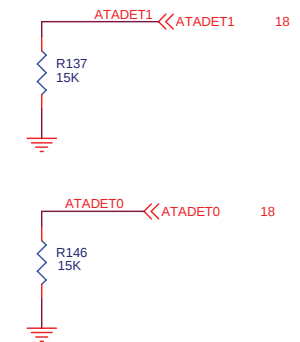
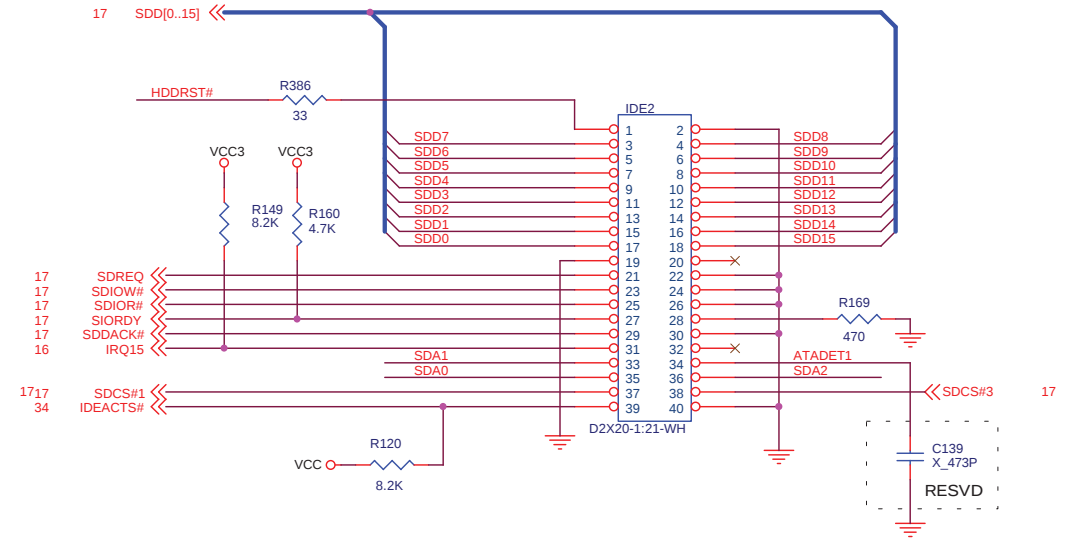


Title		
FWH		
Size	Document Number	Rev
B	MS-6545	2.0
Date:	Tuesday, July 16, 2002	Sheet 18 of 41

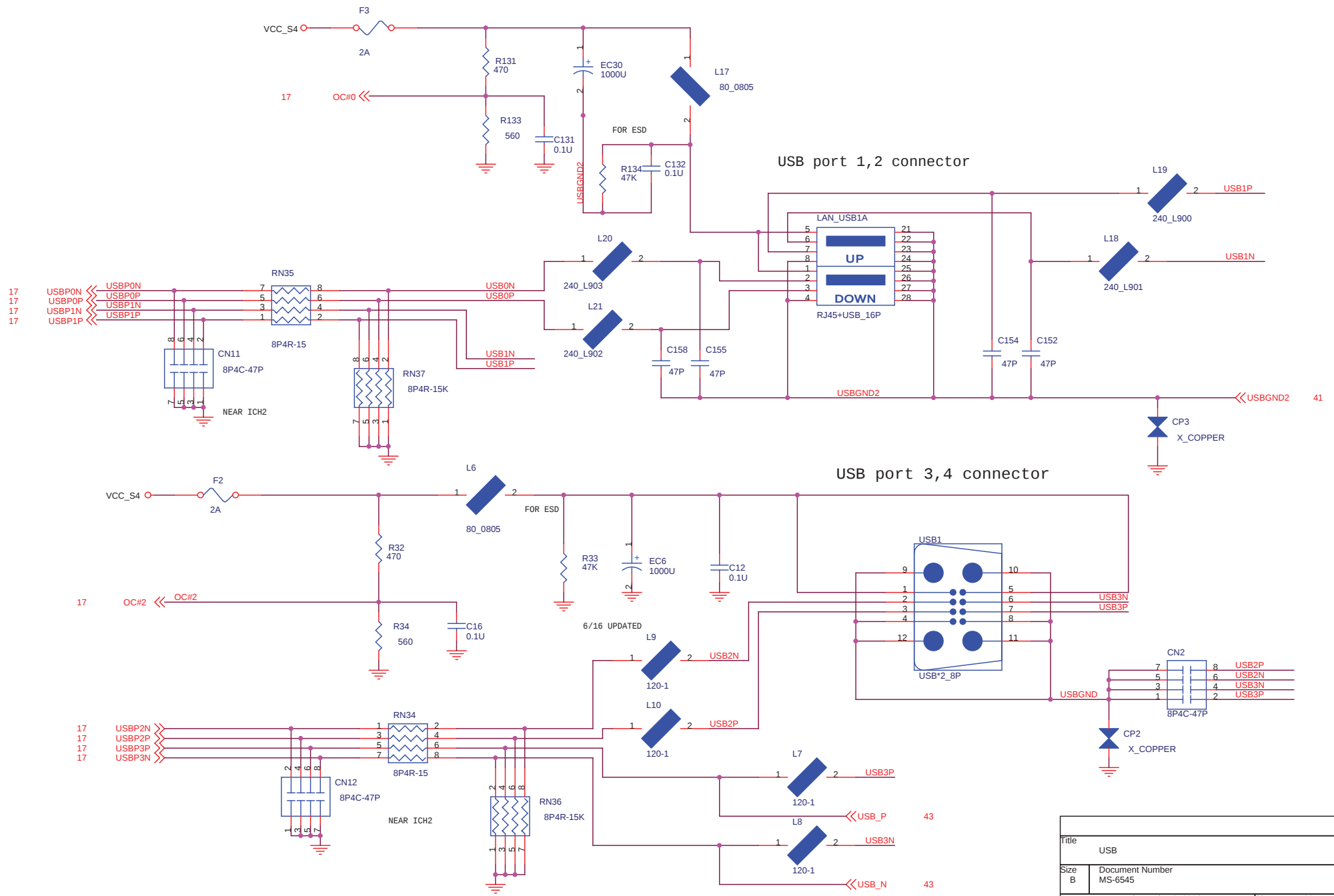
PRIMARY IDE CONN.



SECONDARY IDE CONN.

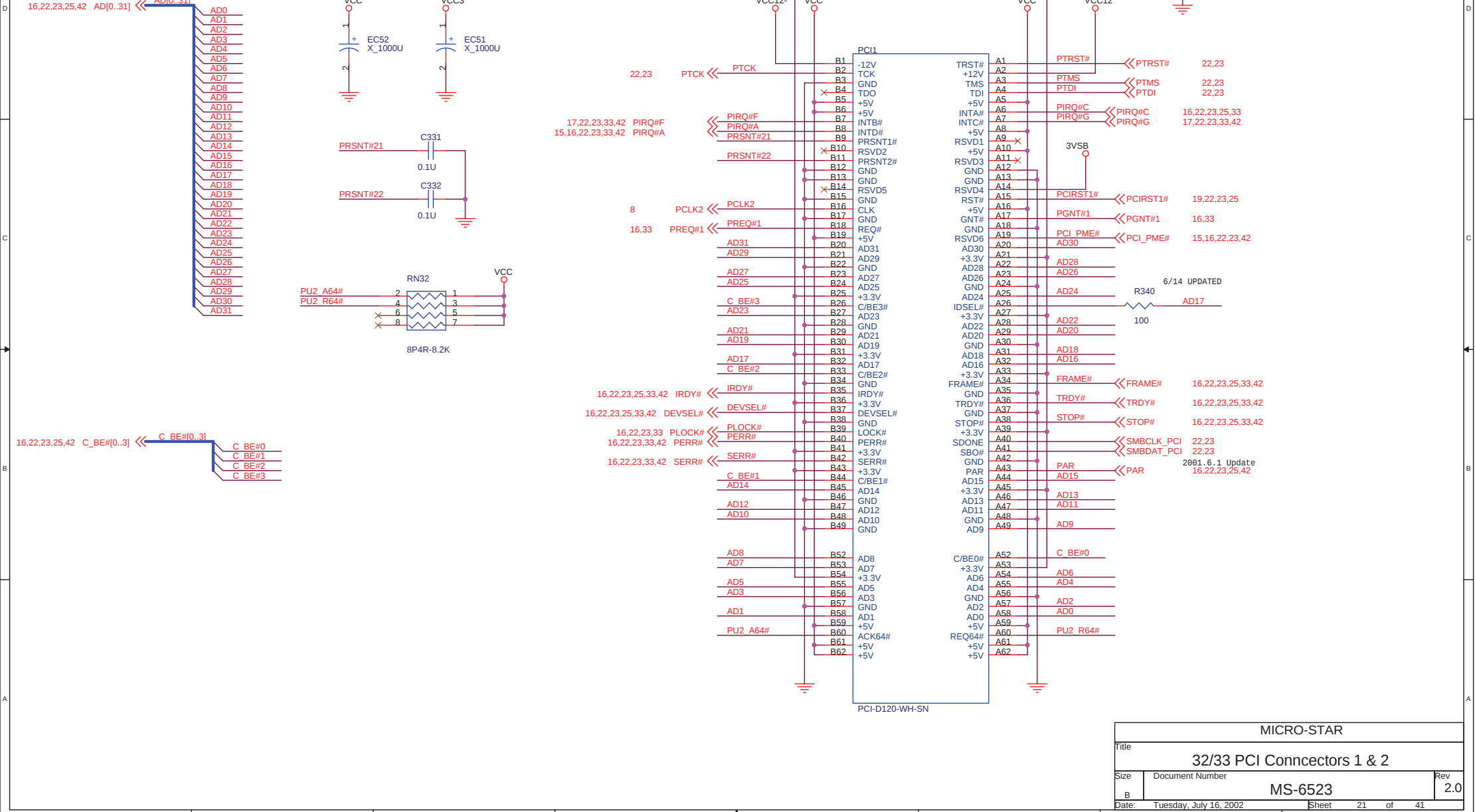


Title			IDE
Size	Document Number	Rev	2.0
B	MS-6545		
Date:	Tuesday, July 16, 2002	Sheet	19 of 41

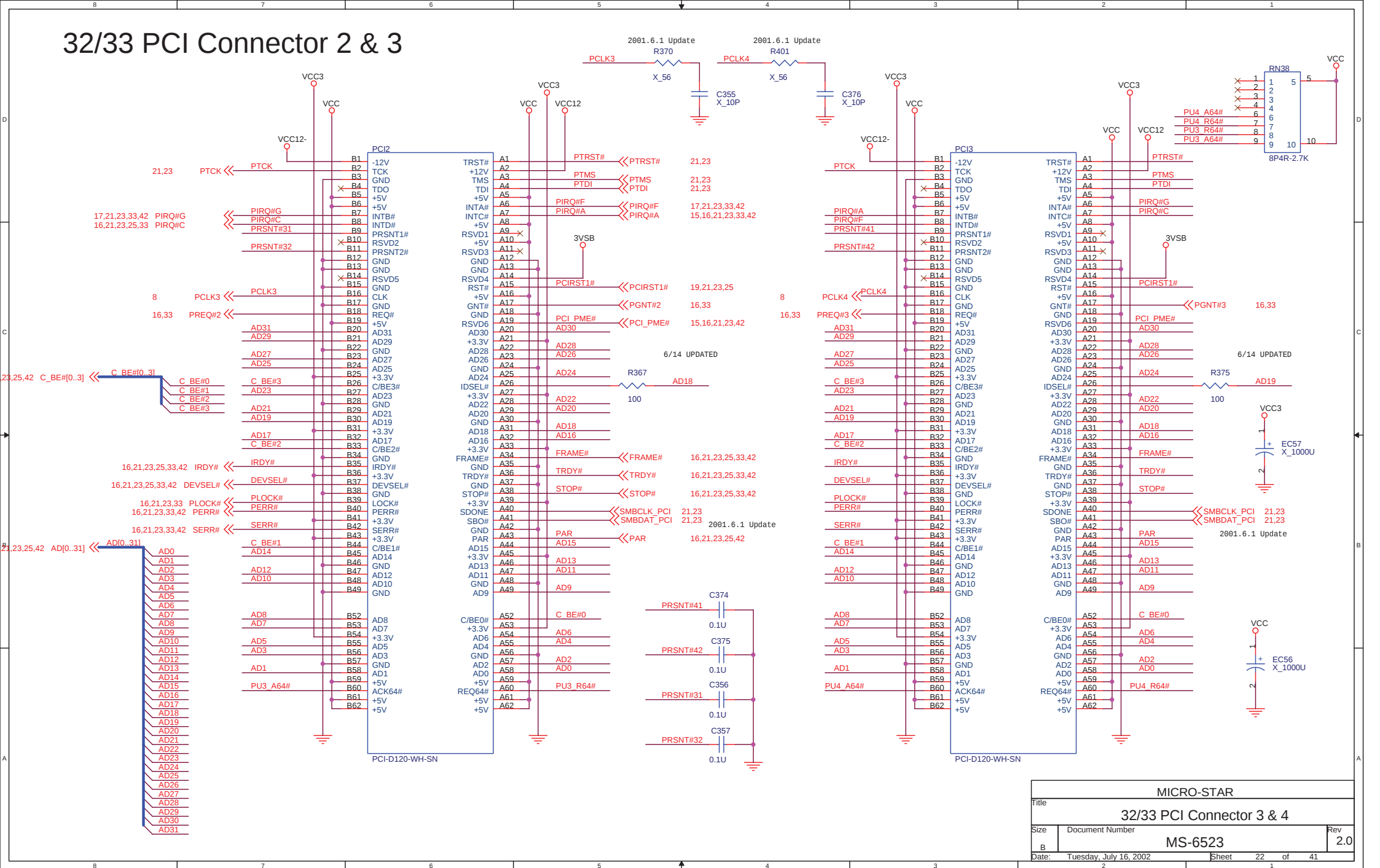


Title			
USB			
Size B	Document Number MS-6545		Rev 2.0
Date:	Tuesday, July 16, 2002	Sheet 20 of 41	

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

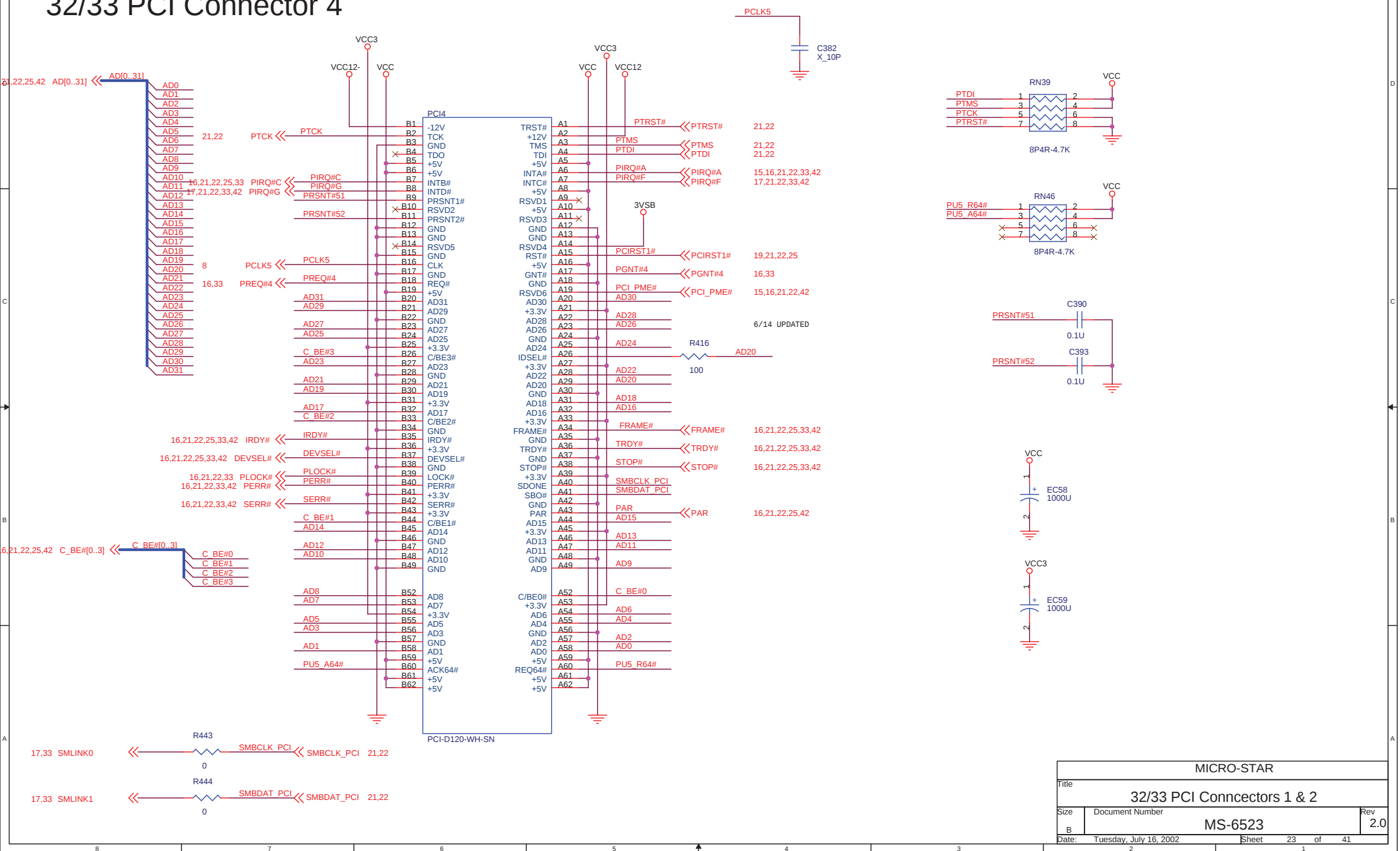


32/33 PCI Connector 2 & 3

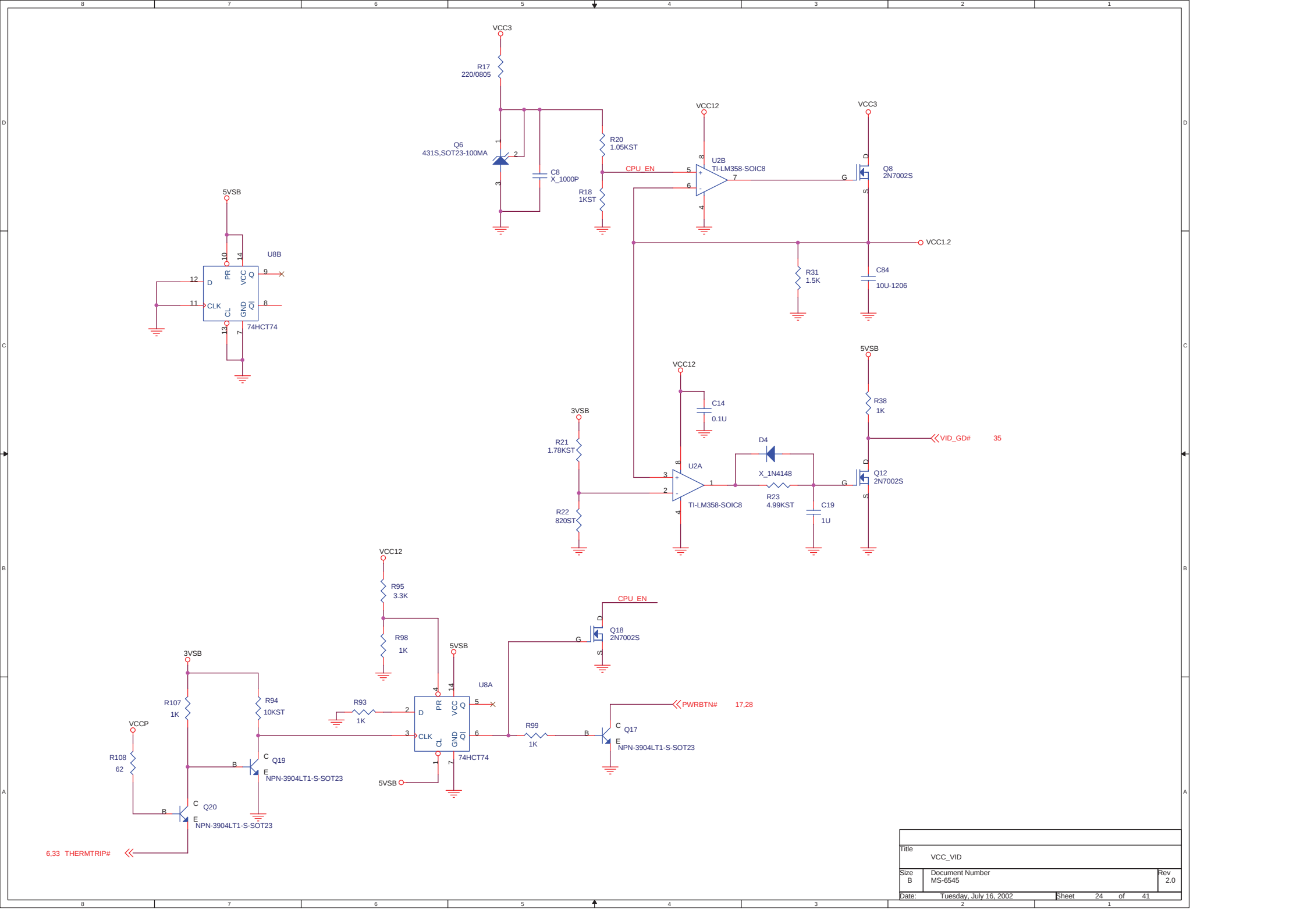


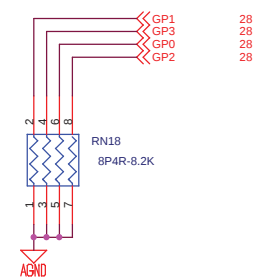
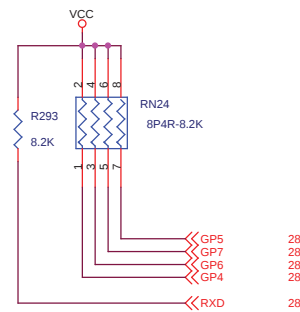
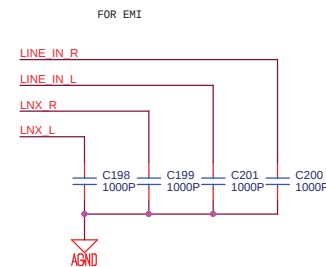
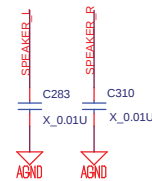
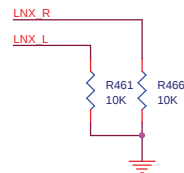
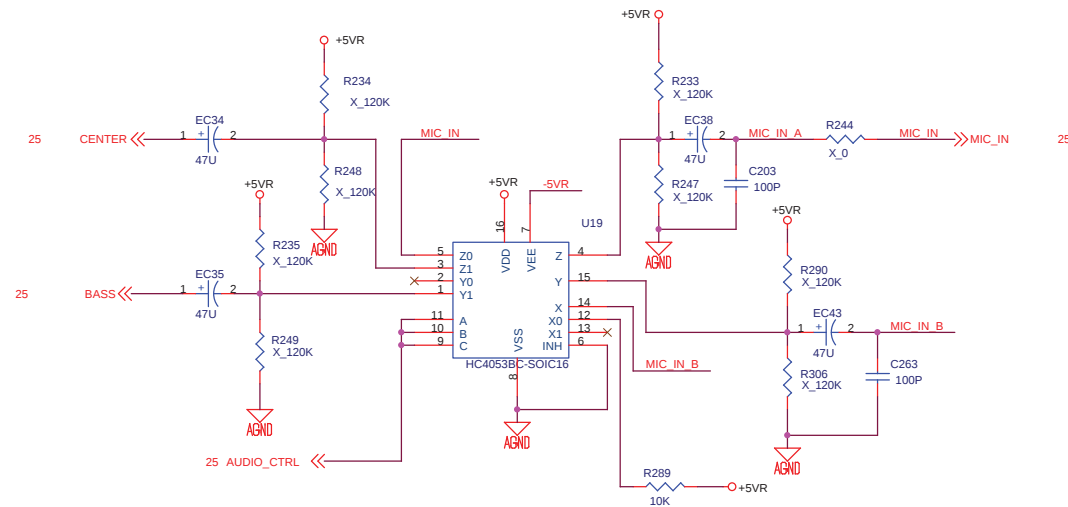
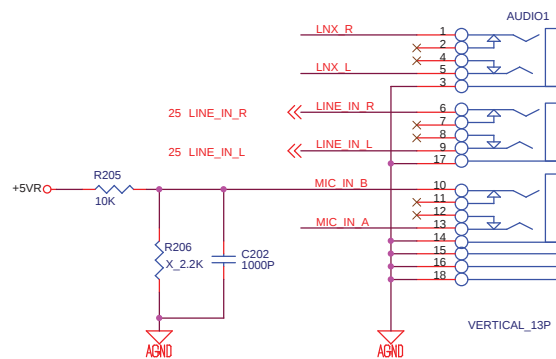
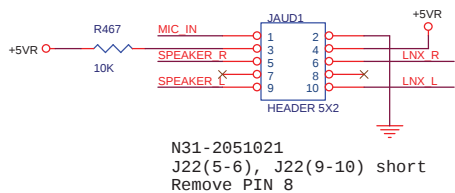
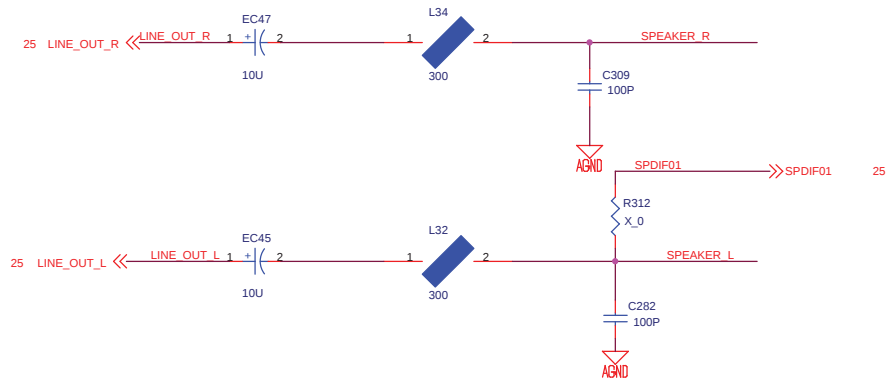
MICRO-STAR			
Title			
32/33 PCI Connector 3 & 4			
Size	Document Number		Rev
B	MS-6523		2.0
Date:	Tuesday, July 16, 2002	Sheet	22 of 41

32/33 PCI Connector 4



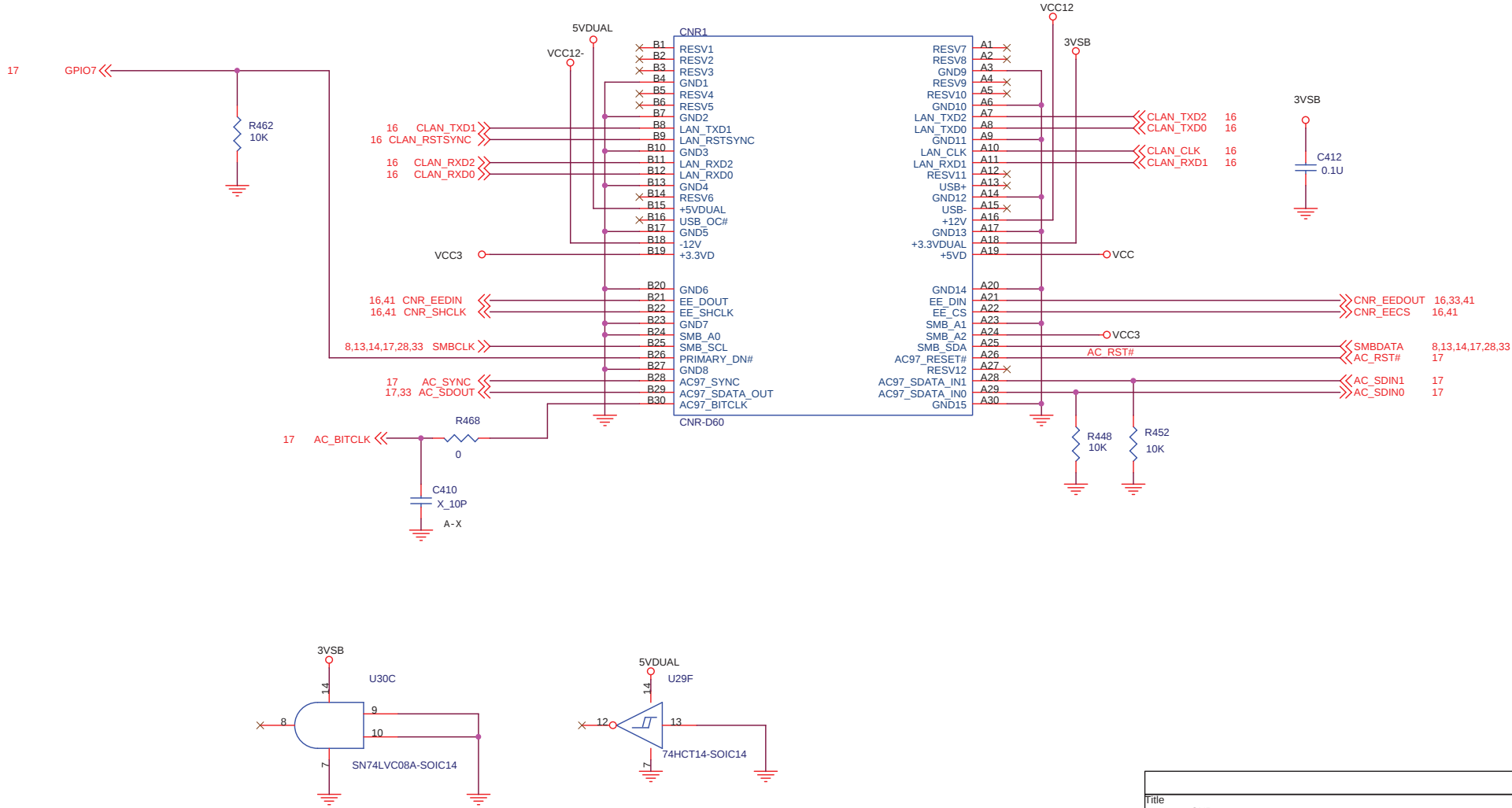
MICRO-STAR			
Title			
32/33 PCI Connectors 1 & 2			
Size	Document Number		Rev
B	MS-6523		2.0
Date:	Tuesday, July 16, 2002	Sheet	23 of 41





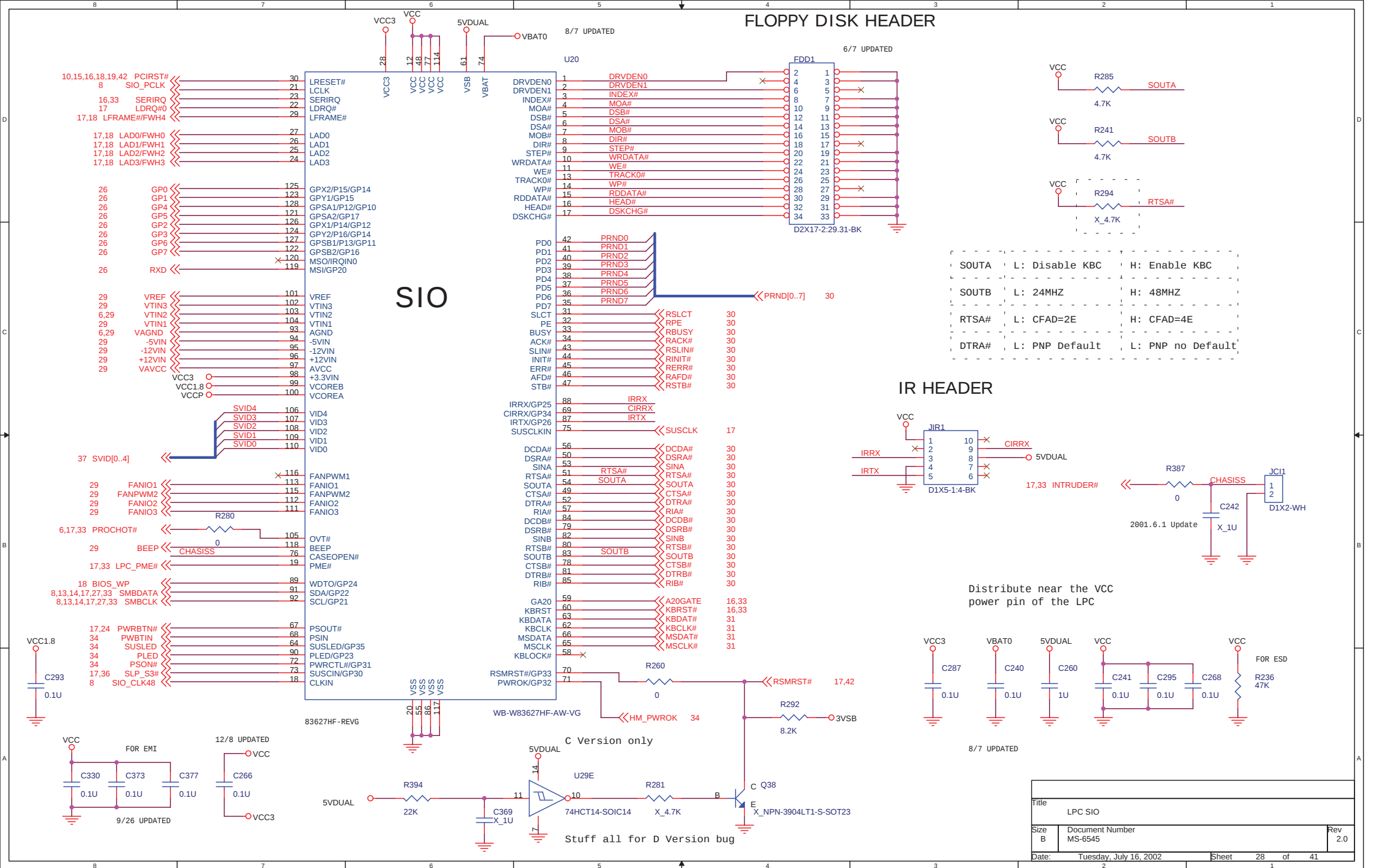
Title			MIDI/GAME Port	
Size	Document Number	Rev		2.0
CustomMS-6545				
Date:	Tuesday, July 16, 2002	Sheet	26	of 41

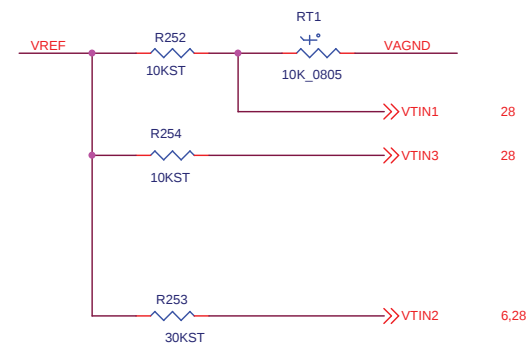
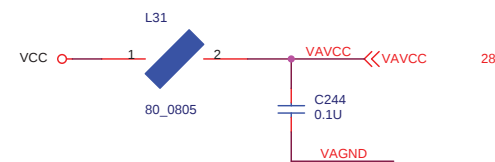
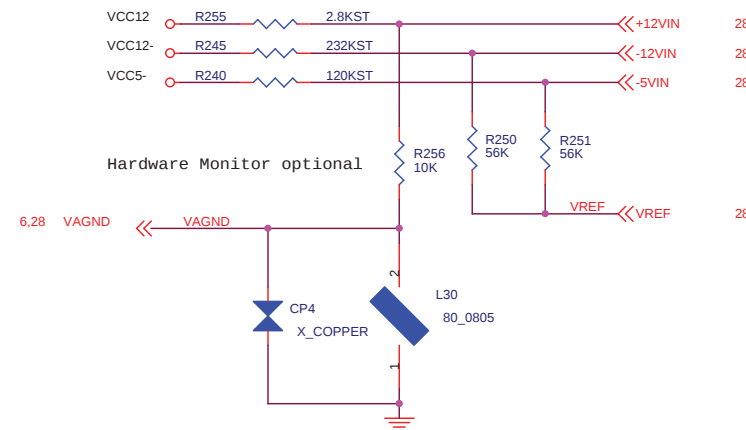
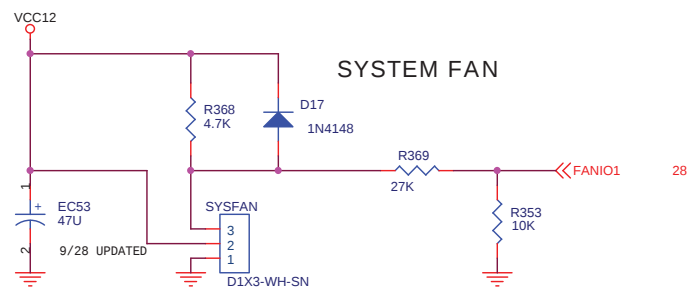
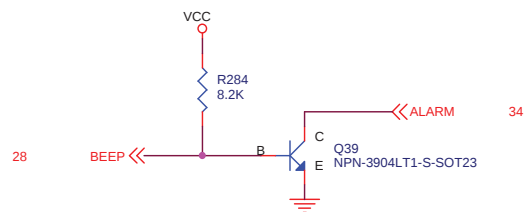
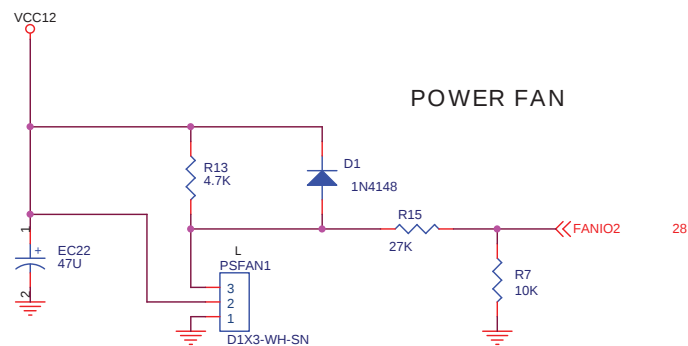
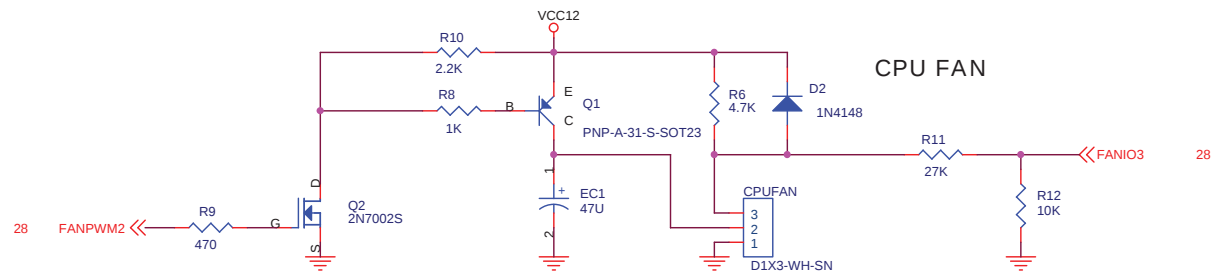
JP1	AUDIO DOWN
1 - 2	Enable onboard audio codec
2 - 3	Disable onboard audio codec



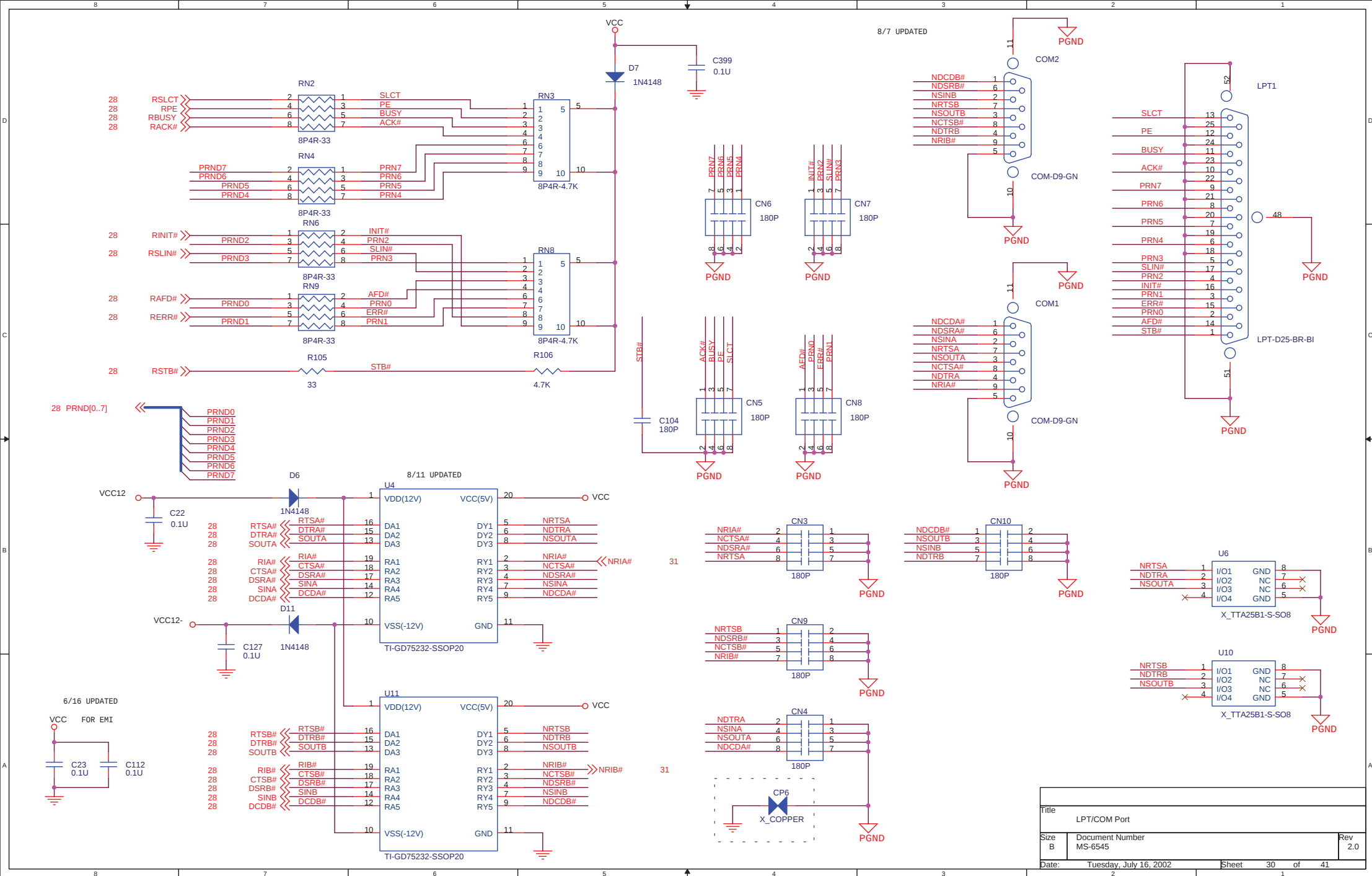
Title		
CNR		
Size	Document Number	Rev
B	MS-6545	2.0
Date:	Tuesday, July 16, 2002	Sheet 27 of 41

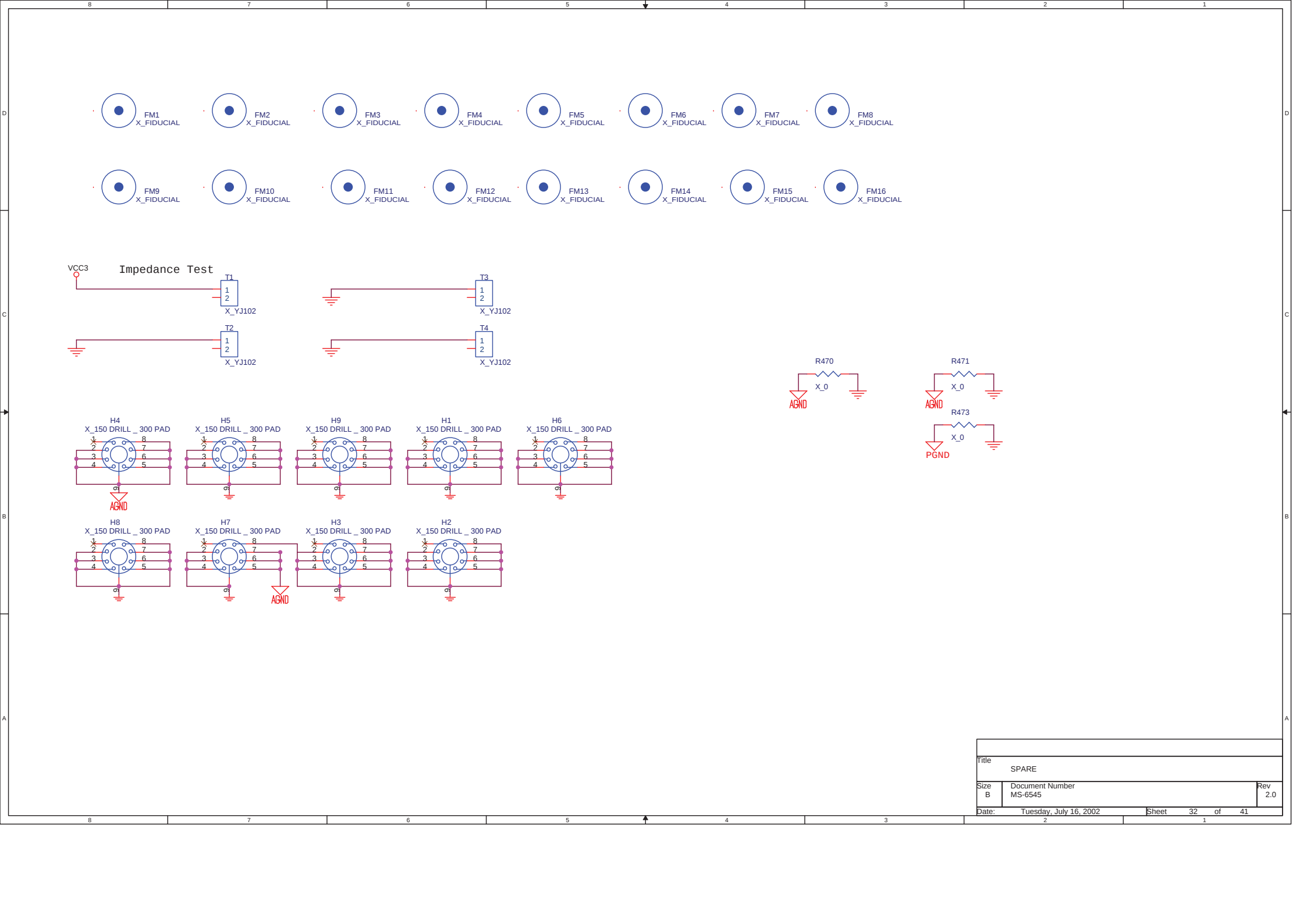
FLOPPY DISK HEADER





Title		
Hardware Monitor		
Size	Document Number	Rev
B	MS-6545	2.0
Date:	Tuesday, July 16, 2002	Sheet 29 of 41

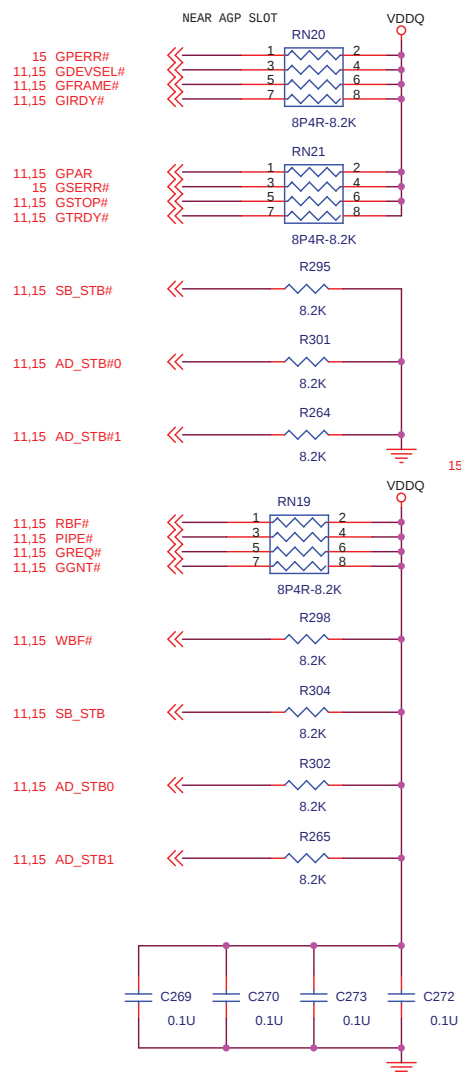




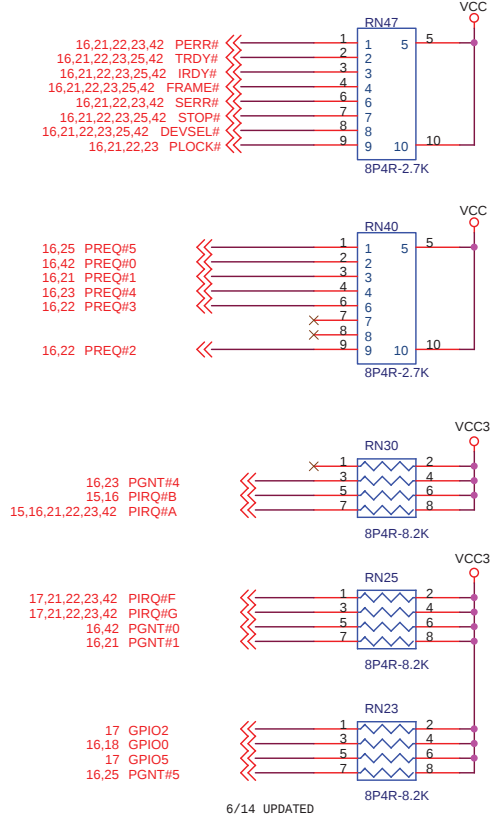
Title			SPARE
Size	Document Number		Rev
B	MS-6545		2.0
Date:	Tuesday, July 16, 2002	Sheet	32 of 41

PULL-UP RESISTORS

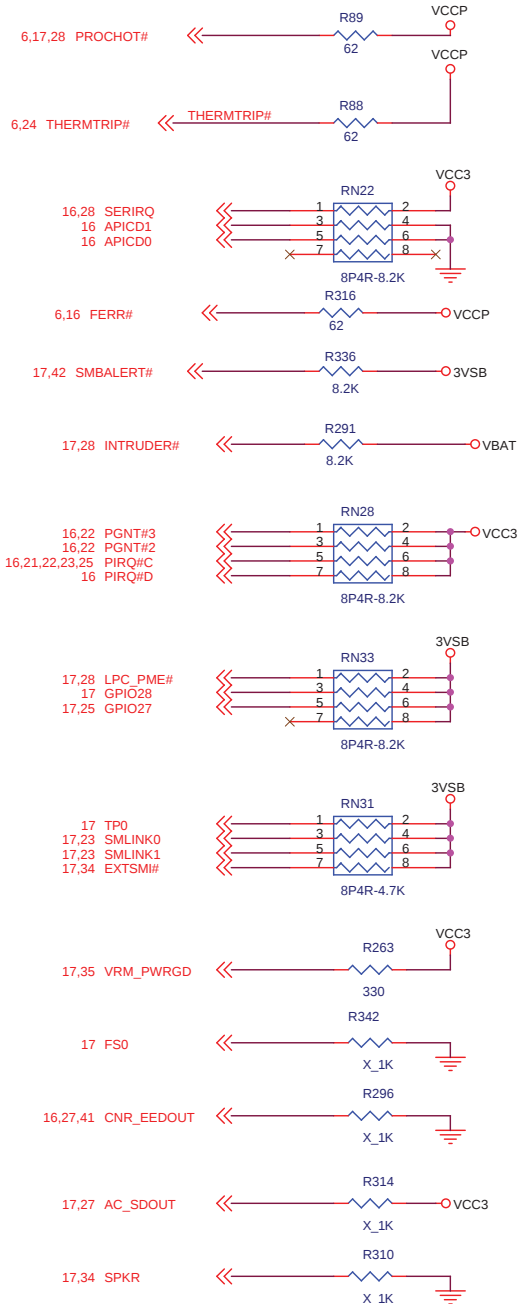
AGP



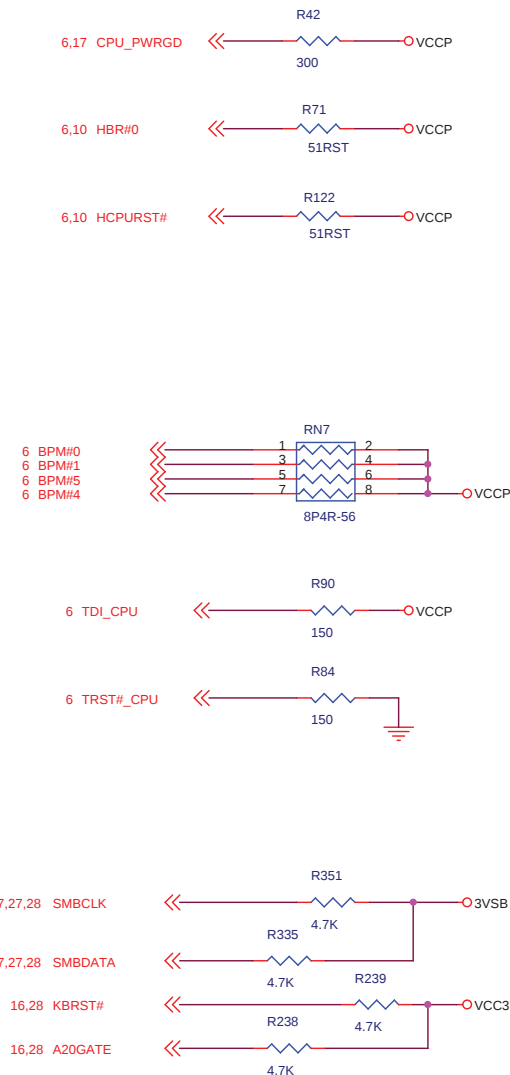
PCI BUS



ICH

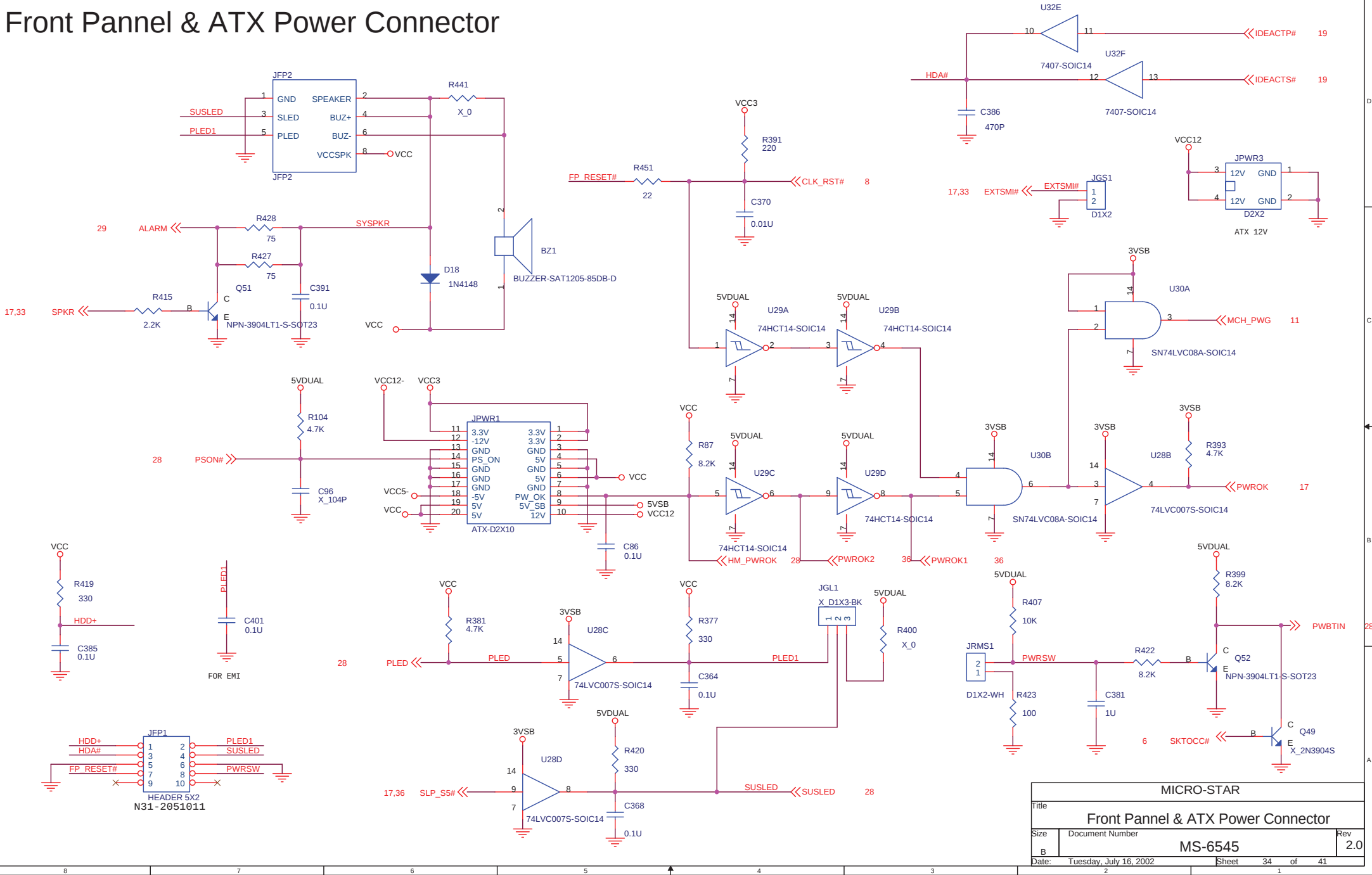


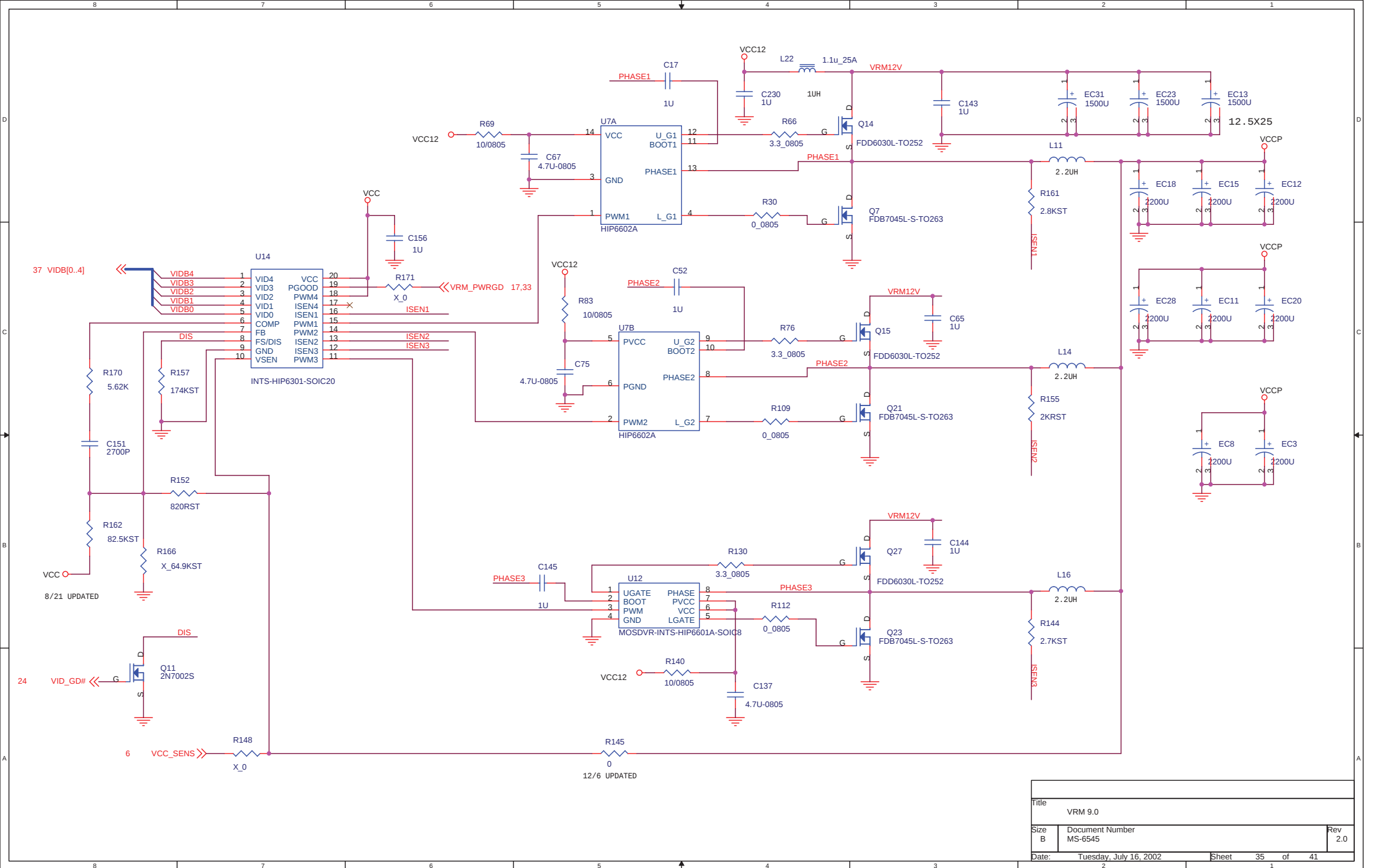
CPU

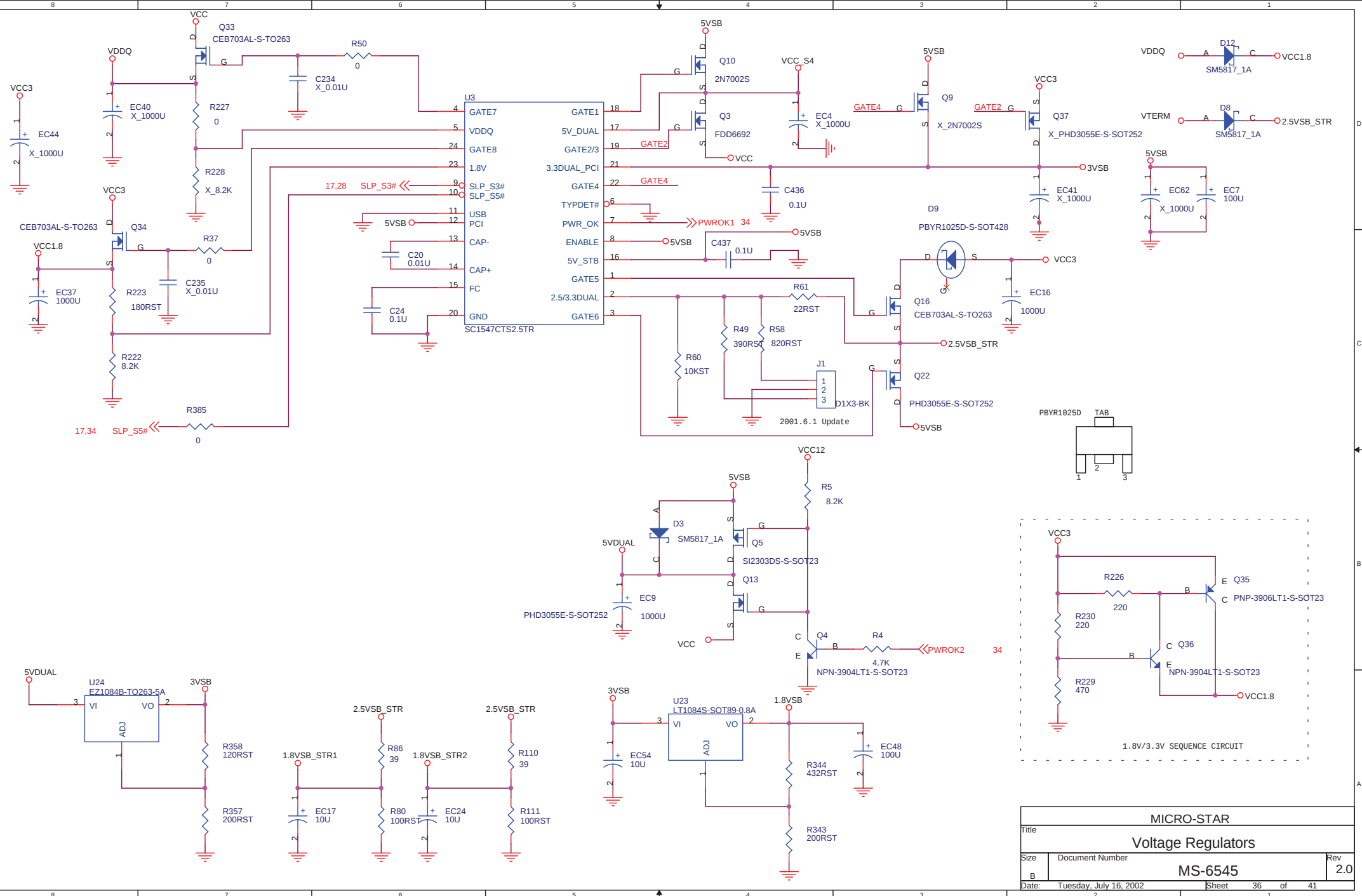


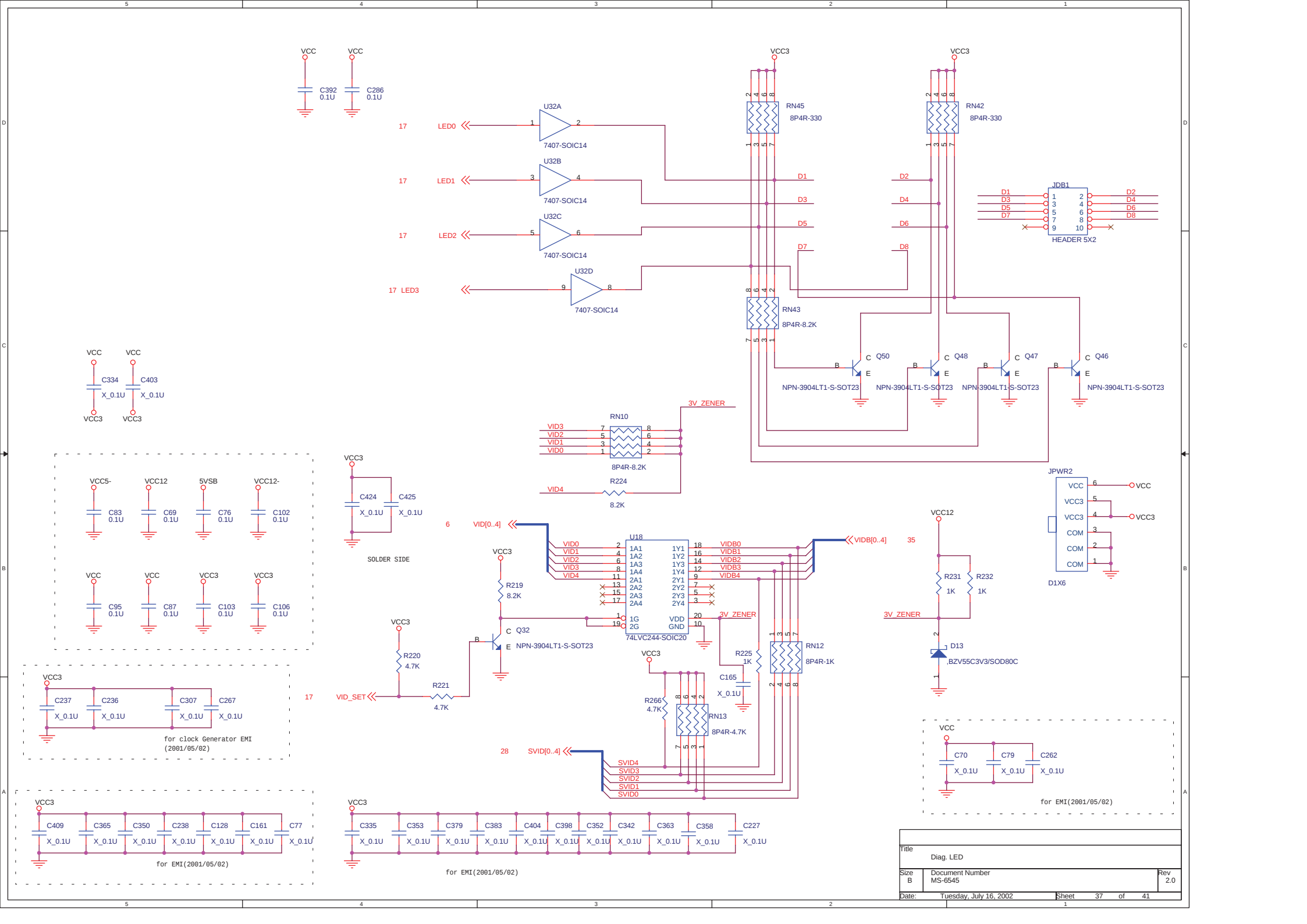
Title		
Pull-up Resistors		
Size	Document Number	Rev
B	MS-6545	2.0
Date:	Tuesday, July 16, 2002	Sheet 33 of 41

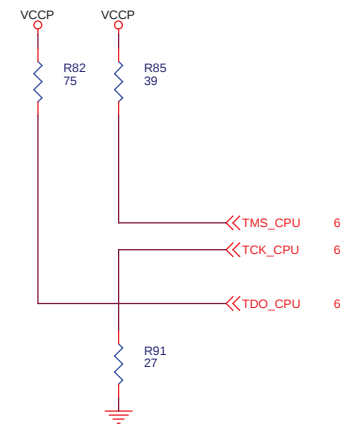
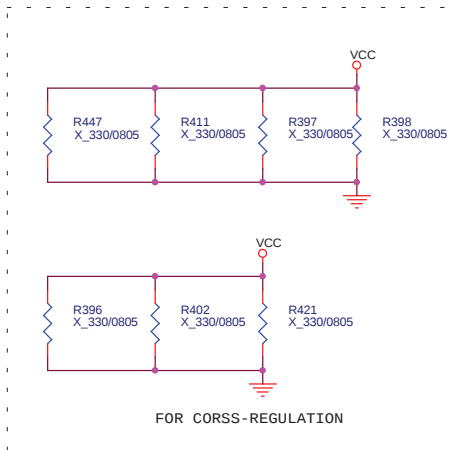
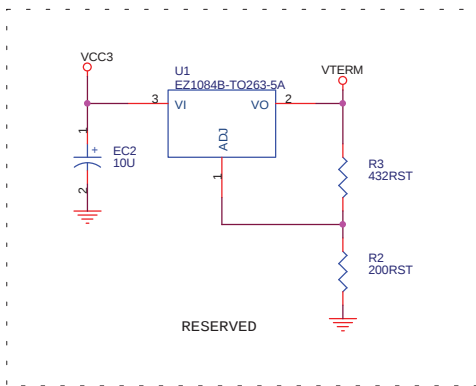
Front Pannel & ATX Power Connector

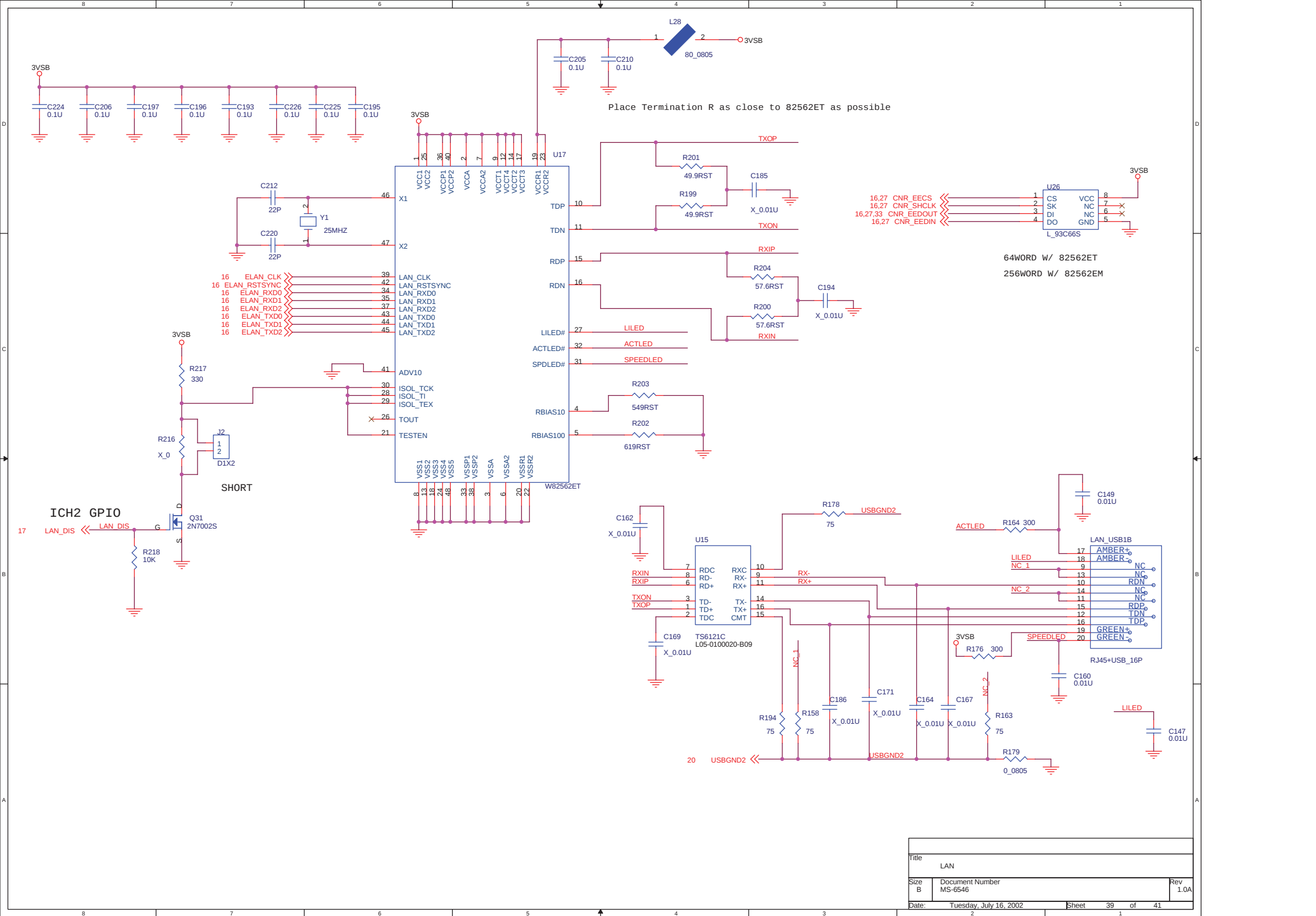


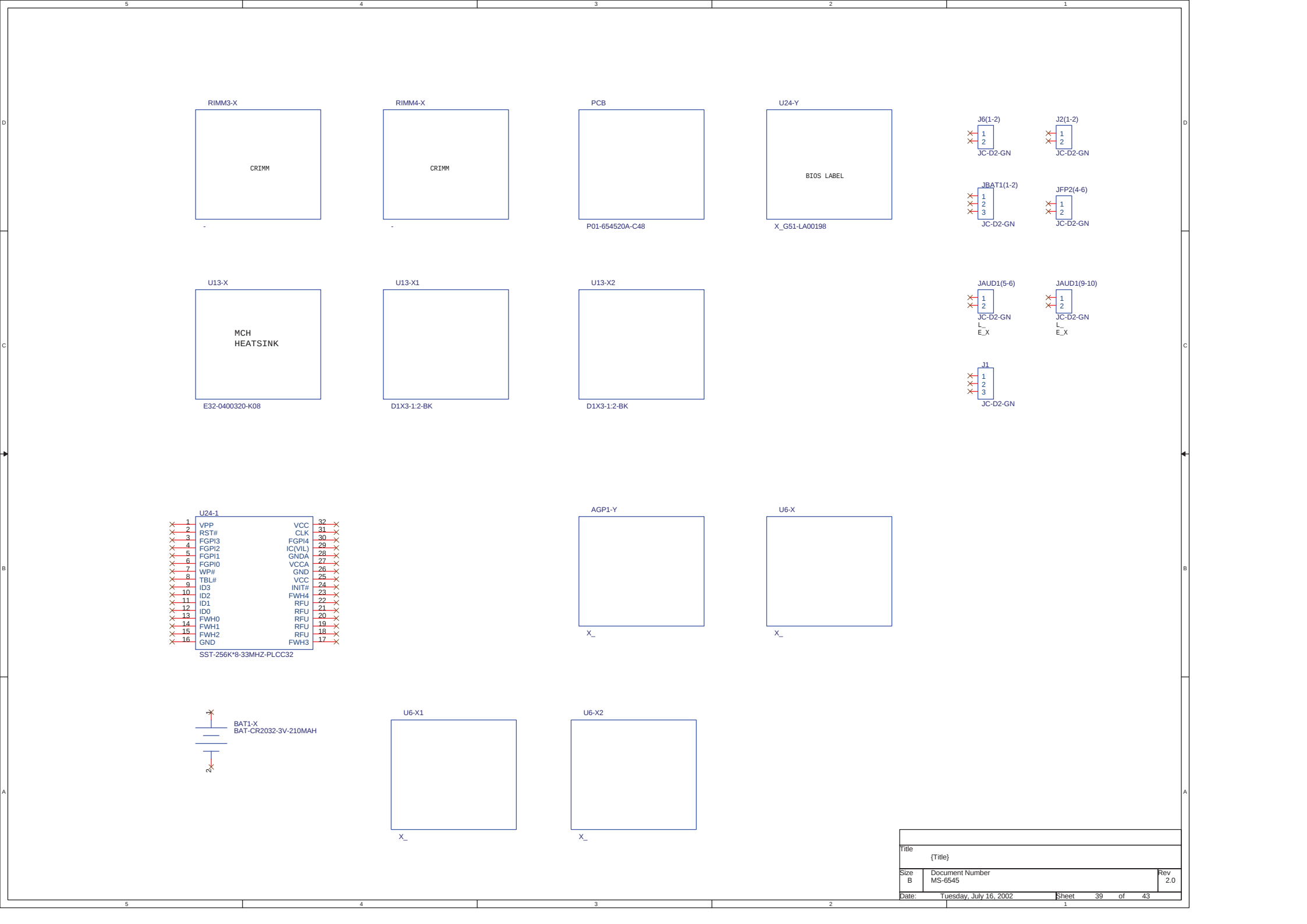


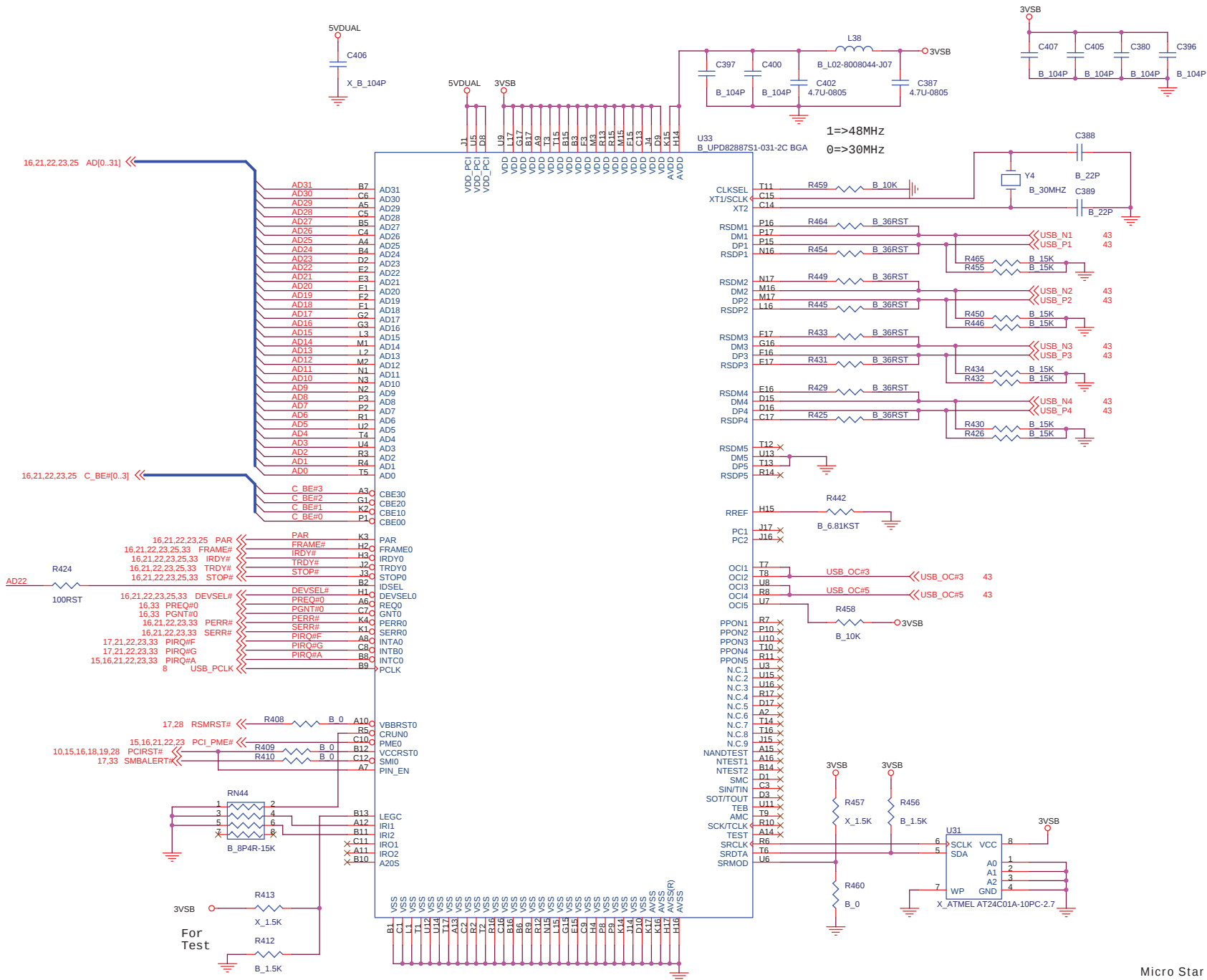






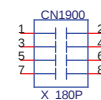
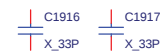
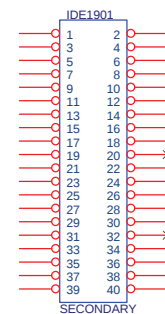
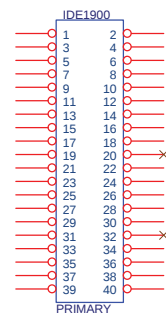
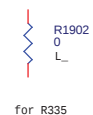
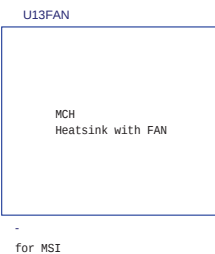
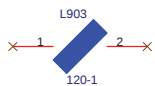
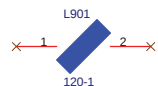
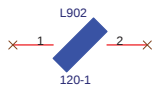
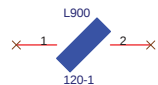
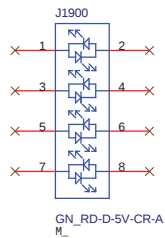
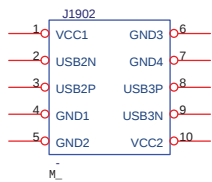
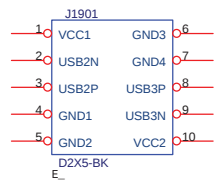






Micro Star Restricted Secret

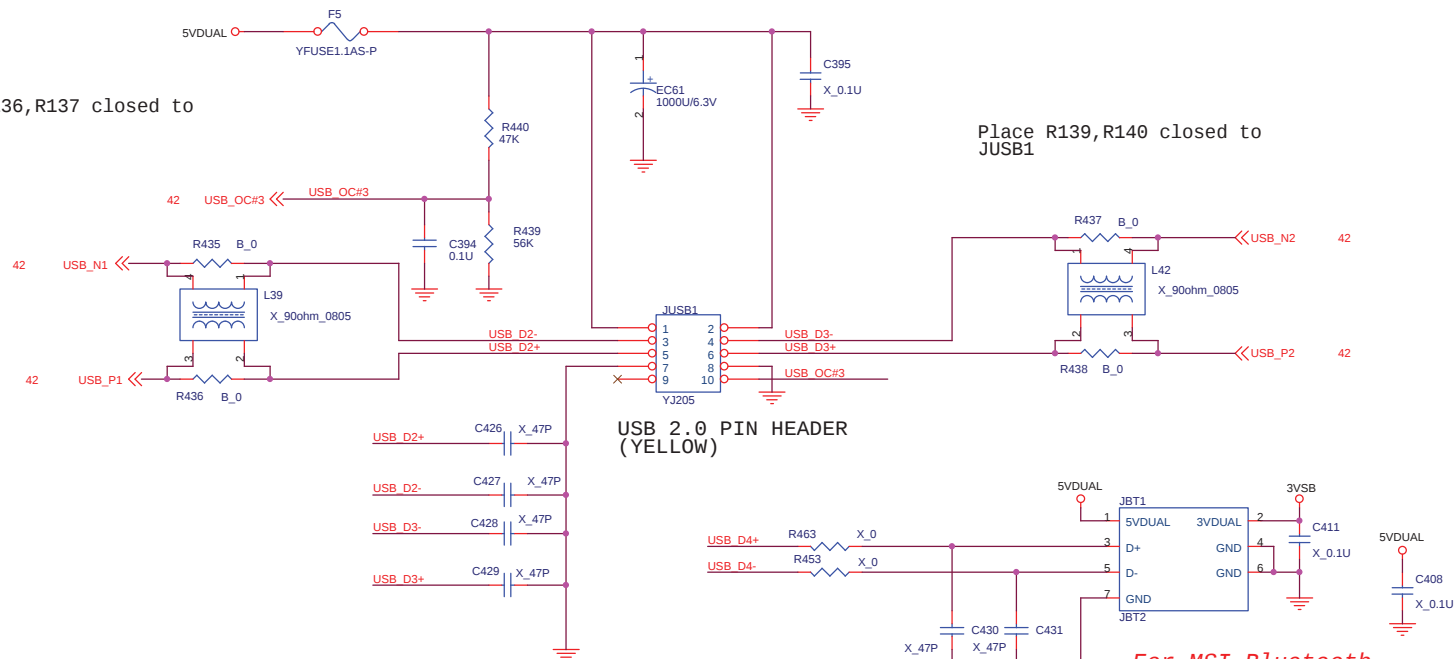
Title		USB 2.0 Host Controller	Rev
Document Number		MS-6380E	0B
MICRO-STAR INT'L		Last Revision Date:	
CO., LTD.		Tuesday, July 16, 2002	
No. 69, Li-De St, Jung-He City,		Sheet	
Taipei Hsien, Taiwan		40 of 41	
http://www.msi.com.tw			



FOR LGE

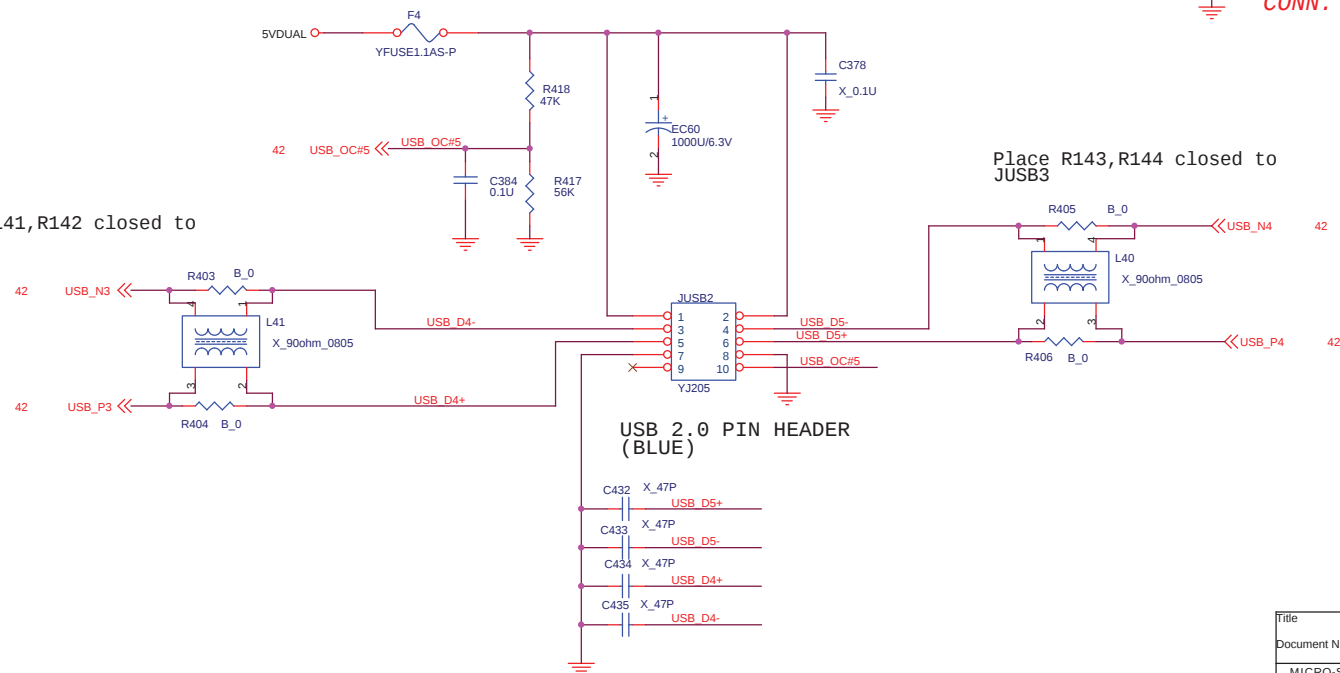
Place R136,R137 closed to JUSB1

Place R139,R140 closed to JUSB1



Place R141,R142 closed to JUSB3

Place R143,R144 closed to JUSB3



Micro Star Restricted Secret

Title	Front USB Port	Rev
Document Number	MS-6380E	0B
MICRO-STAR INT'L CO., LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Tuesday, July 16, 2002	
Sheet	41 of 41	