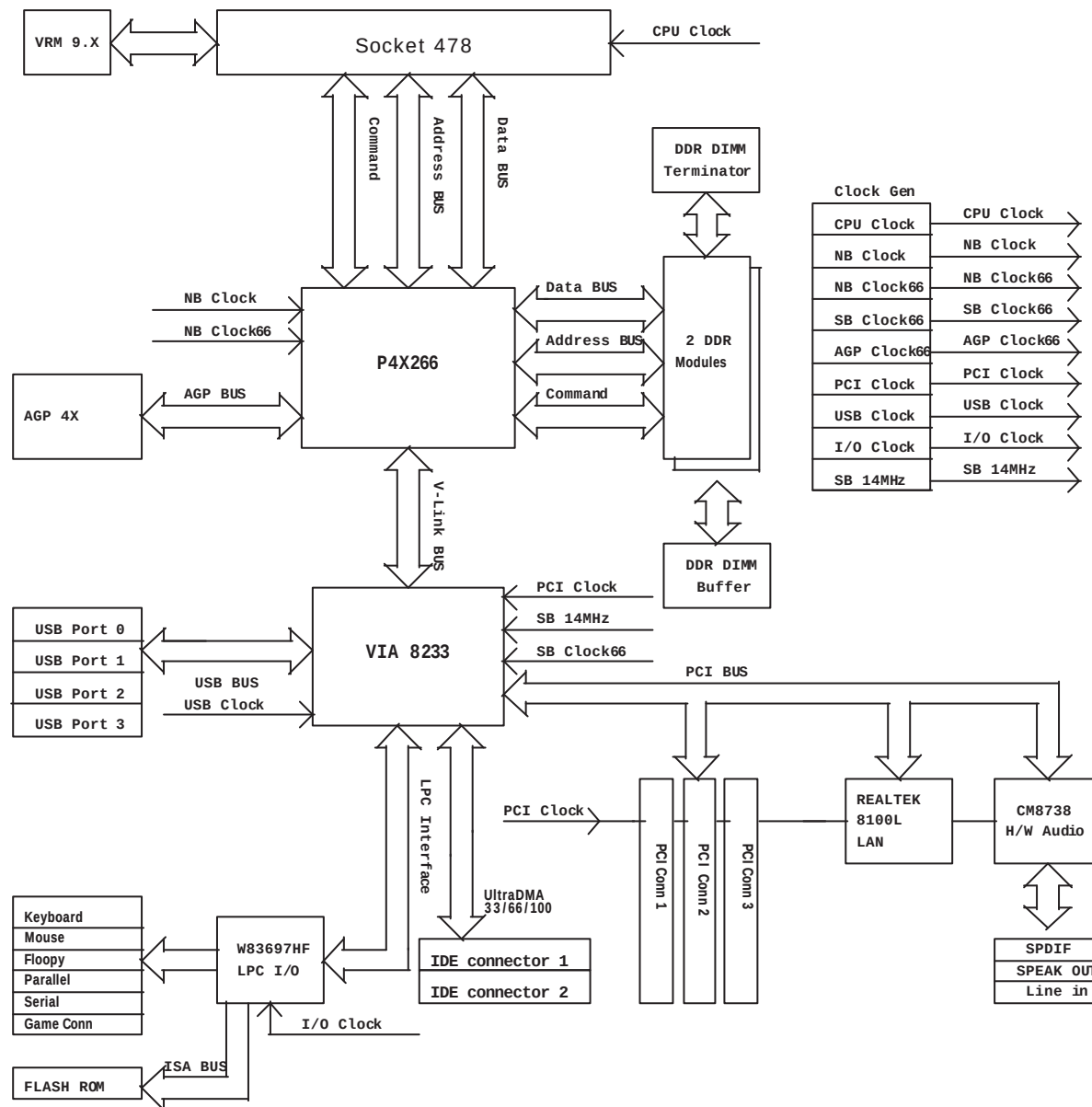
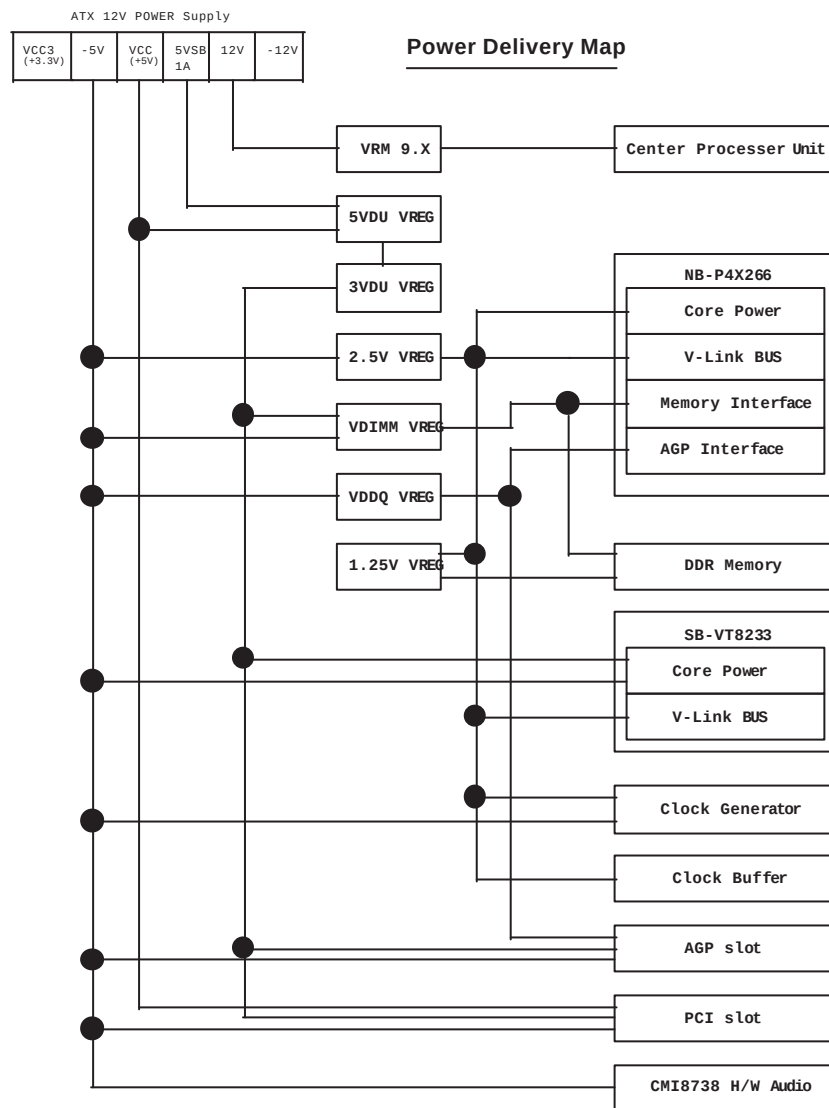




Clock Gen	
CPU Clock	CPU Clock
NB Clock	NB Clock
NB Clock66	NB Clock66
SB Clock66	SB Clock66
AGP Clock66	AGP Clock66
PCI Clock	PCI Clock
USB Clock	USB Clock
I/O Clock	I/O Clock
SB 14MHz	SB 14MHz

- 01-Block drigrane & Cover sheet
- 02-Power Delivery & GP I/O
- 03-Willamette 478 CPU part 1
- 04-Willamette 478 CPU part 2
- 05-Main Clock & DDR Clock buffer
- 06-VRM 9.X
- 07-P4X266 part 1 & 4
- 08-P4X266 part 2
- 09-P4X266 part 3
- 10-AGP slot & Pull-UP/DN resistor
- 11-DIMM 1 & 2
- 12-DDR terminator & regulator
- 13-VT8233 PART 1 & USB PORT
- 14-VT8233 part 2
- 15-VT8233 PART 3 & MS1
- 16-PCI slot 1 & 2 & 3
- 17-CM8738 H/W Audio controller
- 18-Audio Channel Selector & Connector
- 19-ATA66/100 CONNECTOR & KEYBOARD / MOUSE
- 20-Windbond W83697HF LPC I/O & Flash ROM.
- 21-Com/Parallel port
- 22-ATX POWER COM & FRONT PANEL
- 23-Main Power
- 24-Decoupling capacitor
- 25-REALTEK 8100L LAN
- 26-PCB OTHER COMPONENT
- 27-MAINAL

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IRQ# Routing Table					
AGP Slot	PIRQ#A	PIRQ#B			
PCI Slot 1	PIRQ#A	PIRQ#B	PIRQ#C	PIRQ#D	ID select= AD16
PCI Slot 2	PIRQ#B	PIRQ#C	PIRQ#D	PIRQ#A	ID select= AD17
PCI Slot 3	PIRQ#C	PIRQ#D	PIRQ#A	PIRQ#B	ID select= AD18
H/W Audio	PIRQ#D				ID select= AD23
REK-RTL8100	PIRQ#B				ID select= AD25

GPIO Table on SB-VT8233	
GPIO_0	Flash Rom protection Hi: Disable, Low: Enable
GPI_2	Sleep button
GPIO_8	IDE_1 mode detection
GPIO_9	IDE_2 mode detection
GPIO_10	LPC_PME#
GPIO_24	Diag-LED_1
GPIO_25	Diag-LED_2
GPIO_26	PCB version control
GPIO_27	PCB version control
GPIO_30	Diag-LED_3
GPIO_31	Diag-LED_4

GPIO Table on LPC I/O W83697	

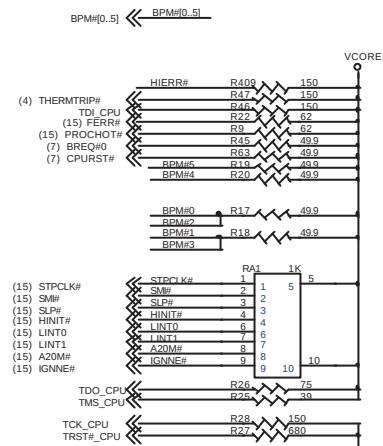
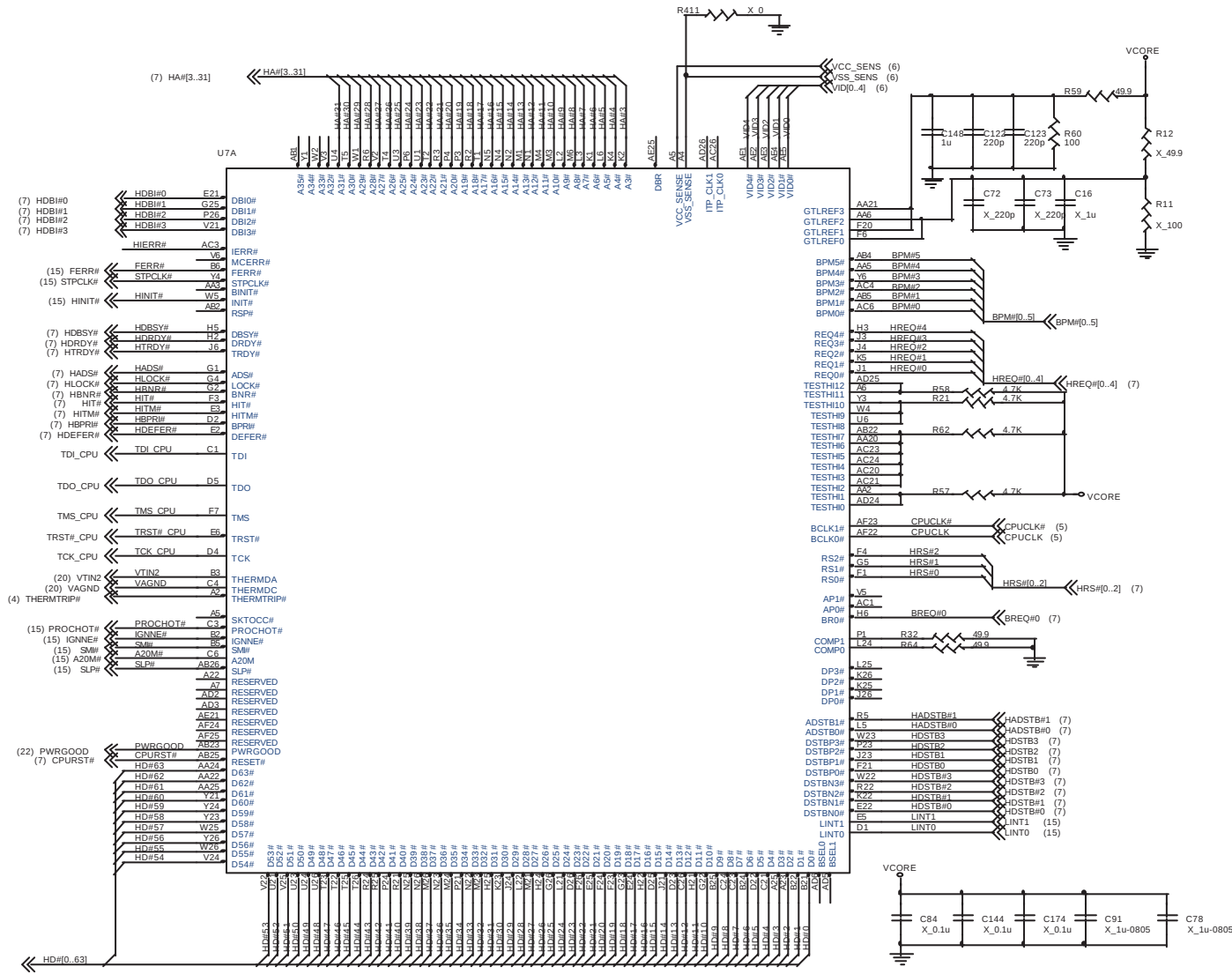
AUDIO : Hardware Audio controller installed

Chipset: VIA P4X266 + VT8233

LPC I/O: Winbond W83697EF

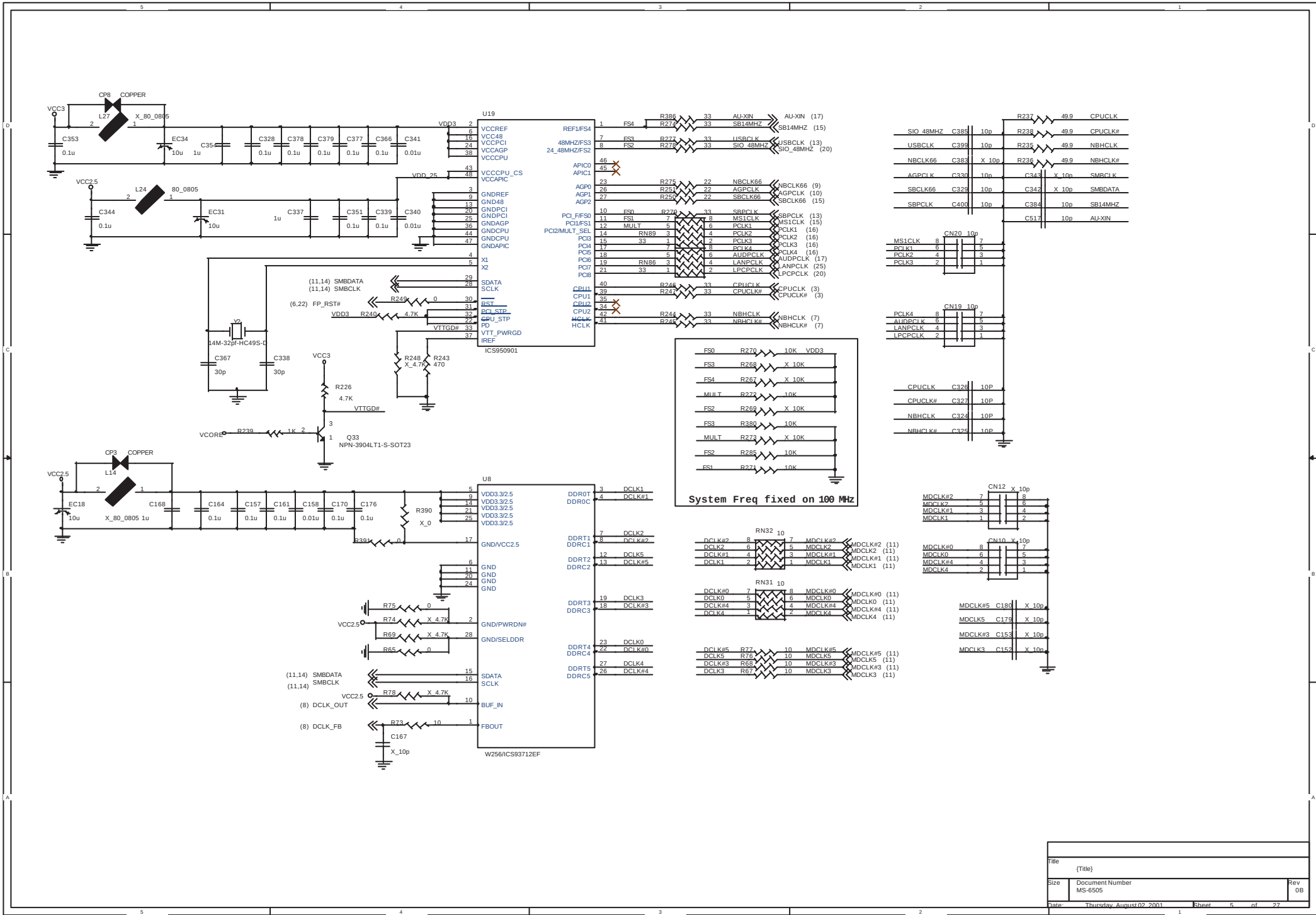
Flashrom: ISA Flashrom

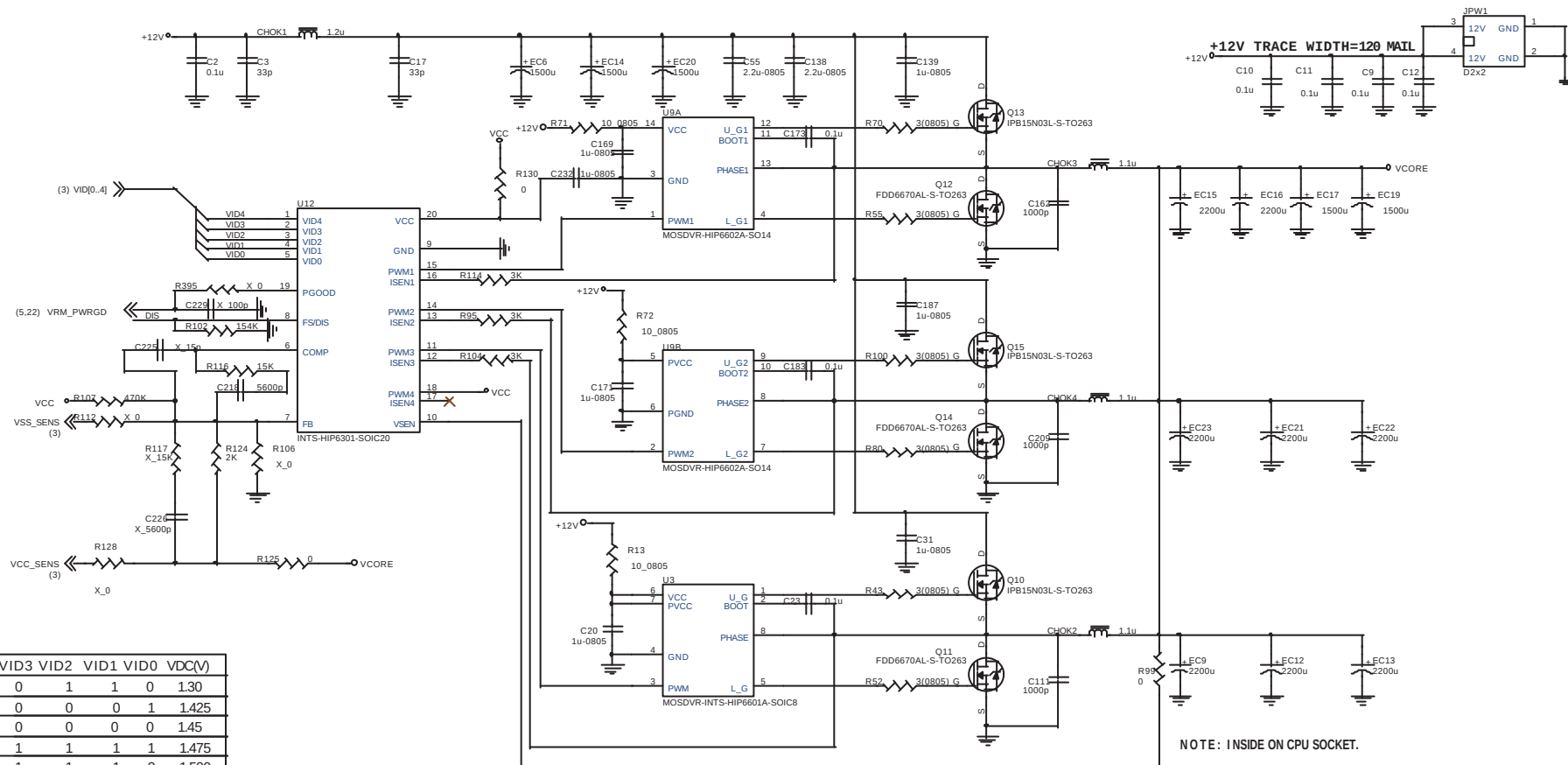
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CPU Pull-up/down resistor

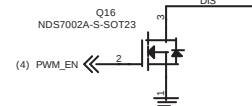
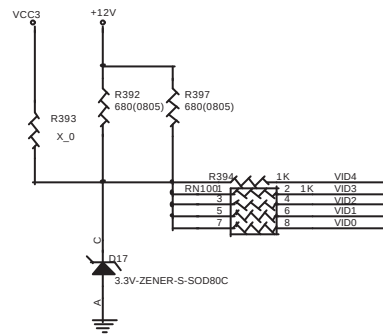
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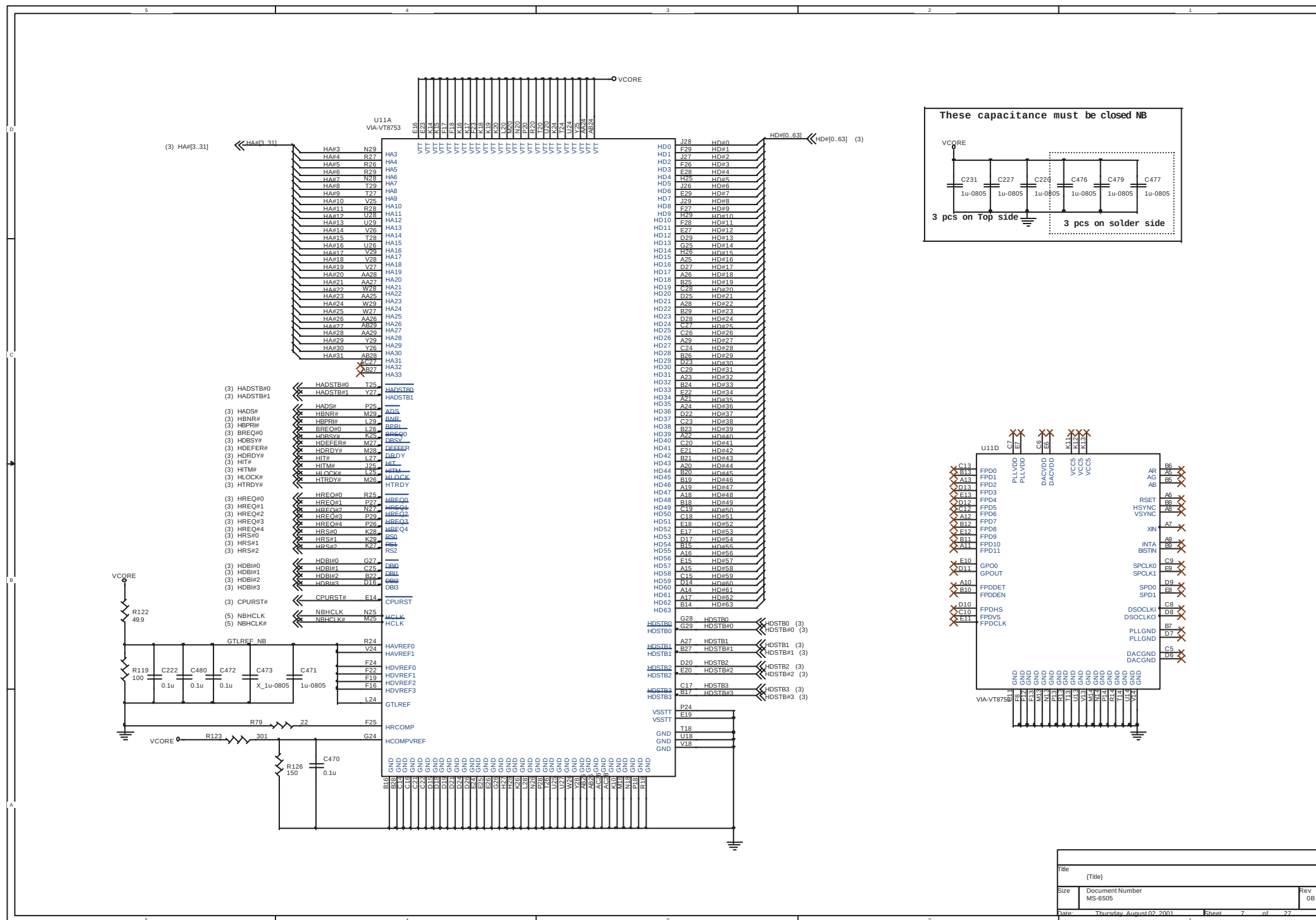


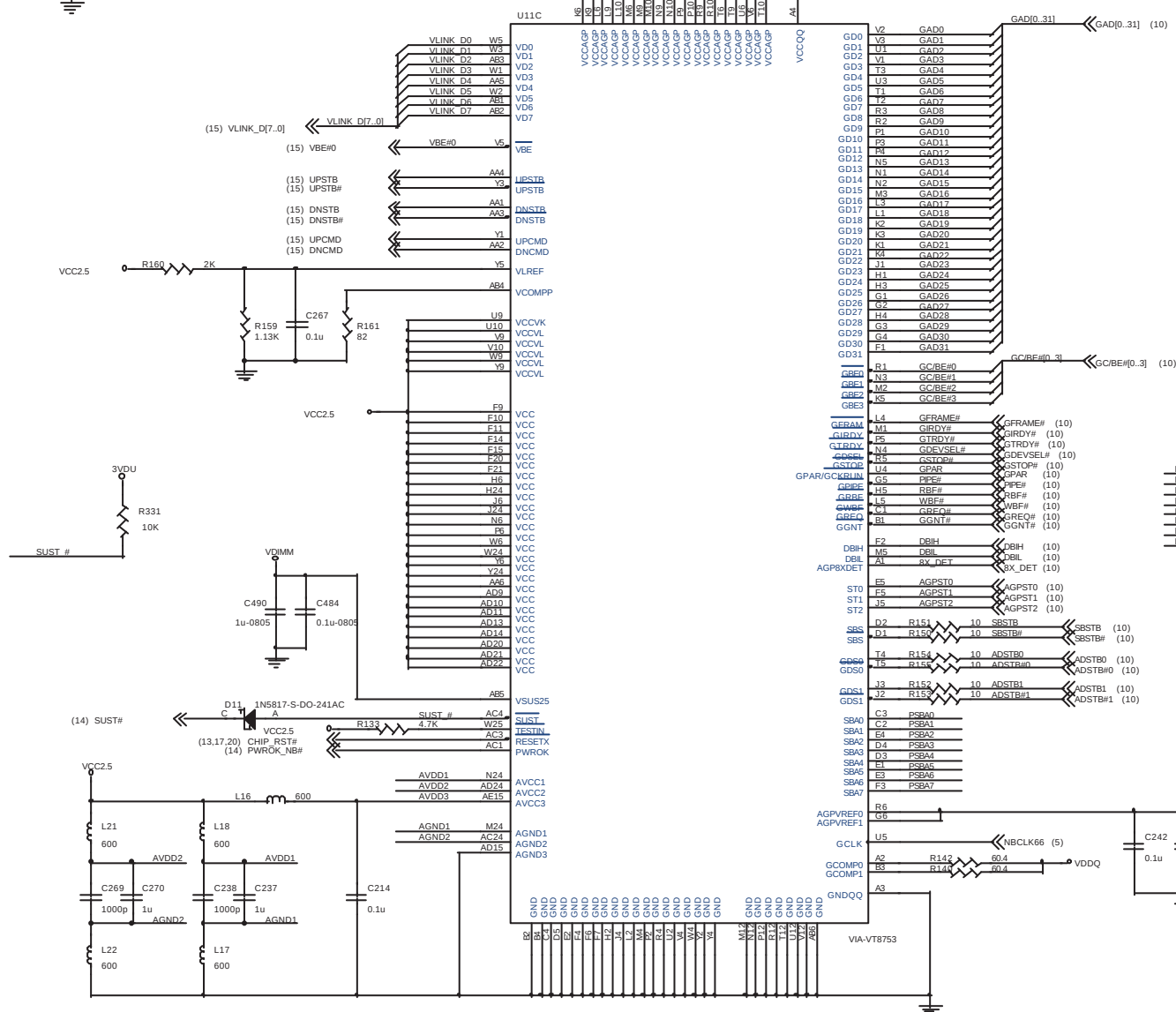
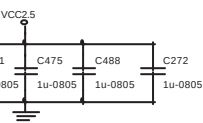
VID4	VID3	VID2	VID1	VID0	VDC(V)
1	0	1	1	0	1.30
1	0	0	0	1	1.425
1	0	0	0	0	1.45
0	1	1	1	1	1.475
0	1	1	1	0	1.500
0	1	1	0	1	1.525
0	1	1	0	0	1.550
0	1	0	1	1	1.575
0	1	0	1	0	1.600
0	1	0	0	1	1.625

VID4	VID3	VID2	VID1	VID0	VDC(V)
0	1	0	0	0	1.650
0	0	1	1	1	1.675
0	0	1	1	0	1.70
0	0	1	0	1	1.725
0	0	1	0	0	1.75
0	0	0	1	1	1.775
0	0	0	1	0	1.80
0	0	0	0	1	1.825
0	0	0	0	0	1.85
1	1	1	1	1	OFF

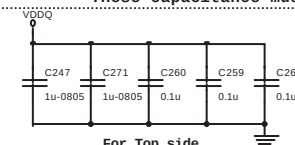


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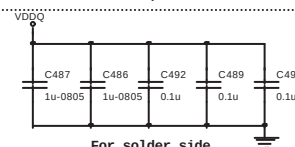




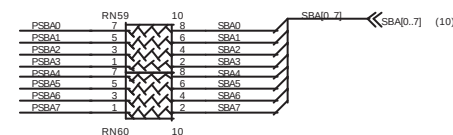
These capacitance must be closed NB

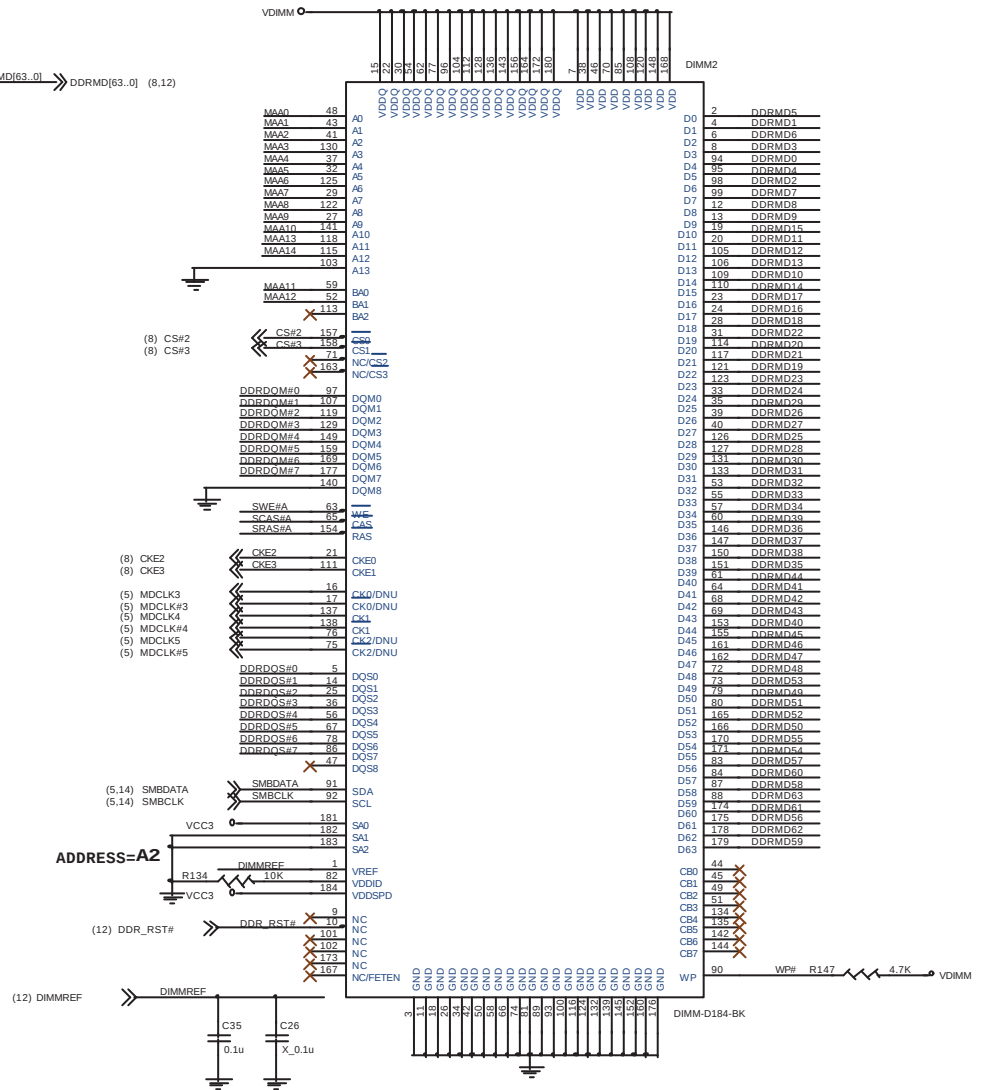
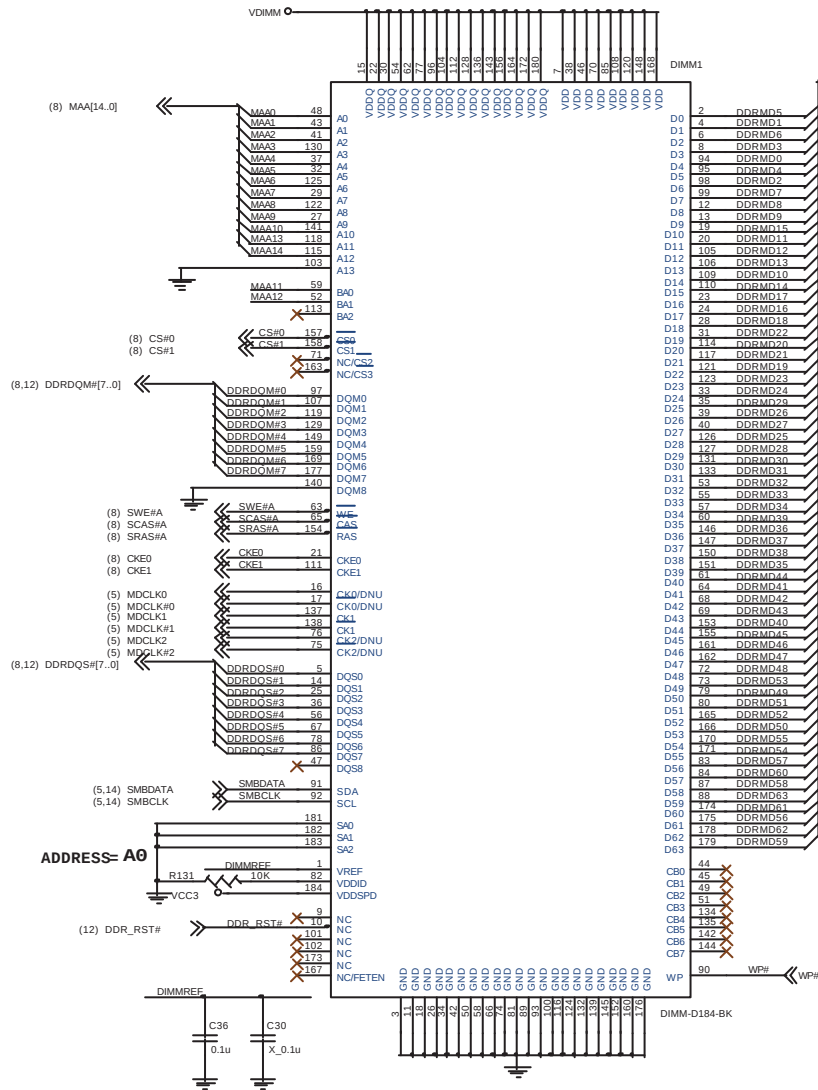


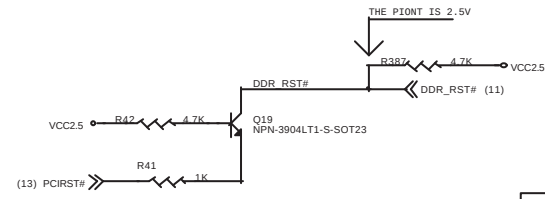
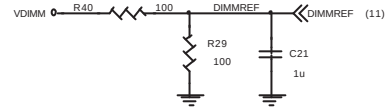
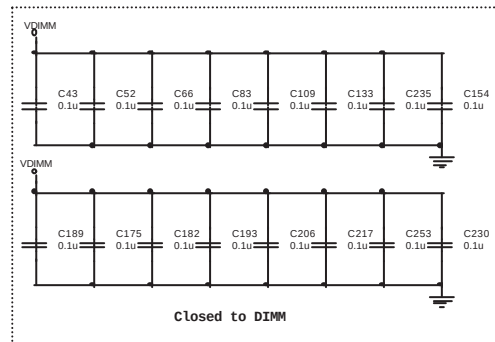
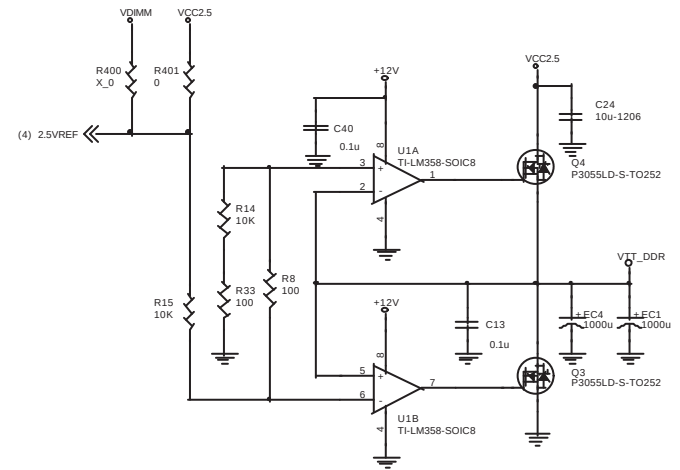
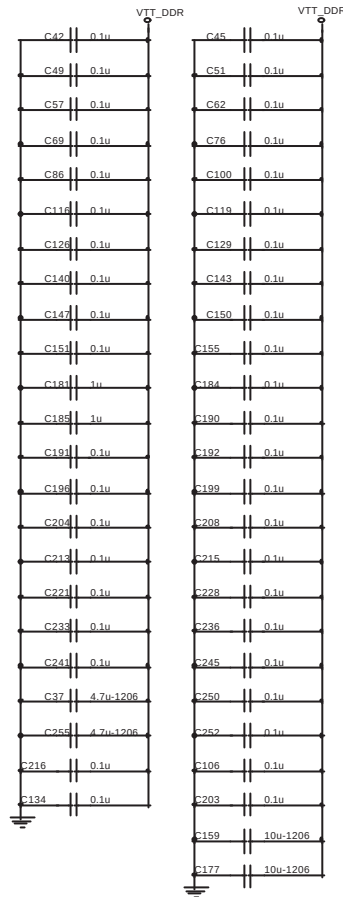
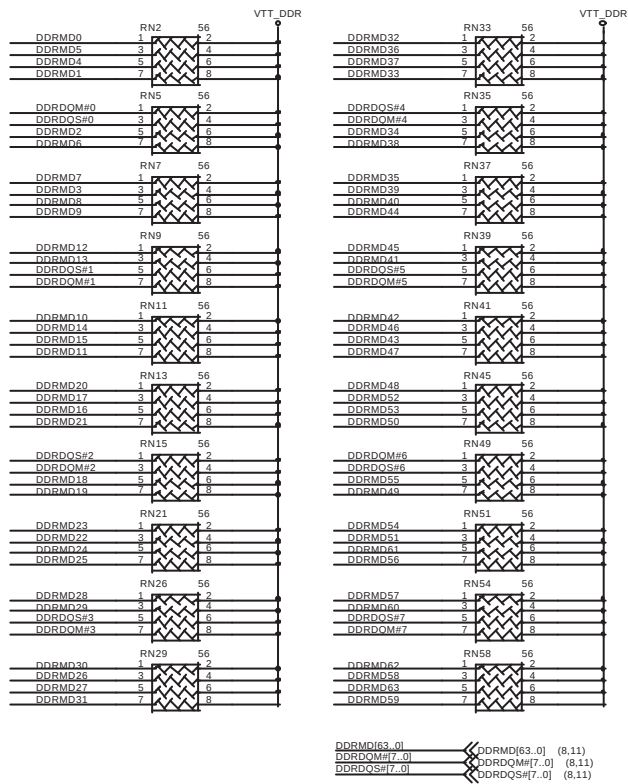
For Top side



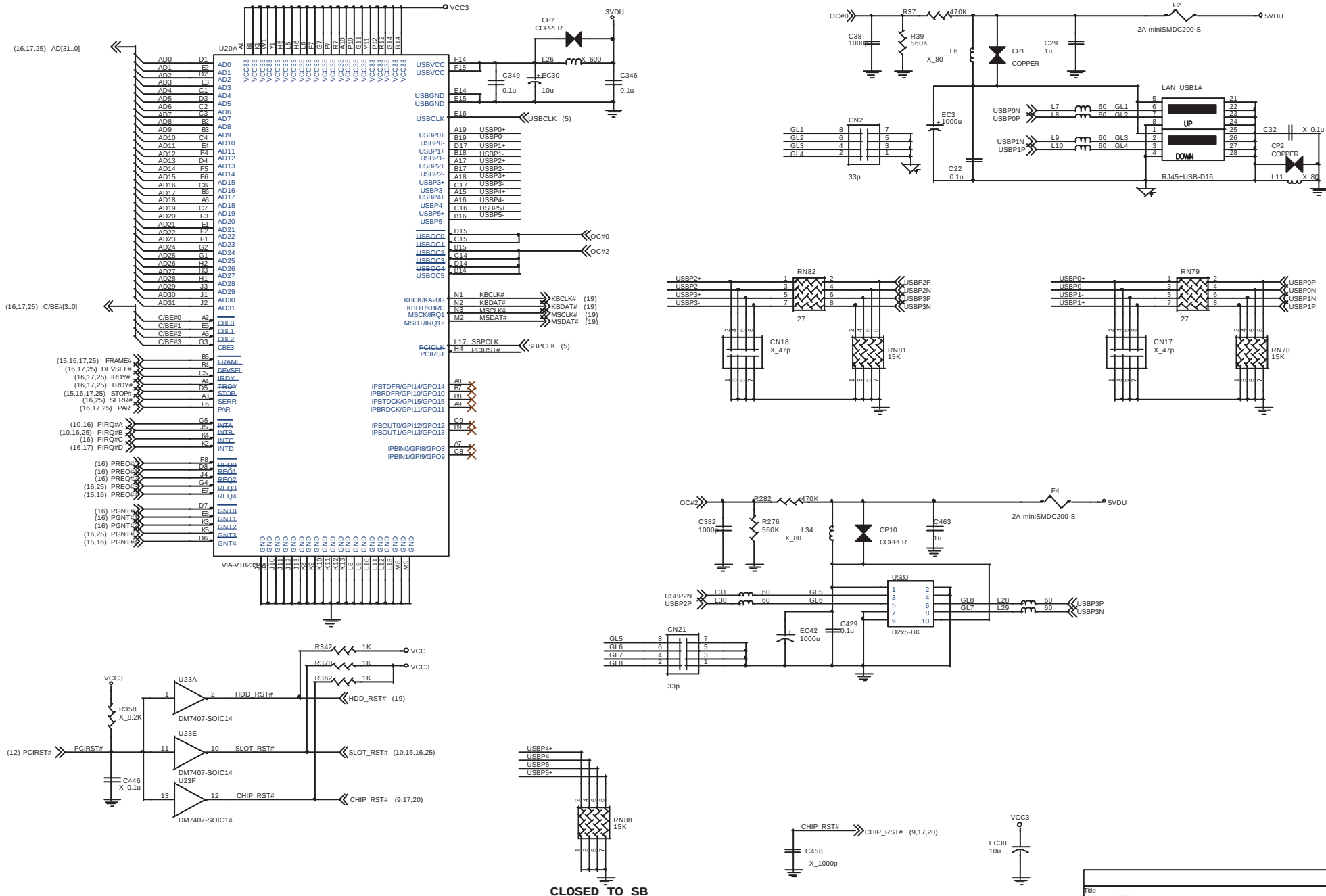
For solder side



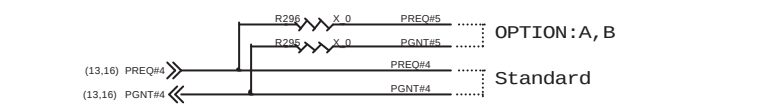
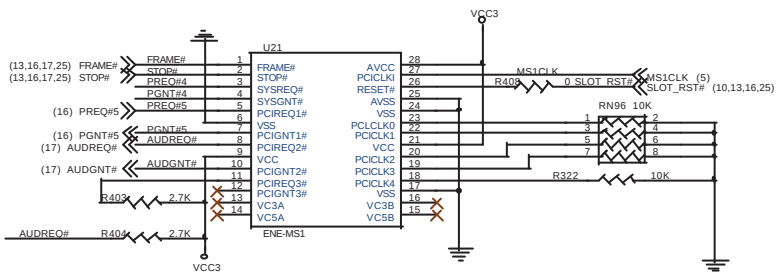
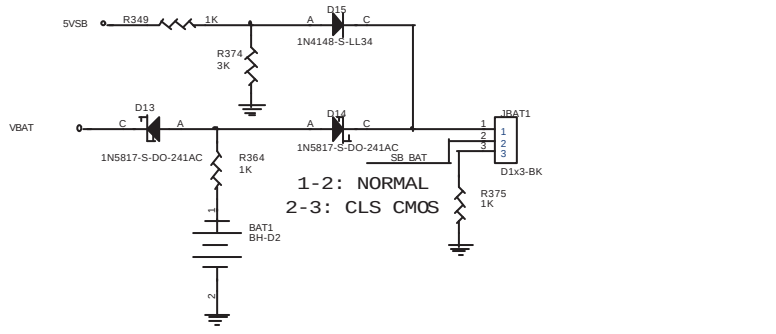




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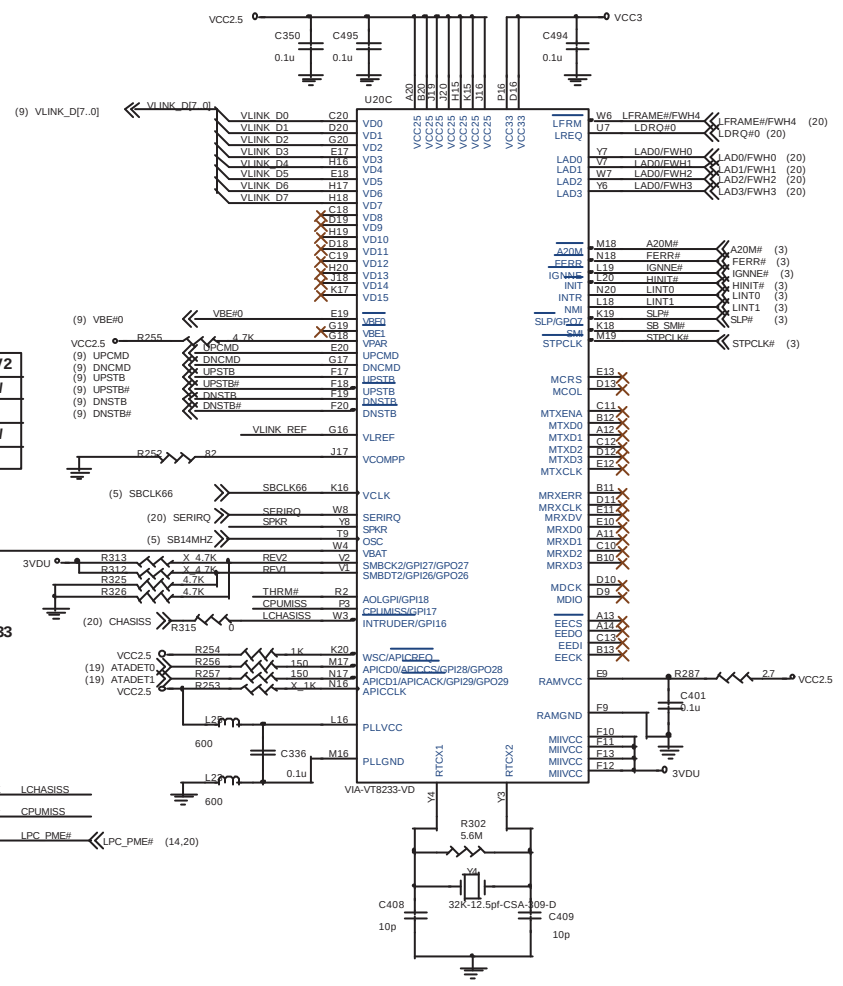
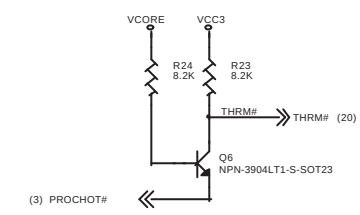
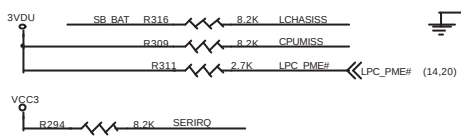
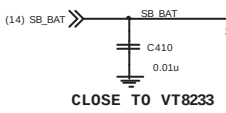


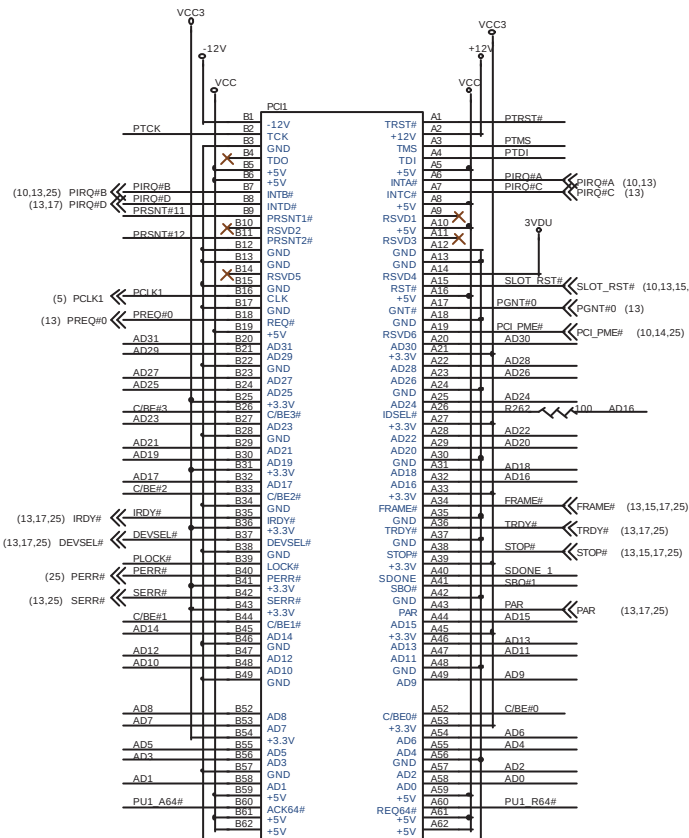
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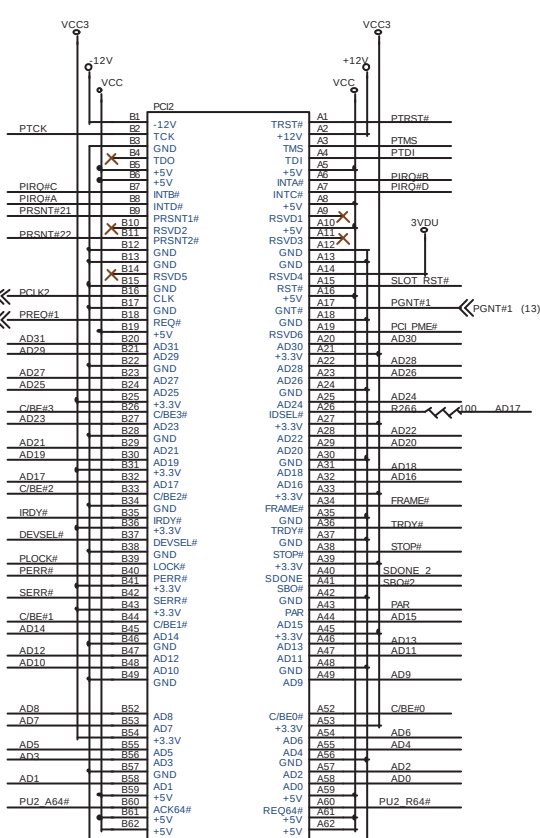
Mode	Installed	Uninstall
Standard:	U33, RN130, R604	R605, R606
Option: A, B	R605, R606	U33, RN130, R604

Version control		
	REV1	REV2
VER: 1.0	LOW	LOW
	LOW	HI
	HI	LOW
	HI	HI

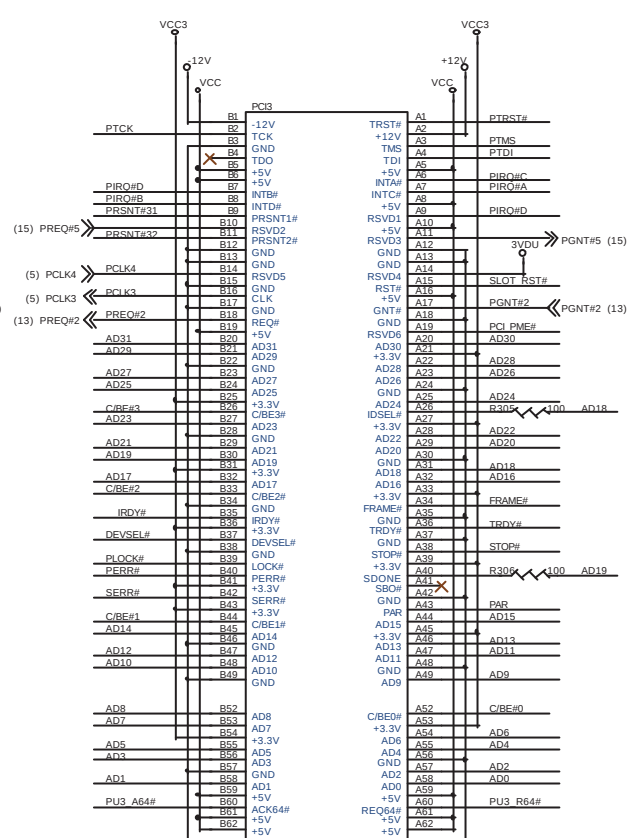




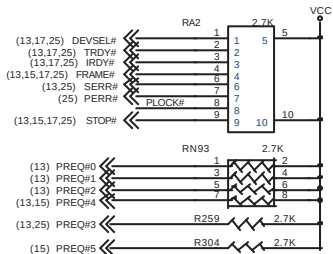
PCI-D120-WH-SN



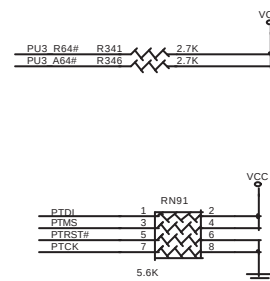
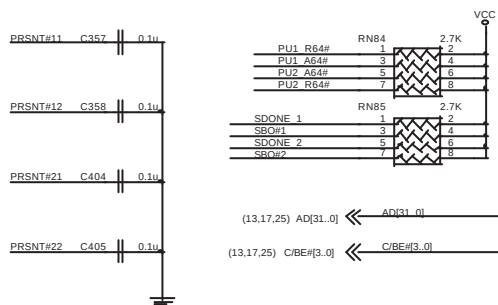
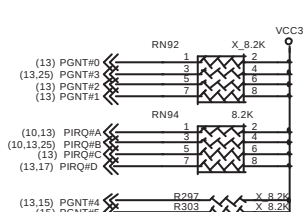
PCI-D120-WH-SN



PCI-D120-WH-SN

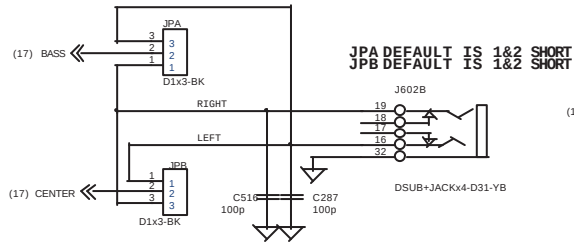


PCI BUS Pull-up/dn resistor

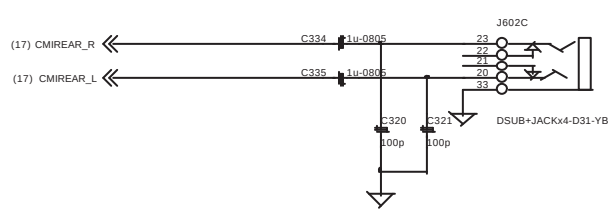


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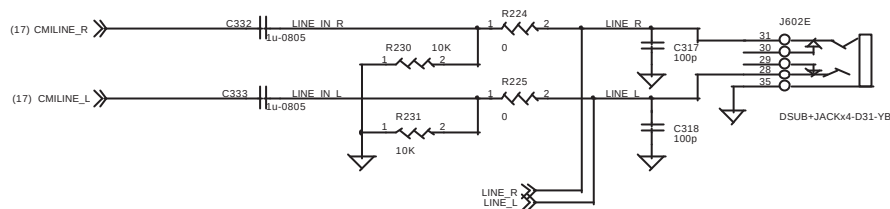
CENTER/BASS JACK



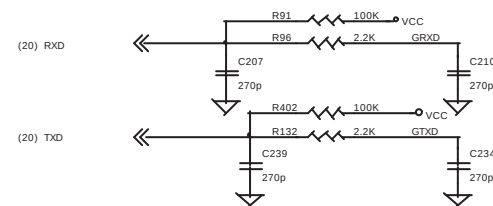
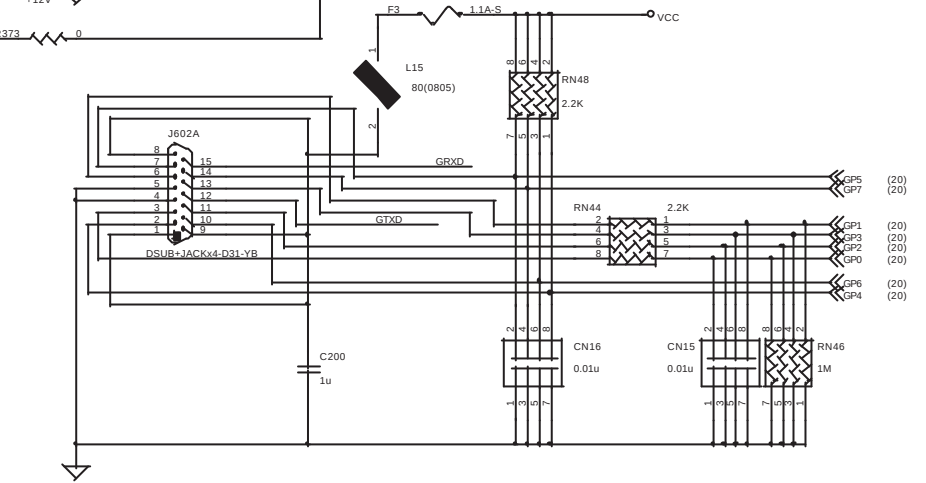
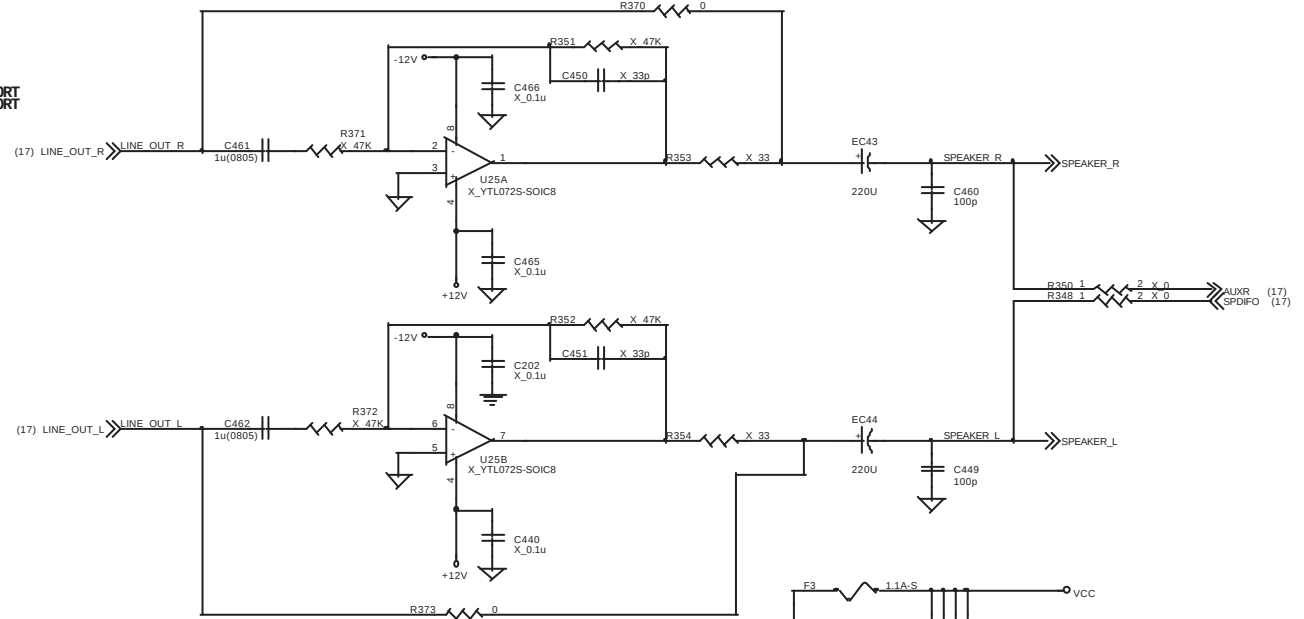
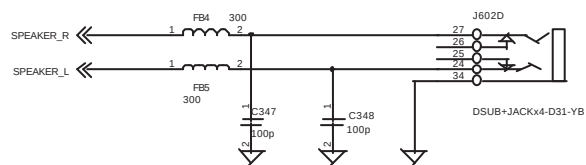
REAL JACK



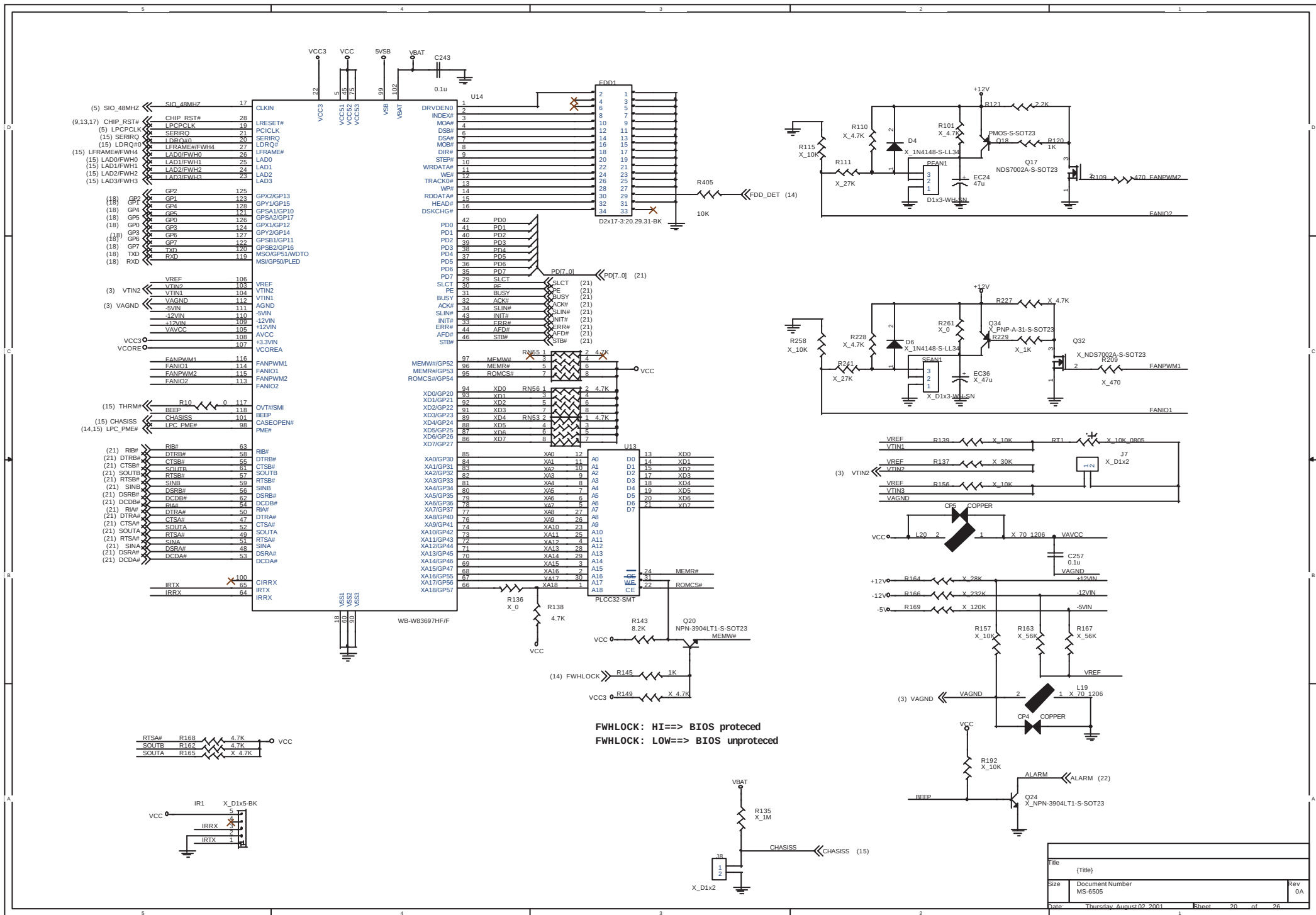
LINE-IN JACK

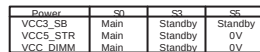


SPEAKER OUT JACK

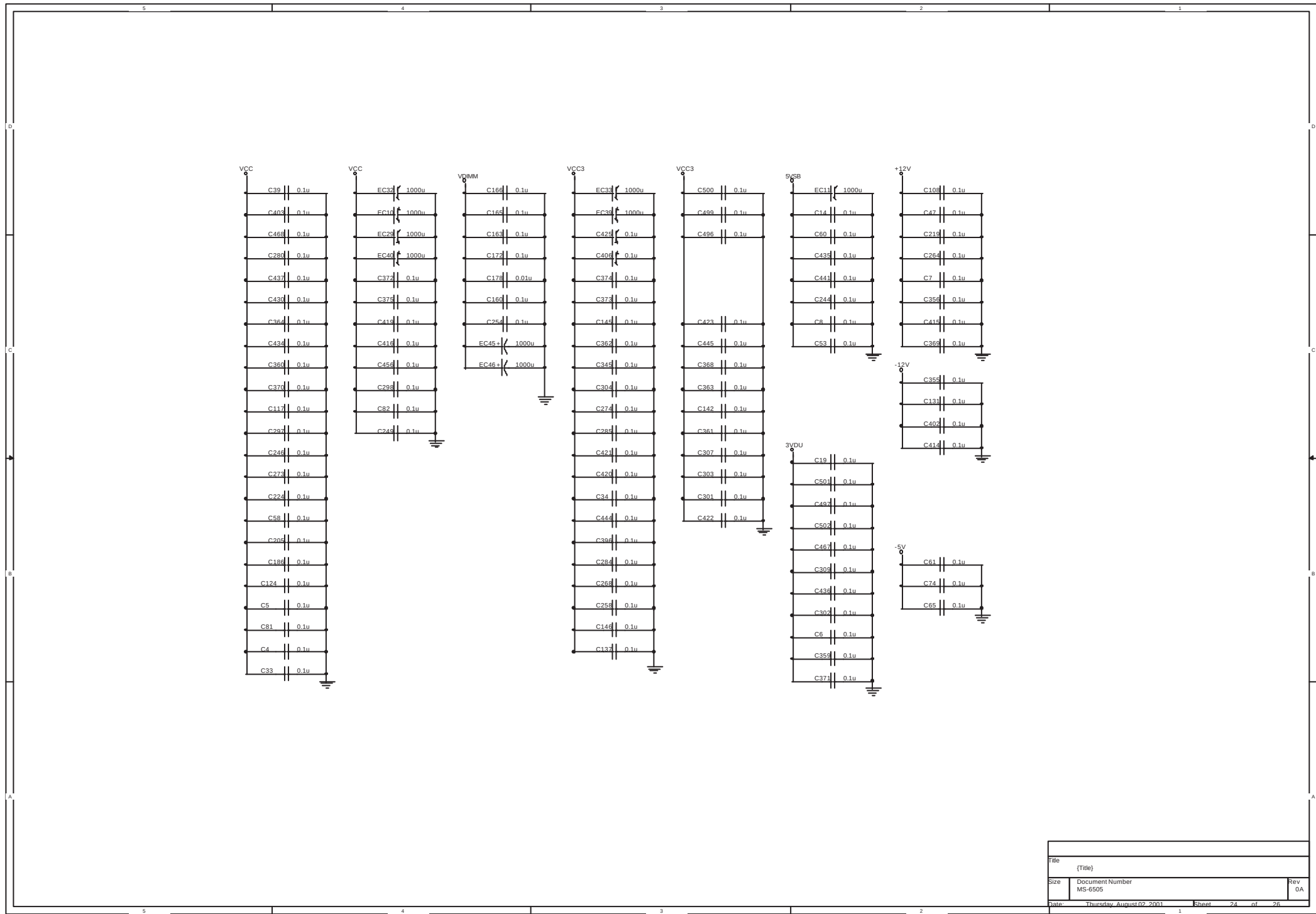


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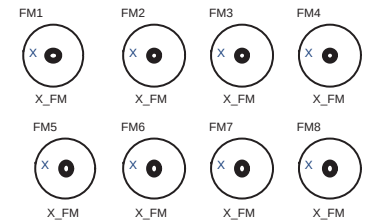
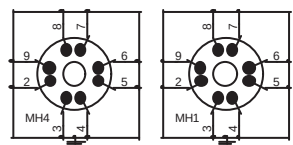
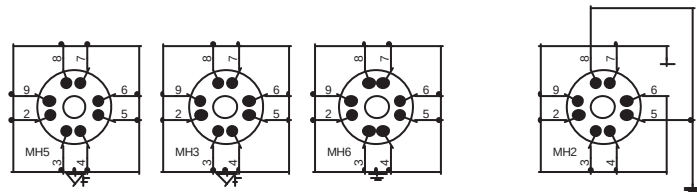
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Title			
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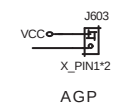
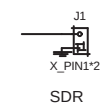


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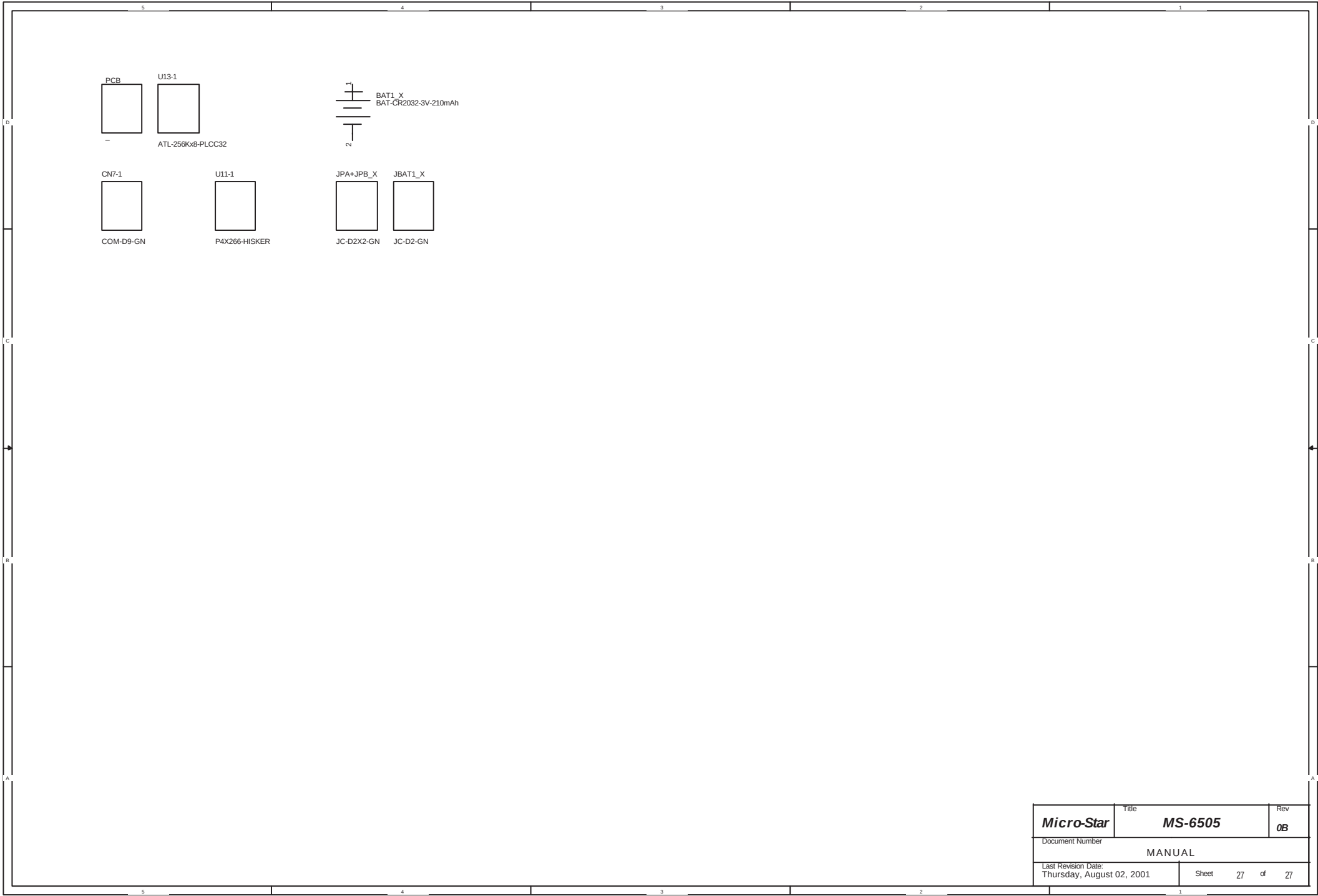
PCB OTHER COMPONENT



SIMULATION TRACE



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Last Revision Date:	Thursday, August 02, 2001			Sheet 27 of 27