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-
0. Gerber-Out MS6398E-300_04-03-91.dsn (To layout)04/03/2002

1. Release BOM MS6398E-300_04-09-91.dsn (Release BOM 1'st time)04/09/2002

2. ECR BOM MS6398E-300_04-24-91.dsn (Release BOM 2'nd time)04/24/2002

3. ECR BOM MS6398E-300_04-29-91.dsn (Release BOM 3'rd time)04/29/2002

4. ECR BOM MS6398E-300_05-23-91.dsn (Release ES-BOM 6398E-05S to MP-BOM 6398E-040)05/23/2002

5. ECR BOM MS6398E-300_07-02-91.dsn (Release ECR for 6398E-040)07/02/2002

MS-6398E / VER:3.0

INTEL (R) Brookdale-G Chipset
Willamette/Northwood 478pin mPGA-B Processor Schematics

CPU: Willamette/Northwood mPGA-478B Processor
System Brookdale-E Chipset:
INTEL MCH 845E + INTEL ICH4

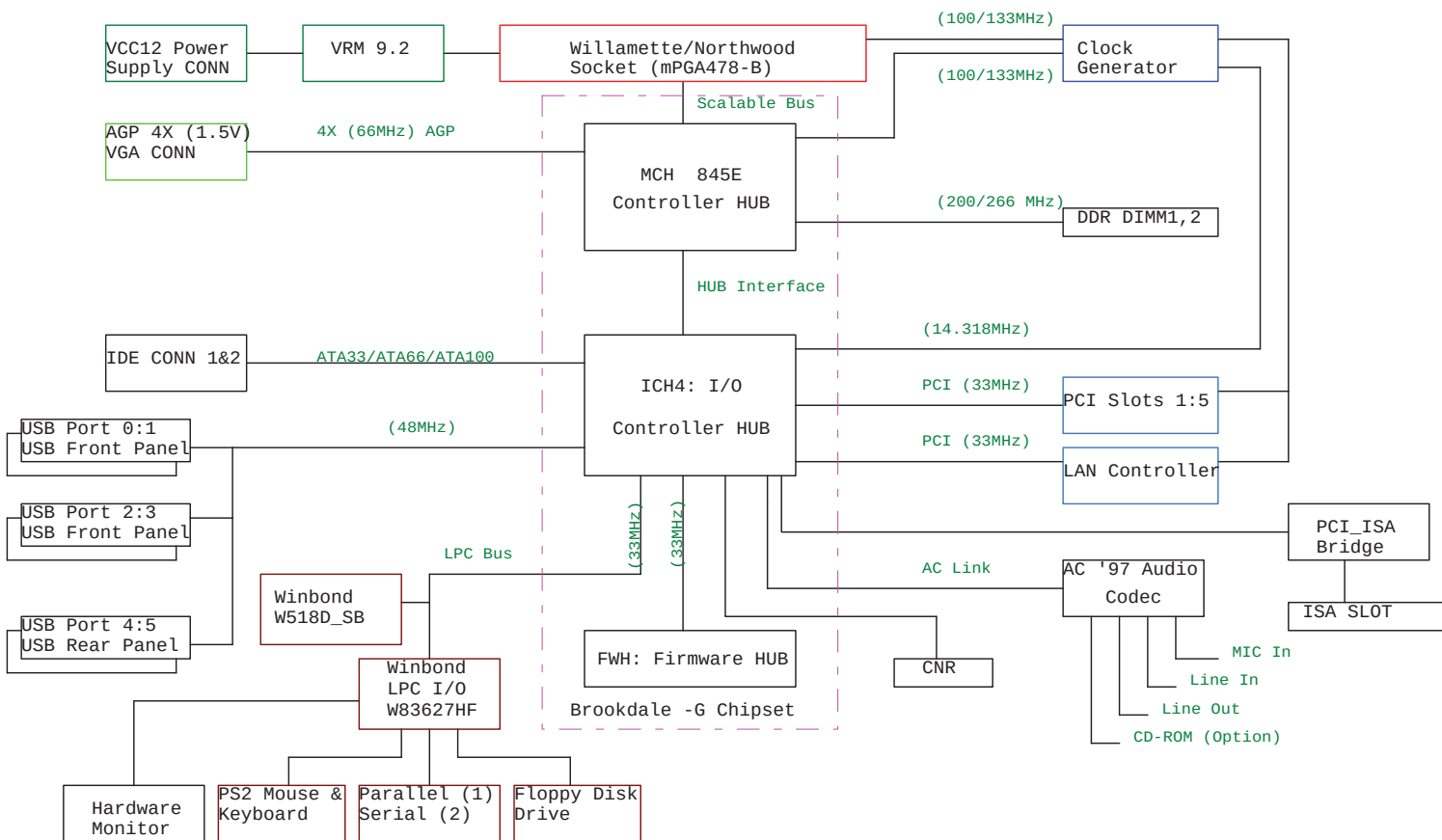
On Board Chipset:
BIOS -- FWH
AC'97 Codec -- ALC201A/650
LPC Super I/O -- W83627HF
LAN -- INTEL 82562EM/ET

Expansion Slots: **AGP2.0 SLOT * 1**
PCI2.2 SLOT * 5
ISA SLOT * 1
CNR SLOT * 1

BOM Option :
MS6398E-V3_OPT : I+L -> Legend BOM => Note : I + L -> 6398E-05S (ES) -> 6398E-040 (MP BOM)

Note : Default : P4+DDR*2+PCI*5+CNR*1+S/W Audio ADI1981
I => Support ISA
L => Support Intel LAN 82562ET

Micro Star Restricted Secret		
Title	COVER SHEET	Rev
Document Number	MS-6398E	300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date:
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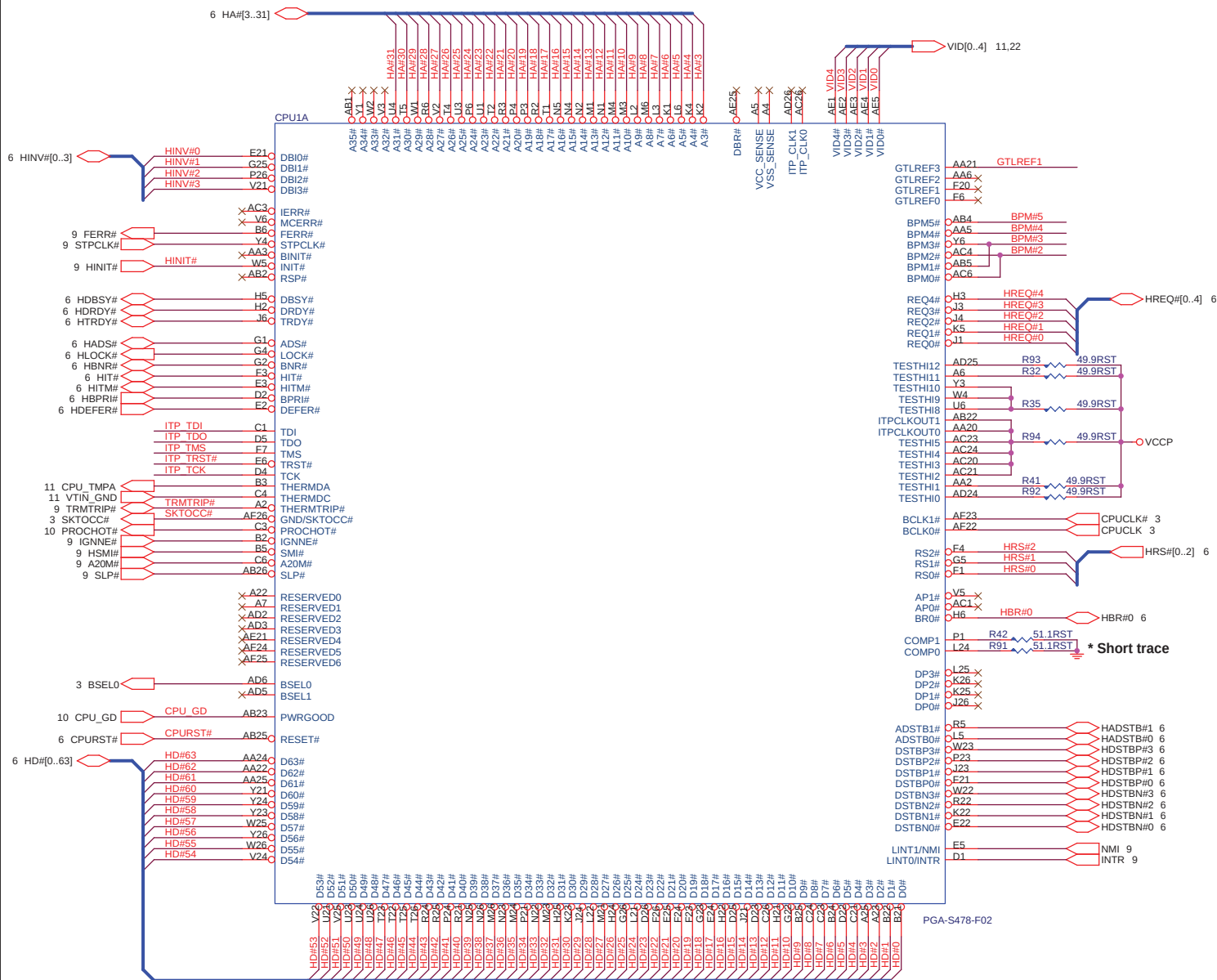


Model option table

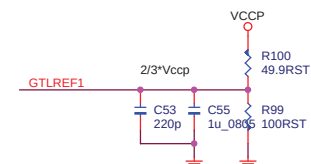
Model type	Function	BOM Config	ERP BOM No.
MS6398E/V3.0	For Legend	L + I	6398E-05S

Micro Star Restricted Secret		
Title	BLOCK DIAGRAM	Rev
Document Number	MS-6398E	300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002
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CPU SIGNAL BLOCK

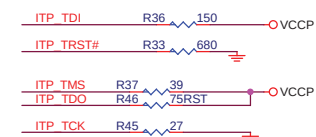


CPU GTL REFERENCE VOLTAGE BLOCK



Every pin put one 220pF cap near it.
Trace Width 7mils, Space 10mils.
Keep the voltage divider within
1.5" of the GETREF pin.

CPU ITP BLOCK

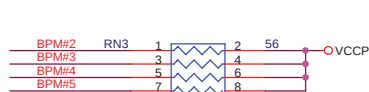


ALL COMPONENTS CLOSE TO CPU

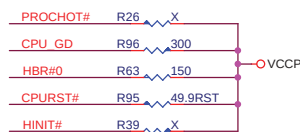
Micro Star Restricted Secret

Title mPGA478-B Intel CPU Socket Part 1		Rev 300
Document Number MS-6398E		
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002
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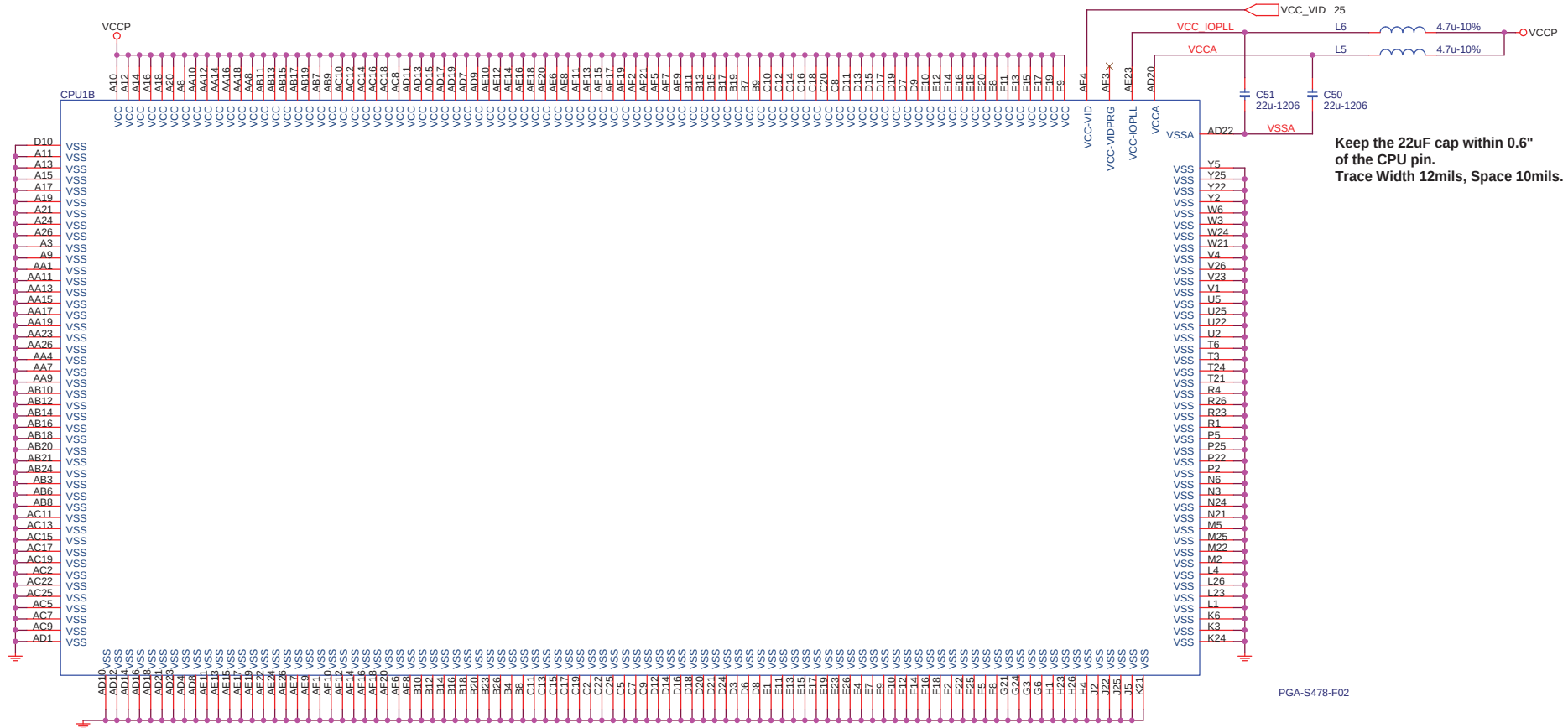
CPU STRAPPING RESISTORS



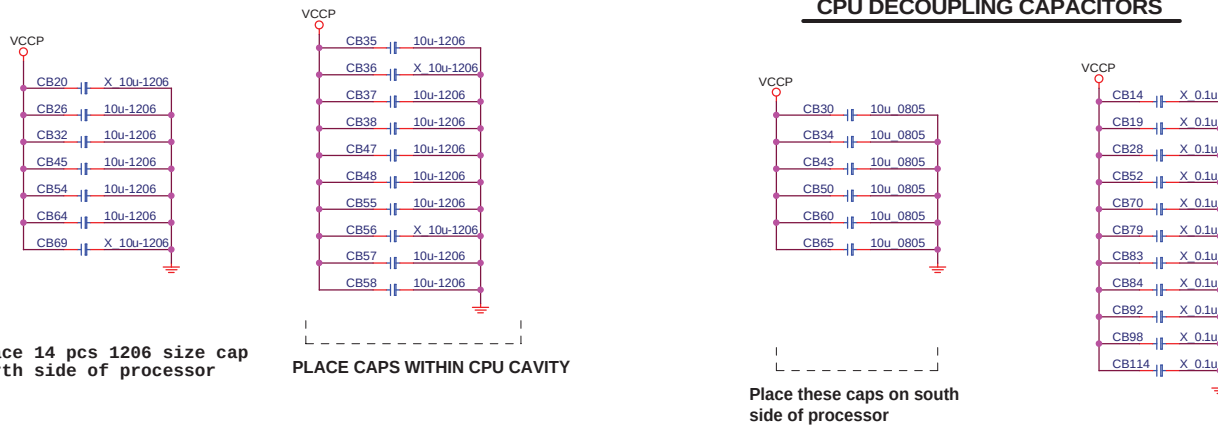
ALL COMPONENTS CLOSE TO CPU



CPU VOLTAGE BLOCK

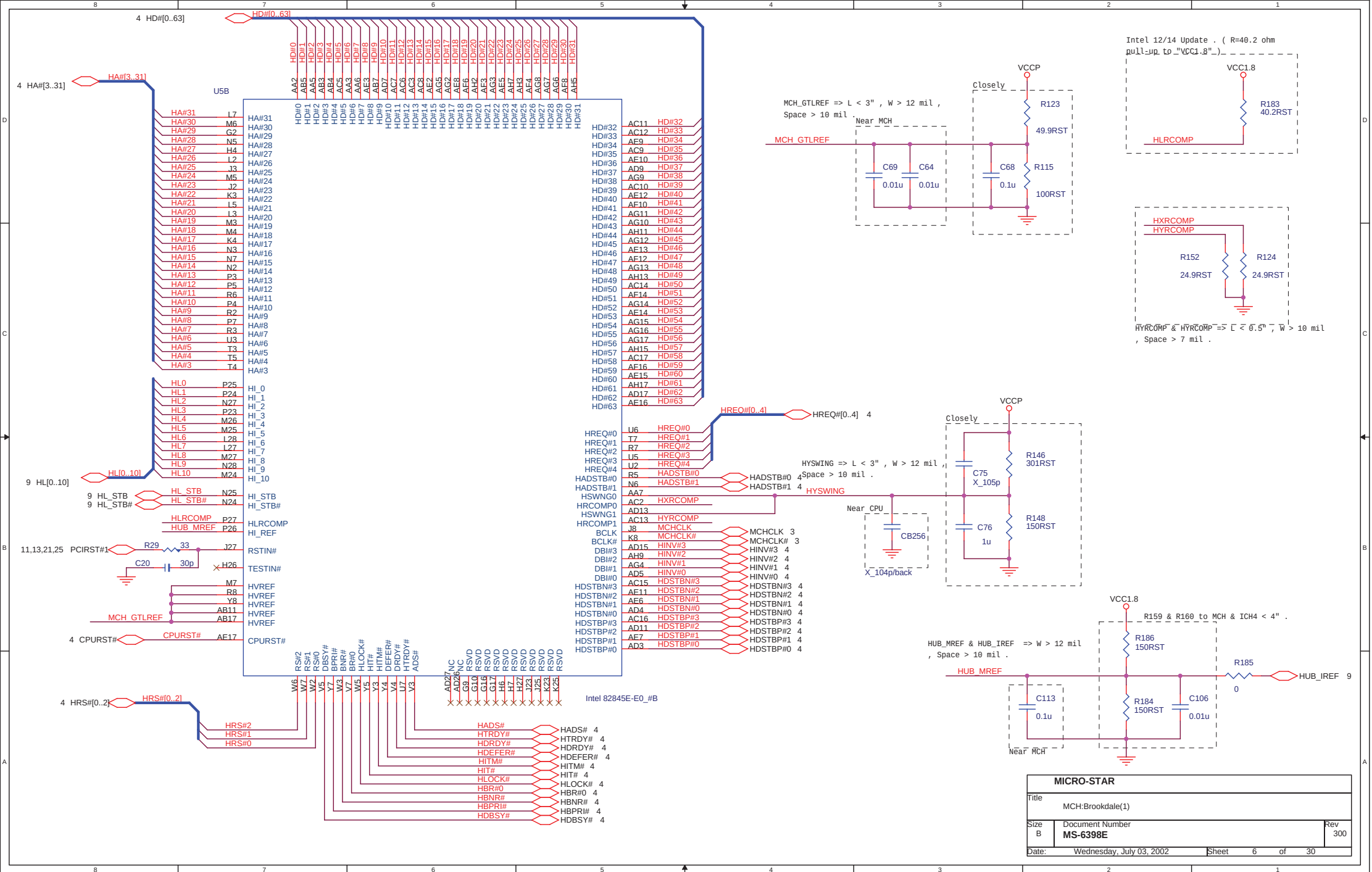


CPU DECOUPLING CAPACITORS



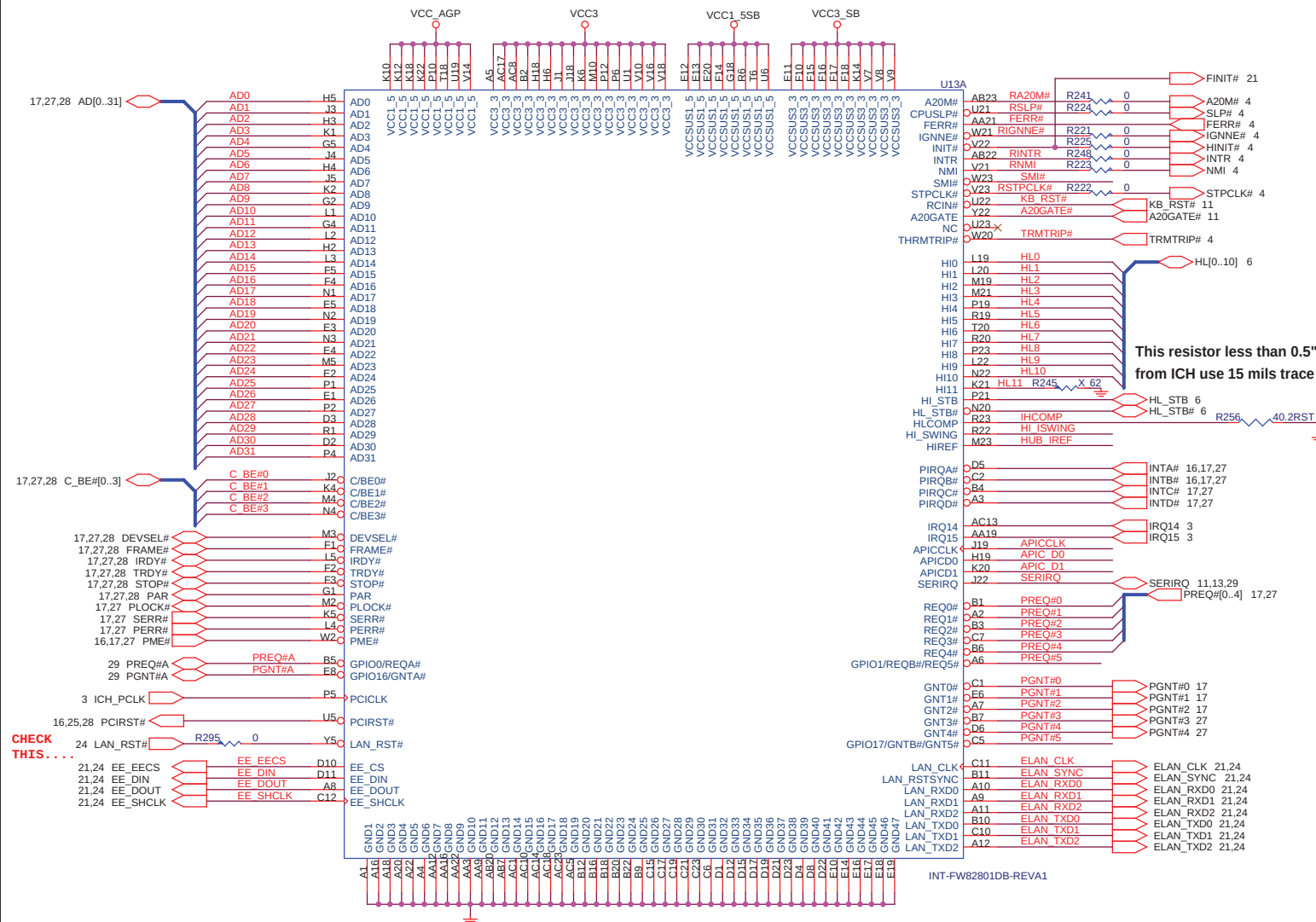
Micro Star Restricted Secret

Title mPGA478-B Intel CPU Socket Part 2		Rev 300
Document Number MS-6398E		
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002 Sheet 5 of 30

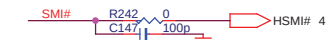


MICRO-STAR		
Title		
MCH:Brookdale(1)		
Size	Document Number	Rev
B	MS-6398E	300
Date:	Wednesday, July 03, 2002	Sheet 6 of 30

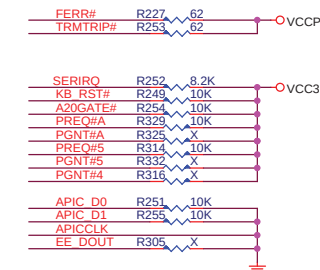
ICH4 PCI / HUB LINK / CPU / LAN / INTERRUPT SIGNALS



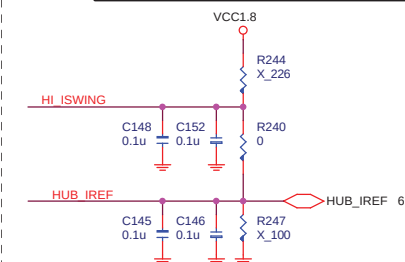
ICH4 SMI# SIGNAL



ICH4 STRAPPING RESISTORS



ICH4 REFERENCE VOLTAGE

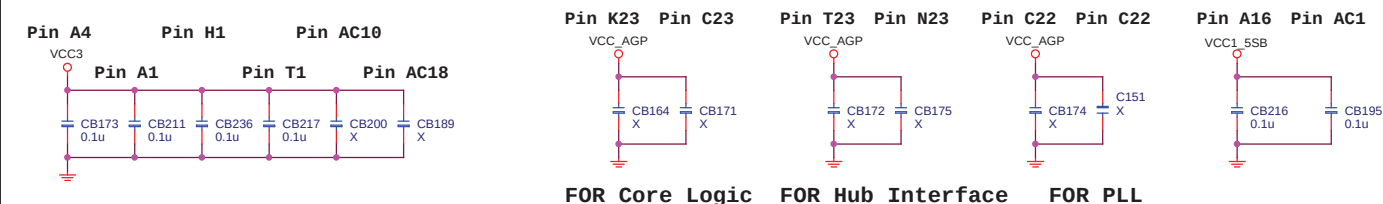


Place Cap. as Close as possible to ICH4 < 0.25"

Trace width use 12 mils and 10mils space

ICH4 DECOUPLING CAPACITORS

Place one 0.1u close to ICH4 <100 mil



FOR Core Logic

FOR Hub Interface

FOR PLL

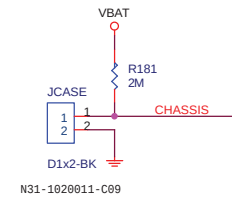
Micro Star Restricted Secret

Title	ICH4 Part 1 (Power,HUB,PCI & CPU I/F)		Rev
Document Number	MS-6398E		300
MICRO-STAR INT'L CO.,LTD.		Last Revision Date:	
No. 69, Li-De St, Jung-He City,		Wednesday, July 03, 2002	
http://www.msi.com.tw		Sheet	30
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**NOTE: LOCATE CLOSE
STATUS PANEL**

Diagram showing the connection for pins 1 to 5 of the D1x5-1.4-BK component:

- Pin 1: VCC5
- Pin 3: IRRX
- Pin 4: IRTX
- Pin 5: Ground (GND)



VCC5

CB169
0.1u
CB153
0.1u
CB163
0.1u

VCC5 ○ R174 4.7K SOUTA

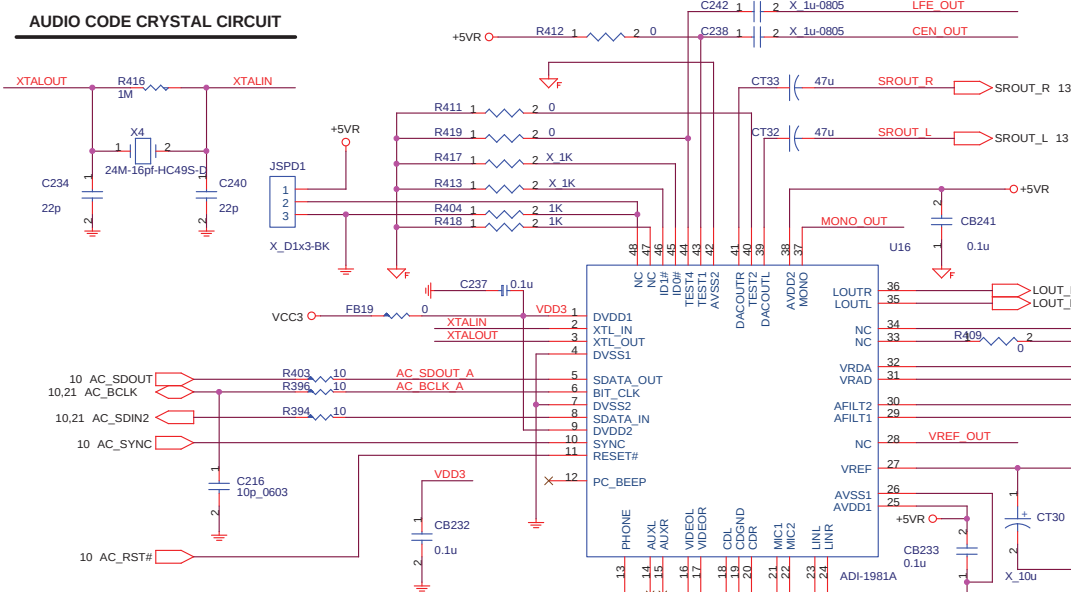
VCC5 ○ R190 4.7K SOUTB

VCC5 ○ R175 X RTSA#

SOUTA	L: Disable KBC	H: Enable KBC
SOUTB	L: 24MHZ	H: 48MHZ
RTSA#	L: CFAD=2E	H: CFAD=4E
DTRA#	L: PNP Default	H: PNP no Default

Title		Rev	
LPC I/O & GAME PORT			
Document Number		300	
MS-6398E			
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002	
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AUDIO CODE CRYSTAL CIRCUIT



AD1881A AC97 CODEC



AUDIO CODE CD / AUX / MODEM IN HEADERS



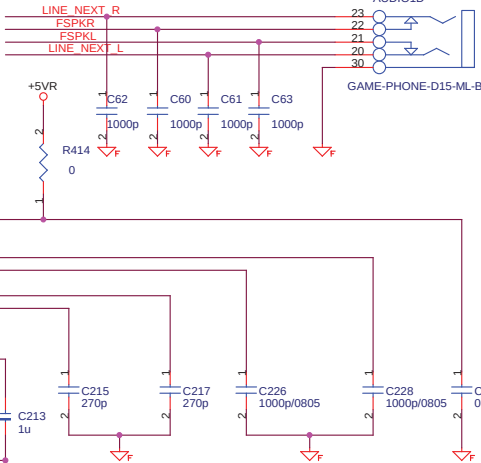
MODEM IN

CD IN

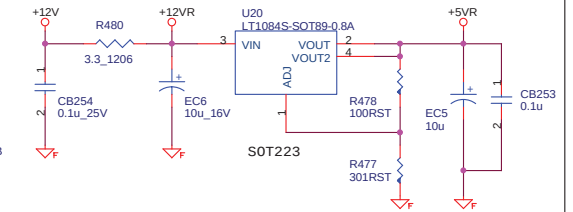
FOR LEGEND

CD IN

SPEAKER OUT JACK

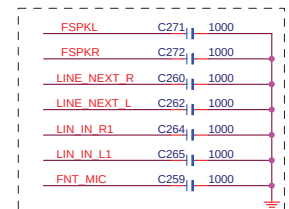


AUDIO CODEC REGULATORS



FOR MSI INTERNAL HEADER

For EMI

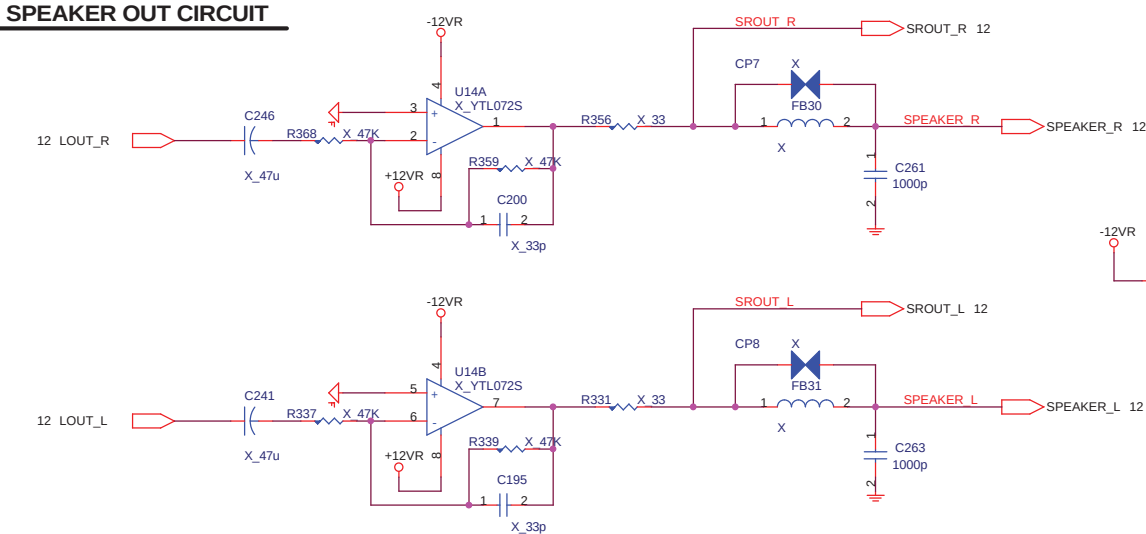


FOR LEGEND

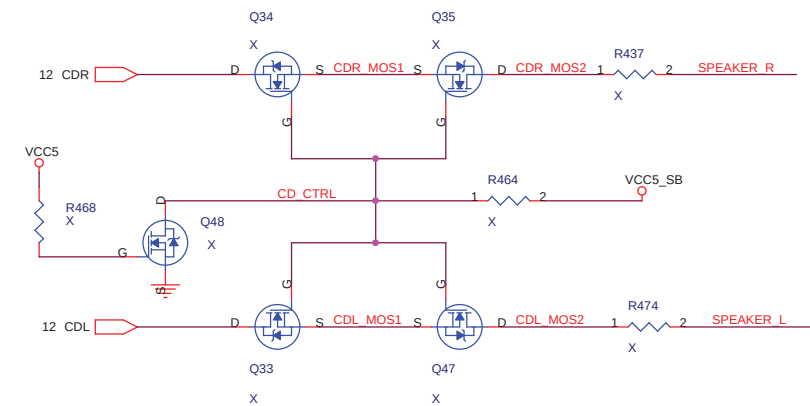
Micro Star Restricted Secret

Title	AC'97 CODEC	Rev
Document Number	MS-6398E	300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002
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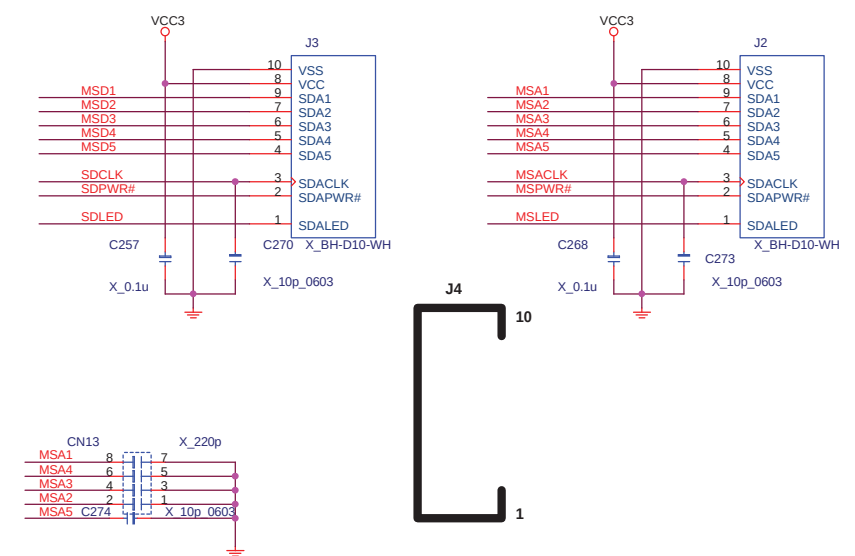
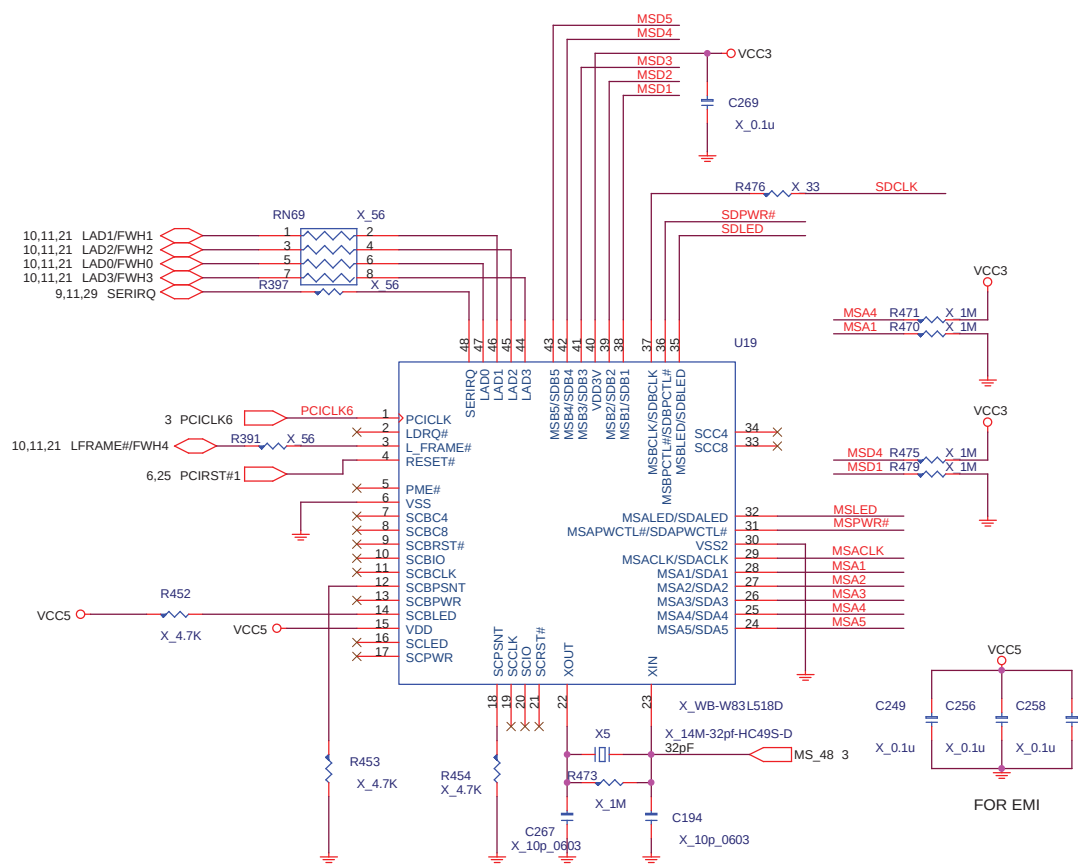
SPEAKER OUT CIRCUIT



CD PANEL PLAY WITHOUT PC POWER_ON



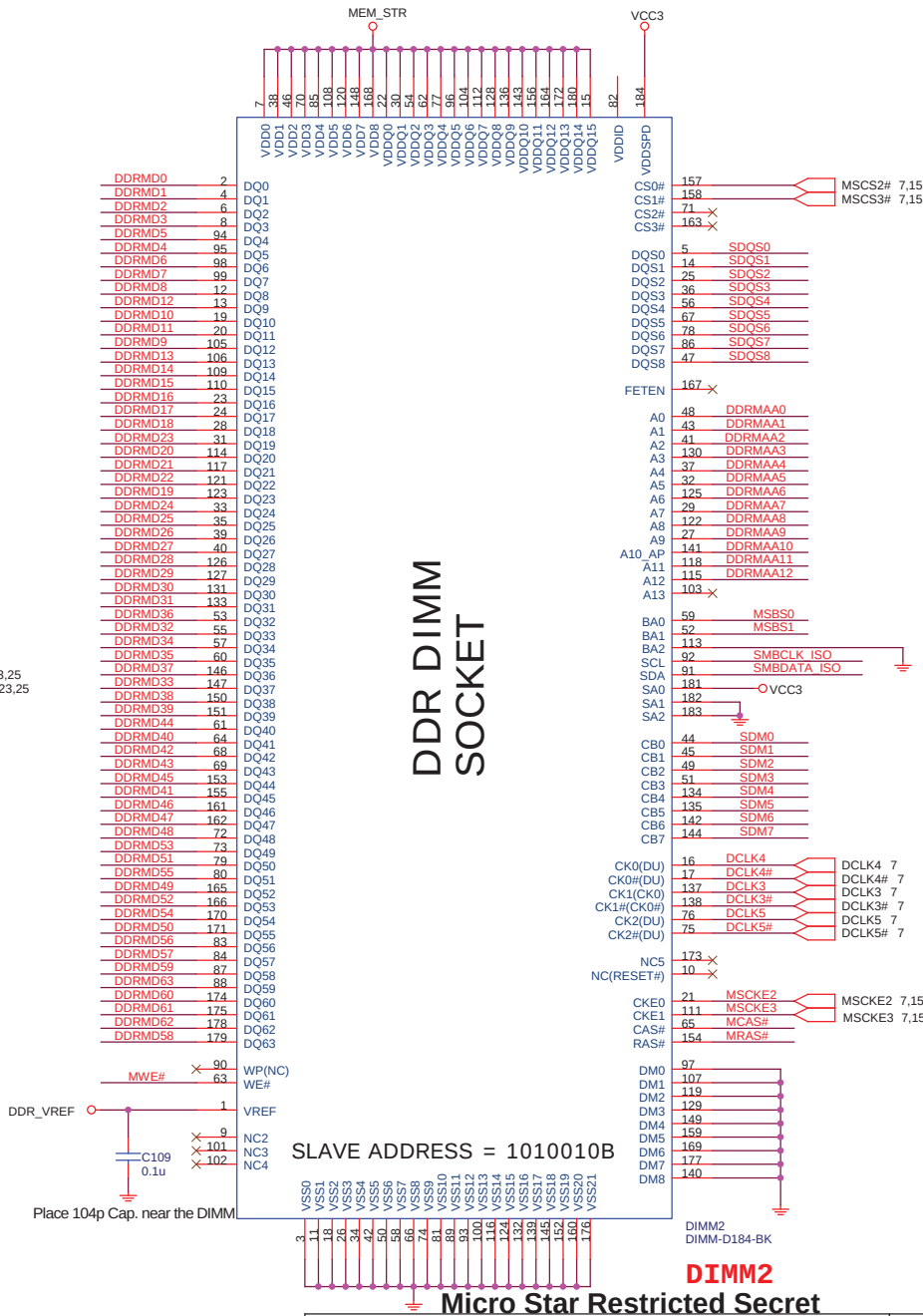
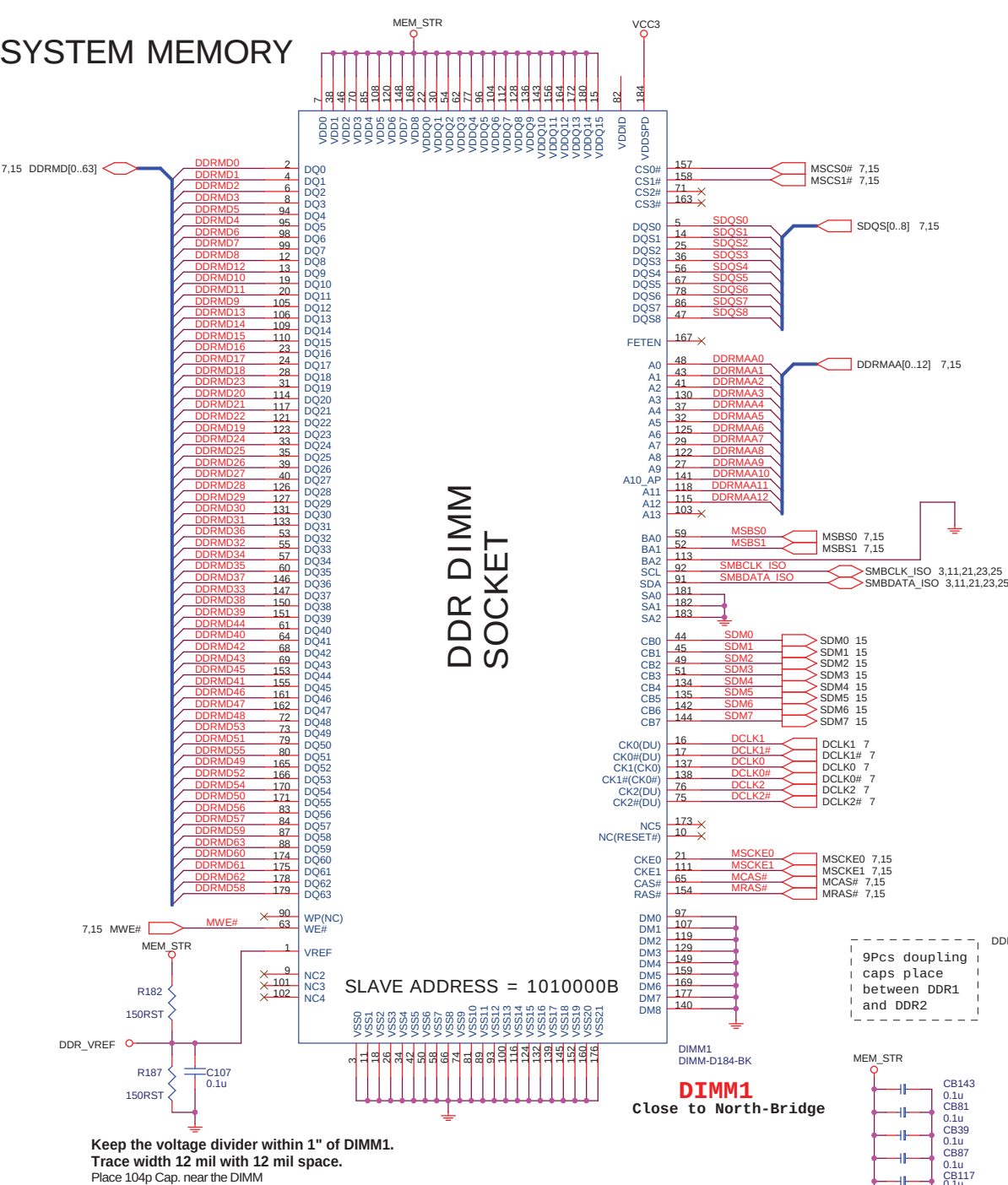
MEMORY STICK CONTROLLER



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Title		Rev
Audio Amplifier & CD Panel Play		300
Document Number		MS-6398E
MICRO-STAR INT'L CO., LTD.		Last Revision Date:
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http://www.msi.com.tw		13 of 30

SYSTEM MEMORY



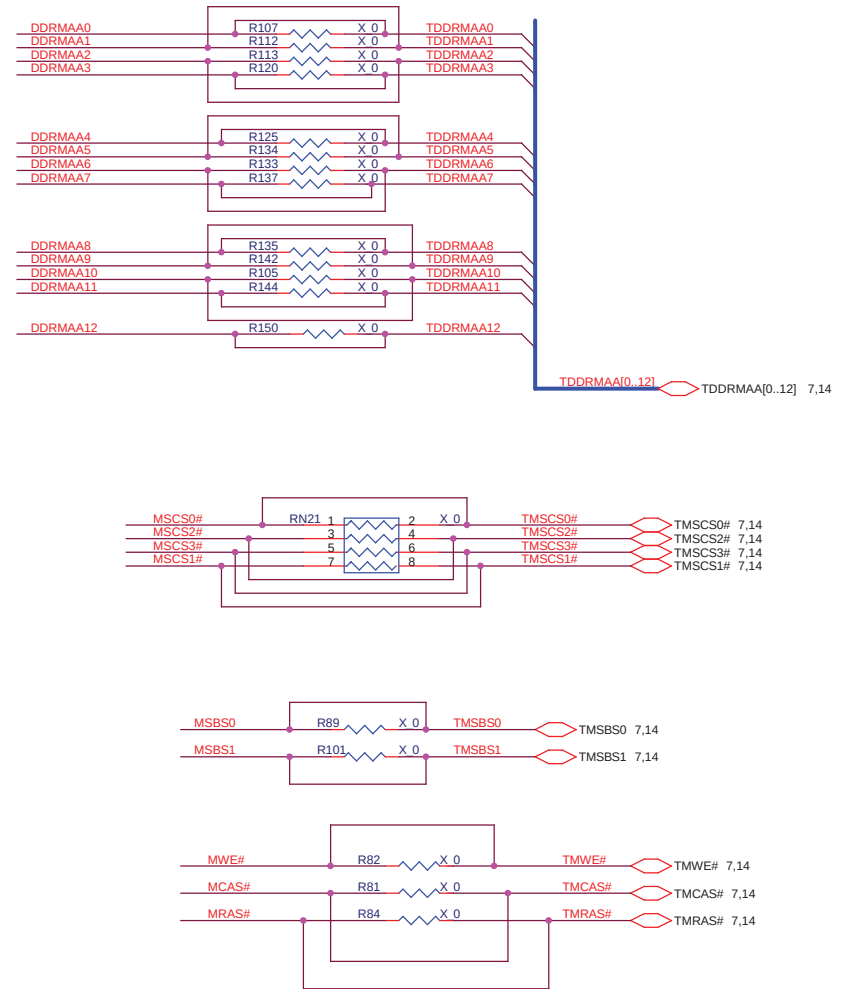
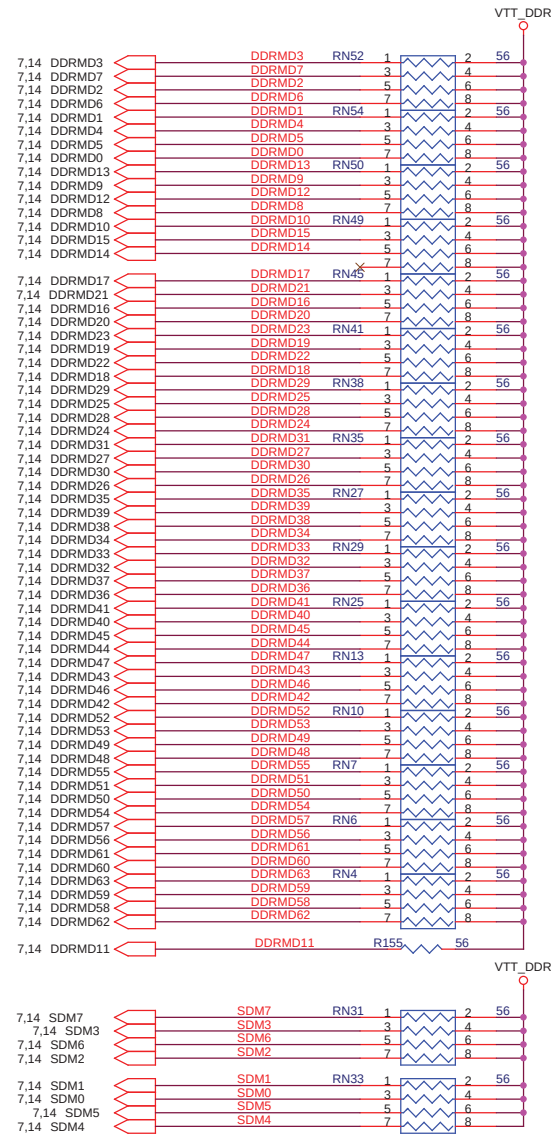
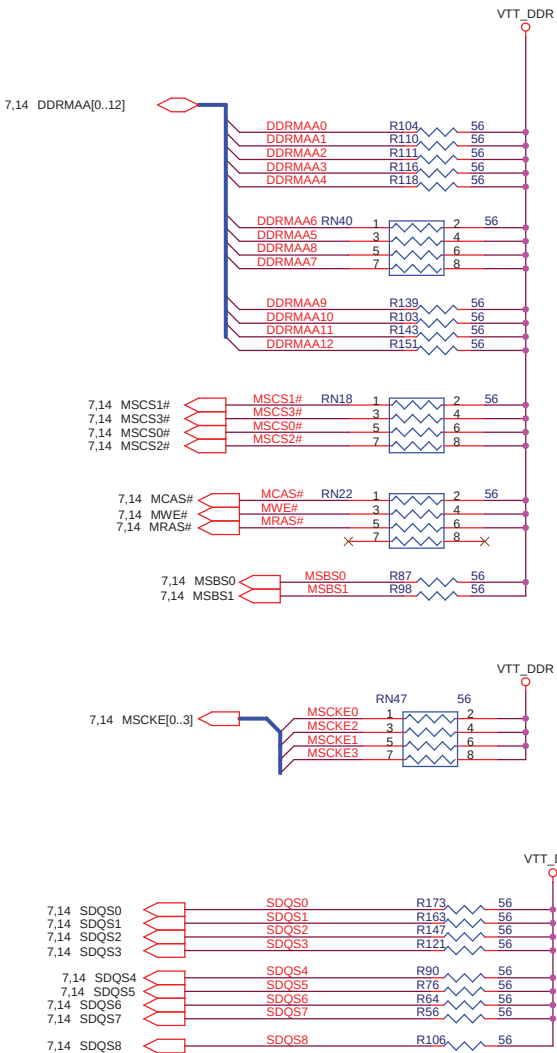
Keep the voltage divider within 1" of DIMM1.
Trace width 12 mil with 12 mil space.
Place 104p Cap. near the DIMM

Place 104p Cap. near the DIMM

Micro Star Restricted Secret

Title	DDR DIMM 1 & 2	Rev
Document Number	MS-6398E	300
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DDR TERMINATORS



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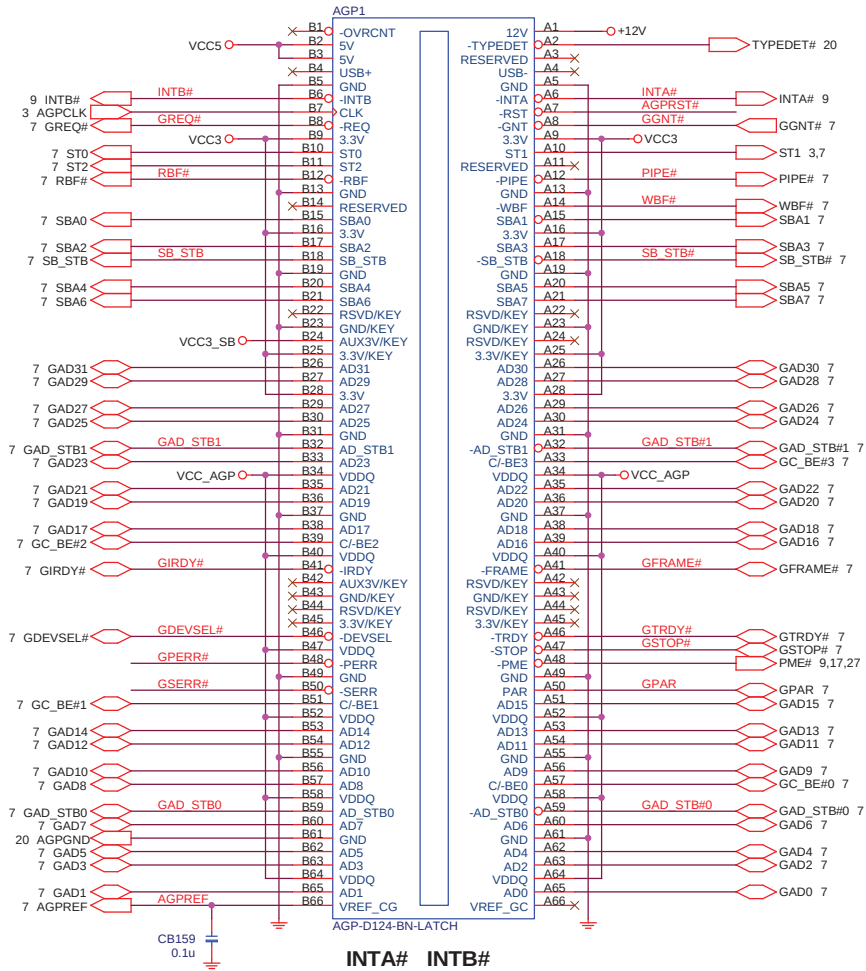
Title	DDR Terminator Resistor	Rev
Document Number	MS-6398E	300
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SDM6
SDQS0

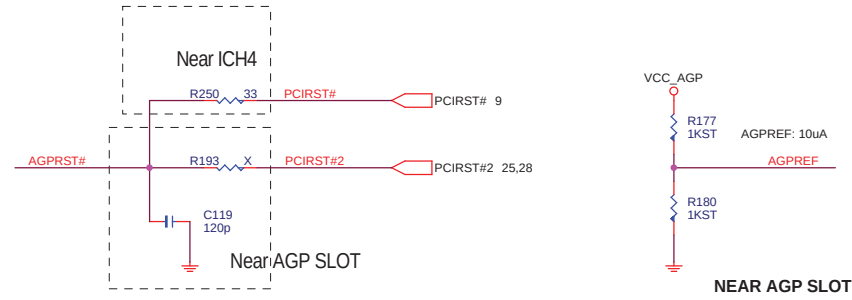
AGP 1.5V 2X/4X SLOT(AGP VER:2.0 COMPLY)

VCC5 = 60mils trace / 15 mils space

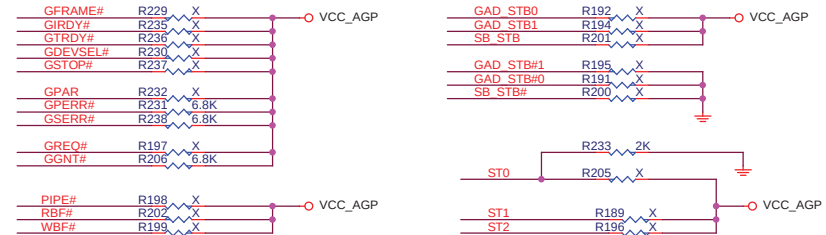
AGP Slot I_{max}
VCC AGP 2.0A
VCC3 6.0A
VCC12 1.0A
VCC5 2.0A
VCC3_SB 0.375A



AGP SIGNAL REFERENCE CIRCUIT



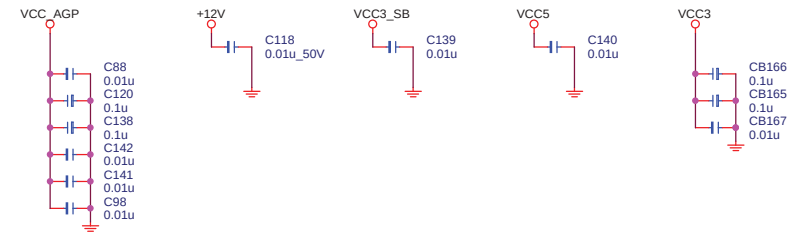
AGP TERMINATION RESISTORS



LESS 100MILS STUB TRACE LENGTH FOR 2/4X
LESS 500MIL FOR 1X MUST BE FOLLOWING.

Place these resistors between AGP slot & GMCH

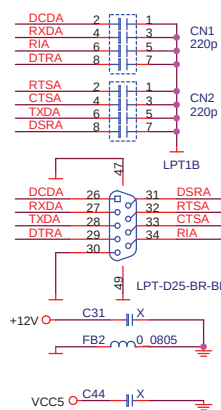
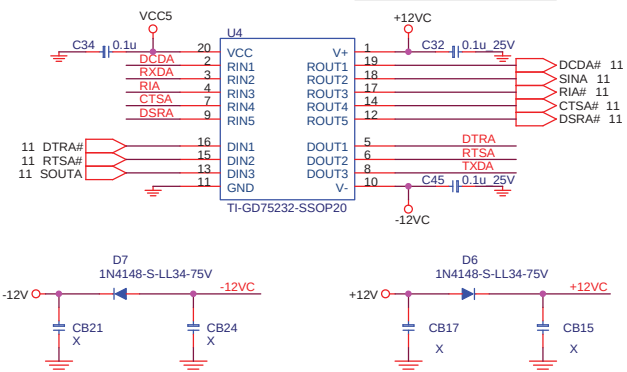
AGP SLOT DECOUPLING CAPACITORS



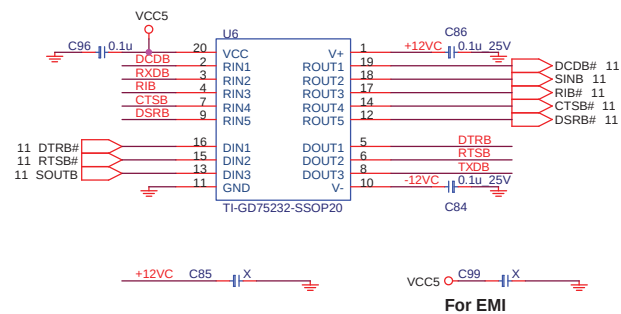
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Title	AGP 2.0	Rev
Document Number	MS-6398E	300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Wednesday, July 03, 2002	
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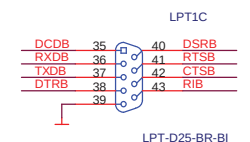
SERIAL PORT 1



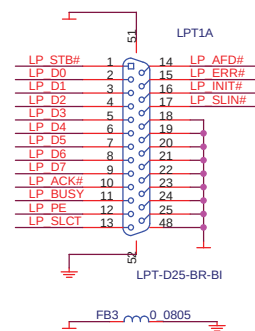
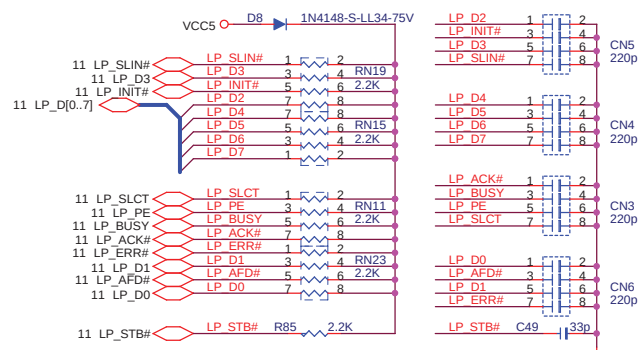
SERIAL PORT 2



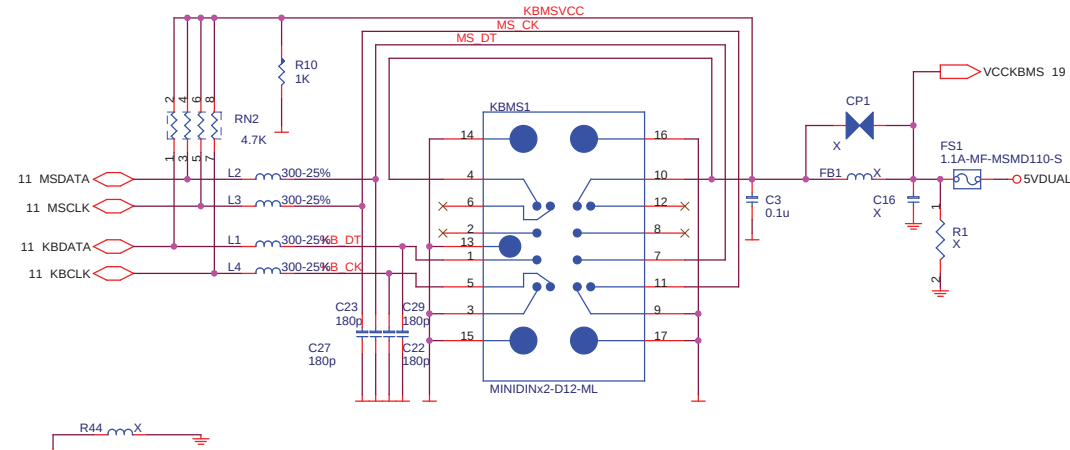
COM2 HEADER



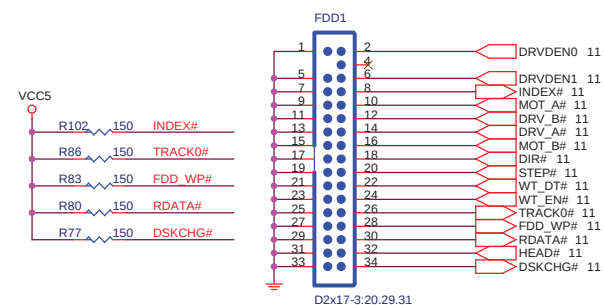
PARALLAL PORT



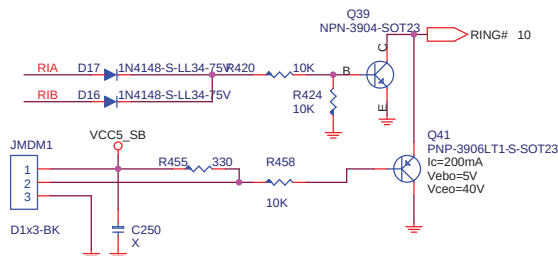
PS2 KEYBOARD & MOUSE CONNECTOR



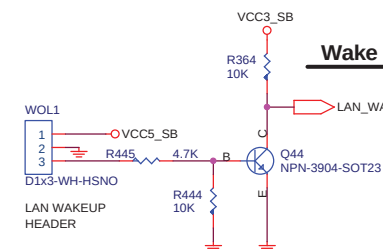
FLOPPY CONNECTOR



Wake On Modem Header



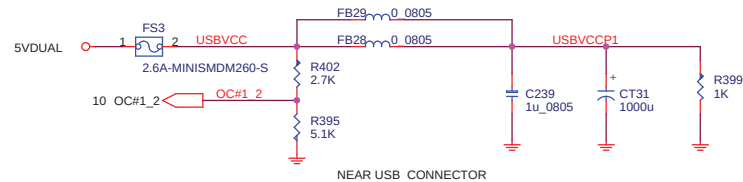
Wake On LAN Header



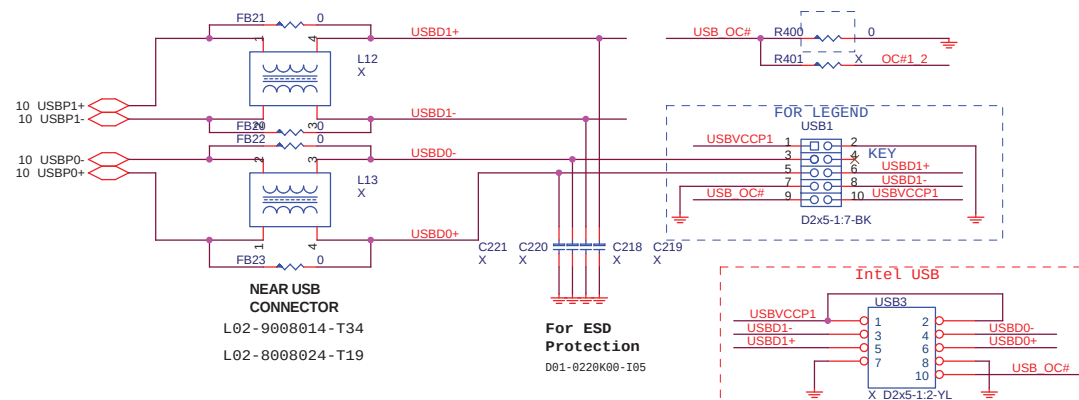
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Title KB,MS,SERIAL,PARALLEL & FLOPPY		Rev
Document Number MS-6398E		300
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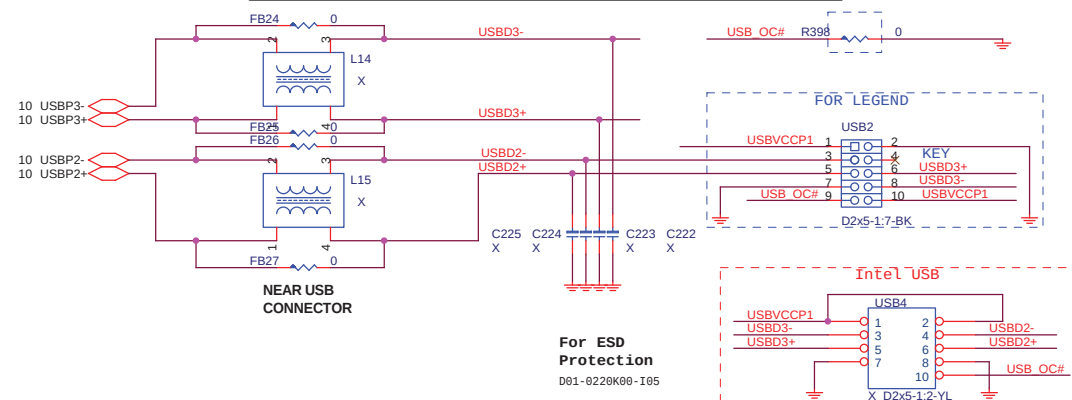
POWER CIRCUIT FOR USB PORT 0,1



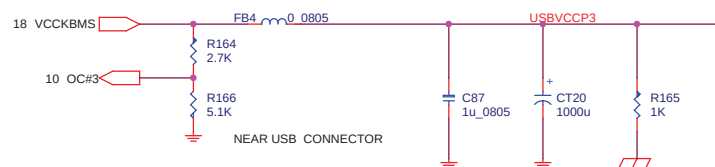
FRONT PANEL USB CONNECTOR FOR USB PORT 0,1



REAR PANEL USB CONNECTOR FOR USB PORT 2,3

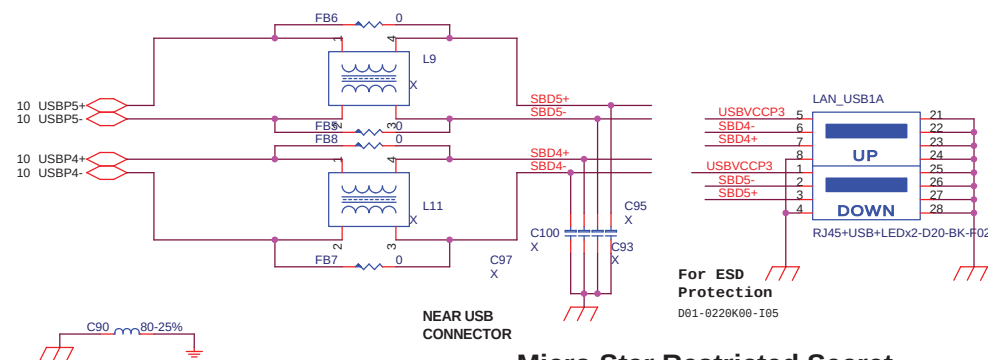


POWER CIRCUIT FOR USB PORT 4,5



- * USB Trace width : 7.5 mils
- * USB Trace Spacing : Low speed 20 mils, High speed 50 mils
- * Differential USB Signlas Trace, Spacing : 7.5 mils
- * USB Power Trace must be 40mils width
- * Length(longest)-Length(shortest)<150mils
- * Maxium trace length <16"

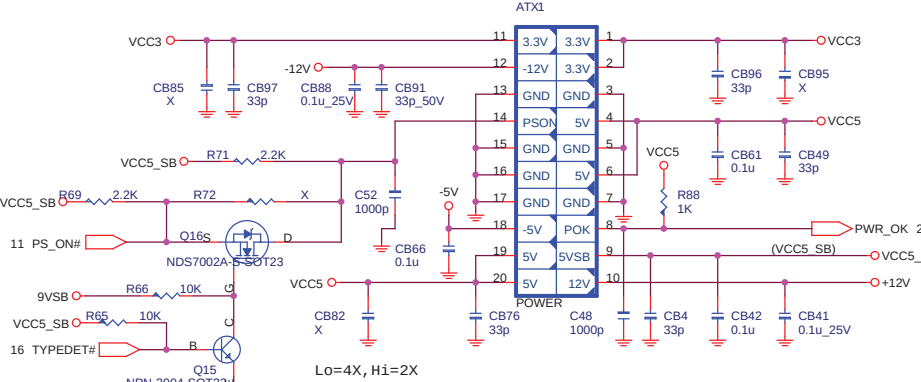
REAR PANEL USB CONNECTOR FOR USB PORT 4,5



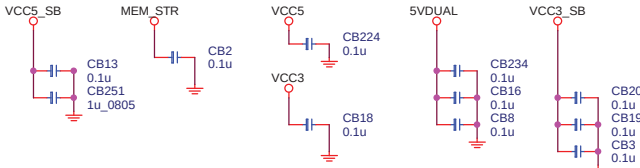
Micro Star Restricted Secret

Title	USB Connectors	Rev
Document Number	MS-6398E	300
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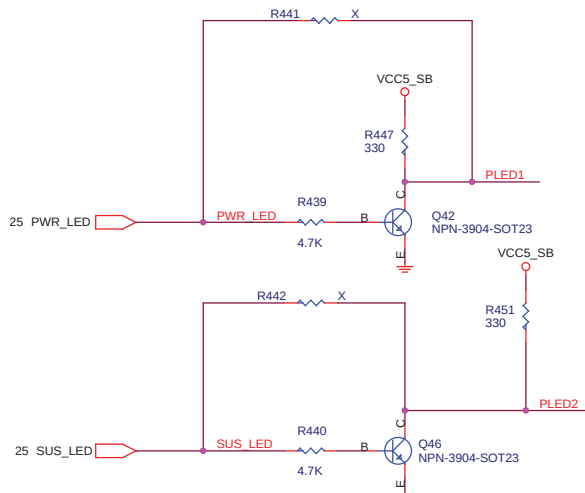
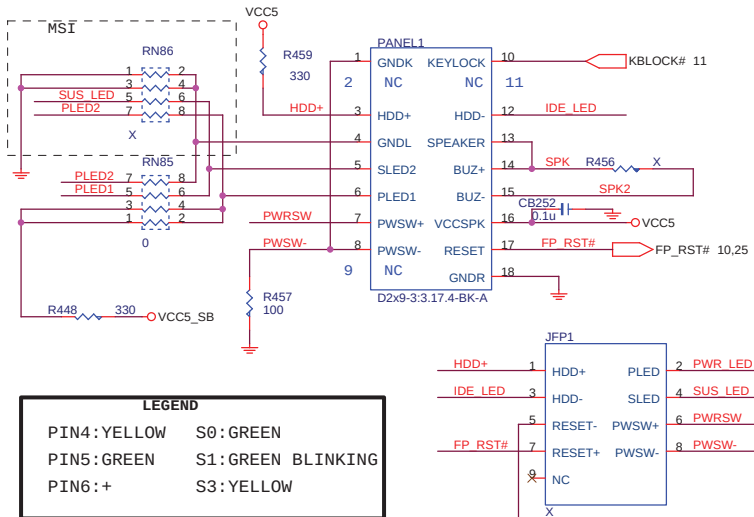
ATX CONNECTOR



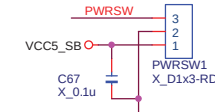
REGULATORS OUTPUT DECOUPLING CAPACITORS



MSI/Legend Front Panel



LEGEND POWER
SWITCH

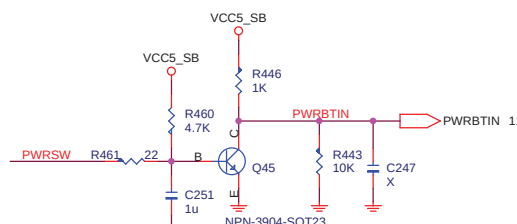


LEGEND DUAL

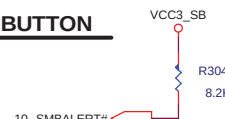
+ - - GREEN
 - - - YELLOW

S0:STEADY GREEN
S1:GREEN BLINKING
S3:STEADY YELLOW
S4/S5:OFF

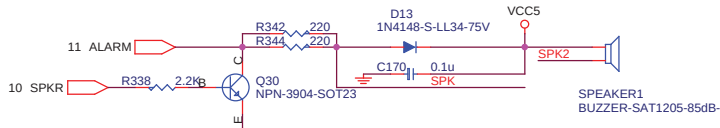
POWER BUTTON



SLEEP BUTTON



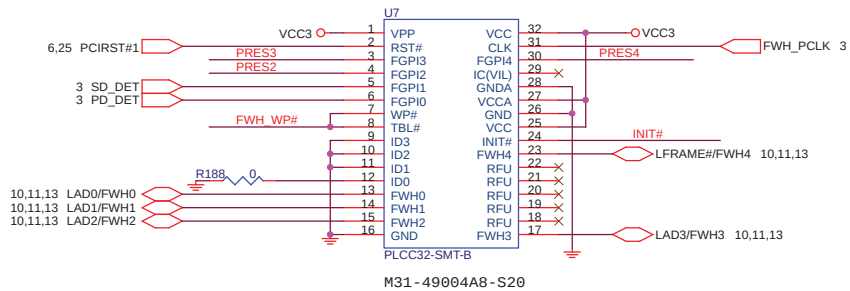
SPEAKER



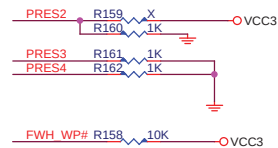
Micro Star Restricted Secret

Title	Front Panel,Power Conn. & Speaker			Rev
Document Number	MS-6398E			300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei, Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002		
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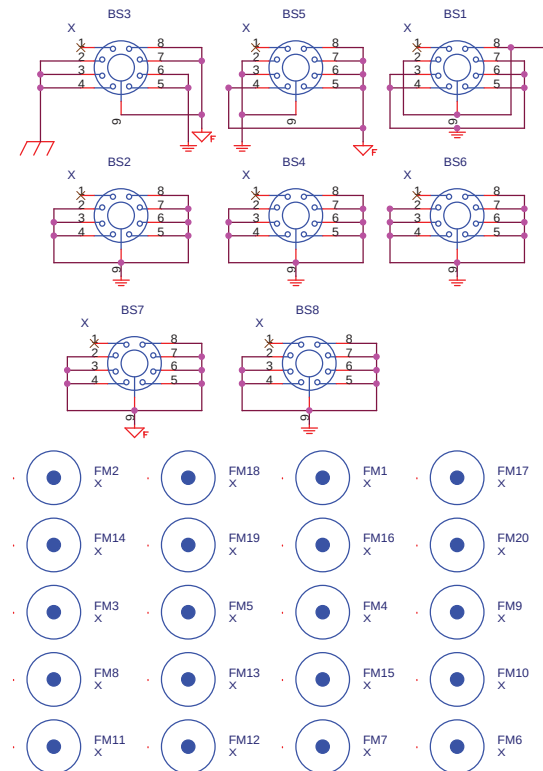
Firmware Hub (FWH)



FWH RESISTORS



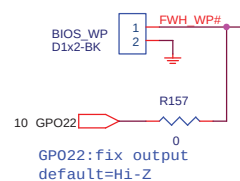
PCB OTHER COMPONENT



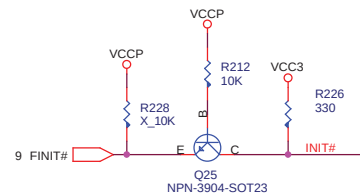
FWH DECOUPLING CAPACITORS



FWH write protect

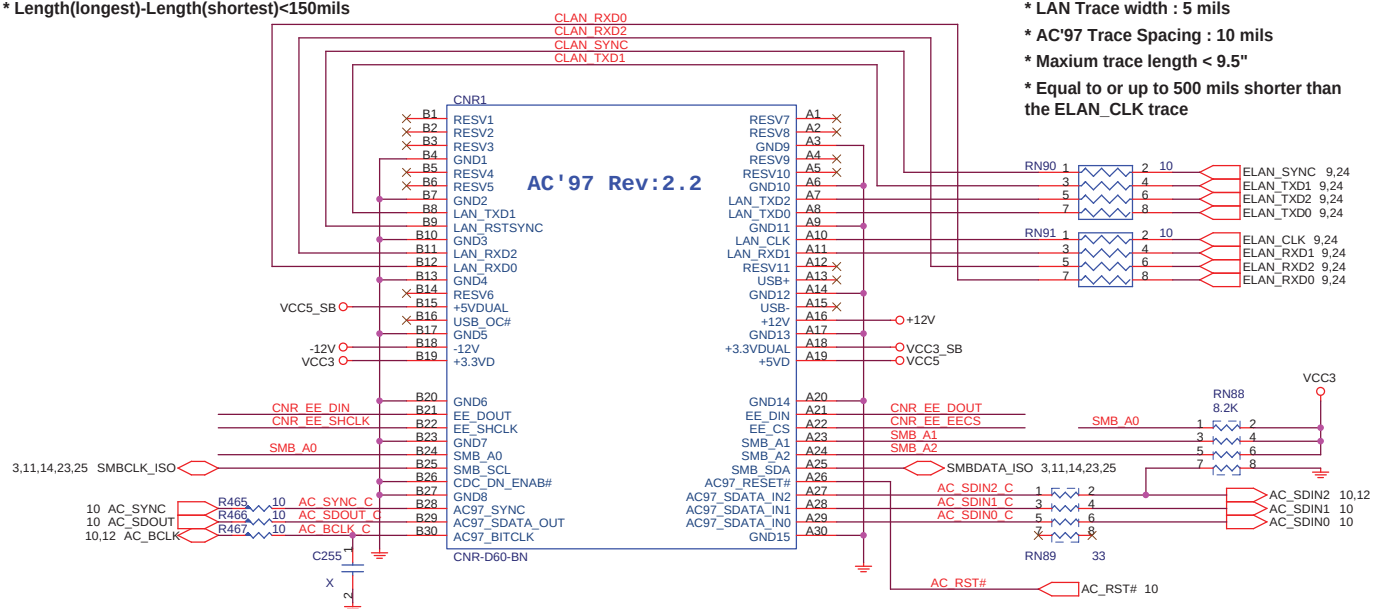


FWH INIT Signal Voltage Translation Block



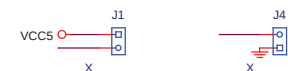
- * AC'97 Trace width : 5 mils
- * AC'97 Trace Spacing : 5 mils
- * Maxium trace length <8"
- * Length(longest)-Length(shortest)<150mils

CNR RISER



- * LAN Trace width : 5 mils
- * AC'97 Trace Spacing : 10 mils
- * Maxium trace length < 9.5"
- * Equal to or up to 500 mils shorter than the ELAN CLK trace

SIMULATION TRACE



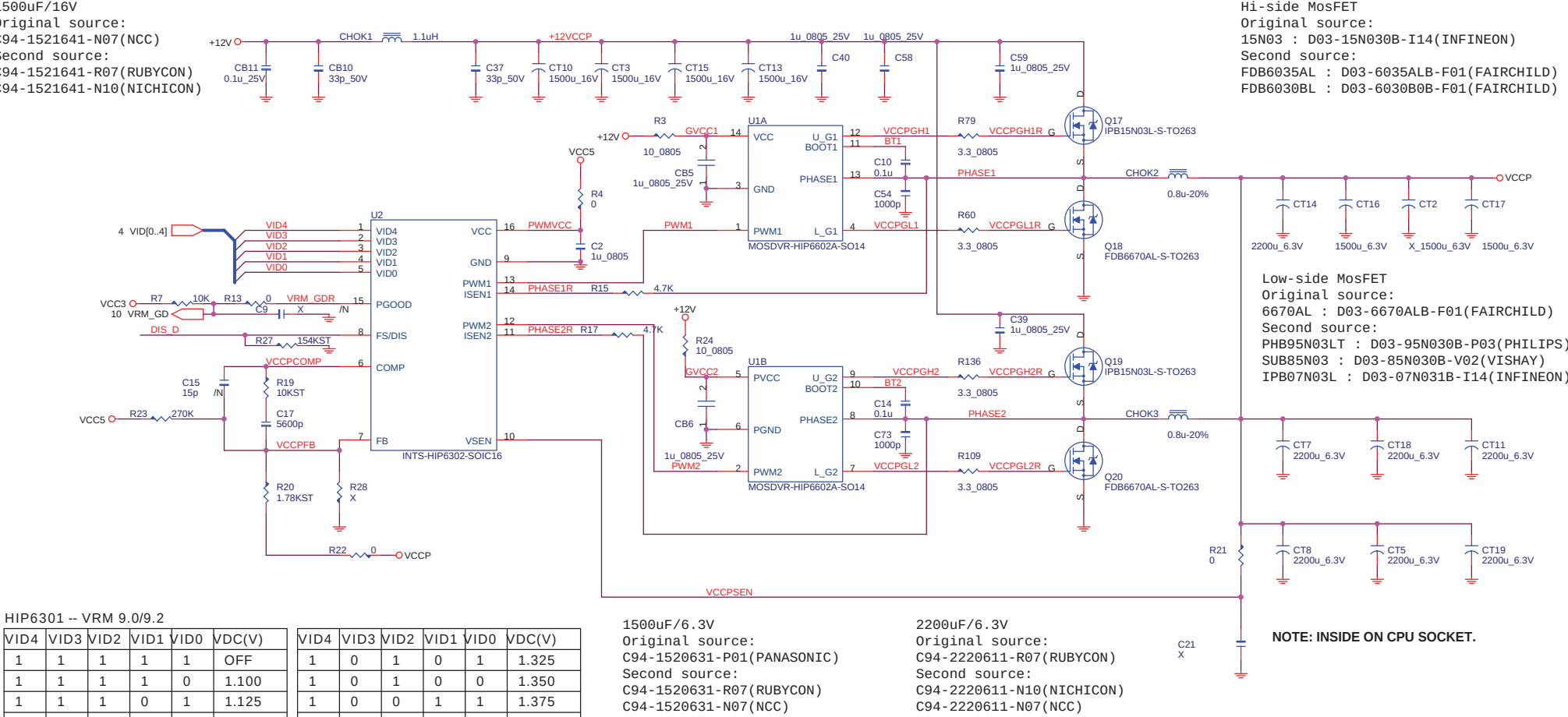
Very close to ICH4



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Title		FWH		Rev
Document Number		MS-6398E		300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002		
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1500uF/16V
Original source:
C94-1521641-N07(NCC)
Second source:
C94-1521641-R07(RUBYCON)
C94-1521641-N10(NICHICON)



HIP6301 -- VRM 9.0/9.2

VID4	VID3	VID2	VID1	VID0	VDC(V)
1	1	1	1	1	OFF
1	1	1	1	0	1.100
1	1	1	0	1	1.125
1	1	1	0	0	1.150
1	1	0	1	1	1.175
1	1	0	1	0	1.200
1	1	0	0	1	1.225
1	1	0	0	0	1.250
1	0	1	1	1	1.275
1	0	1	1	0	1.300
VID4	VID3	VID2	VID1	VID0	VDC(V)
1	0	1	0	1	1.325
1	0	1	0	0	1.350
1	0	0	1	1	1.375
1	0	0	1	0	1.400
1	0	0	0	1	1.425
1	0	0	0	0	1.450
0	1	1	1	1	1.475
0	1	1	1	0	1.500
0	1	1	0	1	1.525
0	1	1	0	0	1.550

1500uF/6.3V
Original source:
C94-1520631-P01(PANASONIC)
Second source:
C94-1520631-R07(RUBYCON)
C94-1520631-N07(NCC)

2200uF/6.3V
Original source:
C94-2220611-R07(RUBYCON)
Second source:
C94-2220611-N10(NICHICON)
C94-2220611-N07(NCC)

2-Phase VRM Output-Chock :
L04-08A7031-W15 &
L04-08A7031-T15

Hi-side MosFET
Original source:
15N03 : D03-15N030B-I14(INFINEON)
Second source:
FDB6035AL : D03-6035ALB-F01(FAIRCHILD)
FDB6030BL : D03-6030B0B-F01(FAIRCHILD)

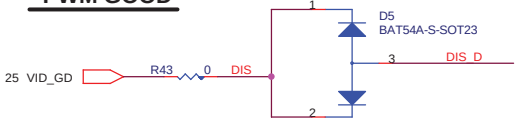
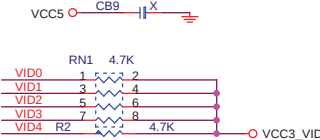
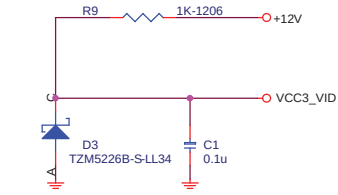
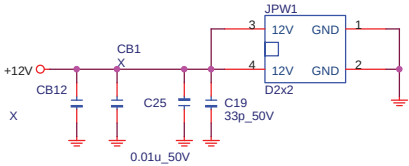
Low-side MosFET
Original source:
6670AL : D03-6670ALB-F01(FAIRCHILD)
Second source:
PHB95N03LT : D03-95N030B-P03(PHILIPS)
SUB85N03 : D03-85N030B-V02(VISHAY)
IPB07N03L : D03-07N031B-I14(INFINEON)

NOTE: INSIDE ON CPU SOCKET.

ATX12V POWER CONNECTOR

VID PULL-UP RESISTORS

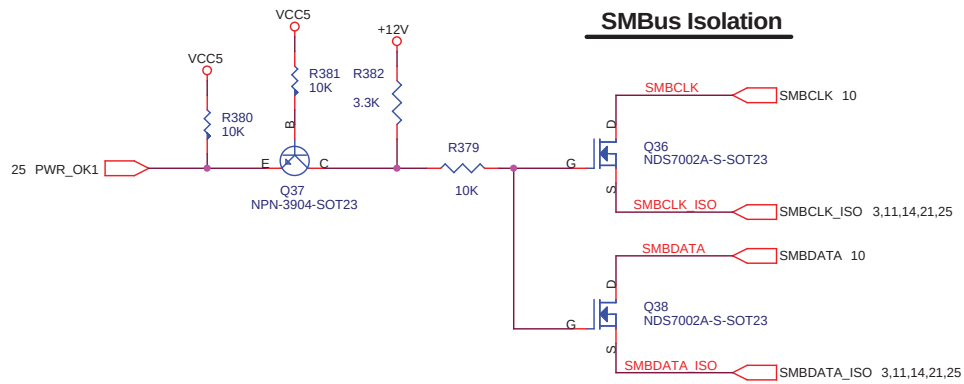
PWM GOOD



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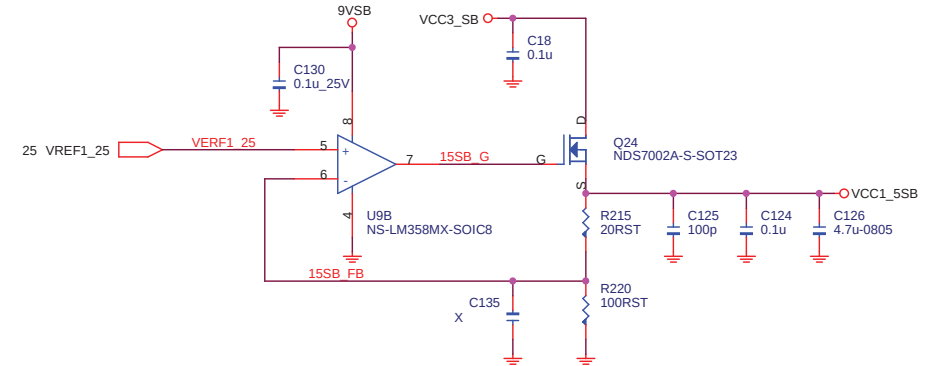
Title	VRM 9.0	Rev
Document Number	MS-6398E	300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw	Last Revision Date: Wednesday, July 03, 2002	
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SMBus Isolation

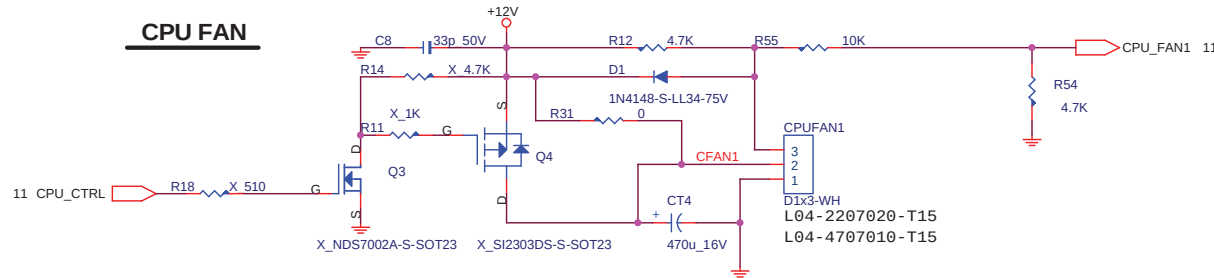


1.5V STANDBY POWER TRANSLATOR

(40mils trace / 20 mils space)

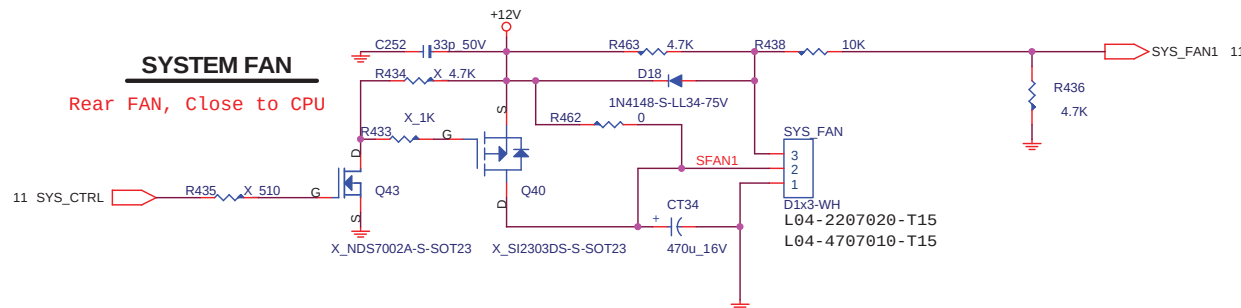


CPU FAN

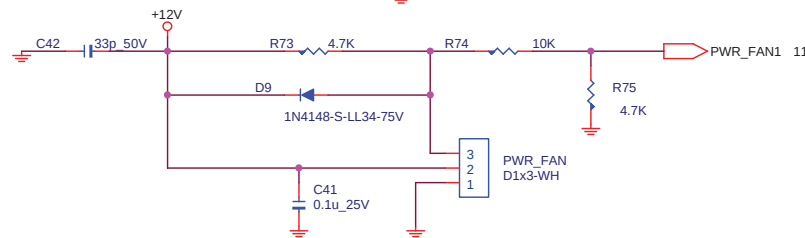


SYSTEM FAN

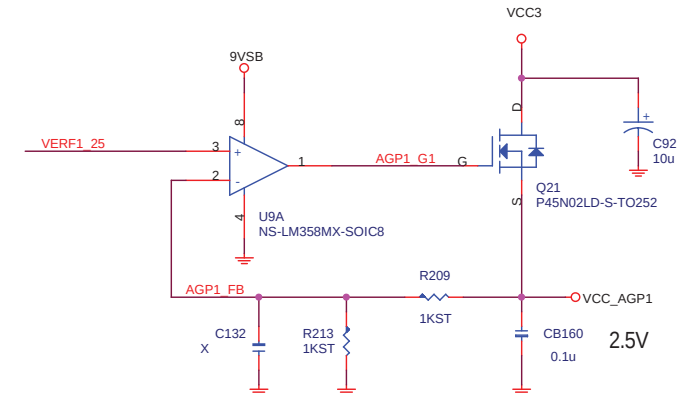
Rear FAN, Close to CPU



POWER FAN



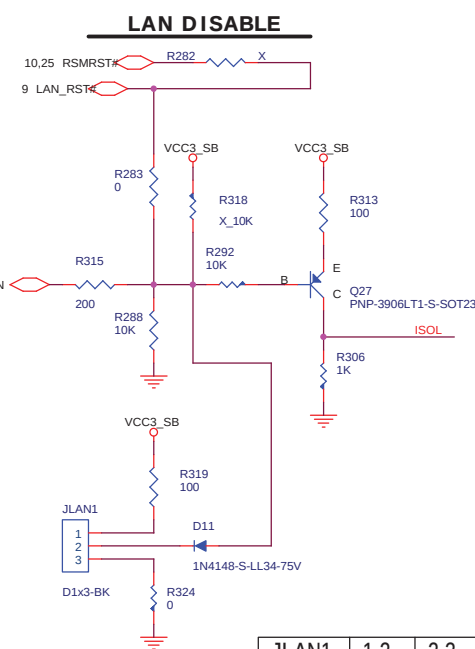
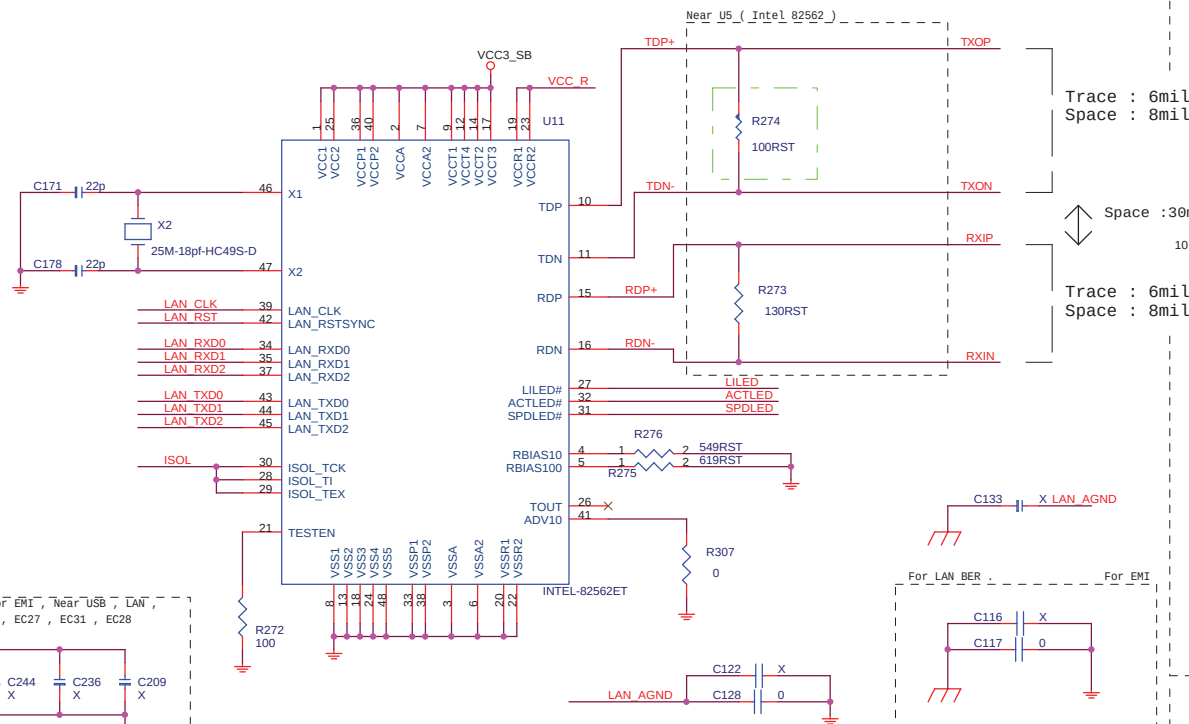
AGP4X & GMCH 1.5V POWER TRANSLATOR



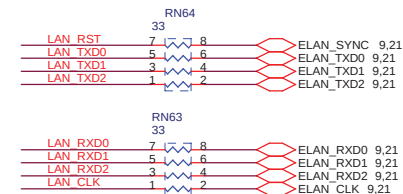
Micro Star Restricted Secret

Title	SMBUS Isolation & FAN Conn.		Rev
Document Number	MS-6398E		300
MICRO-STAR INT'L CO.,LTD.		Last Revision Date:	
No. 69, Li-De St, Jung-He City,		Wednesday, July 03, 2002	
Taipei Hsien, Taiwan		Sheet	of
http://www.msi.com.tw		23	30

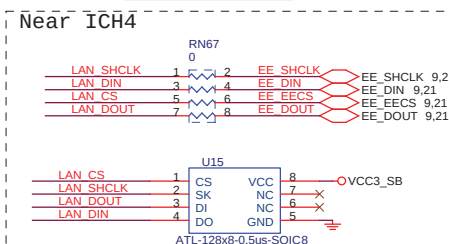
Place Termination R as close to 82562EM as possible



LAN OPTION RESISTORS



LAN EEPROM



Remark:
82562EM with 93C66:M33-26N0103-A26
82562ET with 93C46:M33-1500203-A26 *

Micro-Star	Title MS-6398E	Rev 300
Document Number	LAN INTEL 82562EM/ET	
Last Revision Date: Wednesday, July 03, 2002	Sheet	24 of 30

Power	S0	S3	S5
VCC3_SB	Main	Standby	Standby
VCC5_STR	Main	Standby	Standby
MEM_STR	Main	Standby	Standby

TO 845G & ICH4
TO SMBUS Ctrl

**INPUT 2 AND 3 MUST BE HI LEVEL WHEN USE
OUTPUT 1 AND 2 FOR GPIO FUNCTION

Near
302D

DDR VTT Power

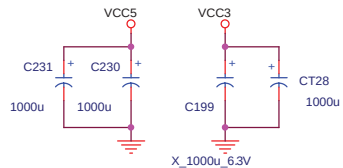
1.25V/2.1A

VTT_DDR

SEL1	VRAM	VRAM_2.5
H	3.3VDUAL	2.5V
TRI-STATE	3.3VSB	2.5V
L	3.3VSTR	1.25V

FOR 3VSB OR 3VSTR
SETTING BY SEL1

Pin 15,19,22,32 Must
reserve capacitor.



** SETTING 3VSTR THEN VRAM_2.5
BECOME TO 1.25 VREF

Place Bulk cap between
GMCH & DIMM slot

SEL0	5VUSB
H	2 MOSFET
L	1 MOSFET

**S50# pin function(Hi level = 5V)
same as 5VUSB(Hi level = 12V)
5VUSB USE 2 MOSFET

5V DUAL Power

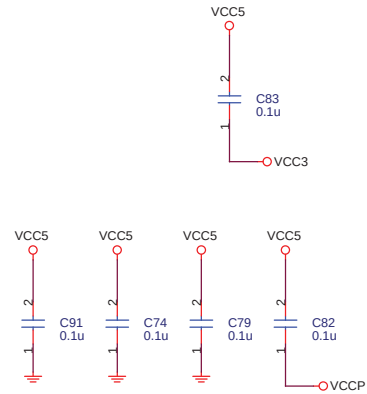
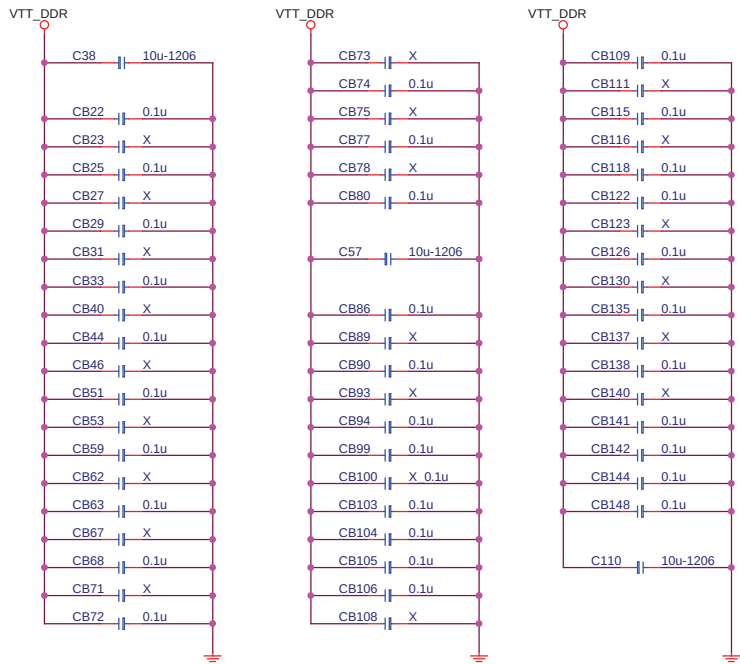
VCC_VID / VID_GOOD

Place MOSFET near CPU

1.8V POWER TRANSLATOR

Title		Rev
W83302D(MS-5) ACPI Controller		300
Document Number		MS-6398E
MICRO-STAR INT'L CO.,LTD.		Last Revision Date: Wednesday, July 03, 2002
No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Sheet 25 of 30

DDR Decoupling Caps



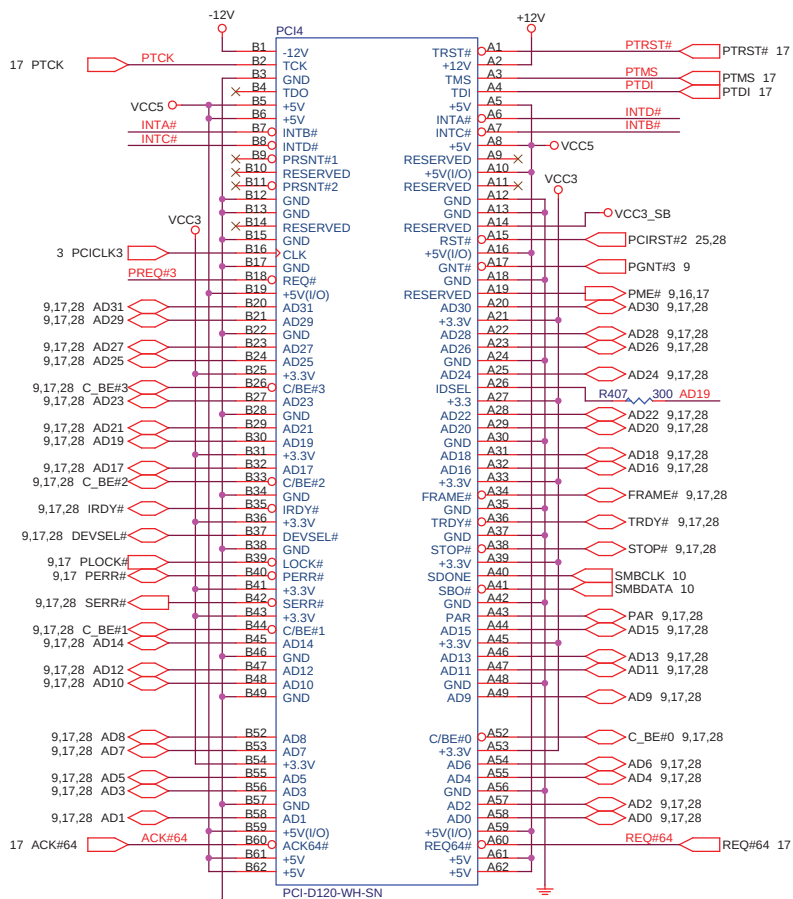
Total 110 signal, need 55 pcs decoupling.

Place for VTT_DDR island

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Title	DDR Bypass CAP	Rev
Document Number	MS-6398E	300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002
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PCI SLOT 1 (PCI VER: 2.2 COMPLY)

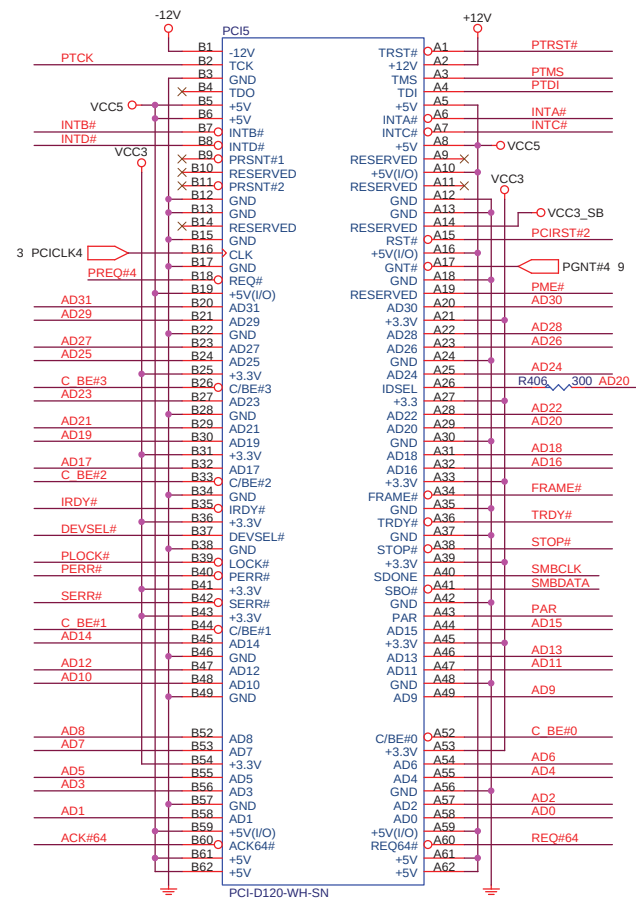


```

IDSEL = AD19
MASTER = PREQ3
INTD#

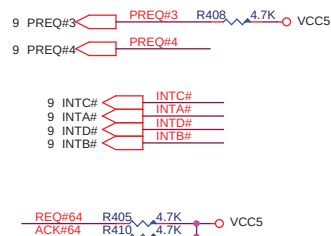
```

PCI SLOT 2 (PCI VER: 2.2 COMPLY)

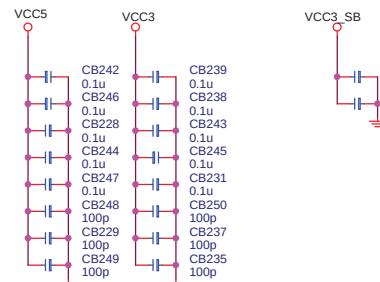


```
IDSEL = AD20
MASTER = PREQ4
INTA#
```

PCI PULL-UP / DOWN RESISTORS



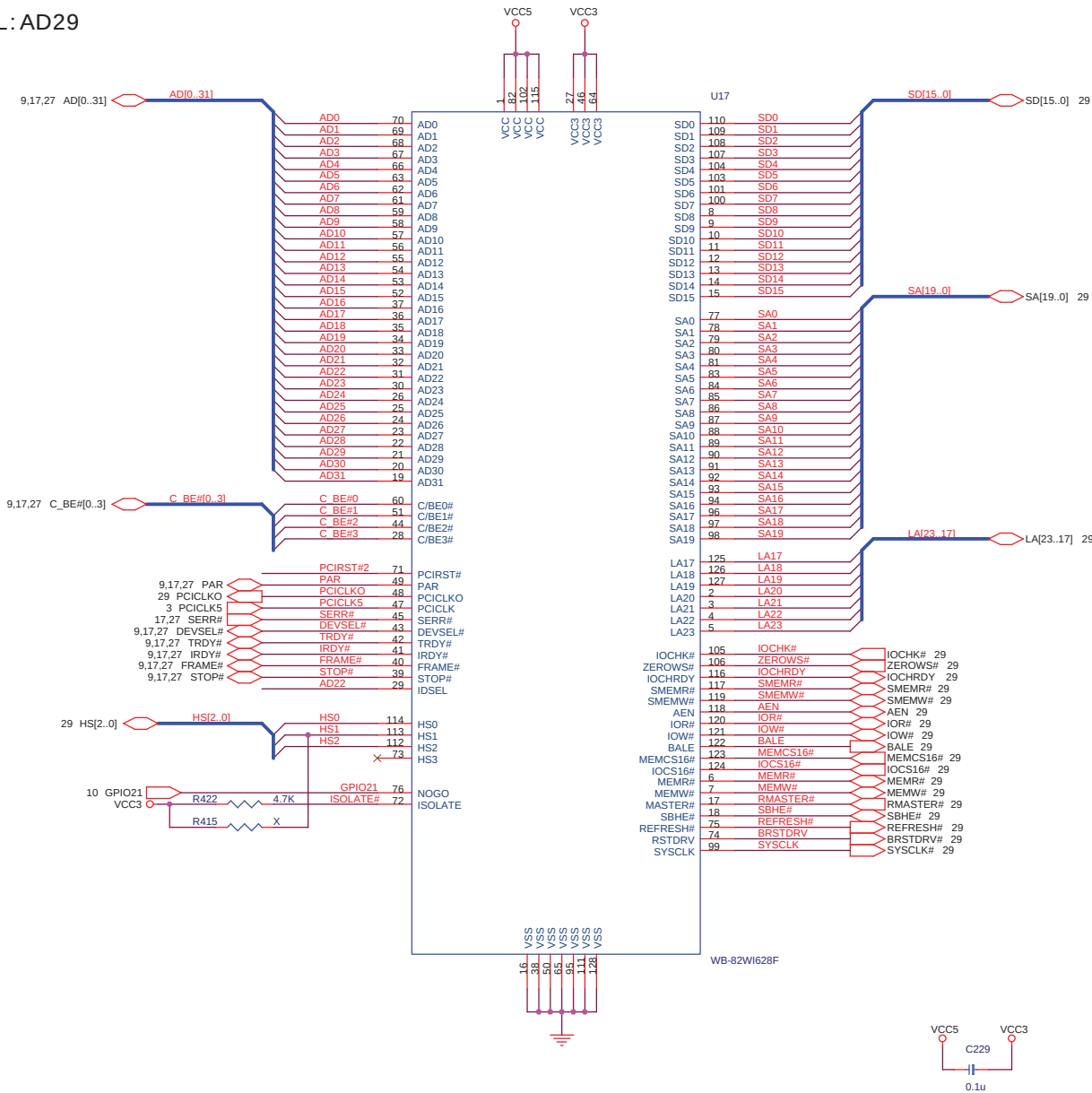
PCI SLOT DECOUPLING CAPACITORS



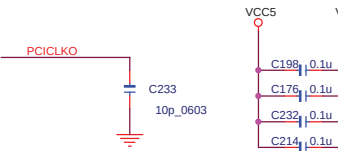
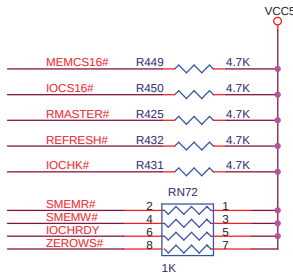
Micro Star Restricted Secret			
Title		Rev	
PCI SLOT 1 & 2 & 3			
Document Number		300	
MS-6398E			
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date:	
		Wednesday, July 03, 2002	
Sheet			
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PCI To ISA Bridge, Part 1

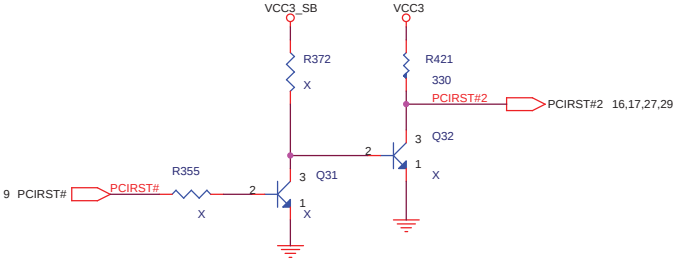
IDSEL: AD29



PCI to ISA Bridge Pull Up / Low



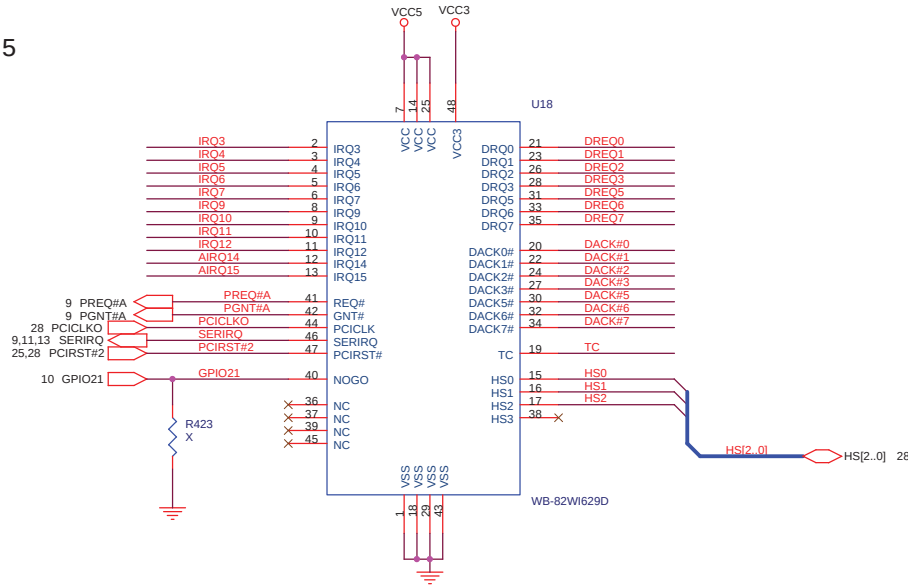
PCI to ISA Bridge Reset



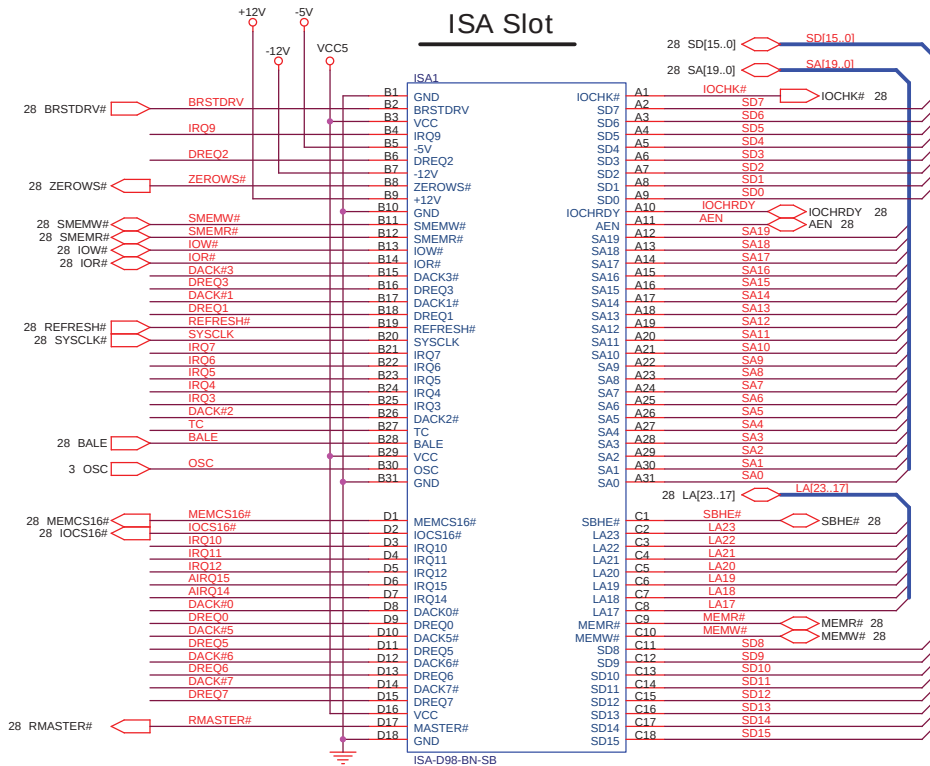
Title			PCI_ISA BRIDGE	
Size	Document Number			Rev
Custom	MS-6398E			300
Date:	Wednesday, July 03, 2002	Sheet	28 of 30	

PCI To ISA Bridge, Part 2

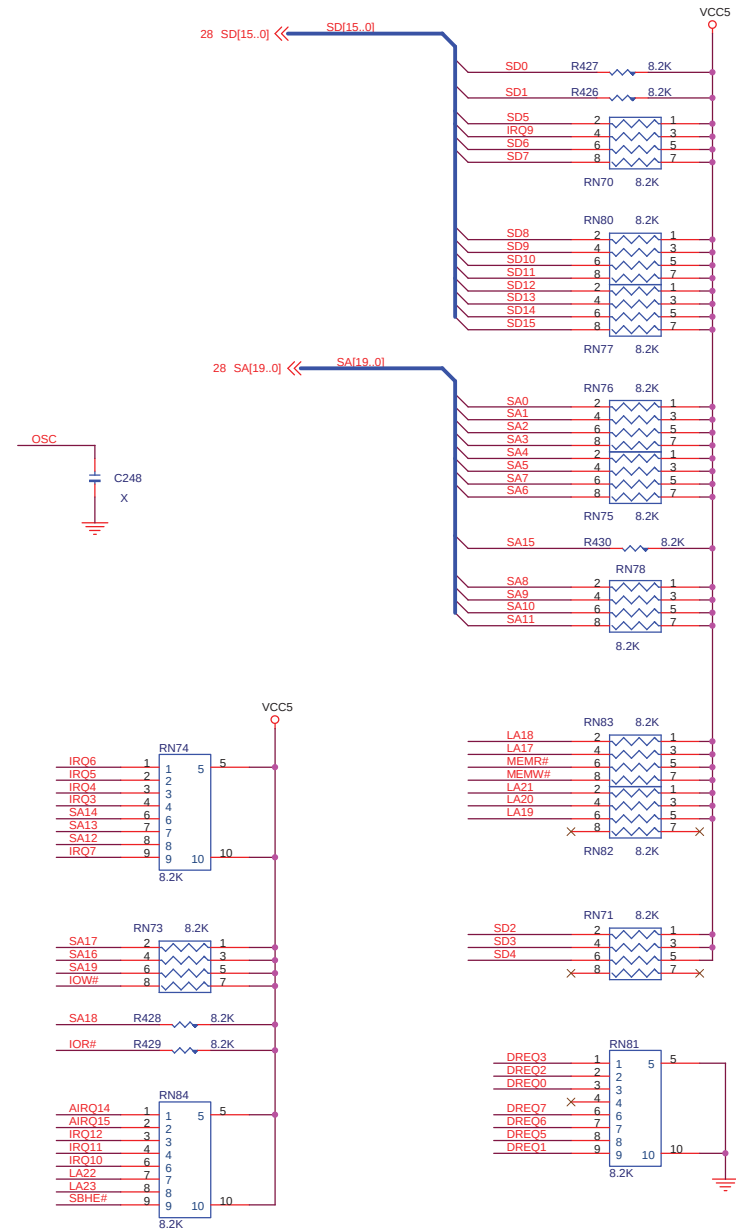
PREQ#5



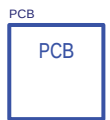
ISA Slot



PCI to ISA Bridge Pull Up / Low



Title			PCI ISA BRIDGE (2)	
Size	Document Number	MS-6398E		Rev
Custom				300
Date:	Wednesday, July 03, 2002	Sheet	29	of 30



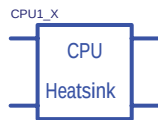
16398E300
P11-6398300-K10



SST-256K*8-33MHz-PLCC32



BAT-B-CR2032-P-3V-220mAh



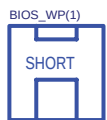
X_Retention Module Socket478

Pin Header Key Protect

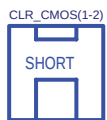
F_AUDIO Pin 8,16
USB1 Pin 4
USB2 Pin 4
USB3 Pin 9
USB4 Pin 9
IR1 Pin 2
COM2 Pin 10



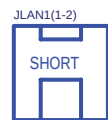
JC-D2x2-GN



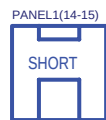
JC-D2-GN



JC-D2-GN



JC-D2-GN



JC-D2-GN



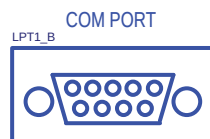
MOS_HK



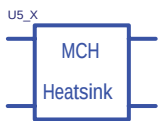
MOS_HK



COM-D9-GN



COM-D9-GN



MCH_HeatsSink



MCH_HK-1



MCH_HK-2



MCH_HK-3



MCH_HK-4

Micro Star Restricted Secret

Title	Manual Parts	Rev
Document Number	MS-6398E	300
MICRO-STAR INT'L CO.,LTD. No. 69, Li-De St, Jung-He City, Taipei Hsien, Taiwan http://www.msi.com.tw		Last Revision Date: Wednesday, July 03, 2002
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Revision	Date	Page	Drscription
3.0	04/09/2002		PCB Gerber Out on 04/03/2002 Sample BOM (ES BOM -> 6398E-05S) Release on 04/09/2002
	07/02/2002	22	For VID pull-up voltage issue , Change R9 from 330 ohm to 1K ohm , Change RN1 & R2 from 1K ohm to 4.7K ohm .
		10	For ICH4 RTC issue , Change R310 to 5.6M ohm , Add R330=20M ohm , Add R343=5.6M ohm .
			For PWR_GD signal qualify , Add pull-down R317=10K ohm .

MICRO-STAR

Title

Revision History

SizeCustom

Document NumberMS-6398ERev300

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