

6309-50B

Schematics List

Page	Description
1,2	Socket 370
3	Clock Generator
4,5,6	VIA 694X/T
7	STR Circuit & Dimm 1
8,9	Dimm 2 & Dimm 3
10	VIA 686A/B & H/W monitor
11	VIA 686A/B & USB circuit
12,13	PCI 1 & PCI 2 & PCI 3 & PCI 4
14	PCI 5 & ISA 1
15	IDE 1 & IDE 2
16	AMR 1
17	BIOS Protection Circuit
18	Serial/Parallel Port & WOL & WOR
19	Keyboard Connector
20	Ratio control circuit & LDO
21	Front Panel & ATX PowerCon & Fan
22	PCI & AGP Pull up/down Resistor
23	ISA Pull up/down Resistor
24	Bypass Cap
25	PWM & VTT power good circuit
26	AC97
27	Audio Connector
28	AGP Slot
29	Jumperless & DLED & USB PC to PC
30	H/W Audio CT5880

DIMM

DIMM	Dimm Pin	Clock	CLK gen Pin
DIMM1	42	DCLK10	20
	125	DCLK11	21
	79	DCLK8	17
	163	DCLK9	18
DIMM2	42	DCLK3	29
	125	DCLK0	28
	79	DCLK1	32
	163	DCLK2	31
DIMM3	42	DCLK4	35
	125	DCLK5	34
	79	DCLK6	38
	163	DCLK7	37

PCI Routing

Device	686B-INT	IDsel	Clock	CLK gen pin
PCI 1	INT#A INT#B INT#C INT#D	AD25	PCICLK1	10
PCI 2	INT#B INT#C INT#D INT#A	AD26	PCICLK2	11
PCI 3	INT#C INT#D INT#A INT#B	AD27	PCICLK3	12
PCI 4	INT#D INT#A INT#B INT#C	AD28	PCICLK4	13
PCI 5	INT#B INT#C INT#D INT#A	AD29	PCICLK5	9
AGP 1	INT#A INT#B		PCICLK1	
CT5880	INT#C		PCICLK5	9

686B GPIO

Signal	PinNo	Type	Function
PD_80P	W11	I	Primary IDE ATA66/100 detection
SD_80P	T14	I	Secondary IDE ATA66/100 detection
FDD_DET	G1	I	FDD detection, Low : Enabled, Hi : Disabled
CMOSP1#	W10	I	CMOS clear, Low : Hold date, Hi : Clear data
OVER_C1	H3	I	USB pont 2 & 3 over current detection
OVER_C0	G5	I	USB pont 0 & 1 over current detection
REXTSMI#	Y10	I	External SMI input
PCID0	V9	O	IOAPIC0
PCID1	T10	O	IOAPIC1
FAN_ON	Y12	O	CPUFAN AND SYSFAN ON/OFF .
GP05	V12	O	BIOS FIASH ENABLE/DISABLE
GPOD	U8	O	OVER VOLTAGE ENABLE/DISABLE
PWRLED	T8	O	POWER LED CONTROL
APIC_REQ	U10	I	APIC REQUEST

MICRO-STAR

File

MS-6309

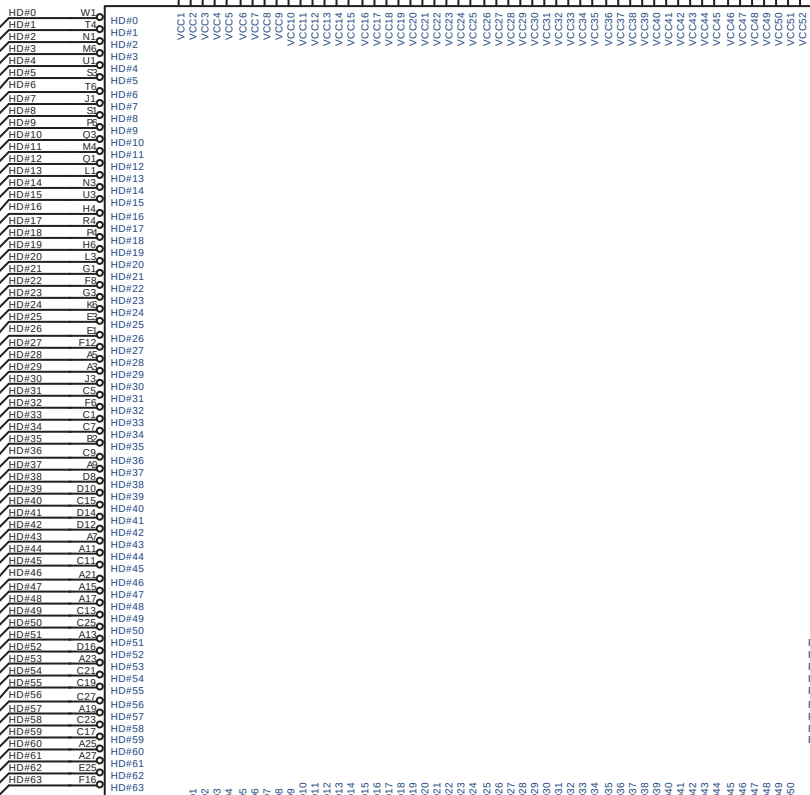
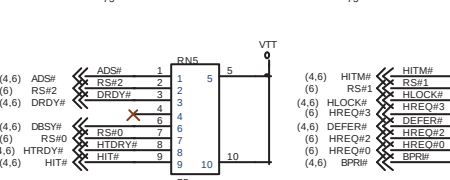
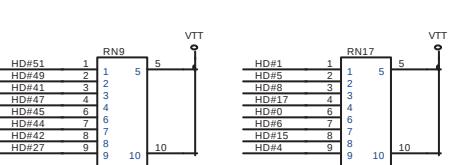
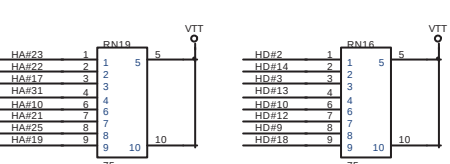
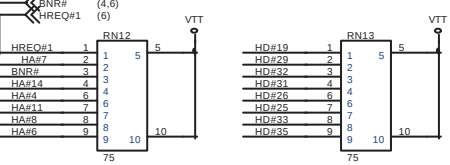
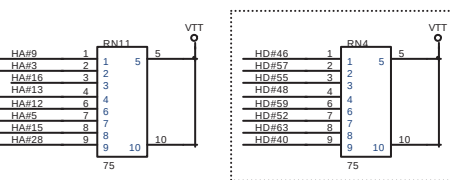
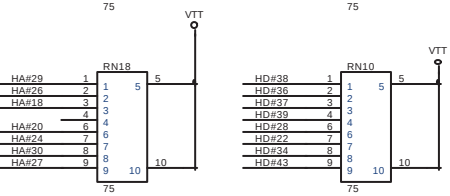
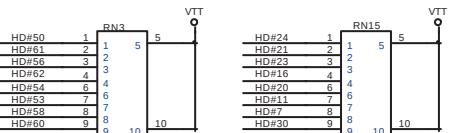
Size B

Document Number
6309-50B Cover PageRev
50B

Date: Wednesday, May 30, 2001

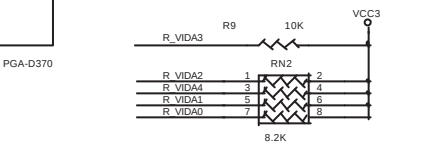
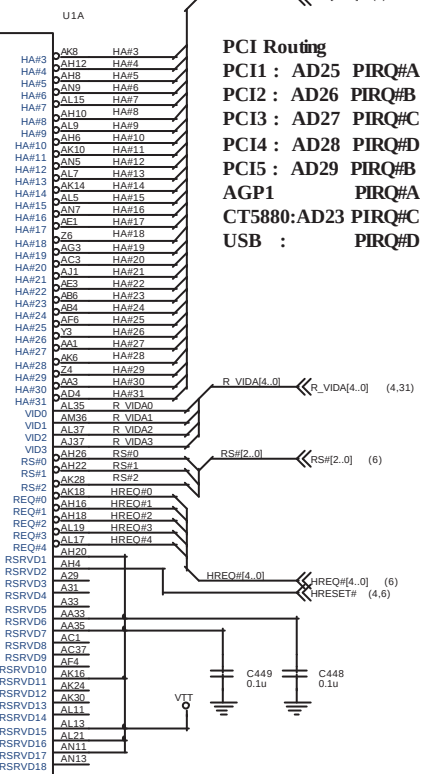
Sheet 1 of 33

GTL TERMINATION



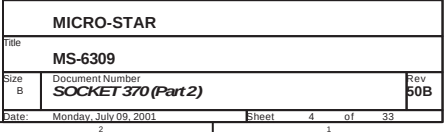
1630950B

PCI Routing
 PCI1 : AD25 PIRQ#A
 PCI2 : AD26 PIRQ#B
 PCI3 : AD27 PIRQ#C
 PCI4 : AD28 PIRQ#D
 PCI5 : AD29 PIRQ#B
 AGP1 PIRQ#A
 CT5880:AD23 PIRQ#C
 USB : PIRQ#D



MICRO-STAR	
MS-6309	
Size B	Document Number
SOCKET 370 (Part 1)	
Date: Monday, July 09, 2001	Sheet 3 of 33

Rev 50B



NAME : S-Desktop,

NOTE : AGP LAYOUT LENGTH LIMITATION.

440BX TO AGP SLOT LENGTH : Min = 1", Max = 9".

(12) SUSTAT#
Stuffing option to enable and test
the POS state.

R116 & R117 must to closed VT82C694X
GCLKI = GCLKOUT + 3000 mIs
Stub to tee should be 1" MAX.

MICRO-STAR

File

MS-6309

Size

Document Number
VT82C694X PCI AND AGP INTERFACE

Rev

50B

Date

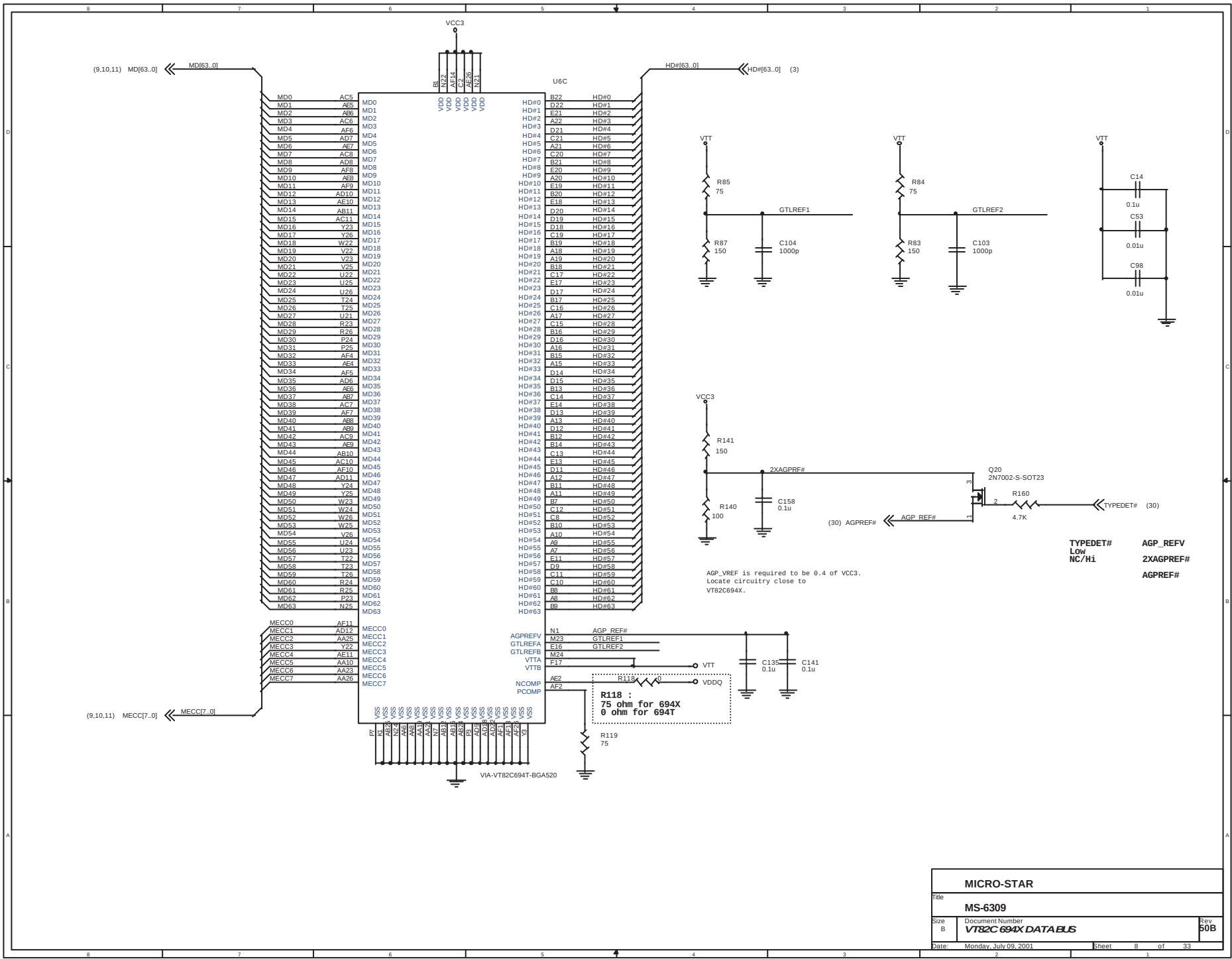
Monday, July 09, 2001

Sheet

7

of

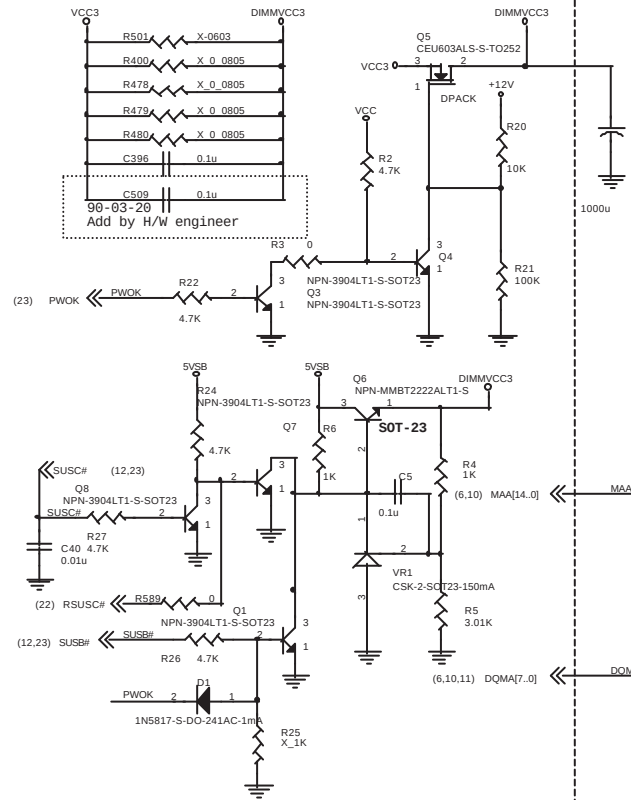
33



IF ENABLE SUSPEND TO DRAM JDIM1 AND JDIM2 SHORT

(8,10,11) MD[63..0]

OPTION:SUSPEND TO DRAM



90-03-20
Add by H/W engineer

YREG431S (SOT-23)

Anode

Reference Cathode

NOTE ON ALL DIMM SOCKETS
Pin 147 should be pulled to a high state to accommodate registered DIMMs.

R71, R90 for Non-STR function

MECC7[0] (8,10,11)

SMBDATA (5,10,11,12,31)

SMBCLK (5,10,11,12,31)

CS_A#0 (6)

CS_A#1 (6)

CS_B#0 (6)

CS_B#1 (6)

WE_A# (6,10)

SCAS_A# (6,10)

SRAS_A# (6,10)

DCLK10

DCLK11

DCLK8

DCLK9

DCLK[11..8] (5)

Slave Address = 1010000b

DIMM-D168-BK-SN

	RSMRST#	SUST1#	SUSA#	SUSB#	SUSC#
On	1	0	1	1	1
POS	1	0	0	1	1
STR	1	0	0	0	1
STD/Soft-off	1	0	0	0	0
Mechanical-off	0	0	0	0	0

MICRO-STAR

File

MS-6309

Size

Document Number

DIMM SOCKET1

Rev

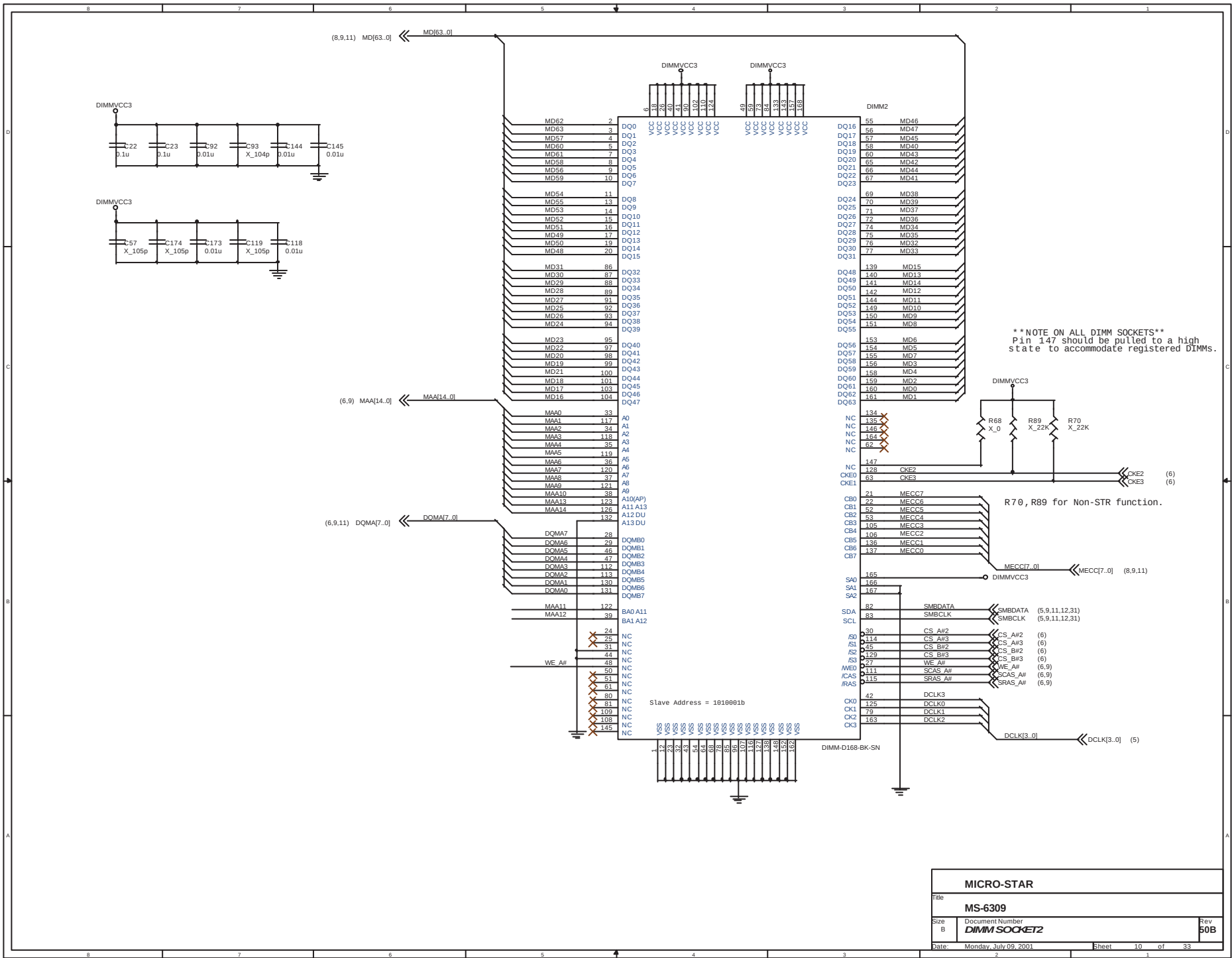
50B

Date

Monday, July 09, 2001

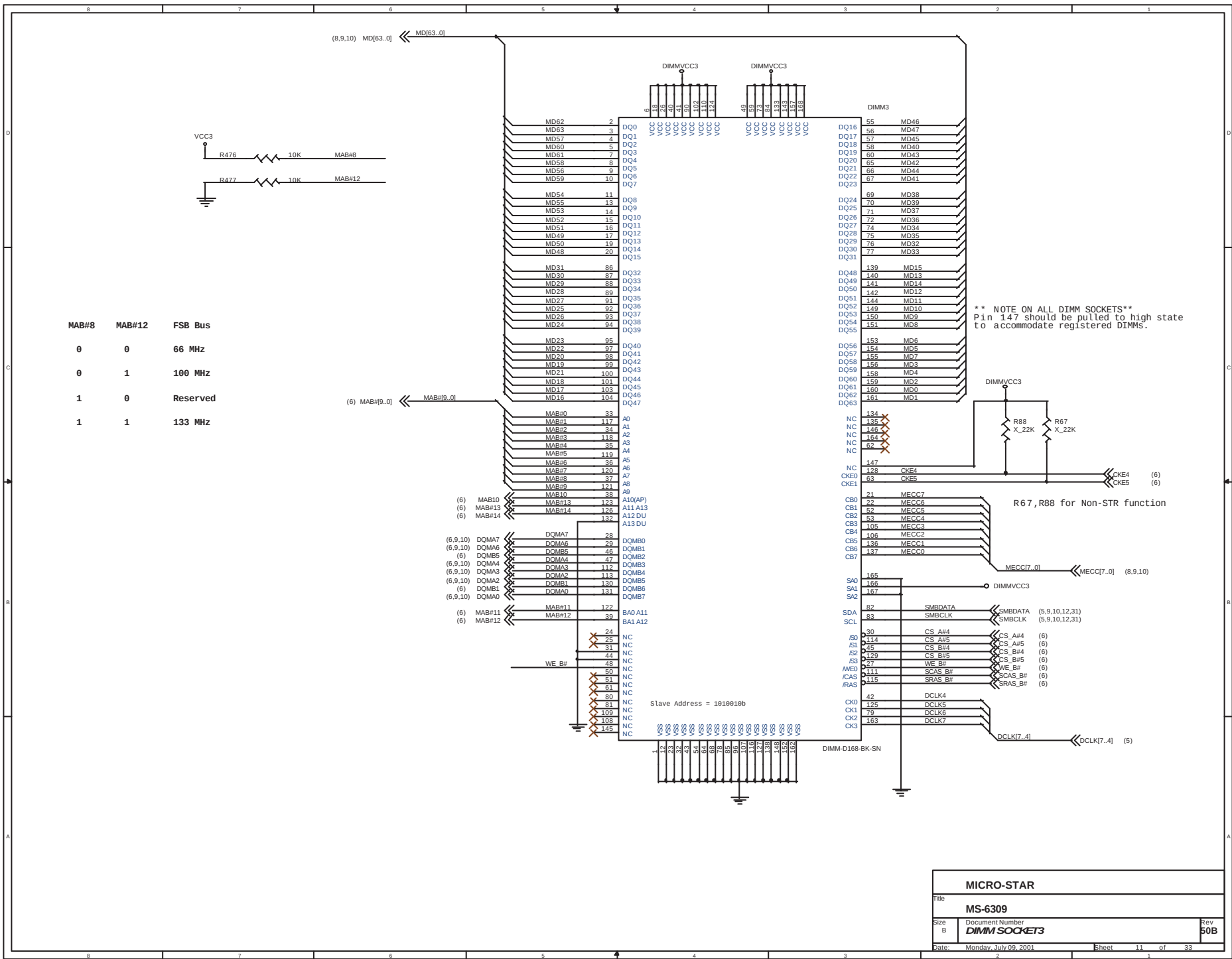
Sheet

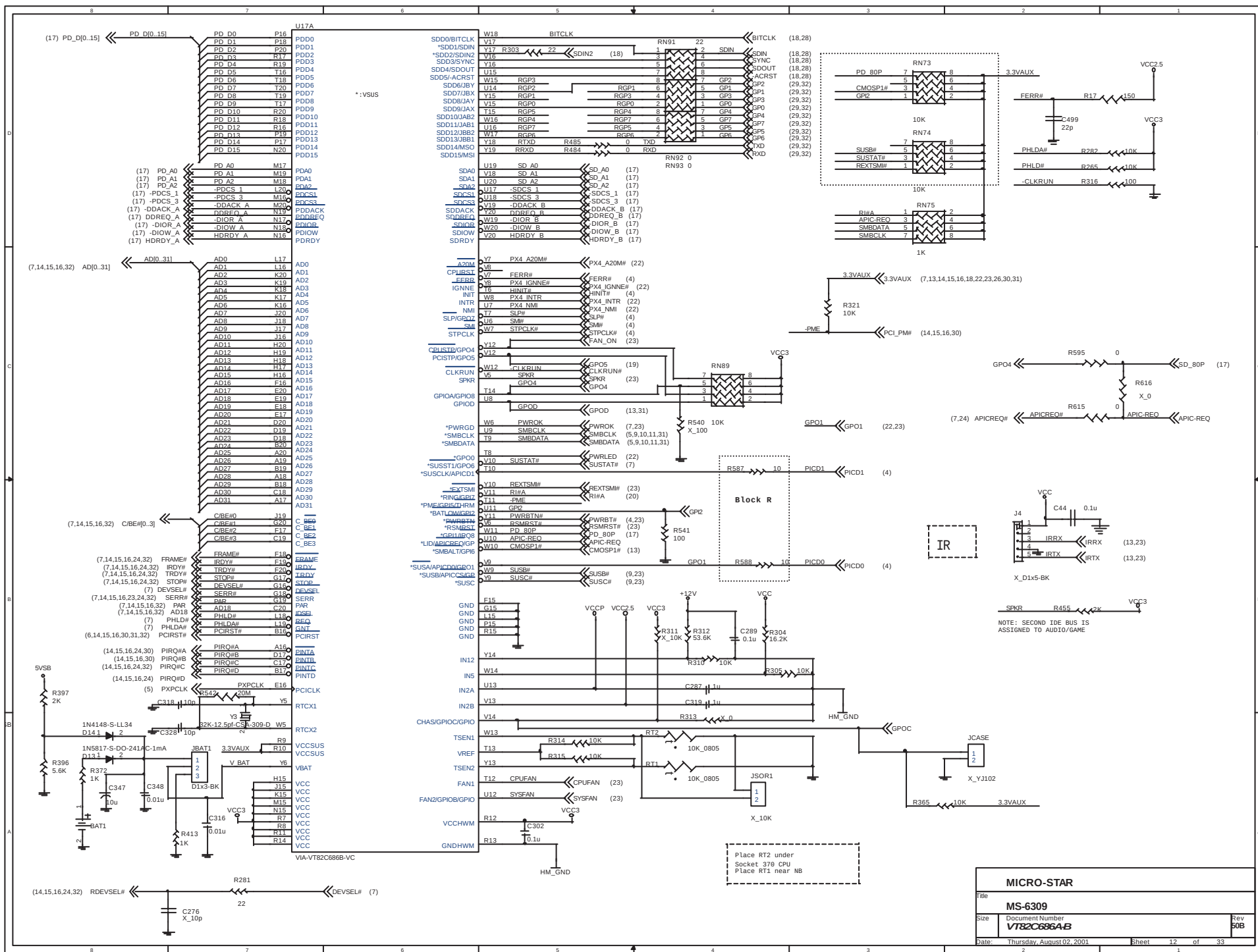
9 of 33

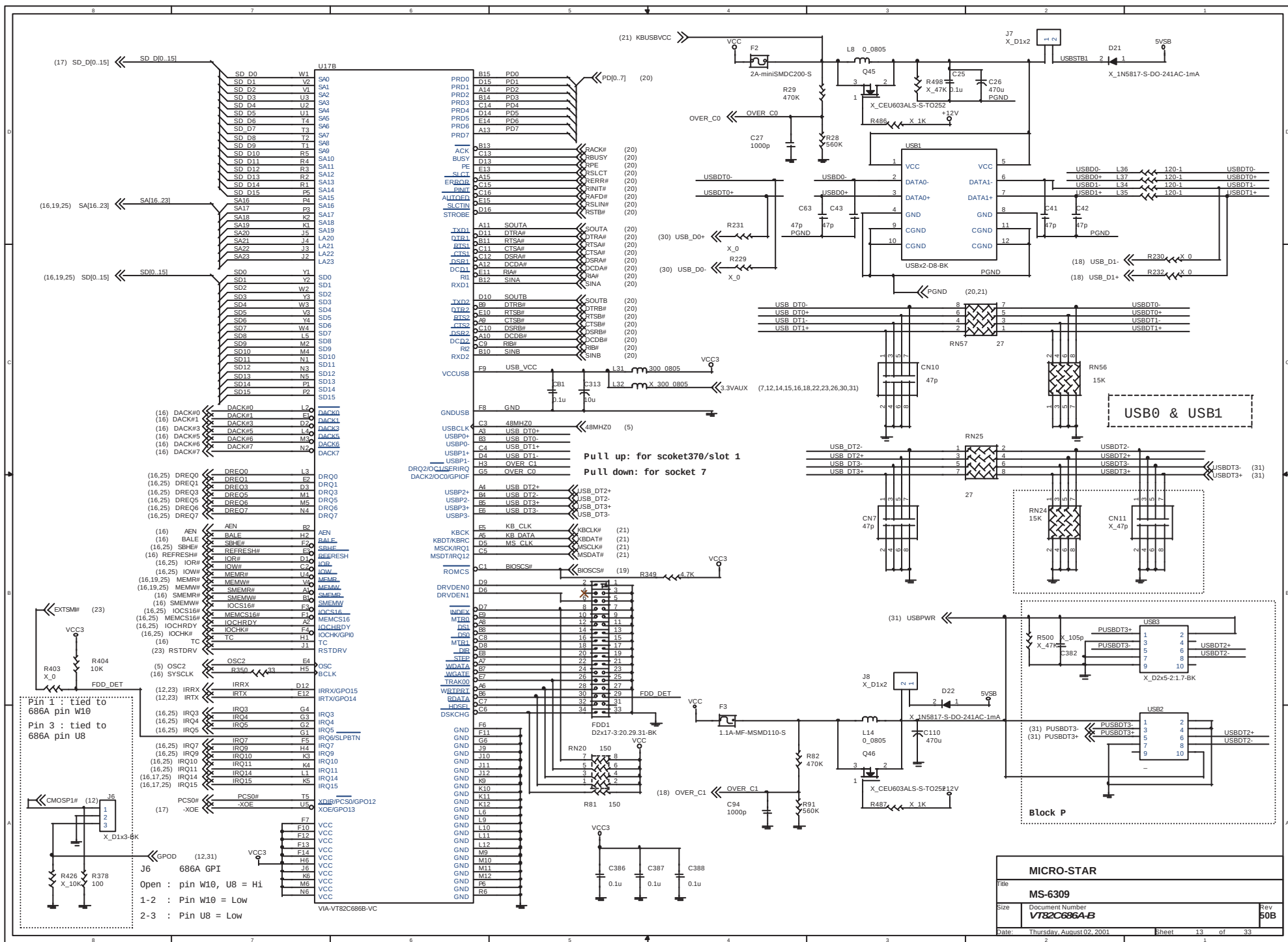


MICRO-STAR

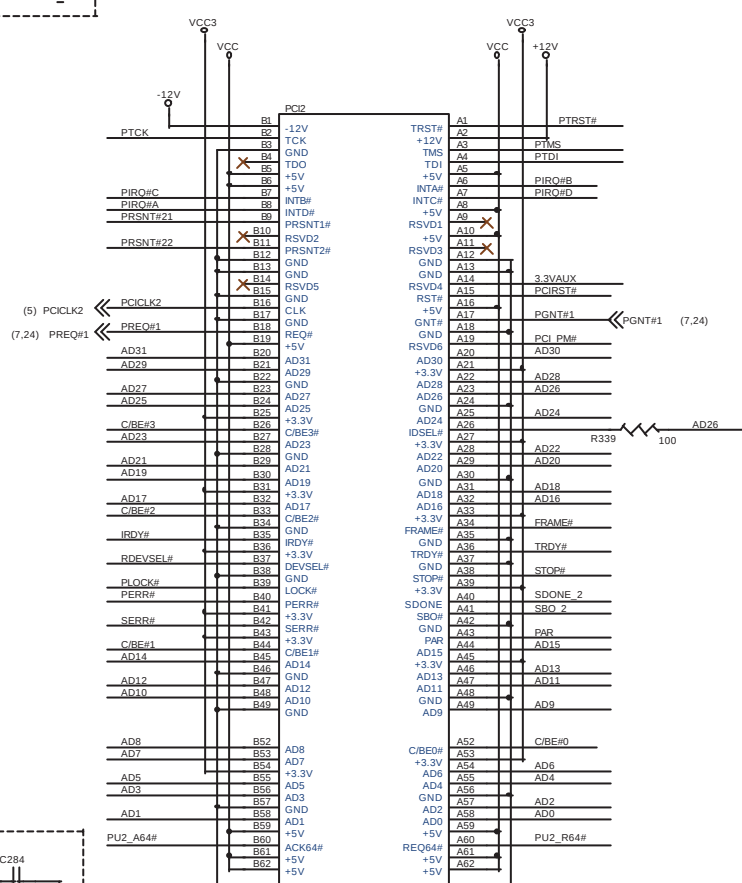
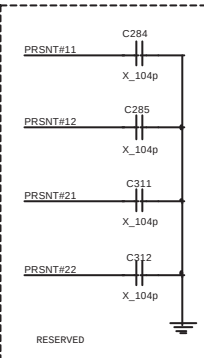
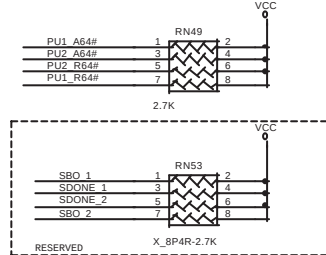
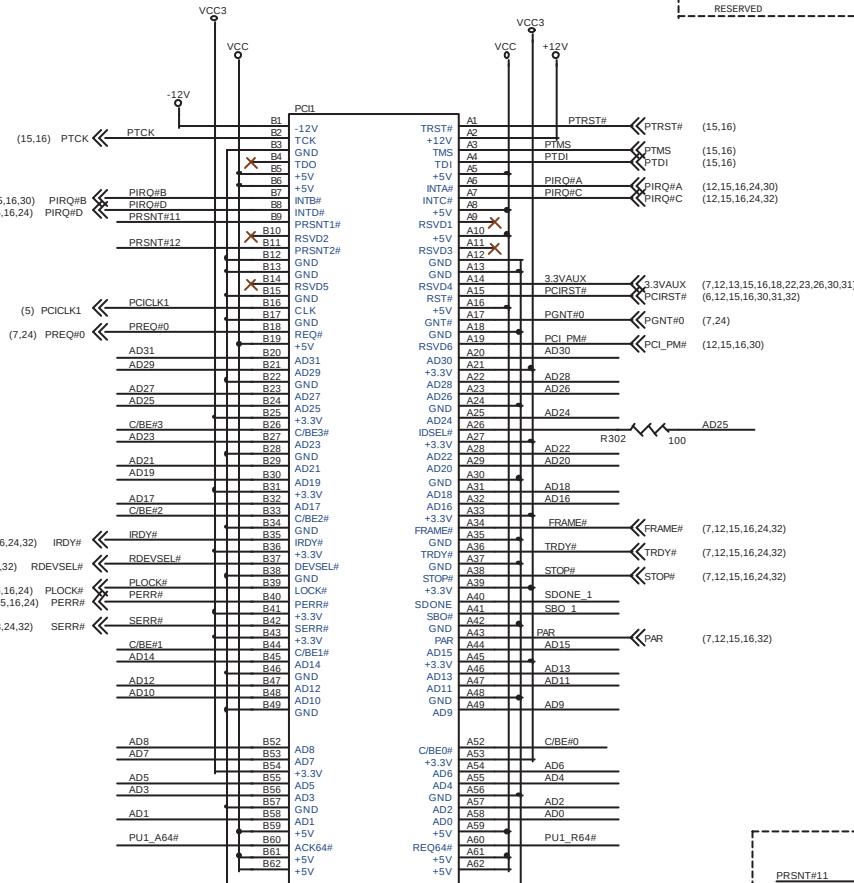
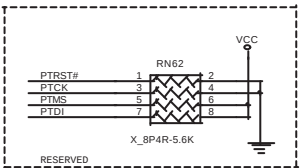
Title				MS-6309			
Size		Document Number			Rev		
B		DIMM SOCKET2			50B		
Date:		Monday, July 09, 2001		Sheet		10 of 33	







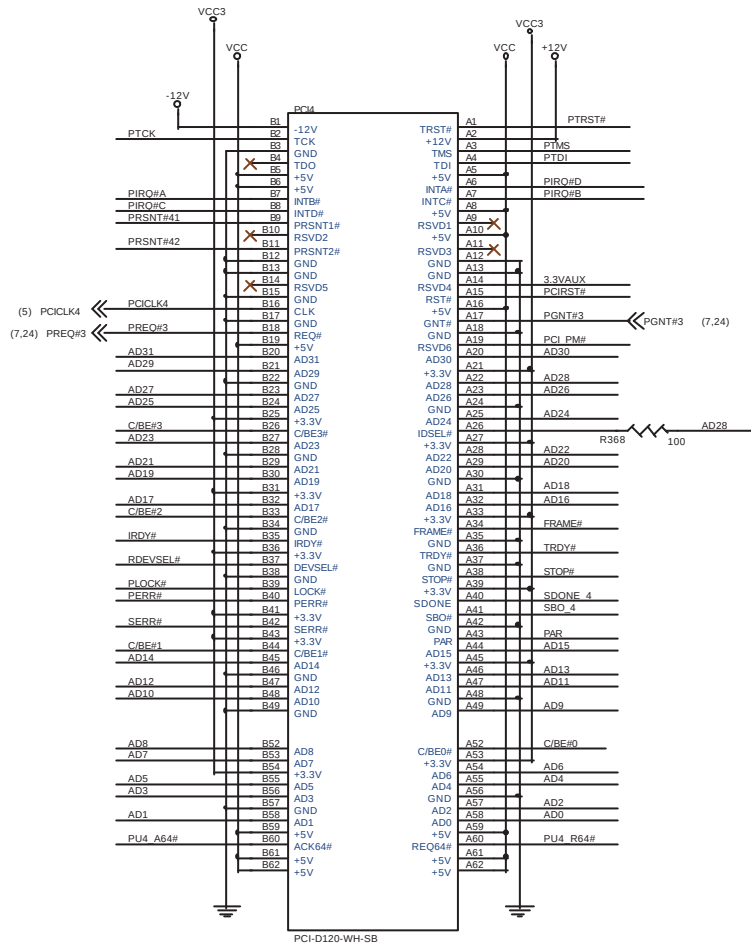
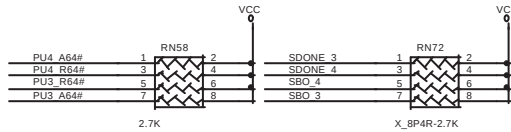
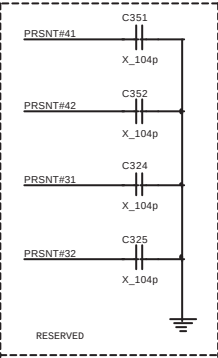
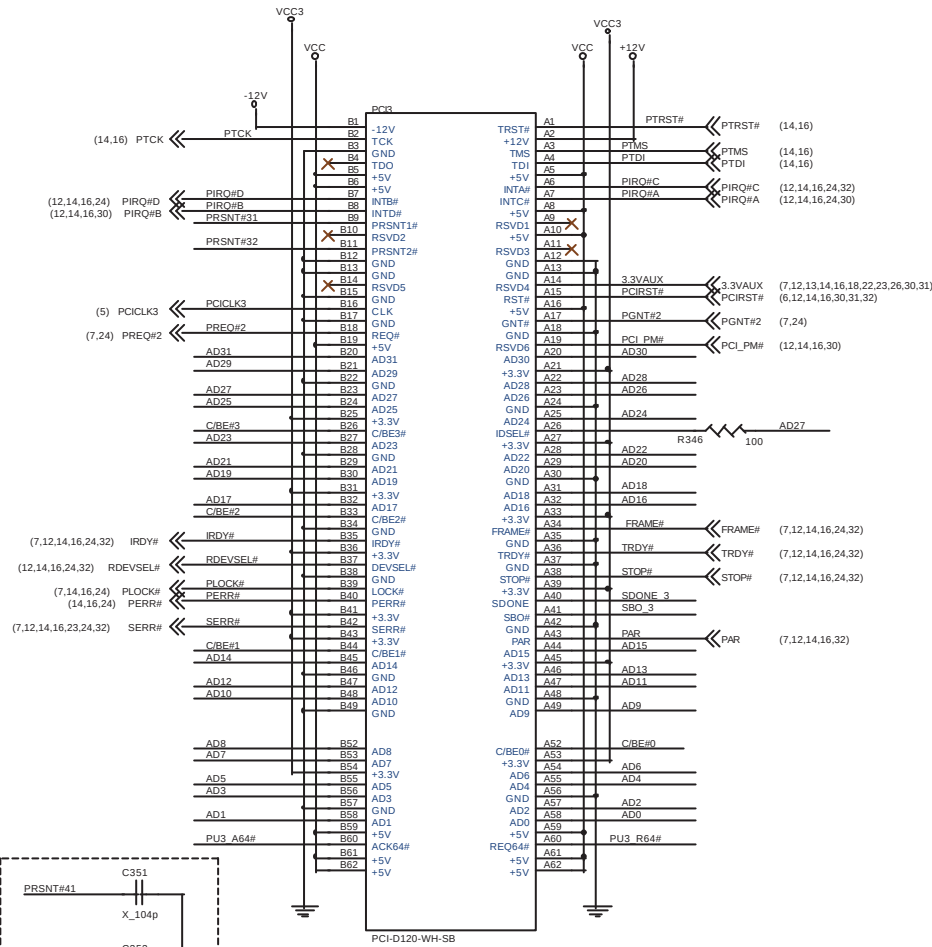
(7,12,15,16,32) AD[31..0] << AD[31..0]
(7,12,15,16,32) C/BE#[3..0] << C/BE#[3..0]



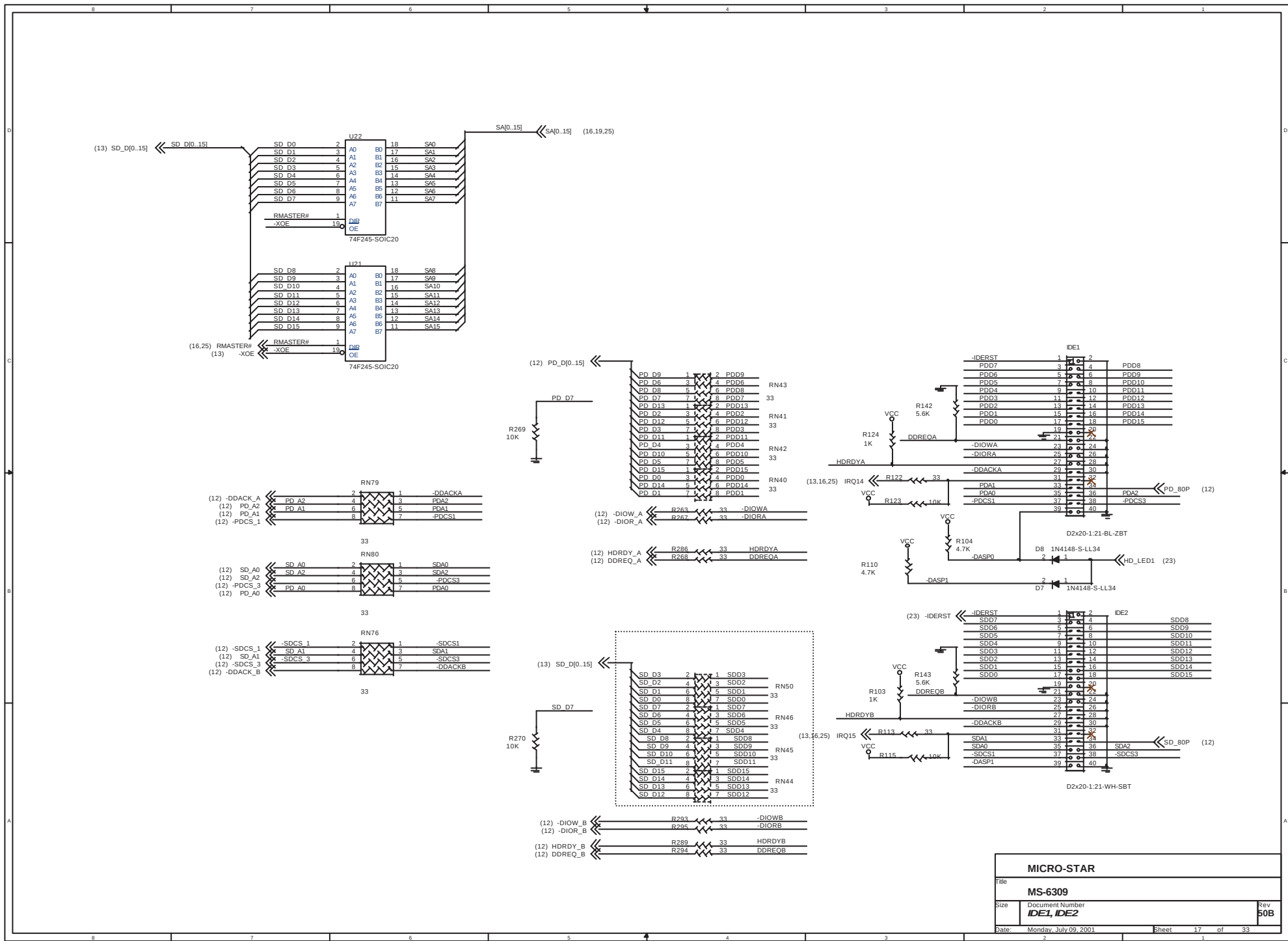
MICRO-STAR			
File			
MS-6309			
Size			
Document Number			
PCI CONNECTOR 1&2			
Date			
Monday, July 09, 2001			
Sheet			
14 of 33			
Rev			
30B			

(7,12,14,16,32) AD[31..0] << AD[31..0]

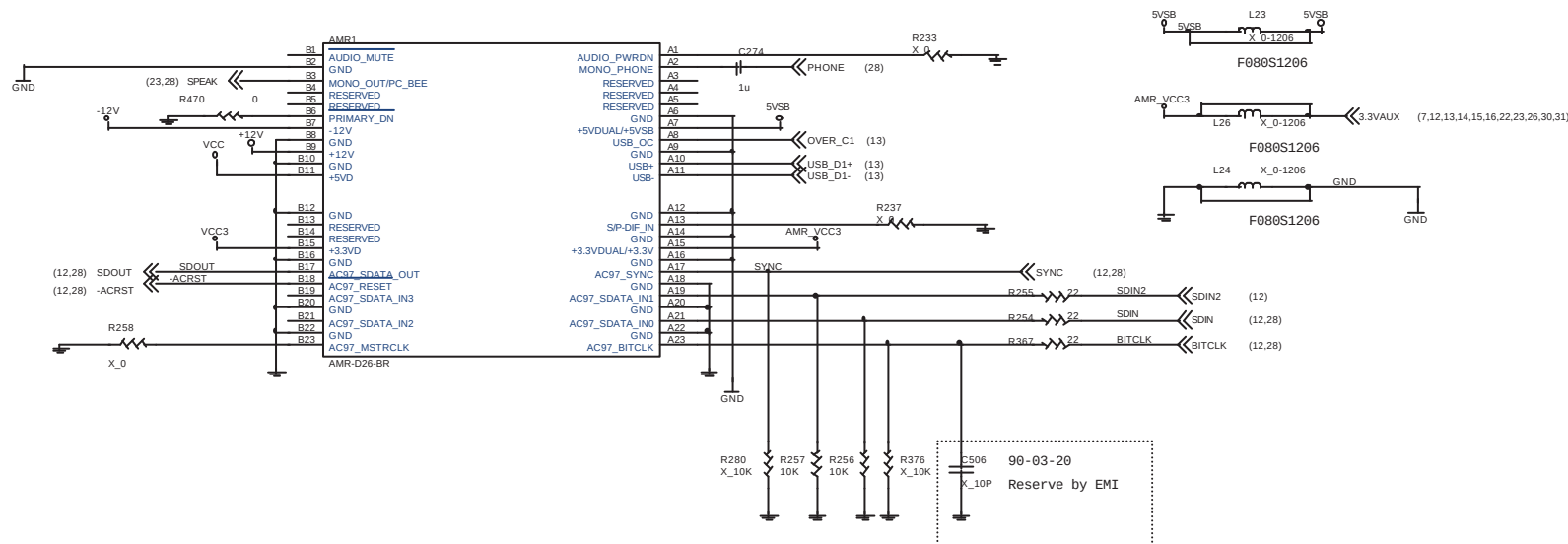
(7,12,14,16,32) C/BE#[3..0] << C/BE#[3..0]



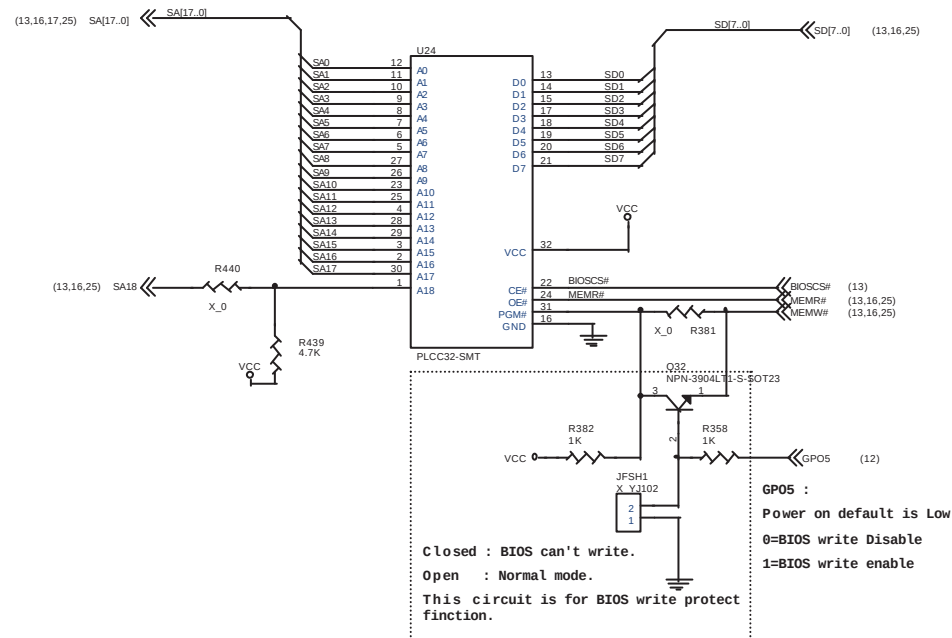
MICRO-STAR		
Title		
MS-6309		
Size B	Document Number <i>PCI CONNECTOR1&2</i>	Rev 50B
Date: Monday, July 09, 2001		Sheet 15 of 33



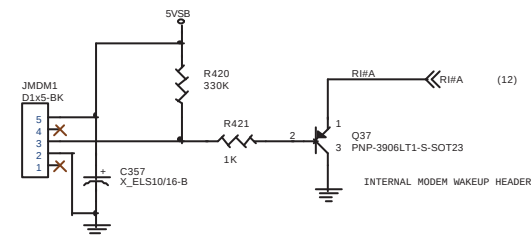
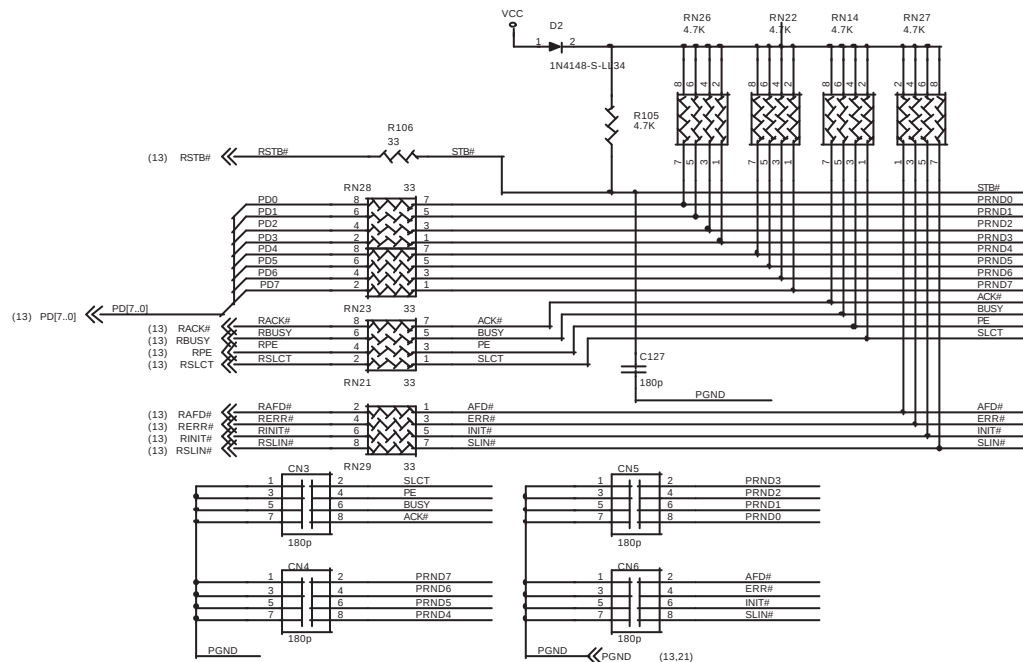
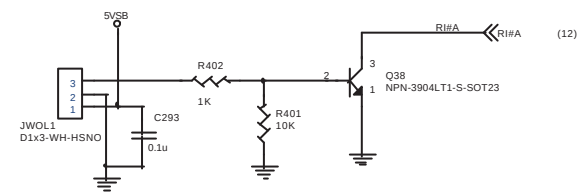
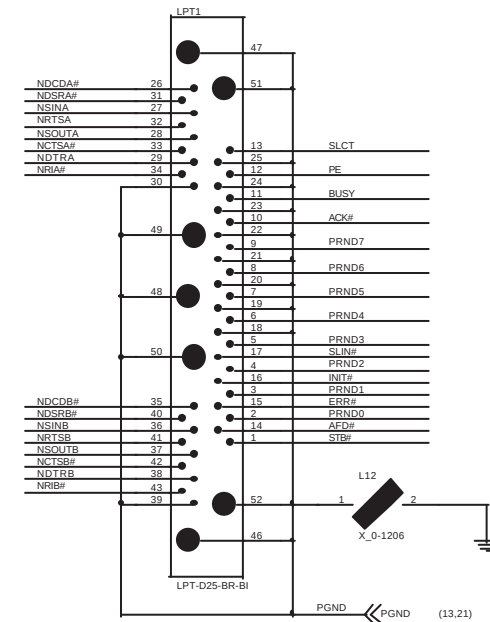
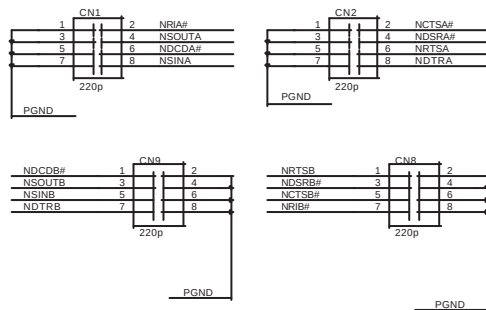
R470 : Install for S/W audio on board.
Remove for H/W audio on board.

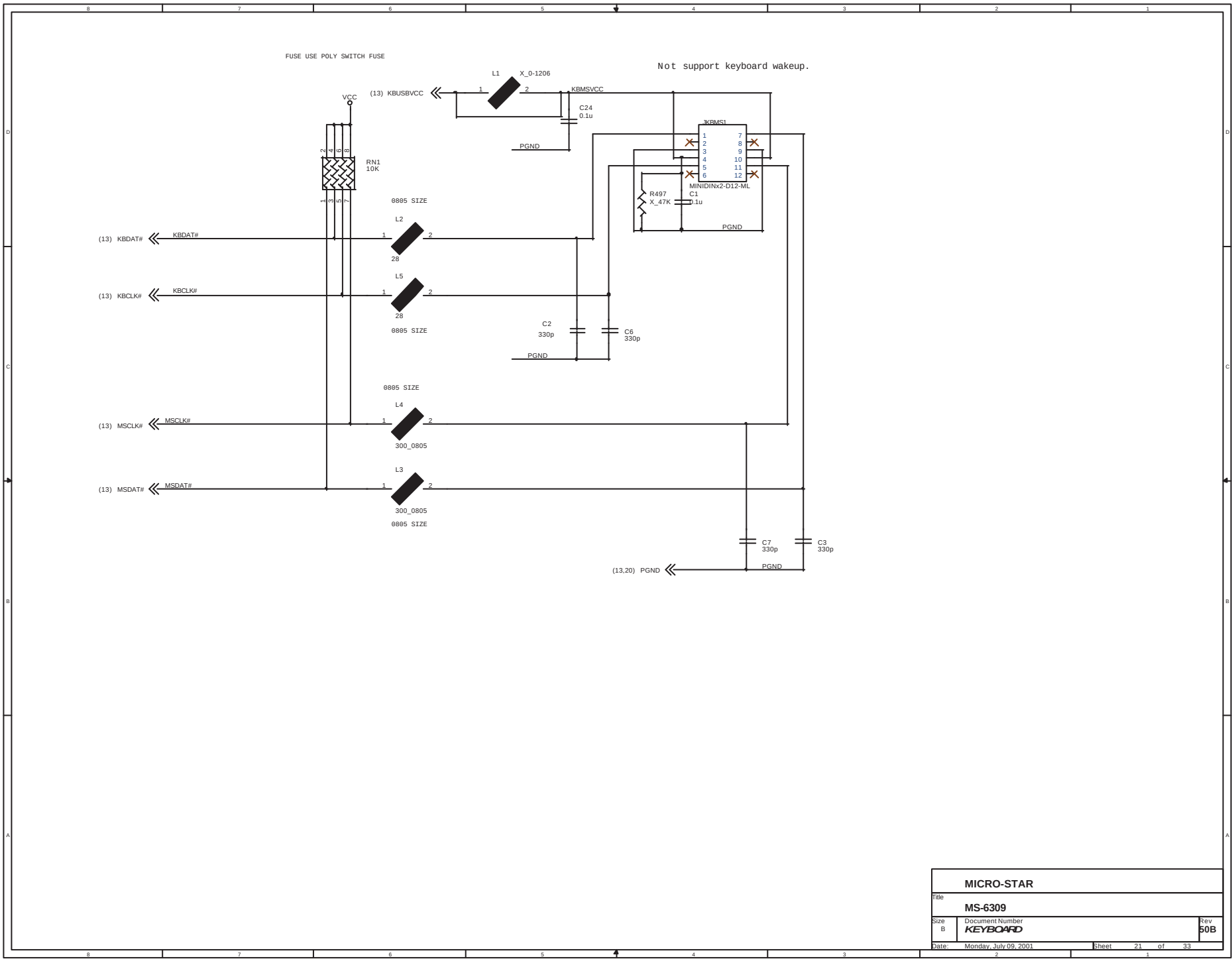


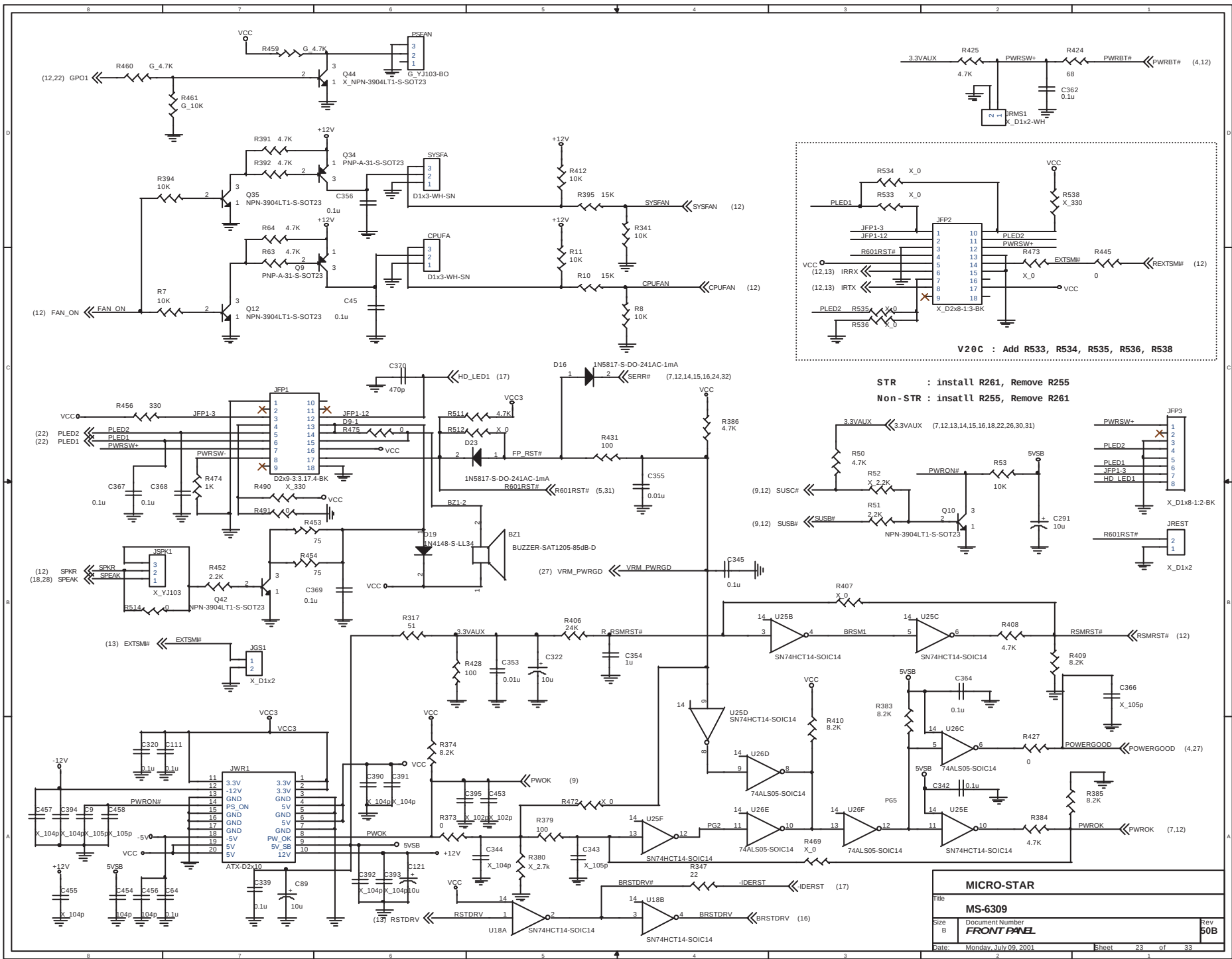
MICRO-STAR		
File	MS-6309	
Size	Document Number	Rev
B	AMR SLOT	50B
Date:	Monday, July 09, 2001	Sheet 18 of 33

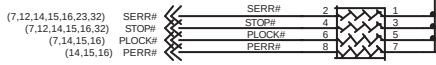
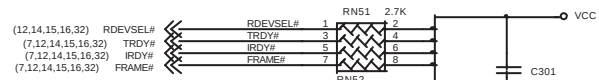


MICRO-SATR		
File		
MS-6309		
Size B		
Document Number		
FLASH MEMORY		
Date: Monday, July 09, 2001		
Sheet 19 of 33		
Rev 50B		

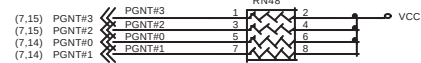




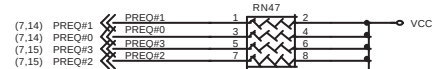
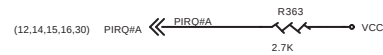




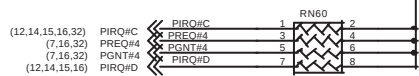
2.7K



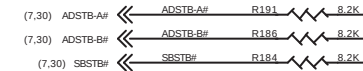
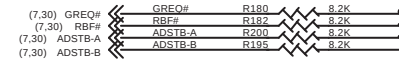
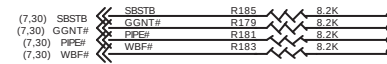
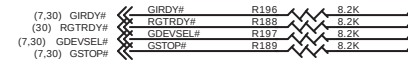
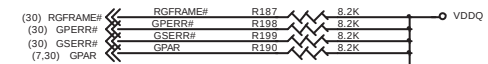
2.7K



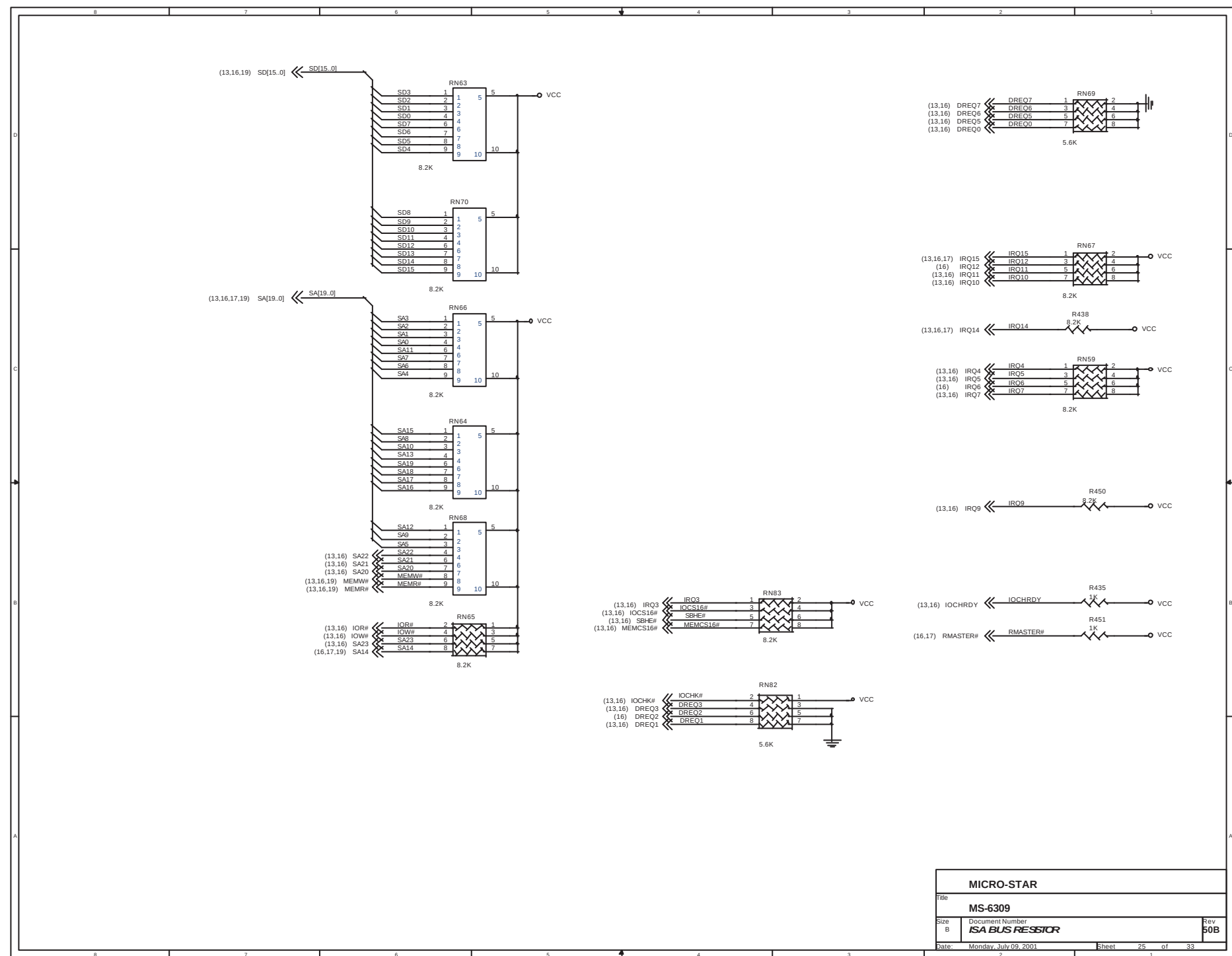
2.7K

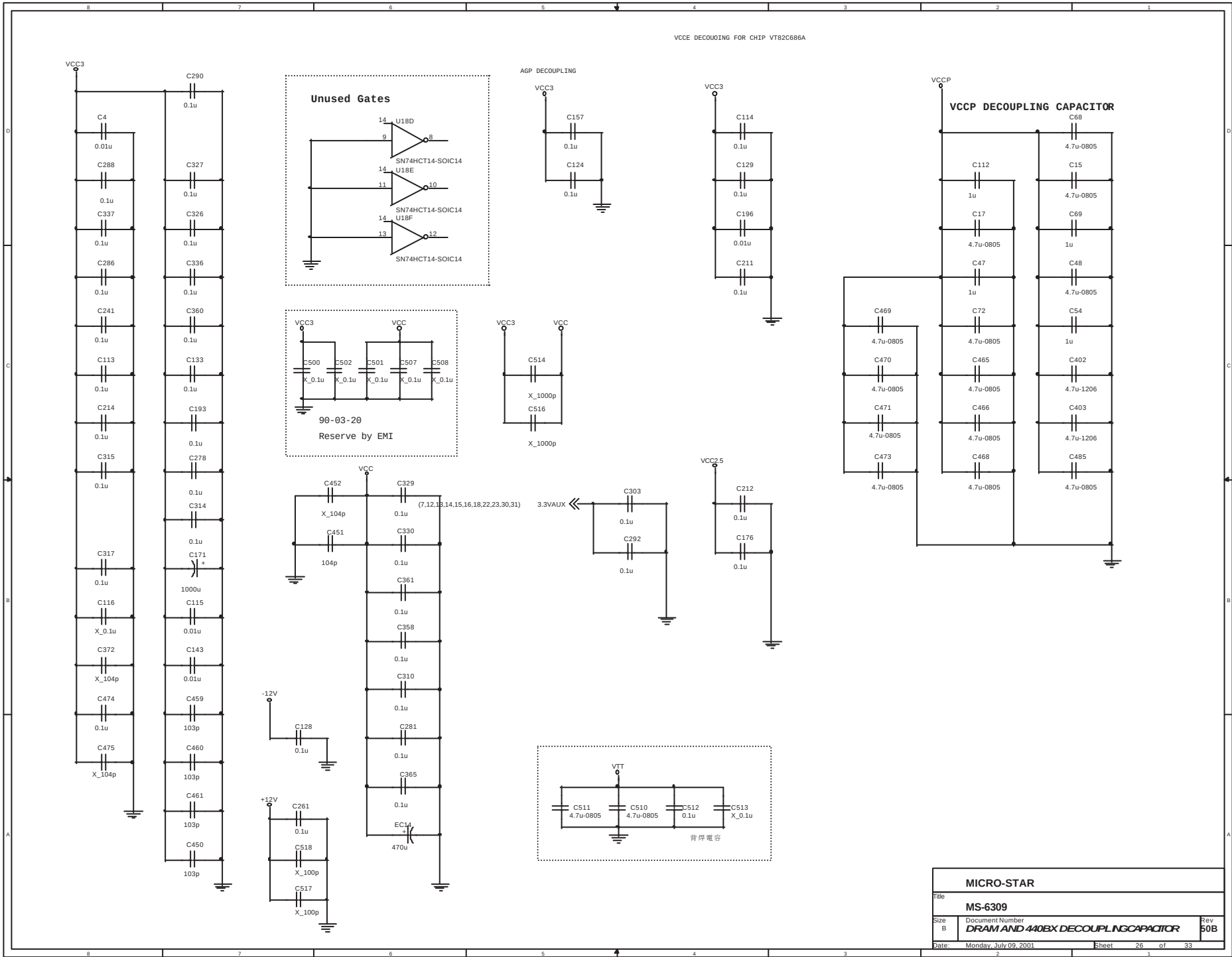


2.7K



MICRO-STAR		
File		
MS-6309		
Size B		
Document Number		Rev
BUS RESISTOR		50B
Date: Monday, July 09, 2001		Sheet 24 of 33

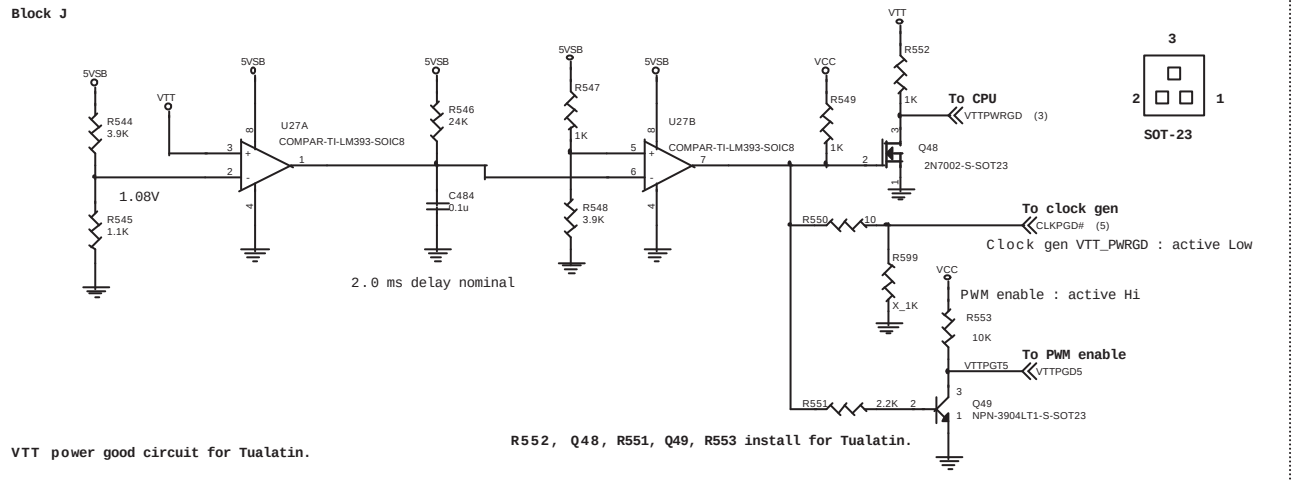
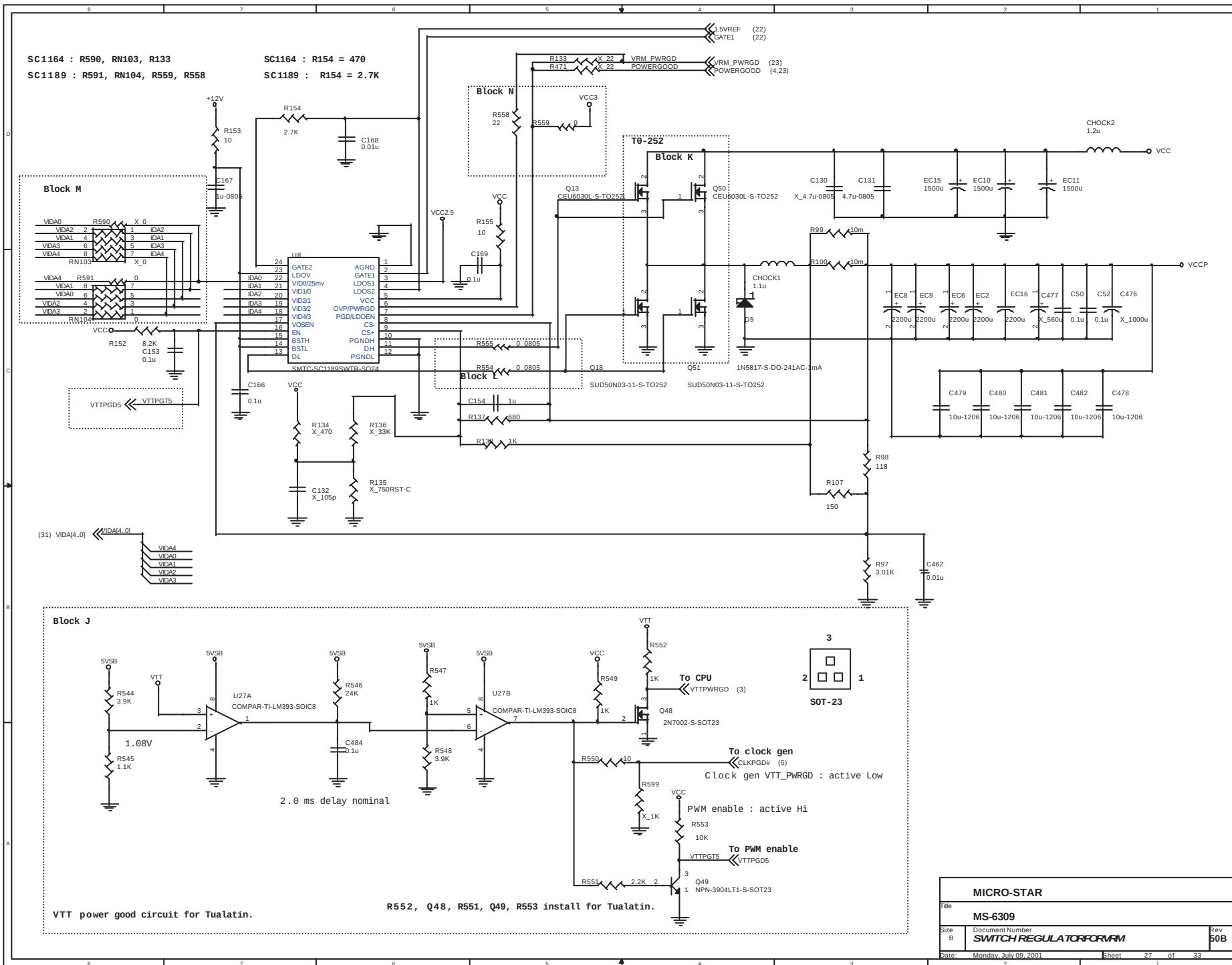




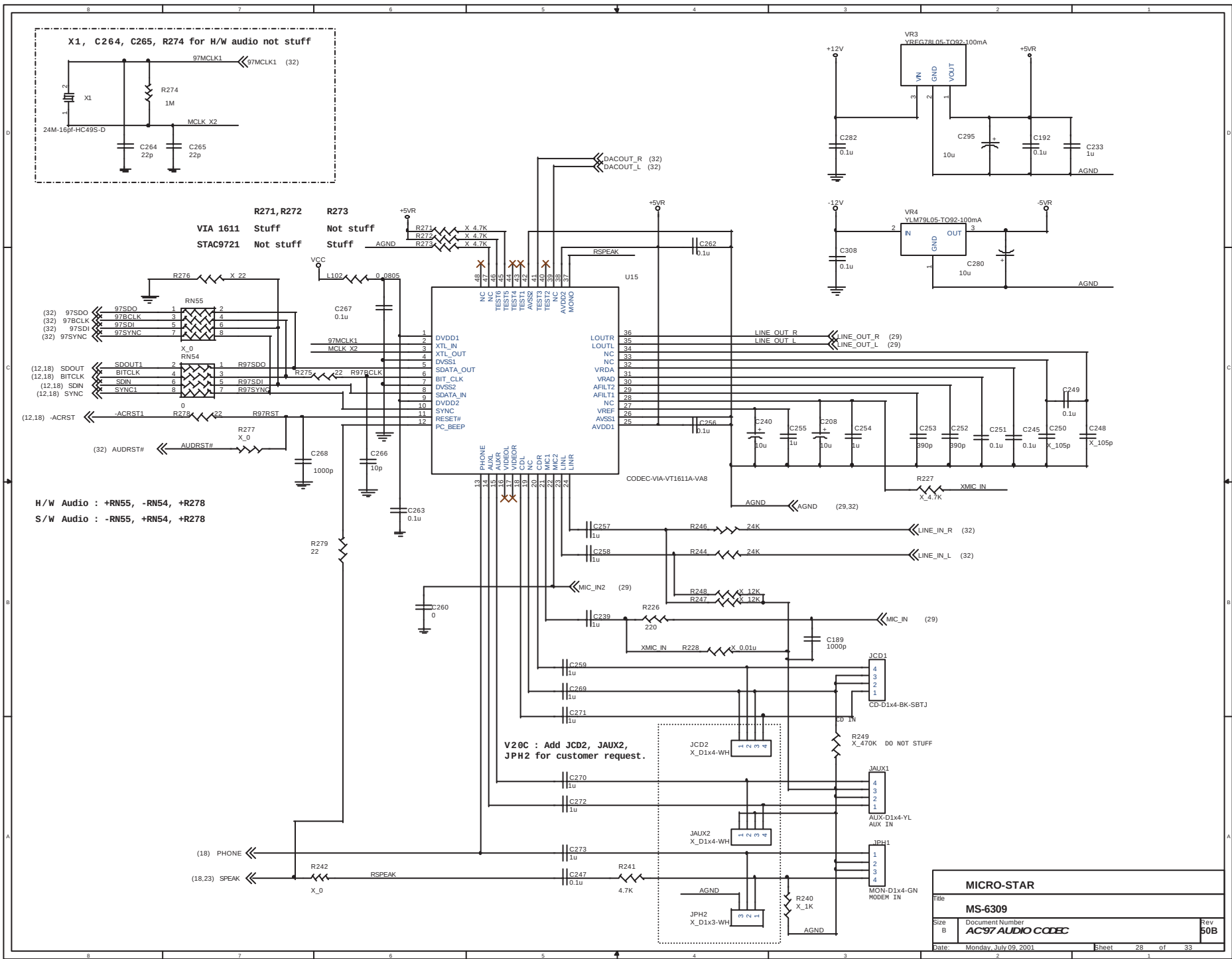
MICRO-STAR			
Title			
MS-6309			
Size B	Document Number		Rev
	DRAM AND 440BX DECOUPLING CAPACITOR		50B
Date:	Monday, July 09, 2001	Sheet 26 of 33	
2		1	

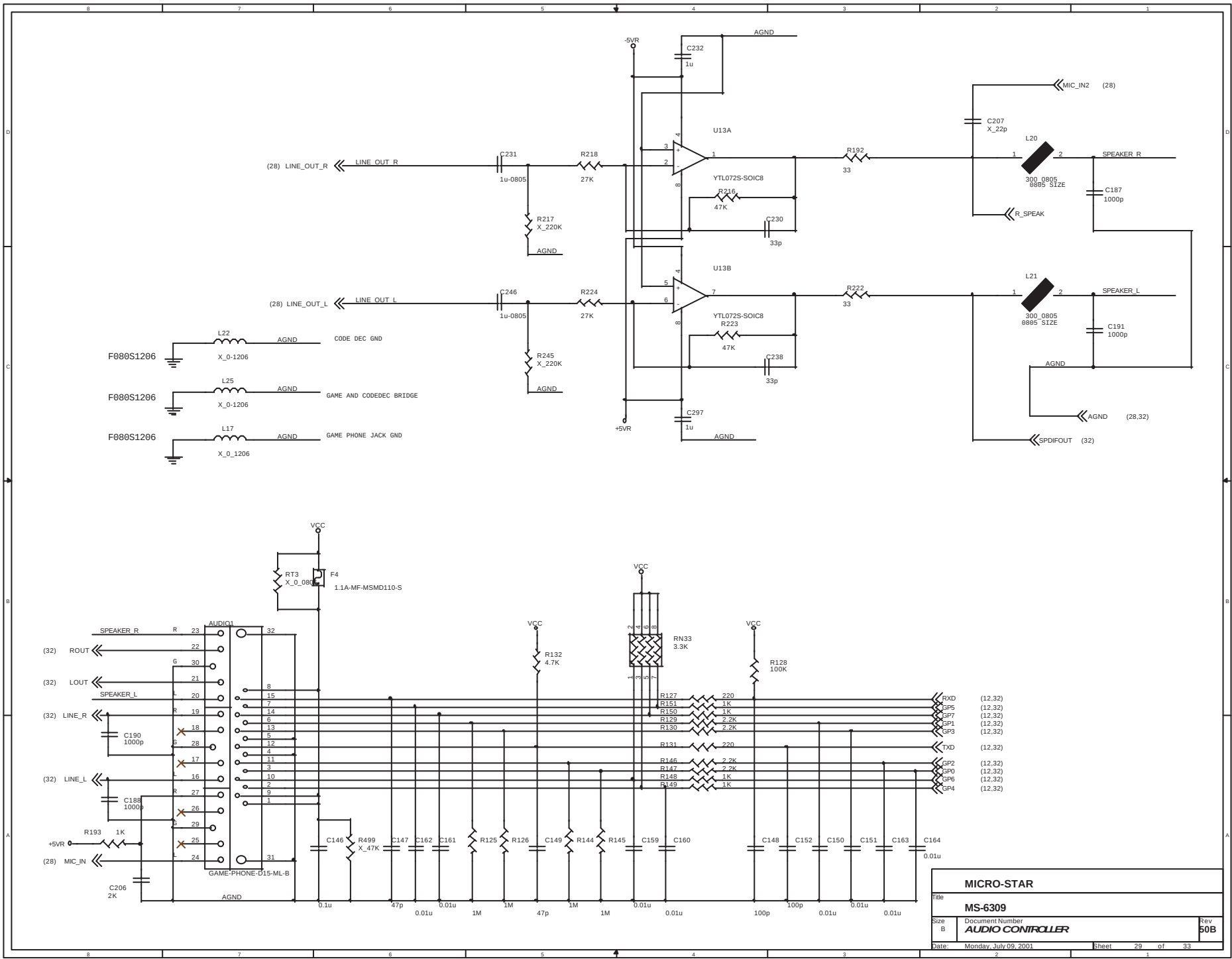
SC1164 : R590, RN103, R133
SC1189 : R591, RN104, R559, R558

SC1164 : R154 = 470
SC1189 : R154 = 2.7K

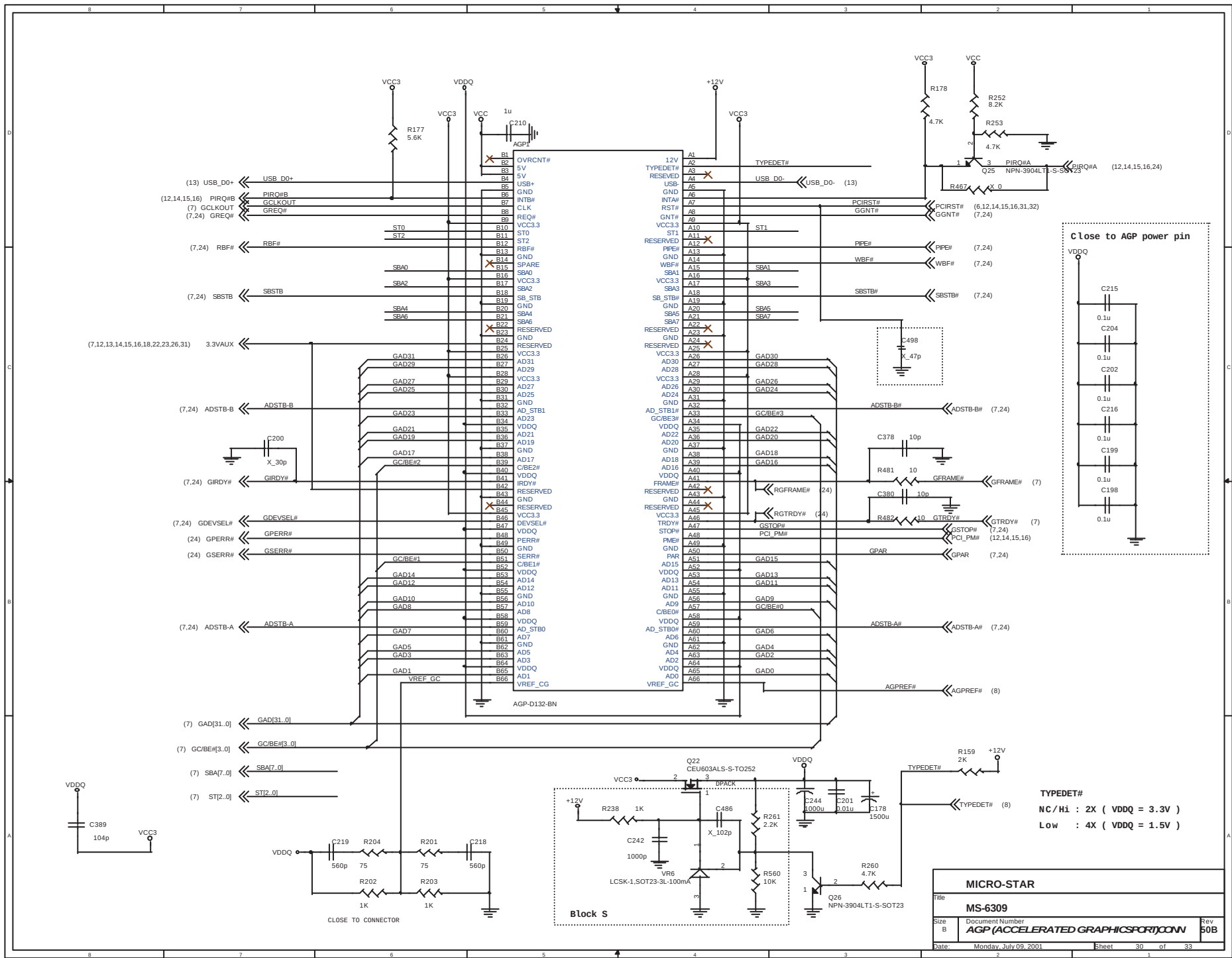


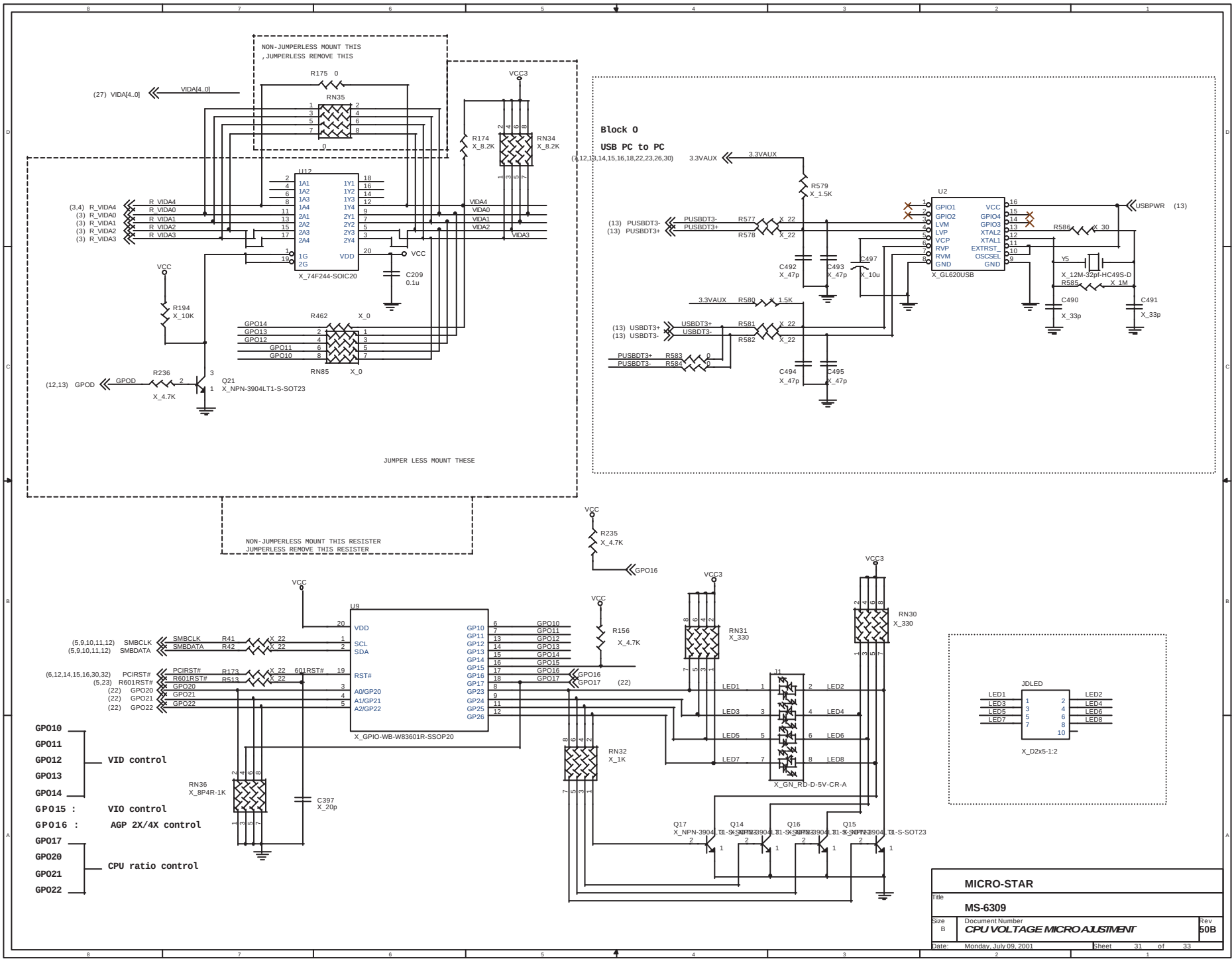
MICRO-STAR		
File	MS-6309	
Size	Document Number	Rev
B	SWITCH REGULATOR FORM	50B
Date:	Monday, July 09, 2001	Sheet 27 of 33

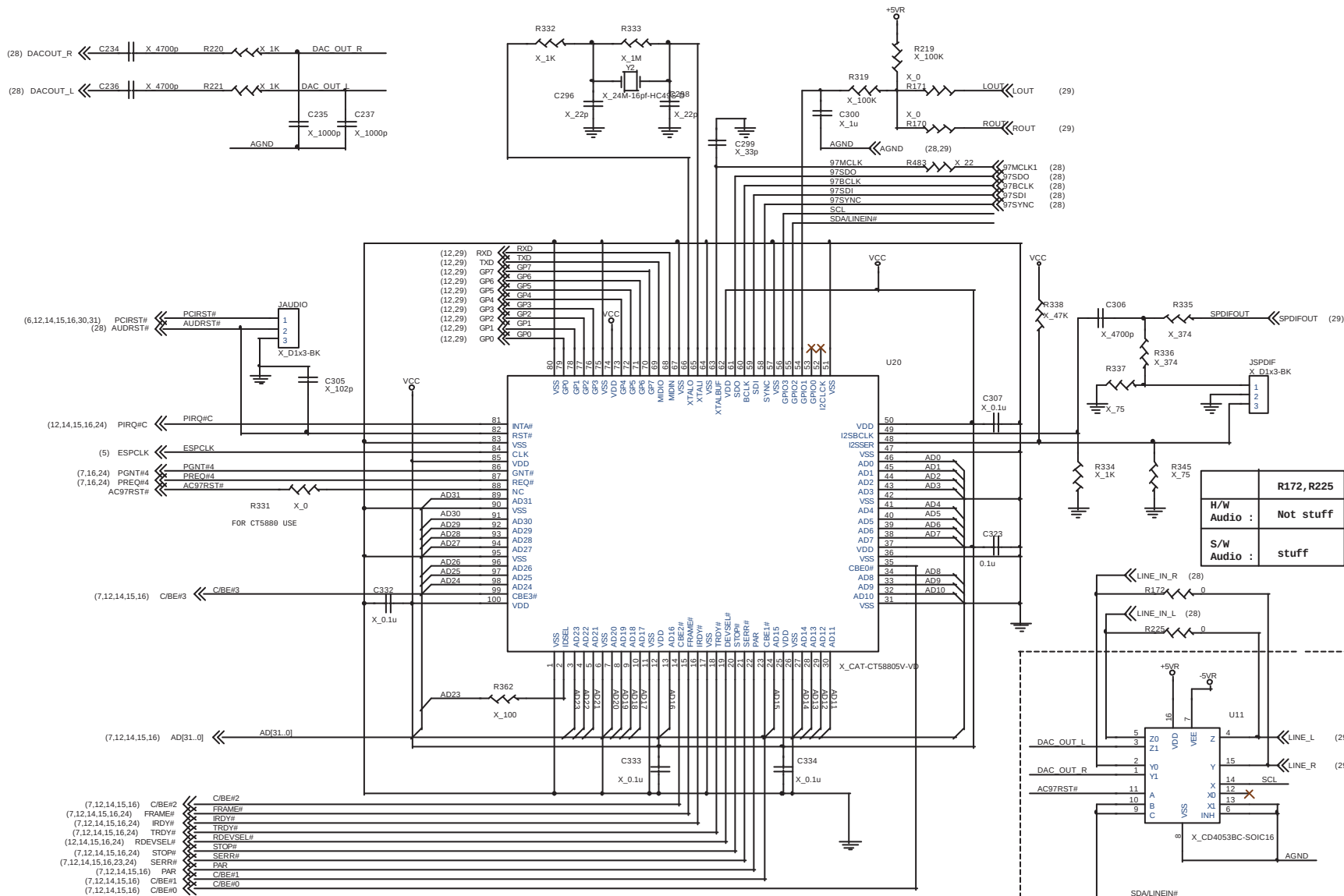




MICRO-STAR		
File	MS-6309	
Size	Document Number	Rev
B	AUDIO CONTROLLER	50B
Date	Monday, July 09, 2001	Sheet 29 of 33







	R172, R225
H/W Audio :	Not stuff
S/W Audio :	stuff

