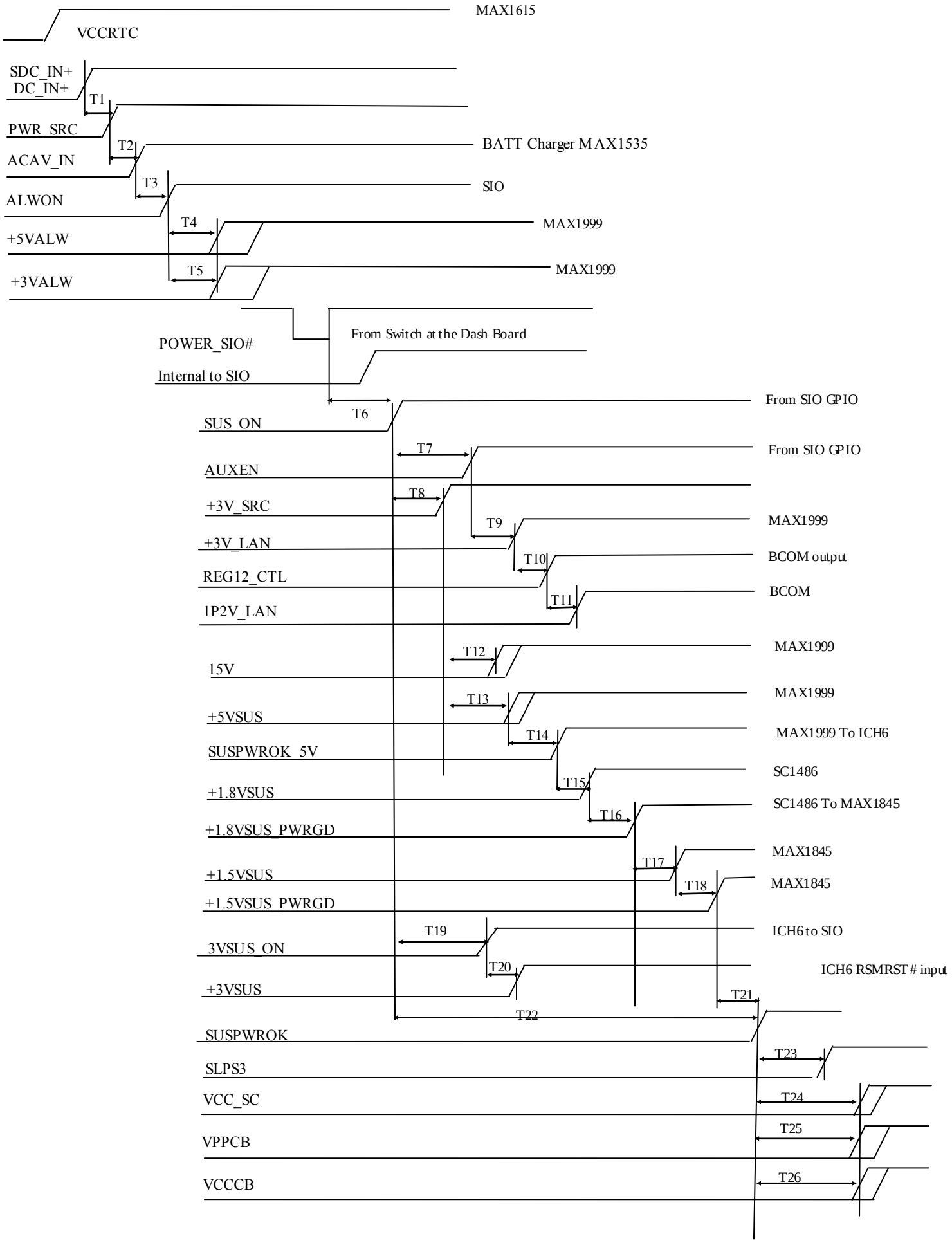
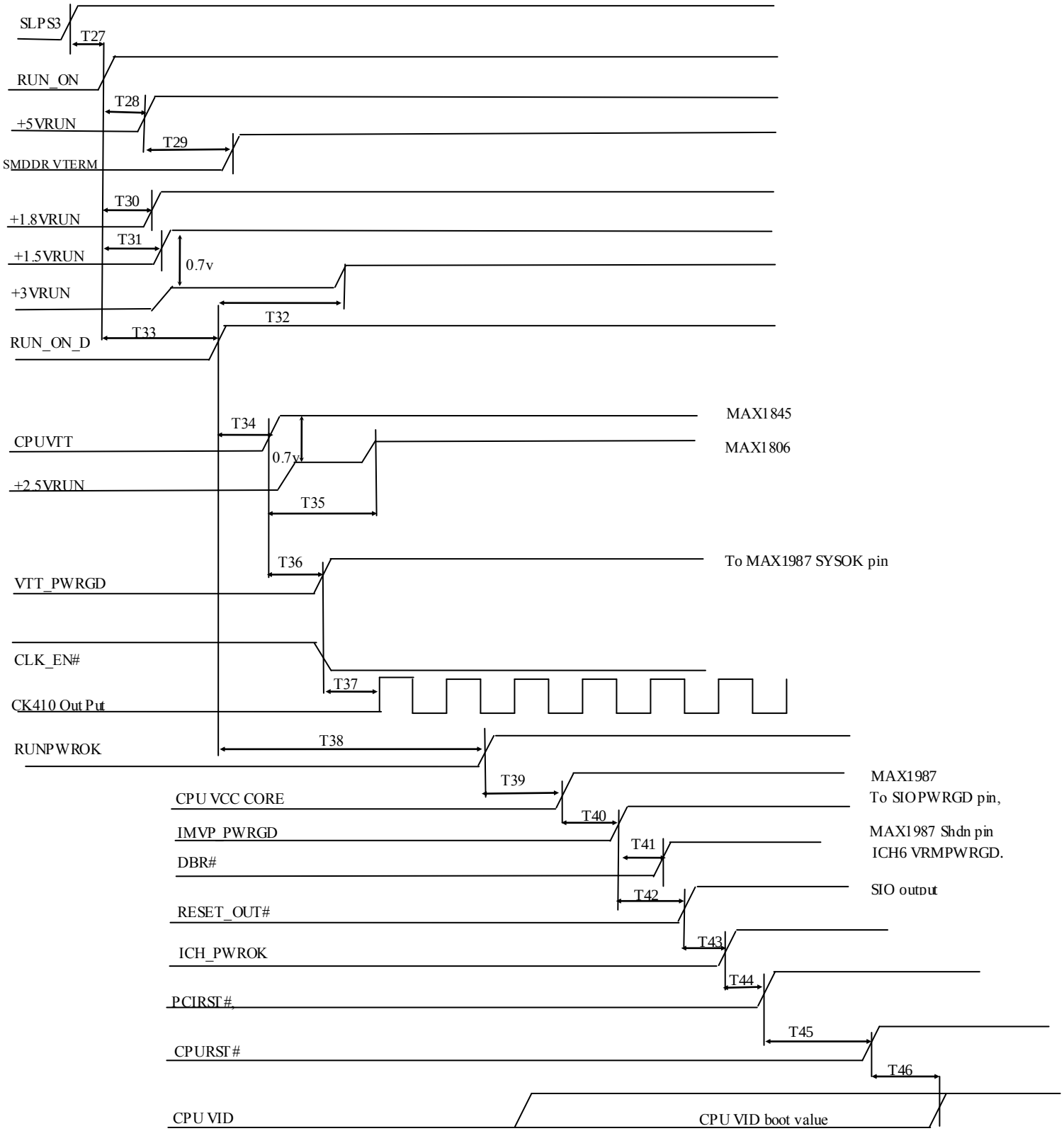
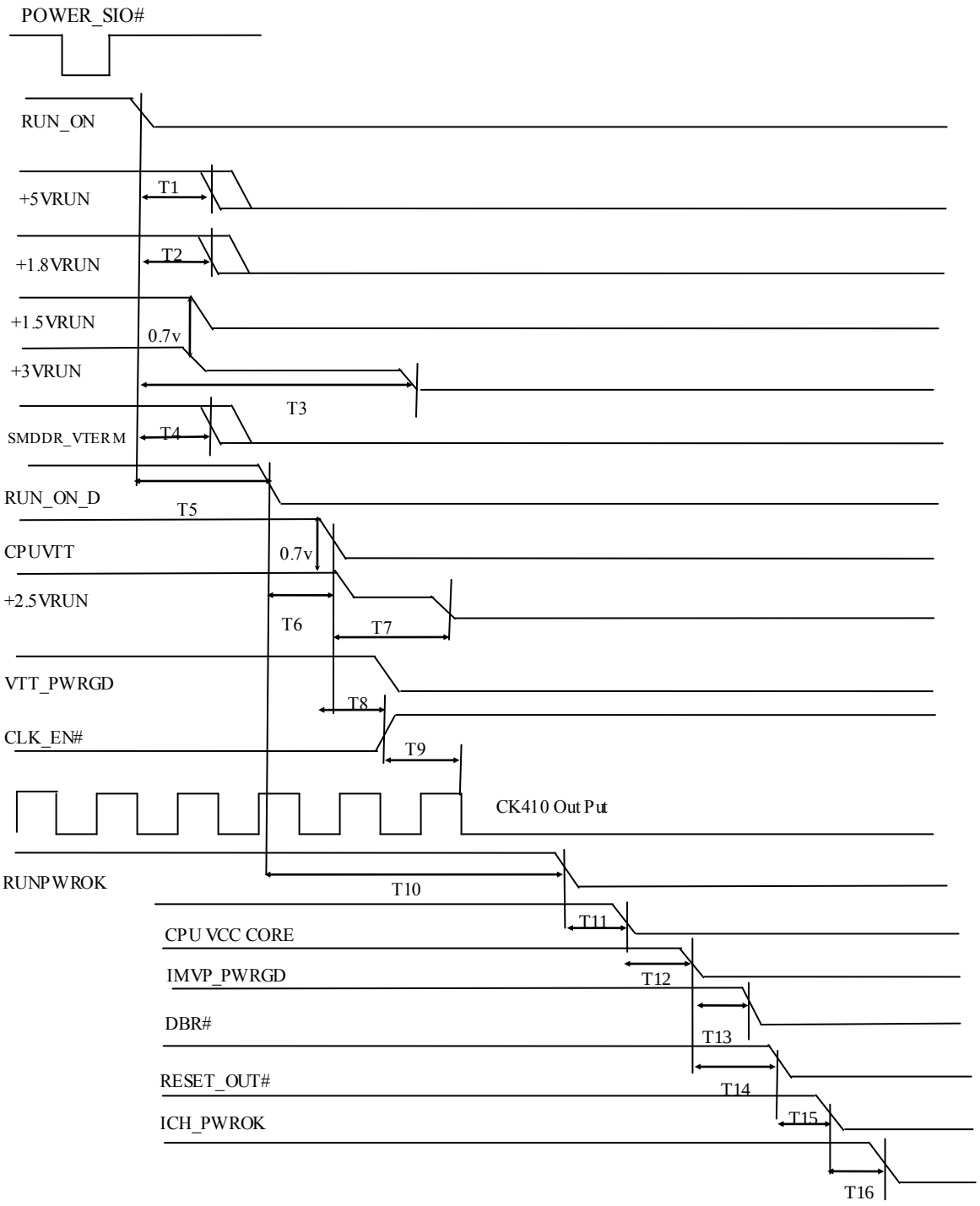
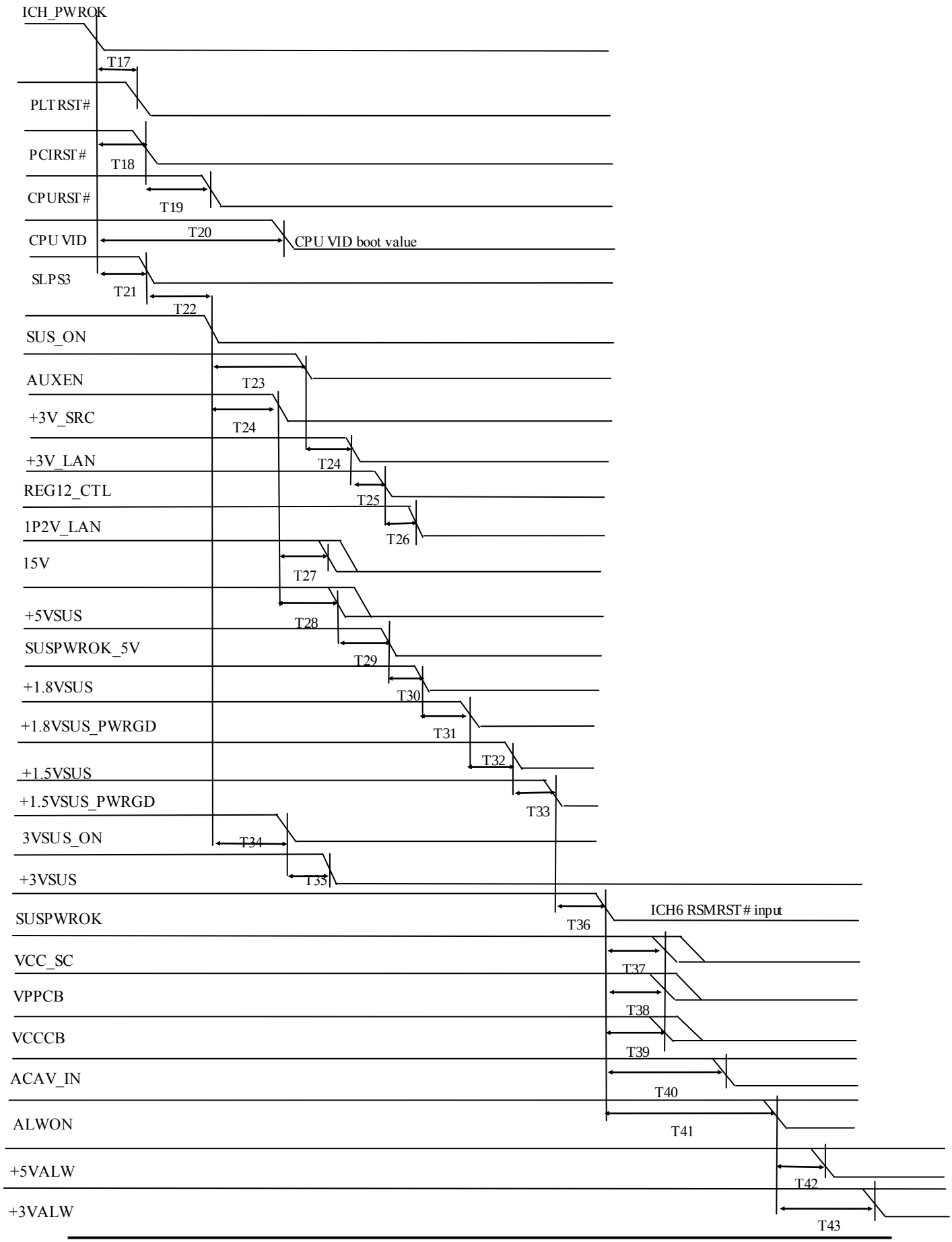
AZEDA Internal GFX Power Up Sequence.




SLPS3
T27
RUN_ON
T28
+5VRUN
T29
SMDDR VTERM
T30
+1.8VRUN
T31
+1.5VRUN
0.7v
+3VRUN
T32
T33
RUN_ON_D
T34
MAX1845
CPUVTT
MAX1806
0.7v
+2.5VRUN
T35
T36
To MAX1987 SYSOK pin
VTT_PWRGD
CLK_EN#
T37
CK410 Out Put
T38
RUNPWROK
T39
MAX1987
To SIOPWRGD pin,
CPU VCC CORE
T40
IMVP_PWRGD
MAX1987 Shdn pin
ICH6 VRMPWRGD.
T41
DBR#
T42
SIO output
RESET_OUT#
T43
ICH_PWROK
T44
PCIRST#,
T45
CPURST#
T46
CPU VID
CPU VID boot value

AZEDA Internal GFX Power down Sequence.




ICH_PWROK
T17
PLTRST#
PCIRST#
T18
CPURST#
T19
CPU VID
T20
CPU VID boot value
SLPS3
T21
T22
SUS_ON
AUXEN
T23
+3V_SRC
T24
+3V_LAN
T24
REG12_CTL
T25
1P2V_LAN
T26
15V
T27
+5VSUS
T28
SUSPWROK_5V
T29
+1.8VSUS
T30
+1.8VSUS_PWRGD
T31
+1.5VSUS
T32
+1.5VSUS_PWRGD
T33
3VSUS_ON
T34
+3VSUS
T35
SUSPWROK
T36
ICH6 RSMRST# input
VCC_SC
T37
VPPCB
T38
VCCCB
T39
ACAV_IN
T40
ALWON
T41
+5VALW
T42
+3VALW
T43