

# Compal Confidential

## Schematic Document

### Cantiga + ICH9

2008 / 12 / 10    Rev:1.0

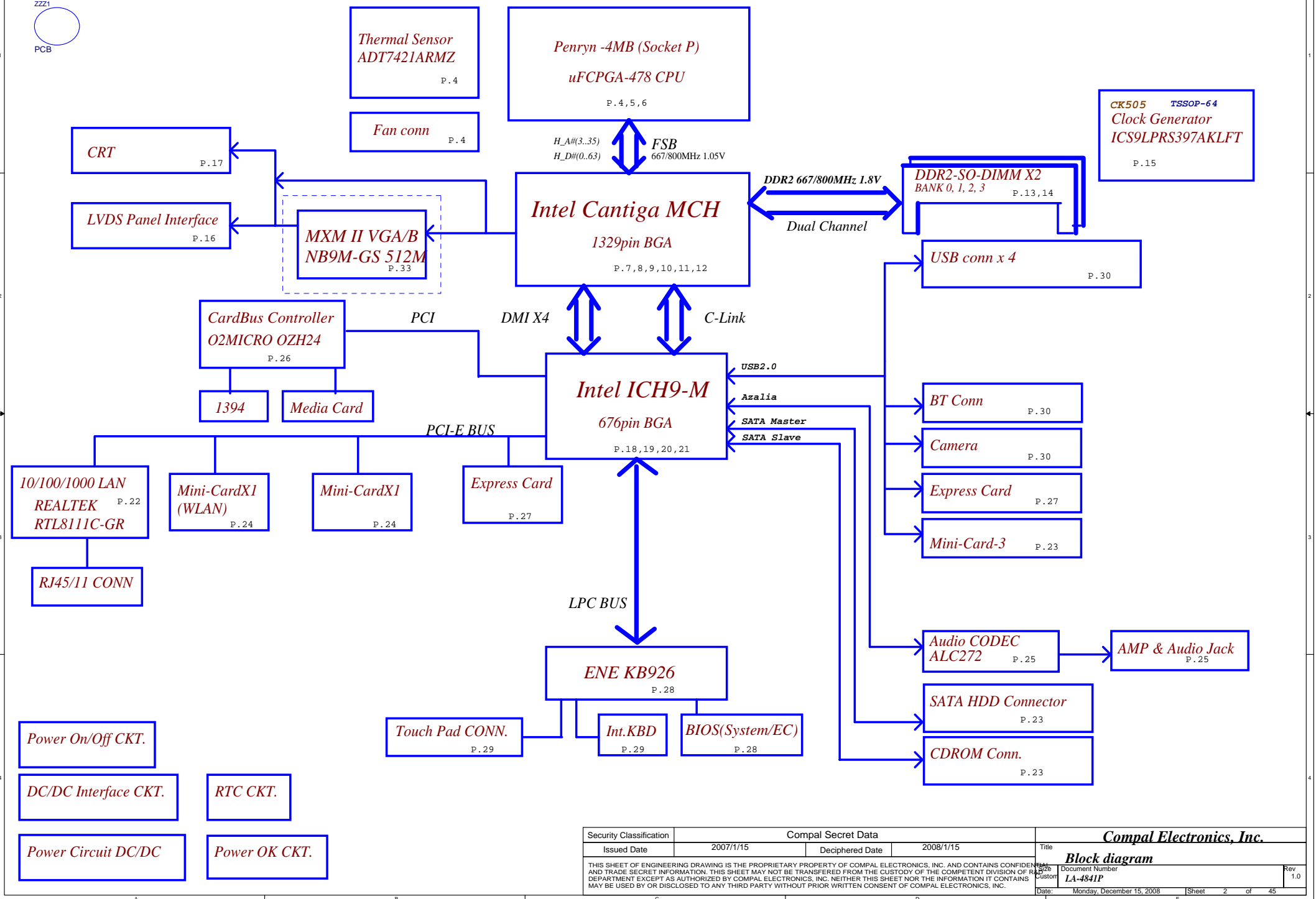
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Compal confidential

File Name : LA-4841P



# MLK 14



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Voltage Rails    ○ MEANS ON    X MEANS OFF

<div>power plane</div> <div>State</div>	+B	+5VALW  +3VALW	+1.8V	+5VS +3VS +1.5VS +0.9V +VCCP +CPU_CORE  +VGA_CORE +2.5VS +1.8VS +1.2VS +0.9VGA
S0	○	○	○	○
S1	○	○	○	○
S3	○	○	○	X
S5 S4/AC	○	○	X	X
S5 S4/ Battery only	○	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

Symbol Note :

 : means Digital Ground

 : means Analog Ground

@ : means just reserve , no build

DEBUG@ : means just reserve for debug.

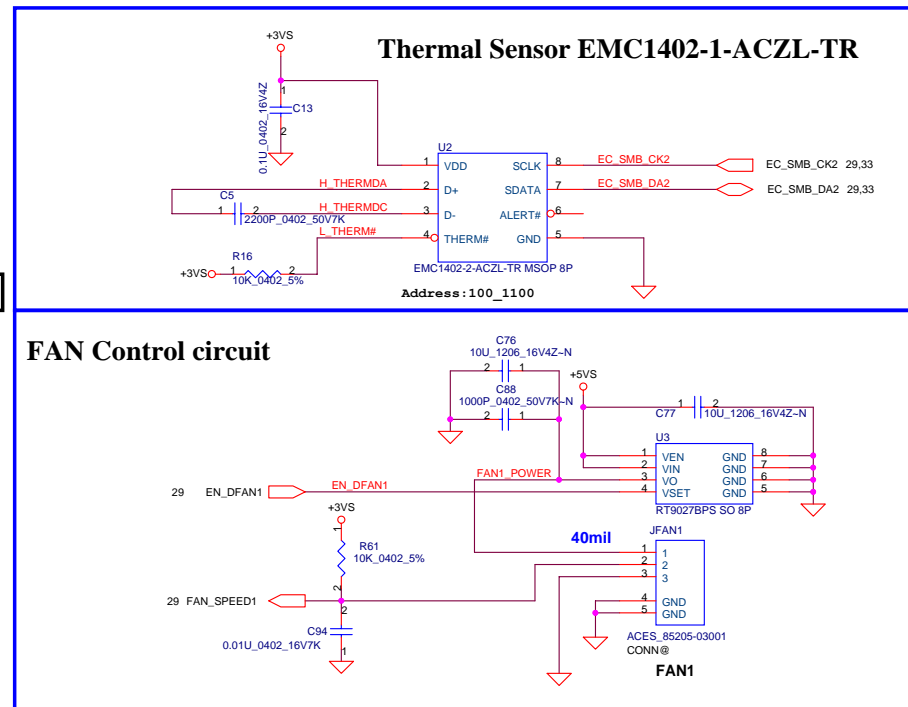
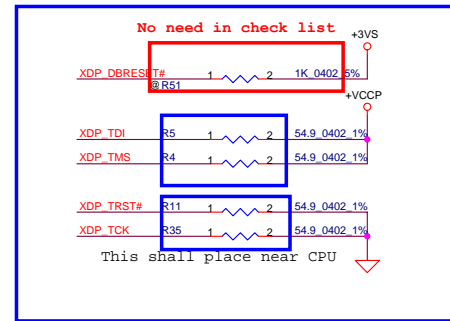
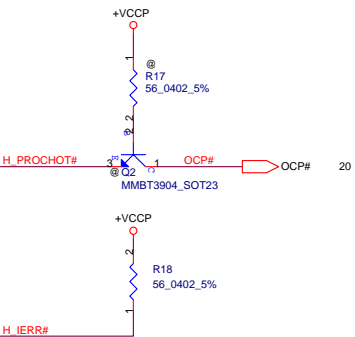
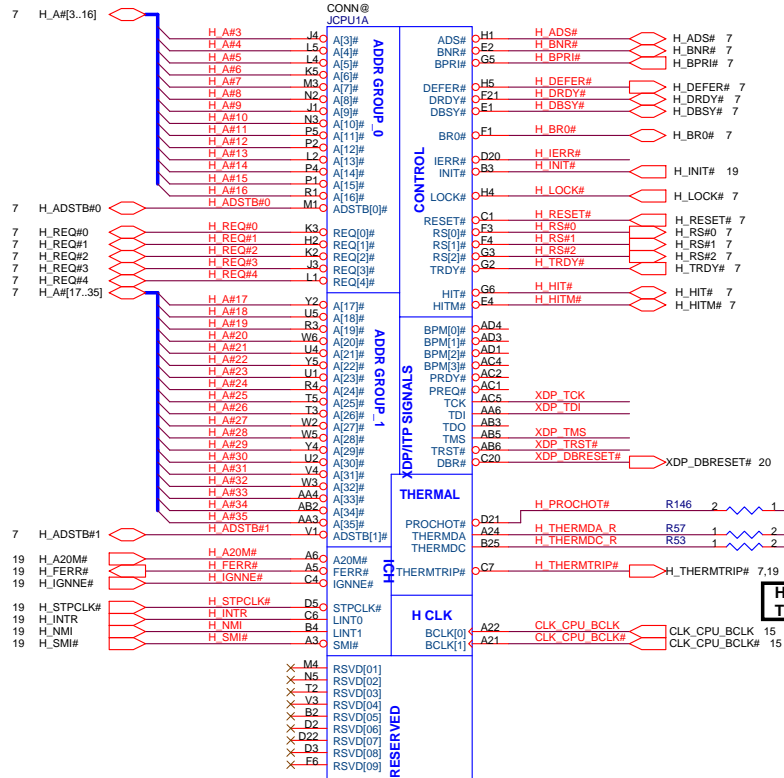
SMBUS Control Table

<div></div>	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR (CPU)	SODIMM	CLK CHIP	MINI CARD	LCD
SMB_EC_CK1 SMB_EC_DA1	KB926	X	V	V	X	X	X	X	X
SMB_EC_CK2 SMB_EC_DA2	KB926	X	X	X	V	X	X	X	X
SMB_CK_CLK1 SMB_CK_DAT1	ICH9	X	X	X	X	V	V	V	X
LCD_CLK LCD_DAT	Cantiga	X	X	X	X	X	X	X	V

I2C / SMBUS ADDRESSING

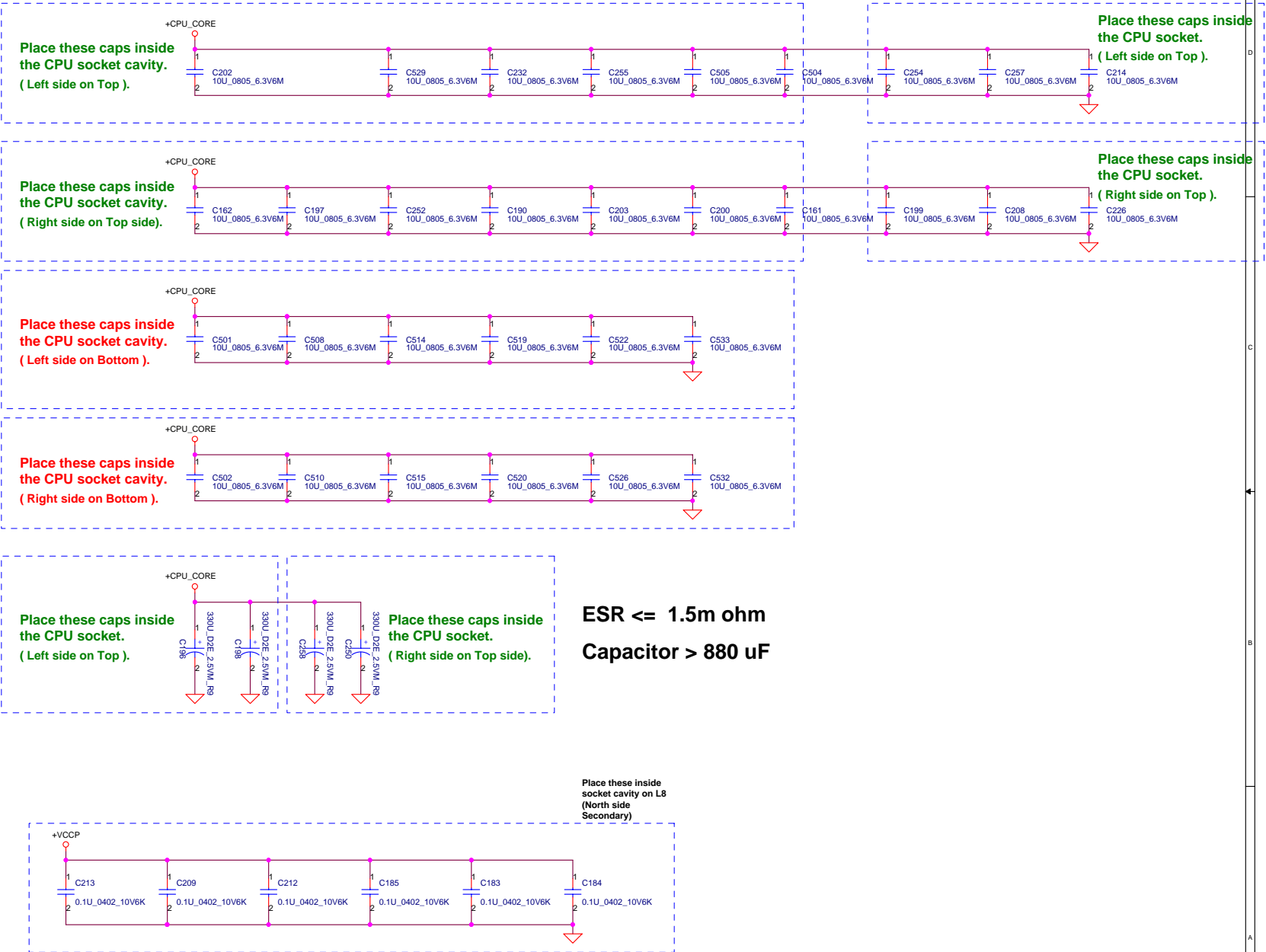
DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0
DDR SO-DIMM 1	A4	1 0 1 0 0 1 0 0
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 0

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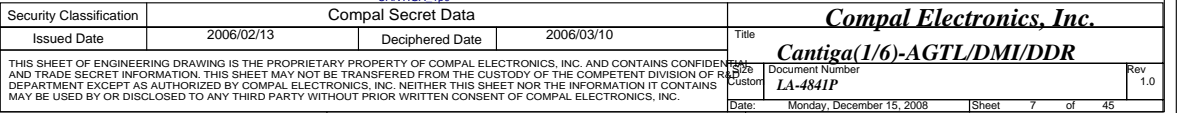


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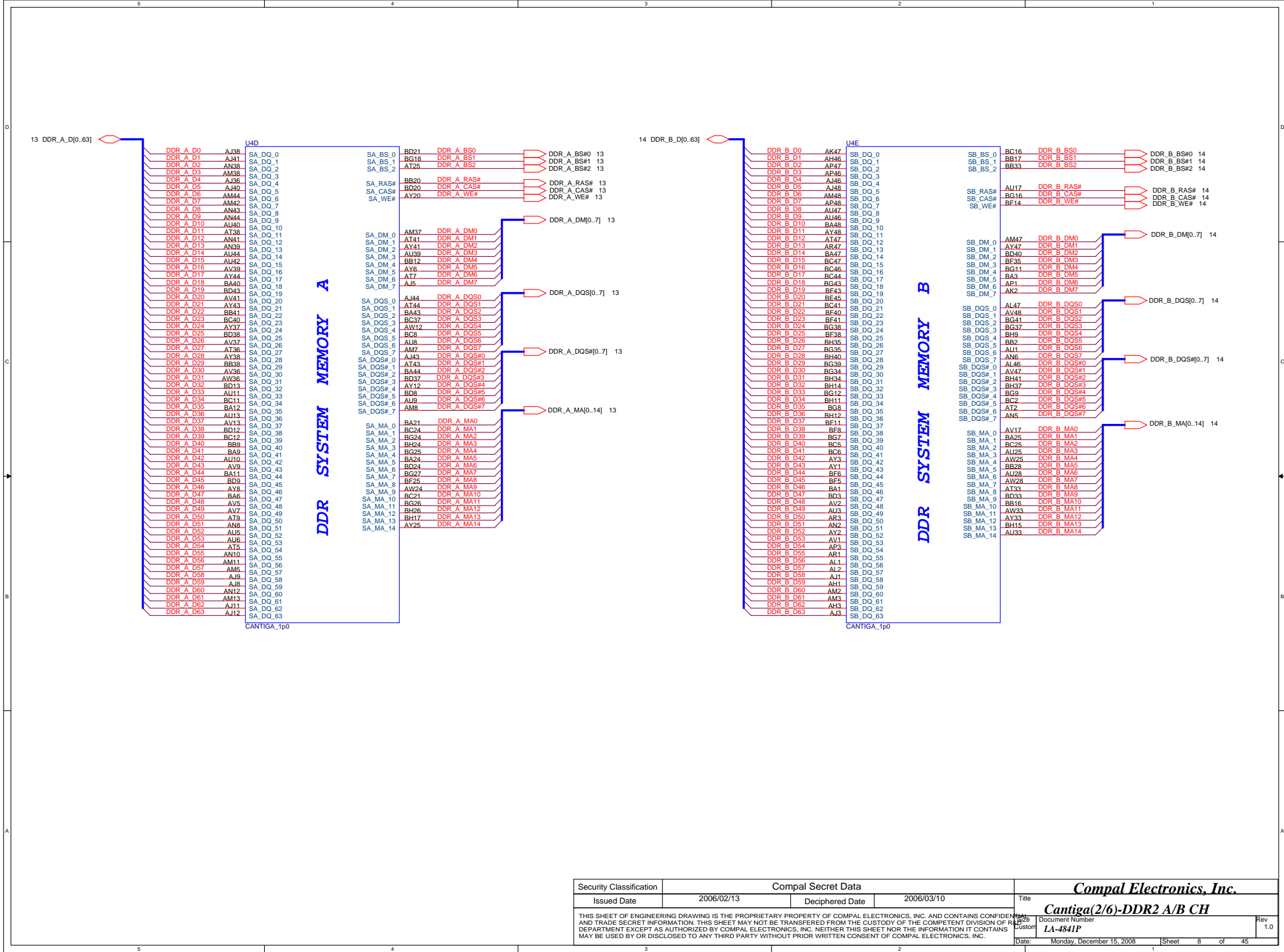




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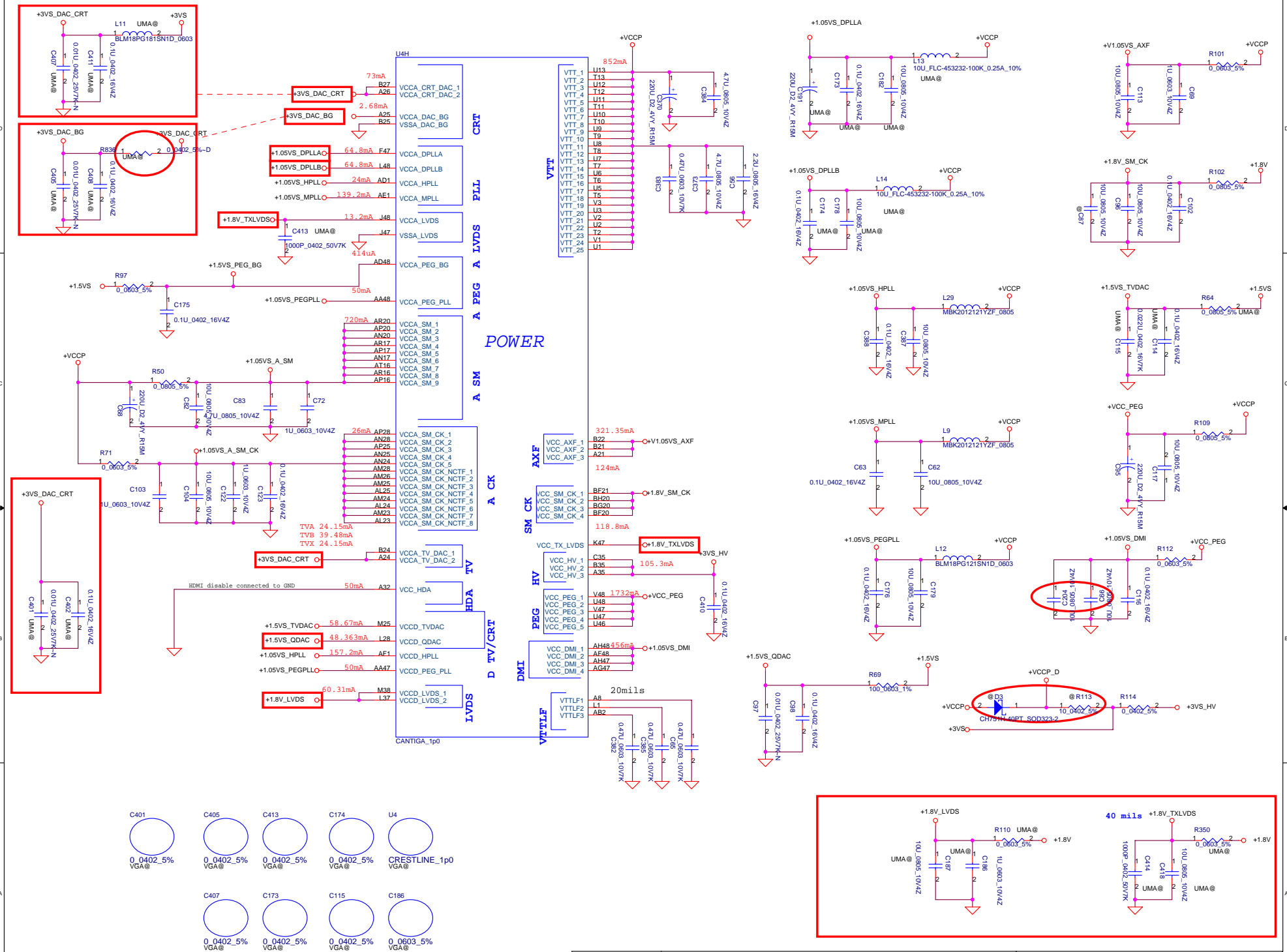


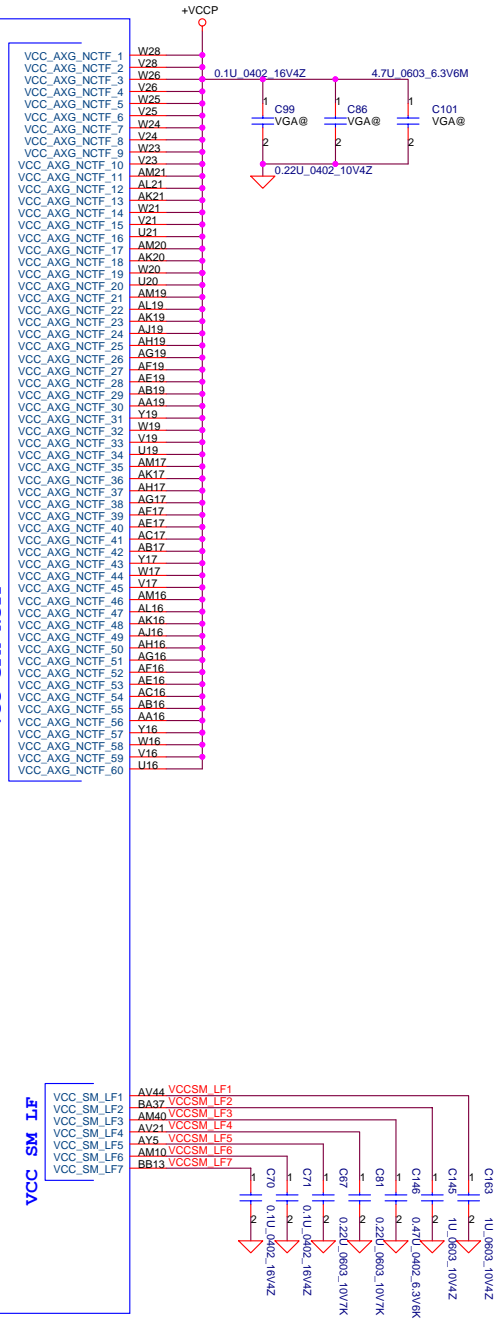




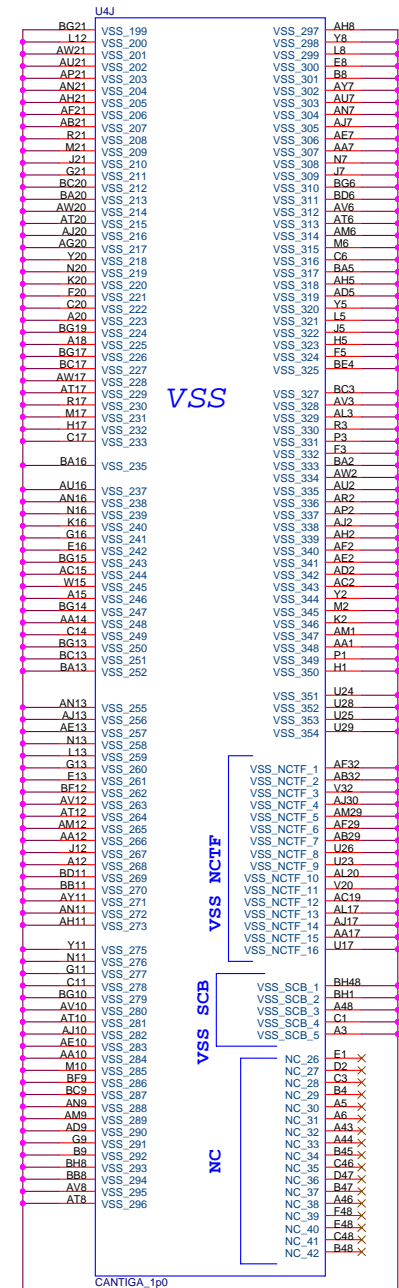
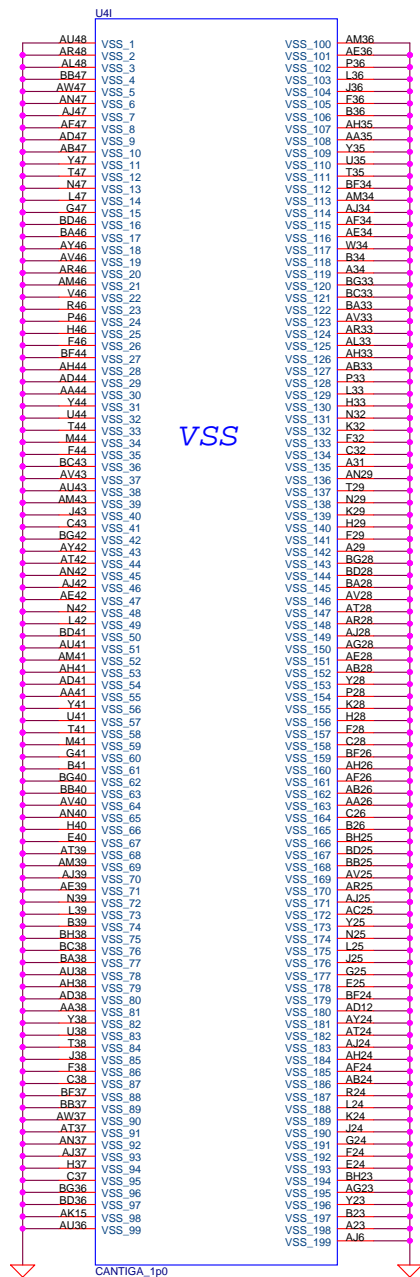




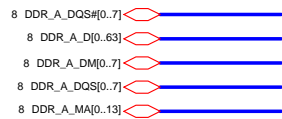




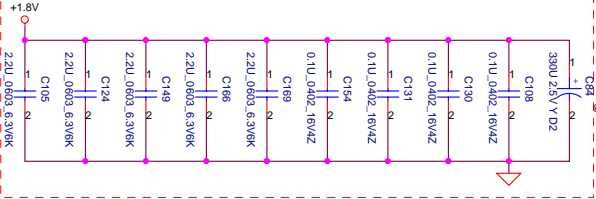
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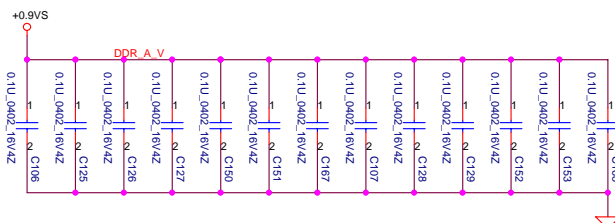
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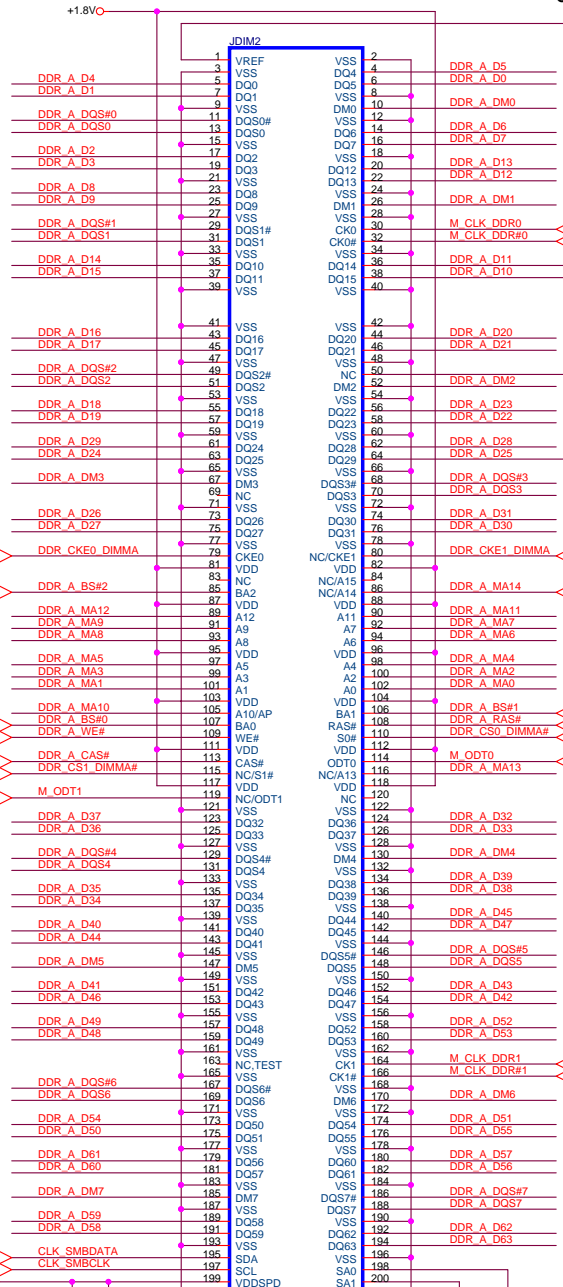
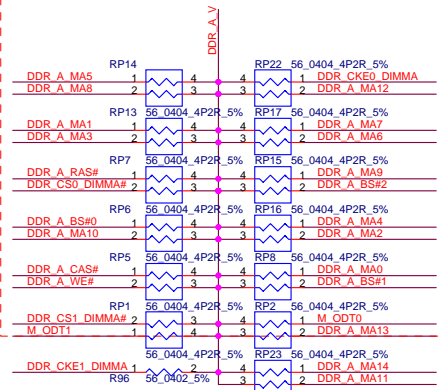
**Layout Note:**  
Place near JDIM1



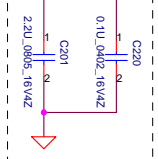
**Layout Note:**  
Place one cap close to every 2 pullup resistors terminated to +0.9V



**Layout Note:**  
Place these resistor closely JP41, all trace length Max=1.5"

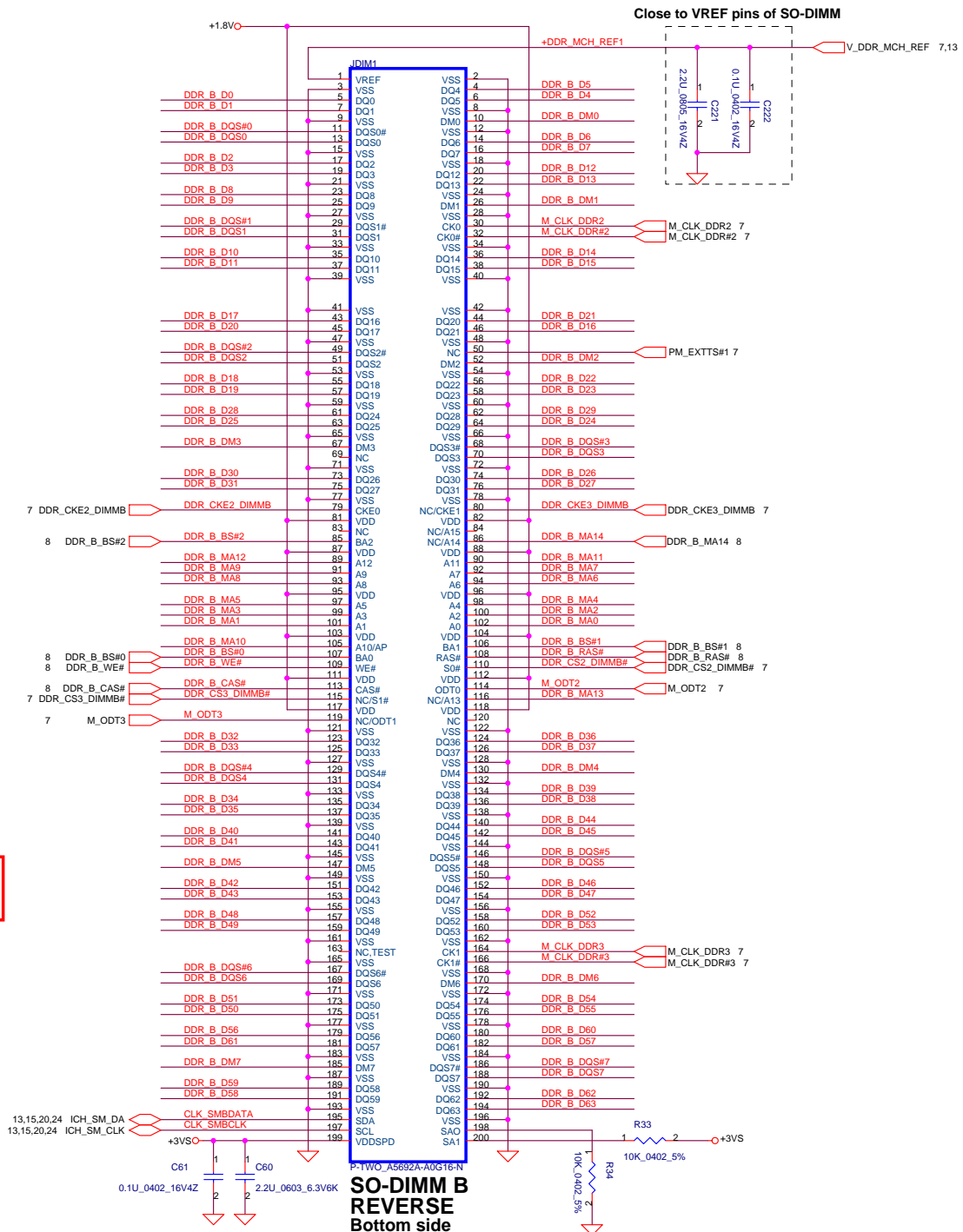
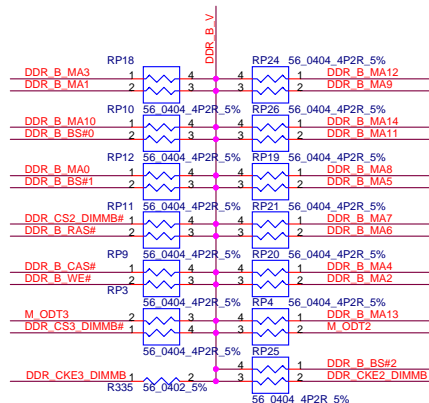


Close to VREF pins of SO-DIMM



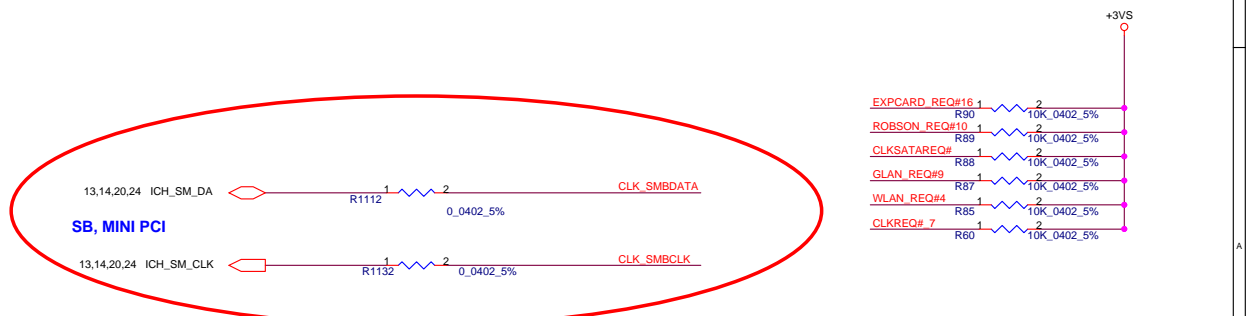
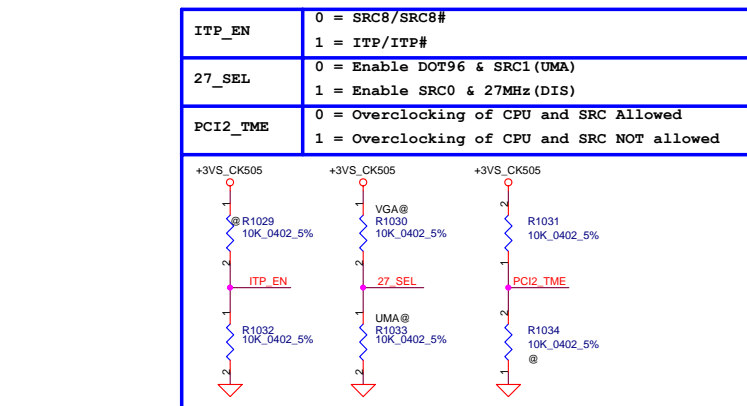
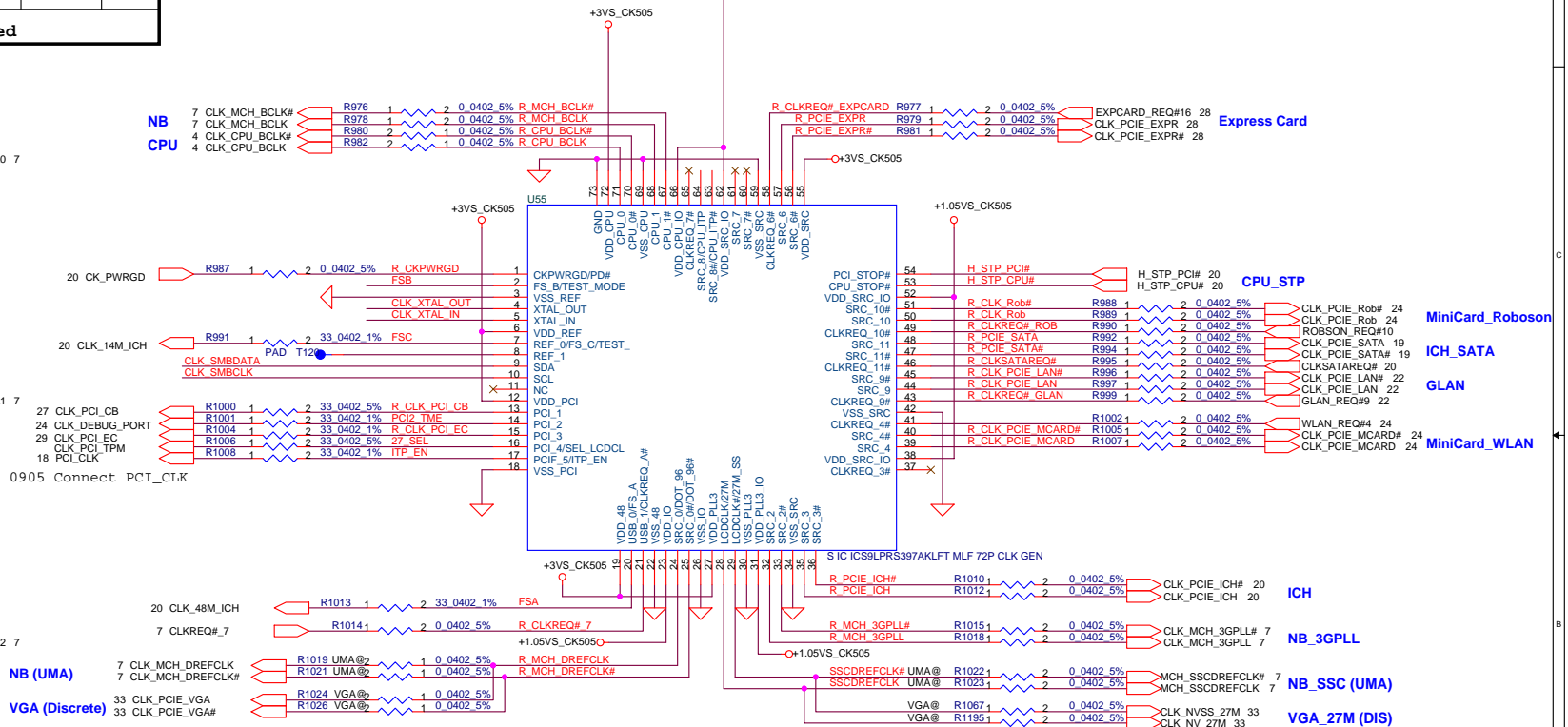
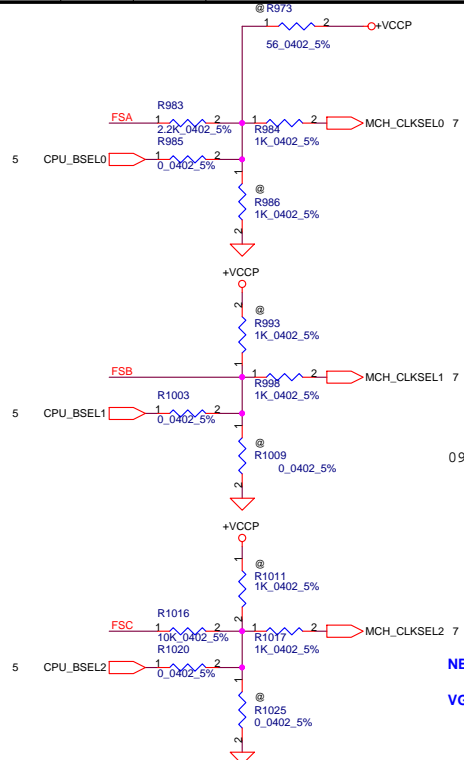
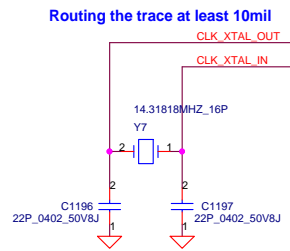
**SO-DIMM A  
REVERSE  
Bottom side**

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FSC CLKSEL2	FSB CLKSEL1	FSA CLKSEL0	CPU MHz	SRC MHz	PCI MHz	REF MHz	DOT_96 MHz	USB MHz
0	0	0	266	100	33.3	14.318	96.0	48.0
0	0	1	133	100	33.3	14.318	96.0	48.0
0	1	0	200	100	33.3	14.318	96.0	48.0
0	1	1	166	100	33.3	14.318	96.0	48.0
1	0	0	333	100	33.3	14.318	96.0	48.0
1	0	1	100	100	33.3	14.318	96.0	48.0
1	1	0	400	100	33.3	14.318	96.0	48.0
1	1	1	Reserved					



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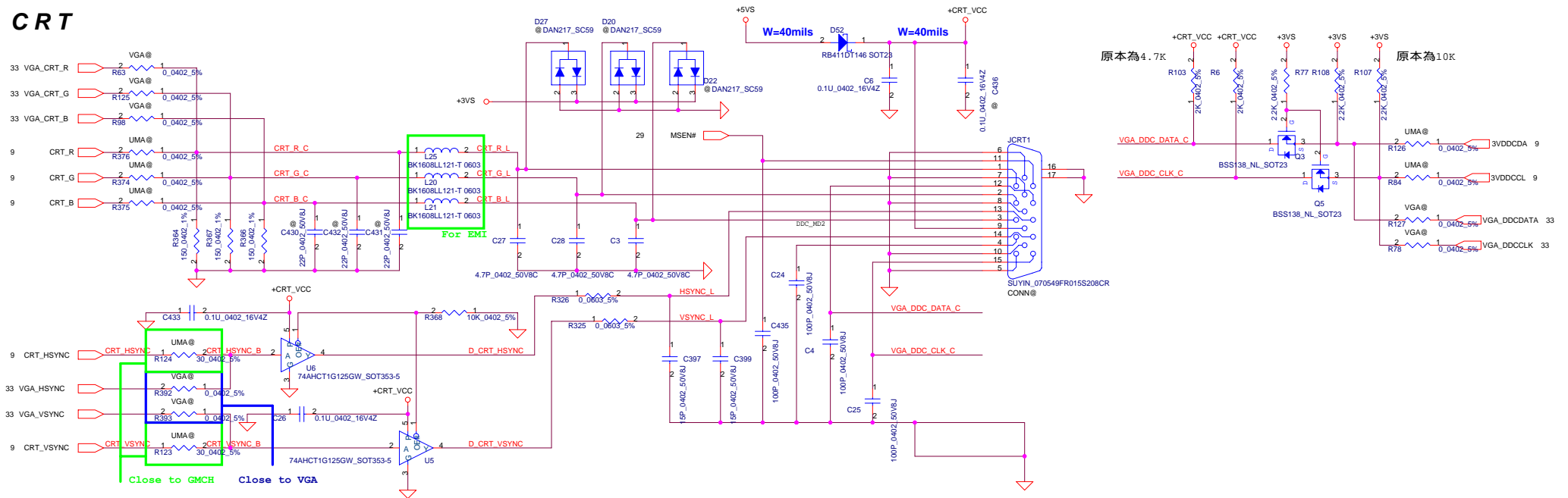
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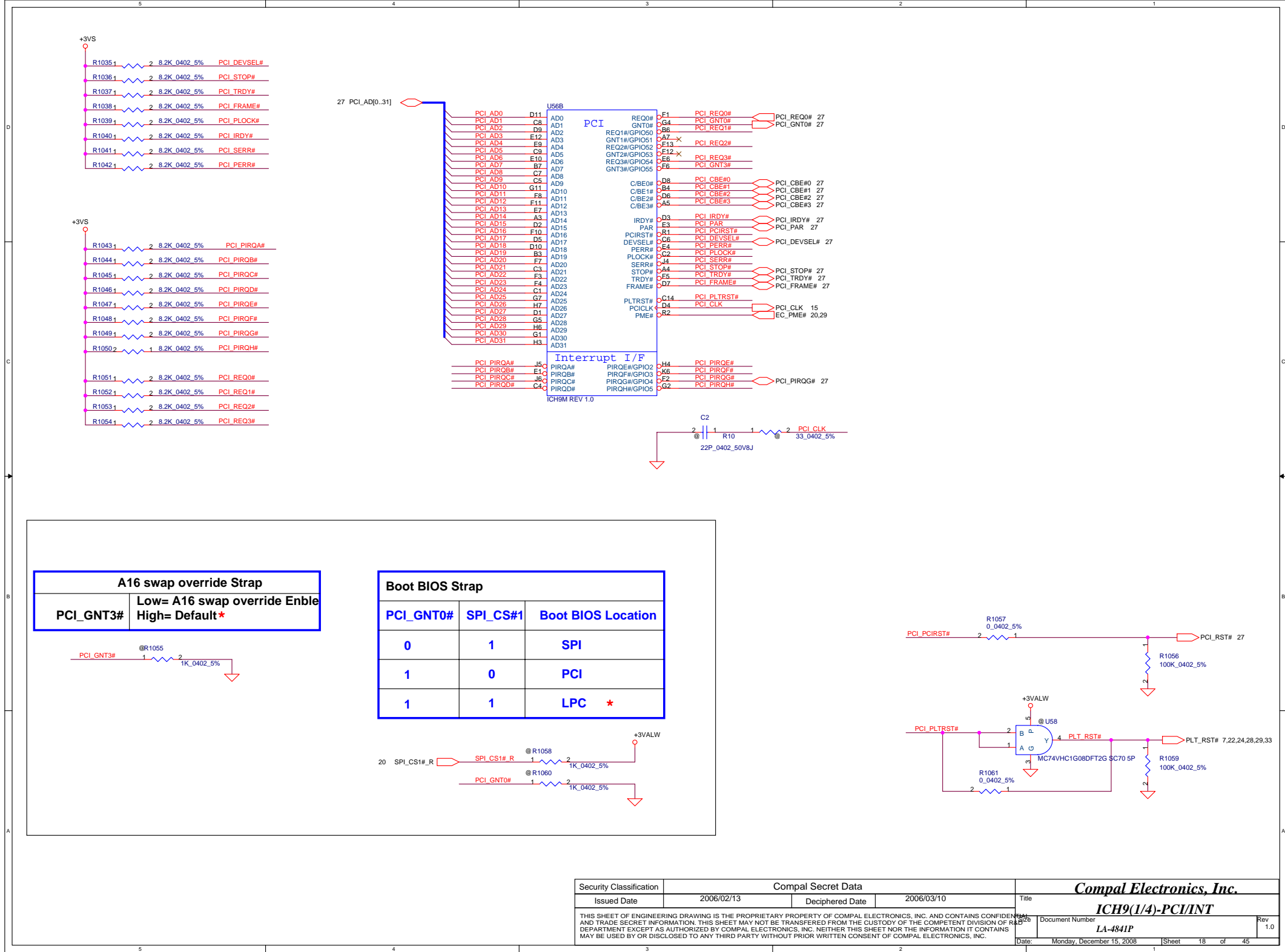


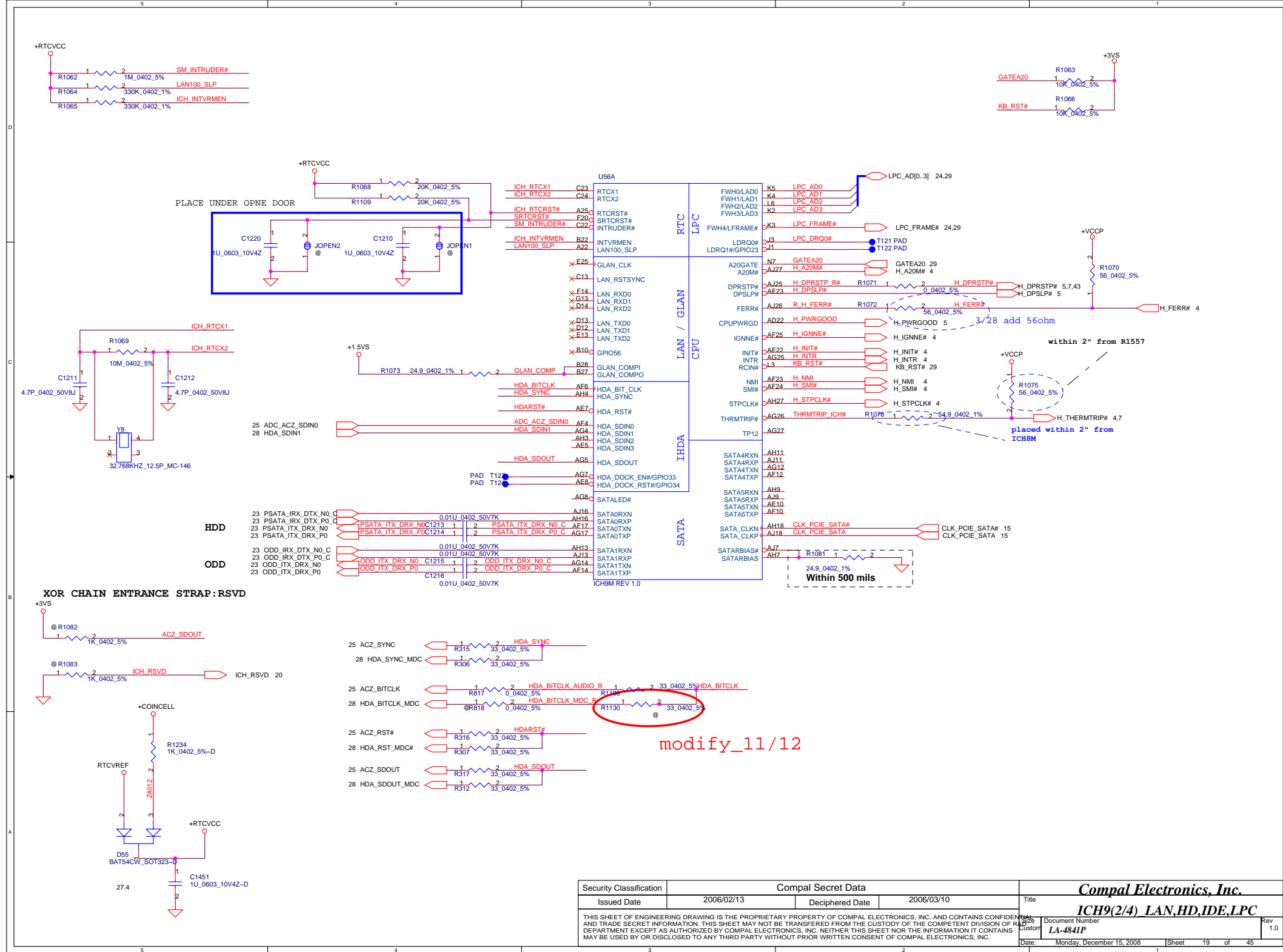


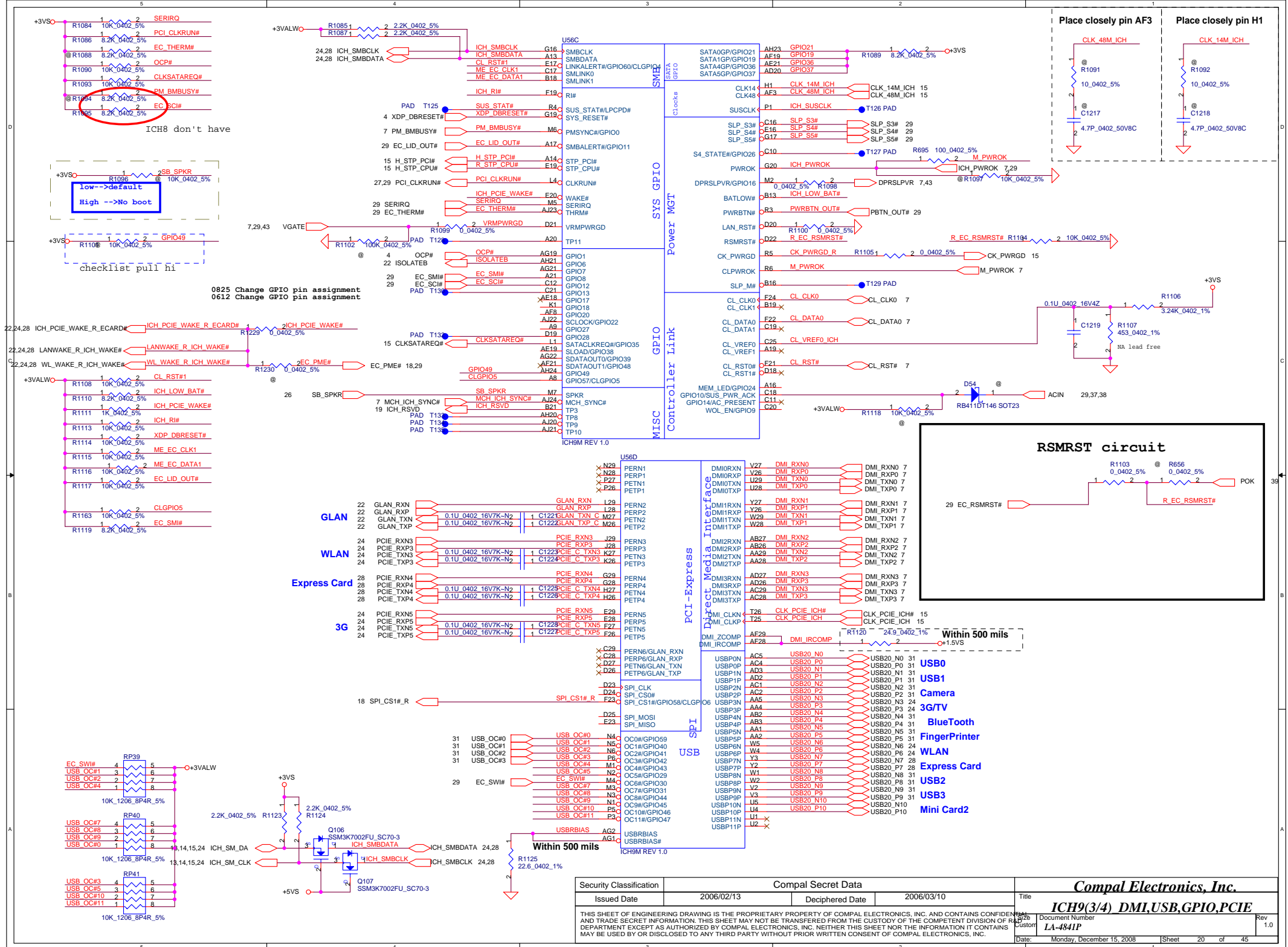
**CRT**

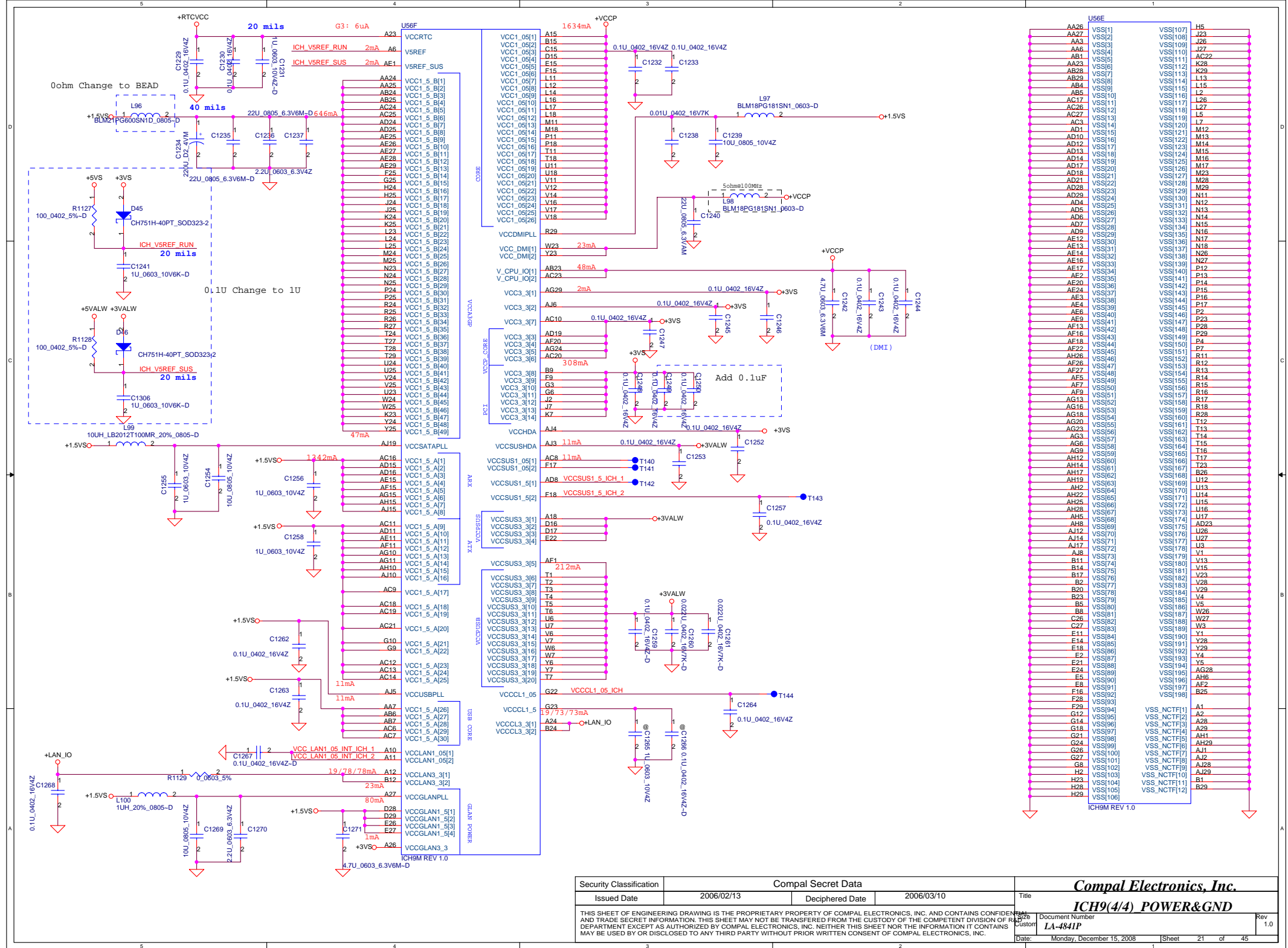


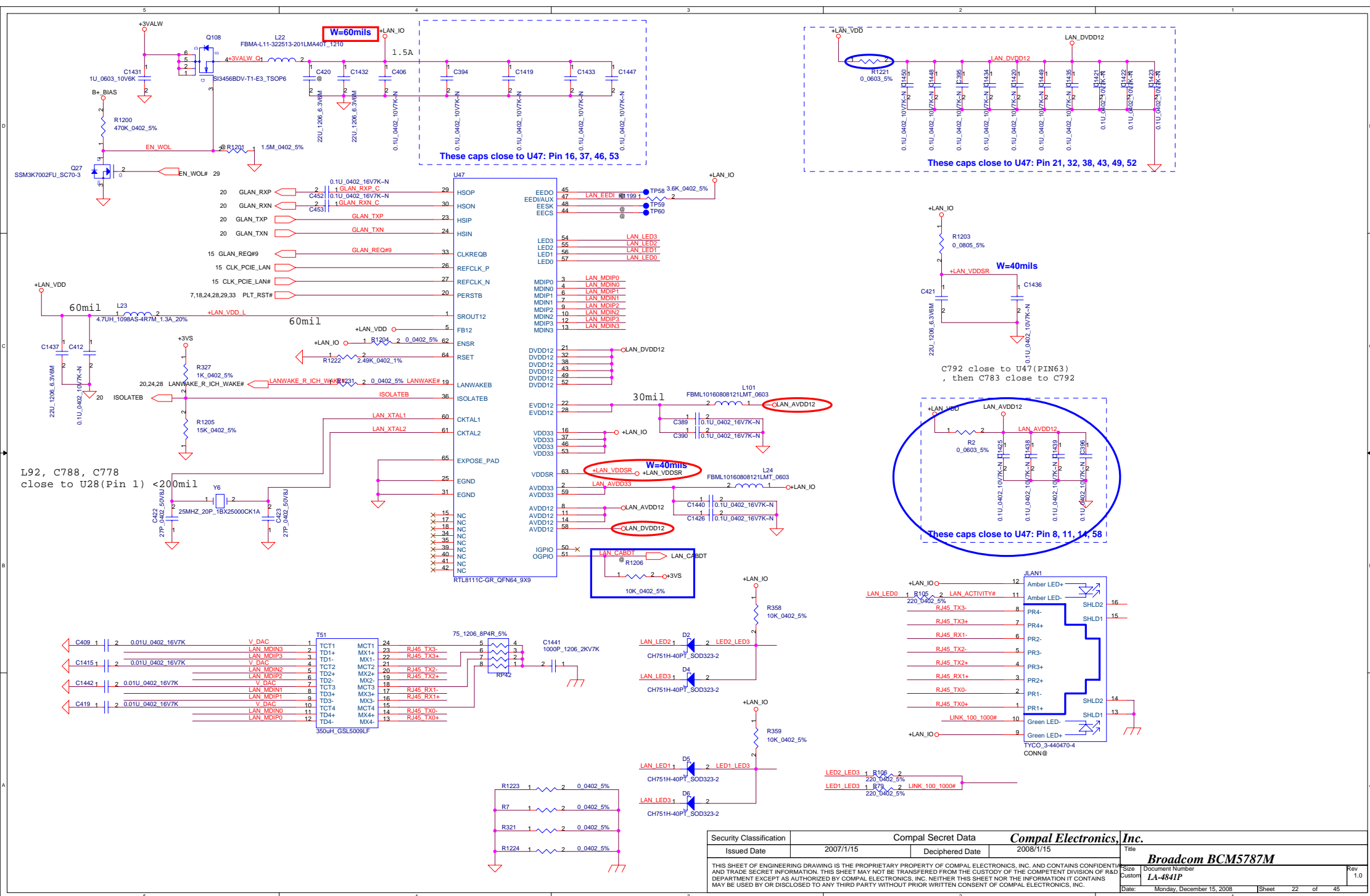
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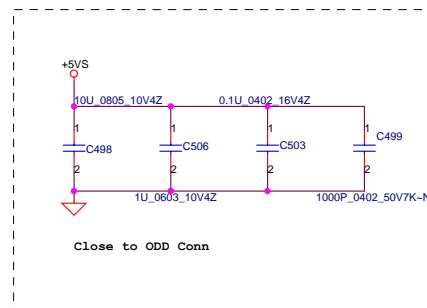
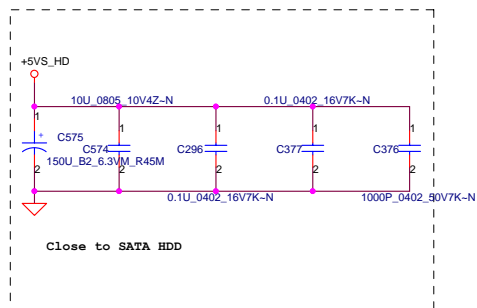
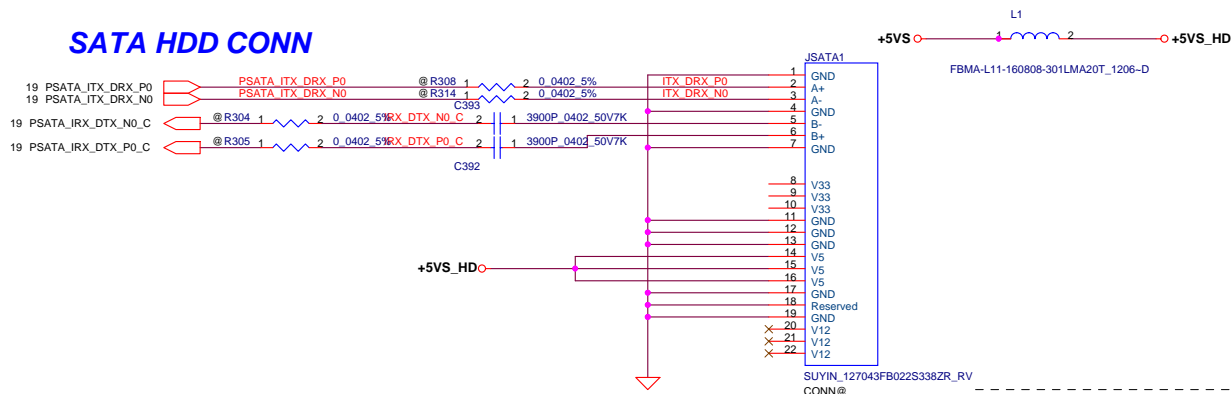




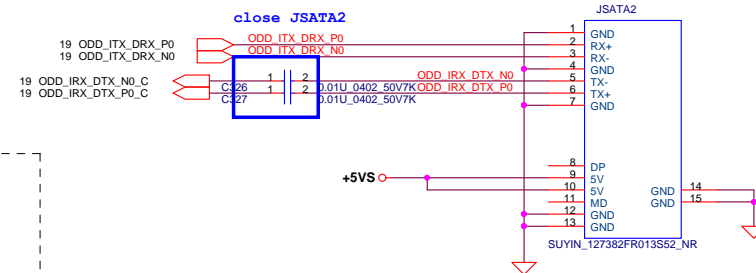




**SATA HDD CONN**



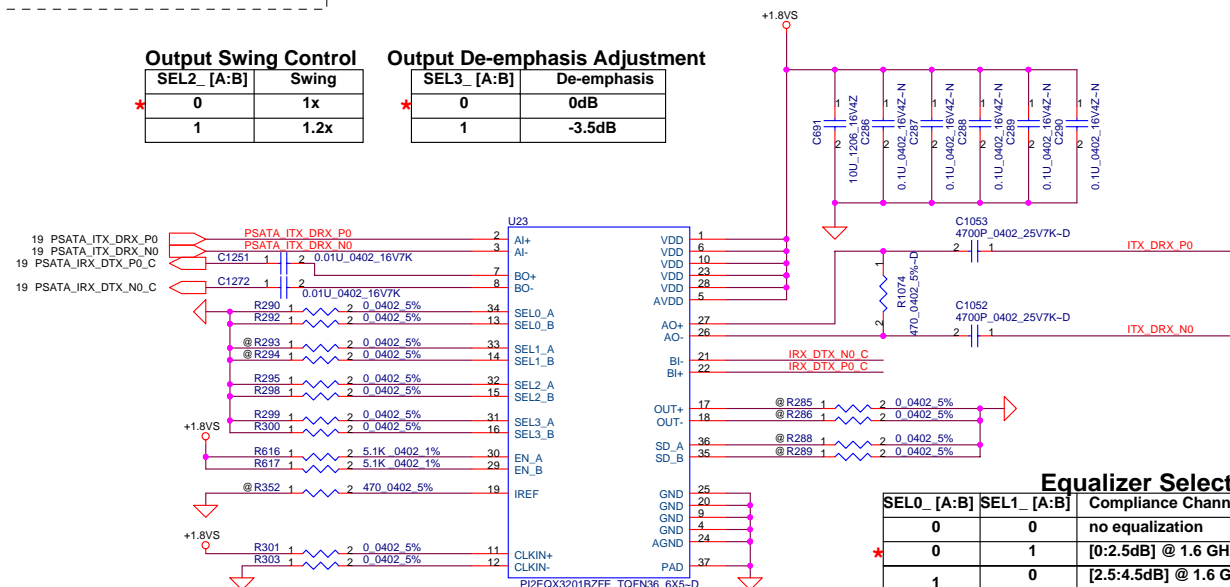
**SATA ODD CONN**



SEL2_ [A:B]	Swing
0	1x
1	1.2x

### Output De-emphasis Adjustment

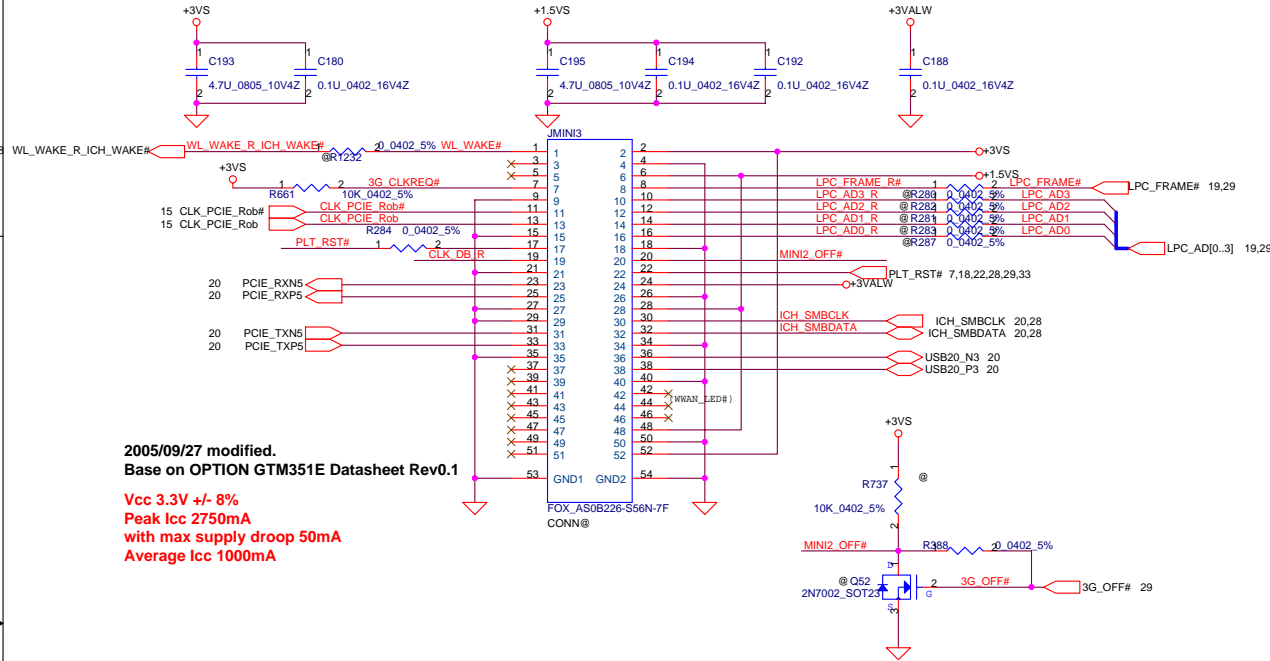
SEL3_ [A:B]	De-emphasis
0	0dB
1	-3.5dB



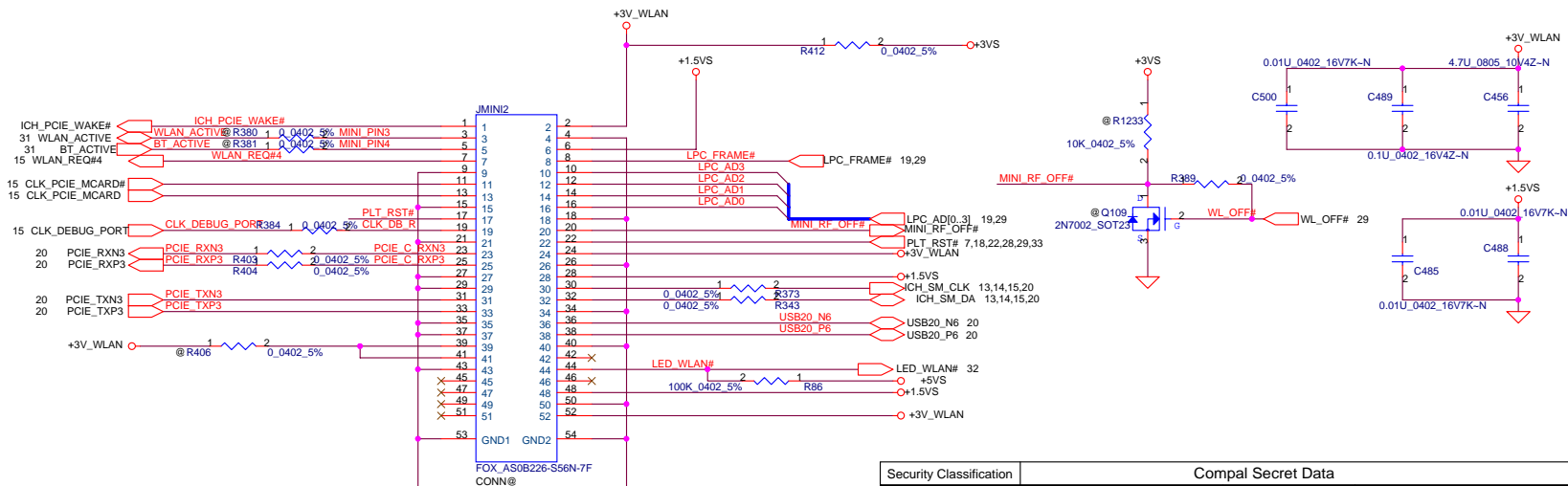
### Equalizer Selection

SEL0_ [A:B]	SEL1_ [A:B]	Compliance Channel
0	0	no equalization
0	1	[0:2.5dB] @ 1.6 GHz
1	0	[2.5:4.5dB] @ 1.6 GHz
1	1	[4.5:6.5dB] @ 1.6 GHz

# Mini-Express Card for 3G Or TV Tuner

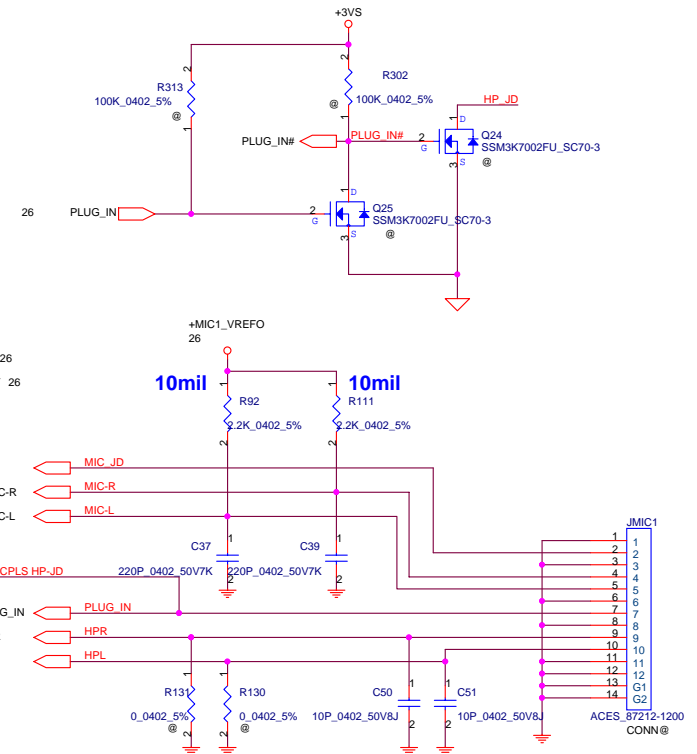
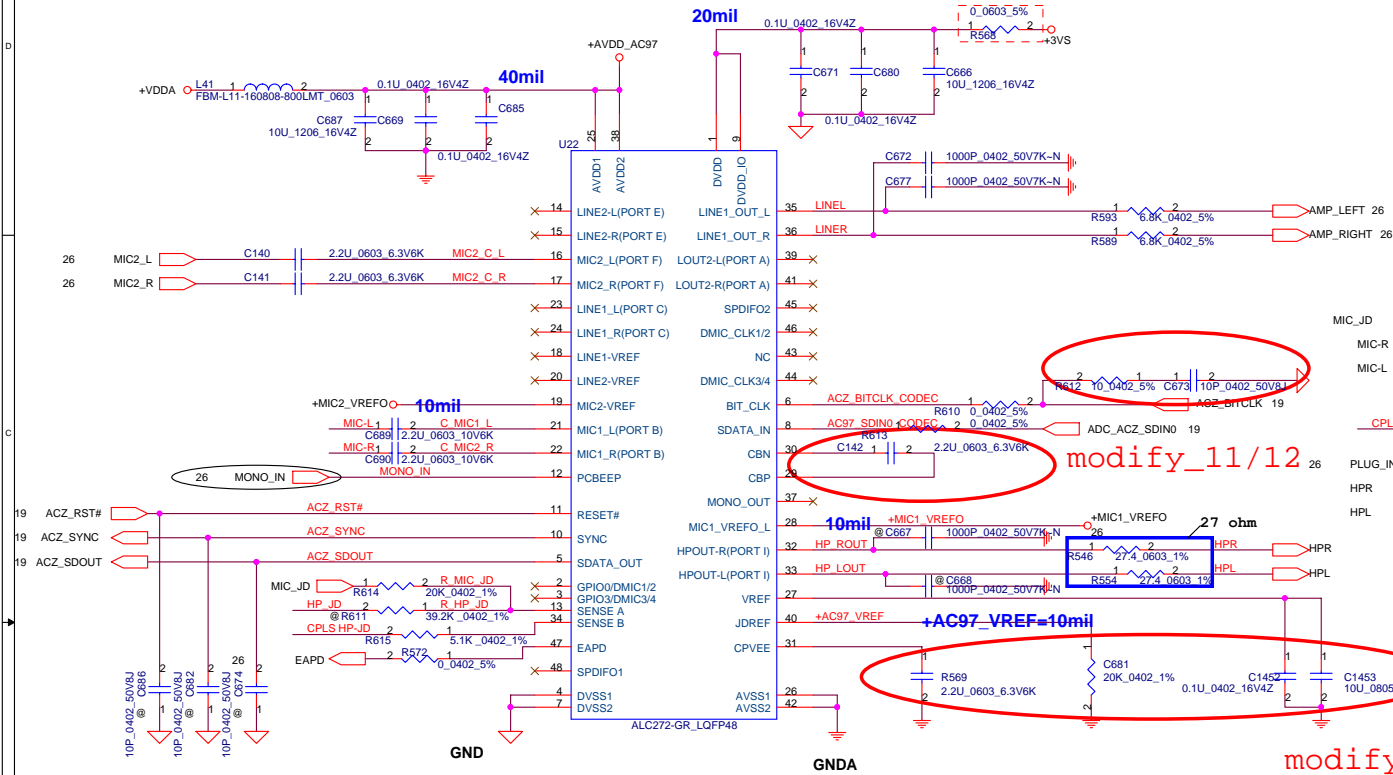


## Mini-Express Card---WLAN



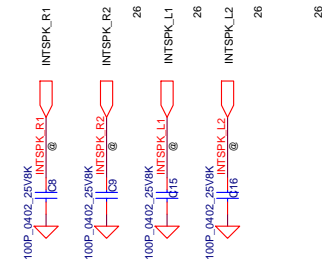
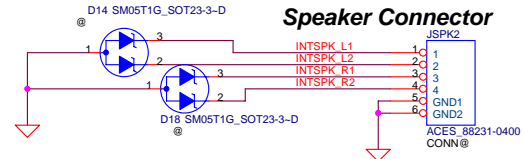
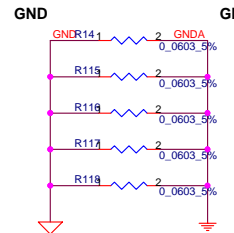
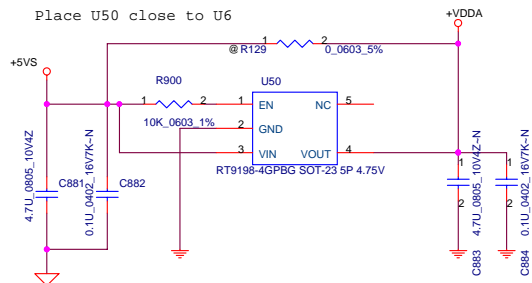
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				Mini-Card/LED			
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### *HD Audio Codec*

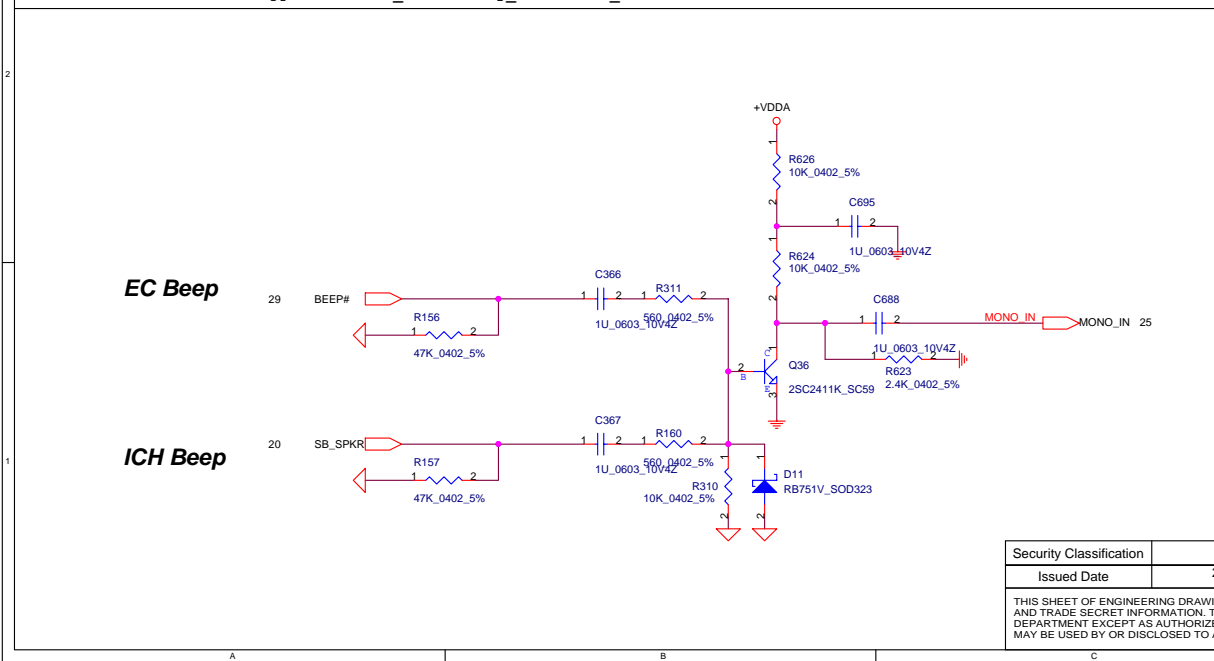
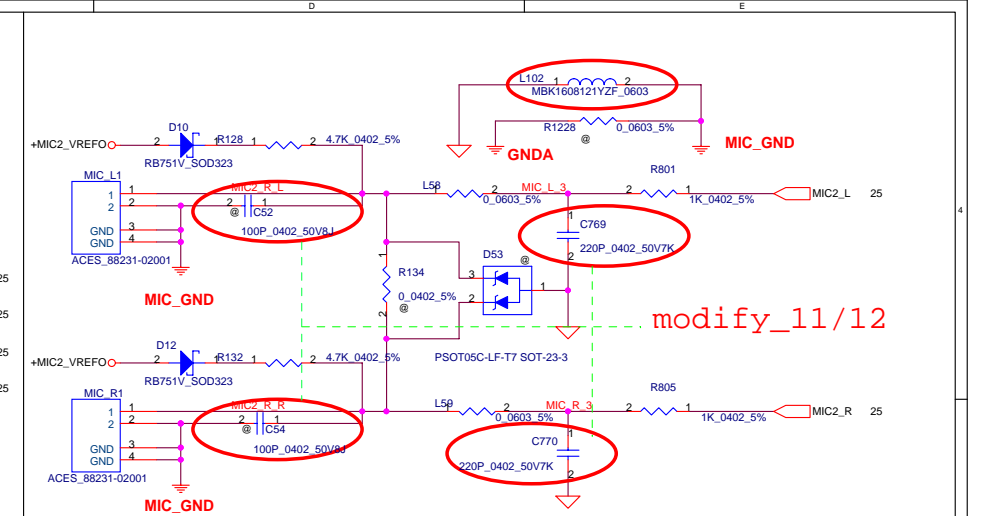
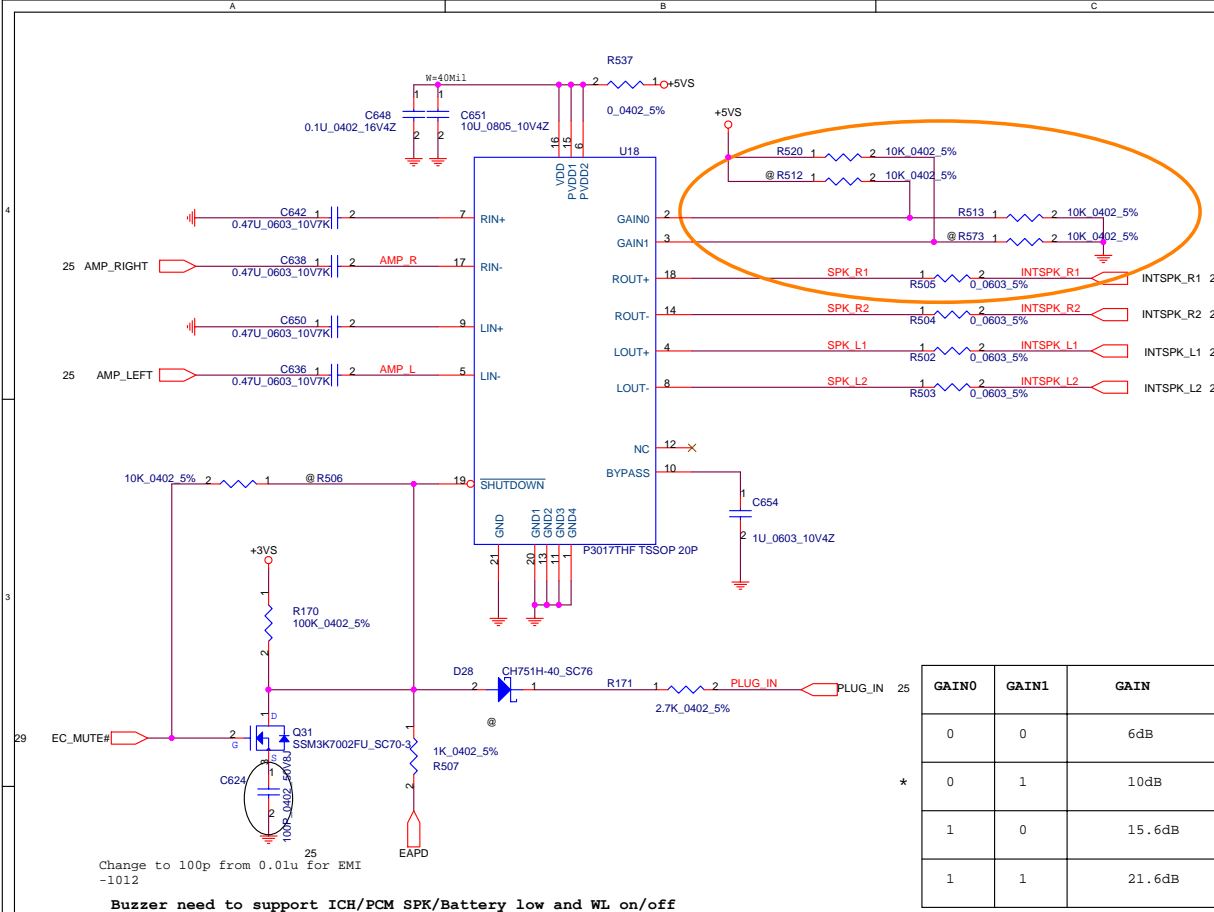


**12/13 Modified this symbol for pin 13 and 14.  
Do not re-copy this symbol for other use**

modify\_11/12

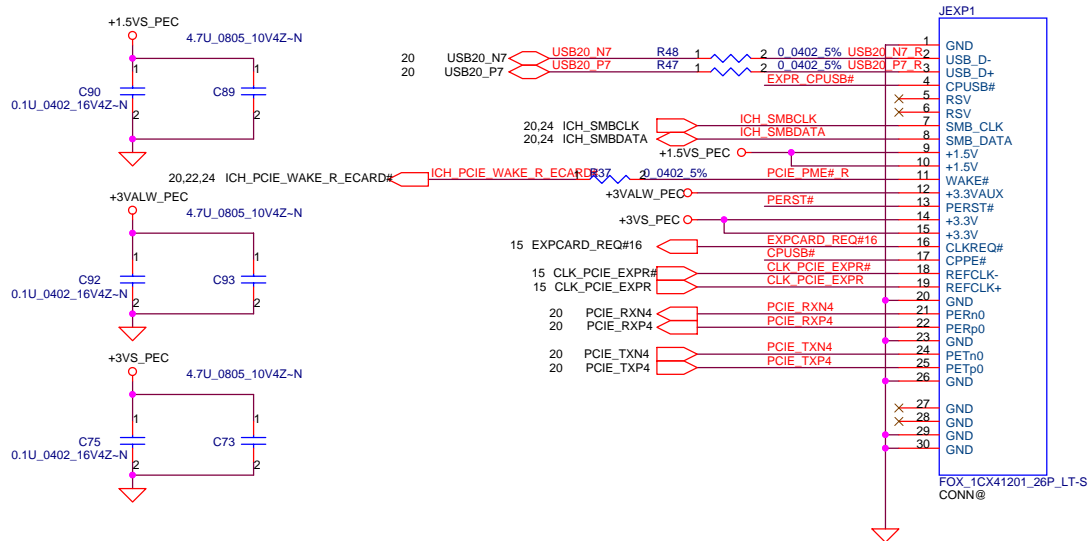
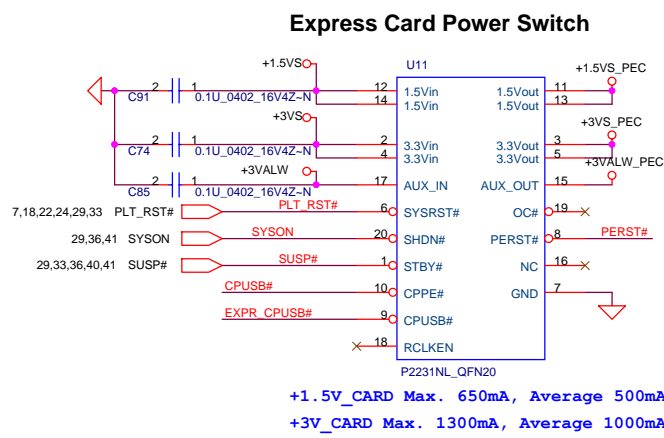


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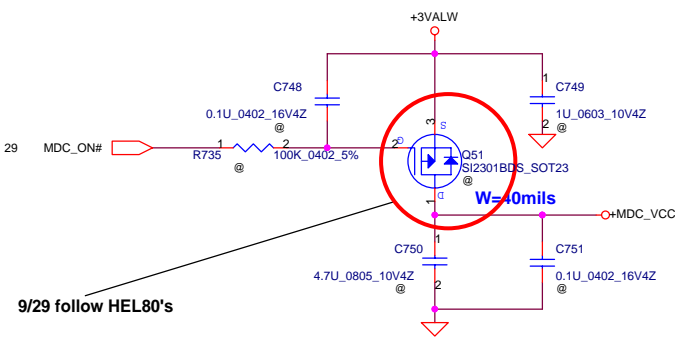
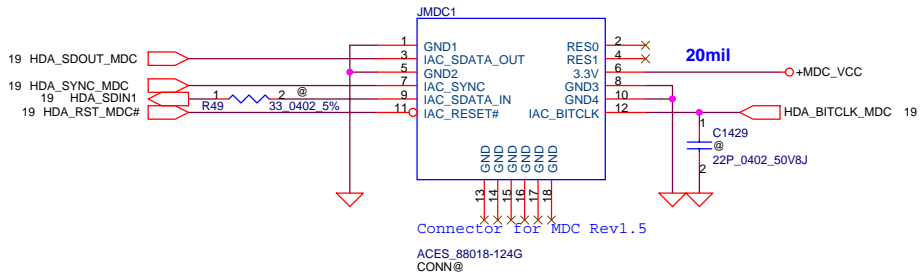




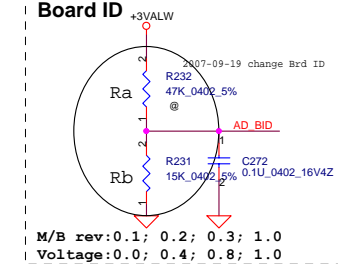
Express card



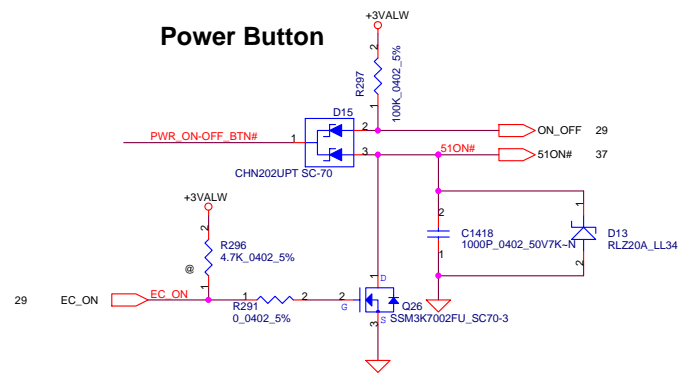
MDC Conn.



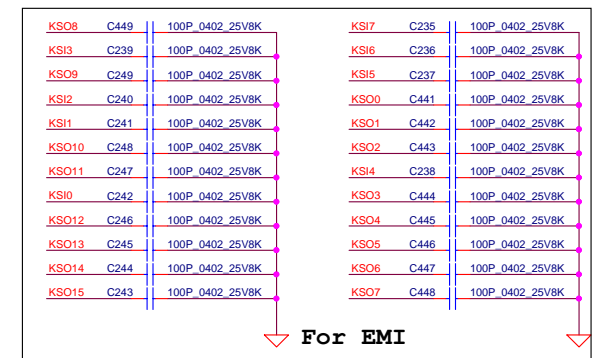
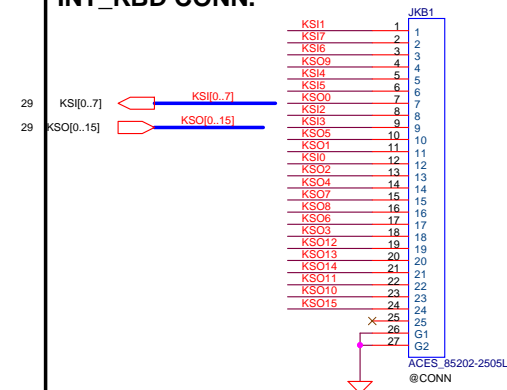
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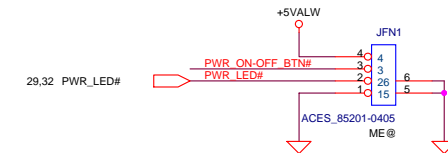
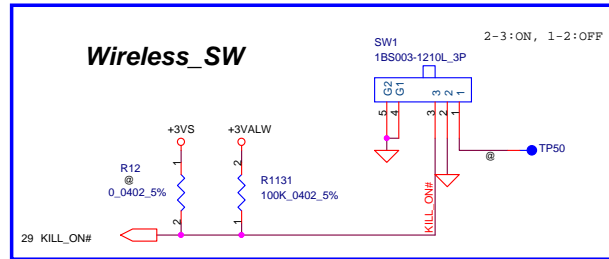




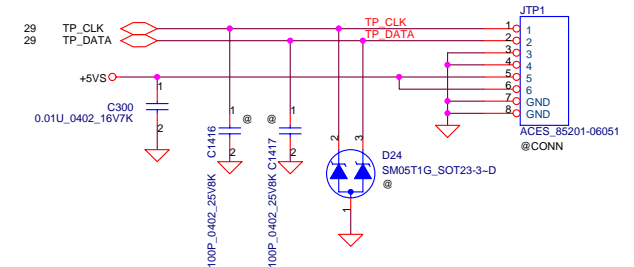
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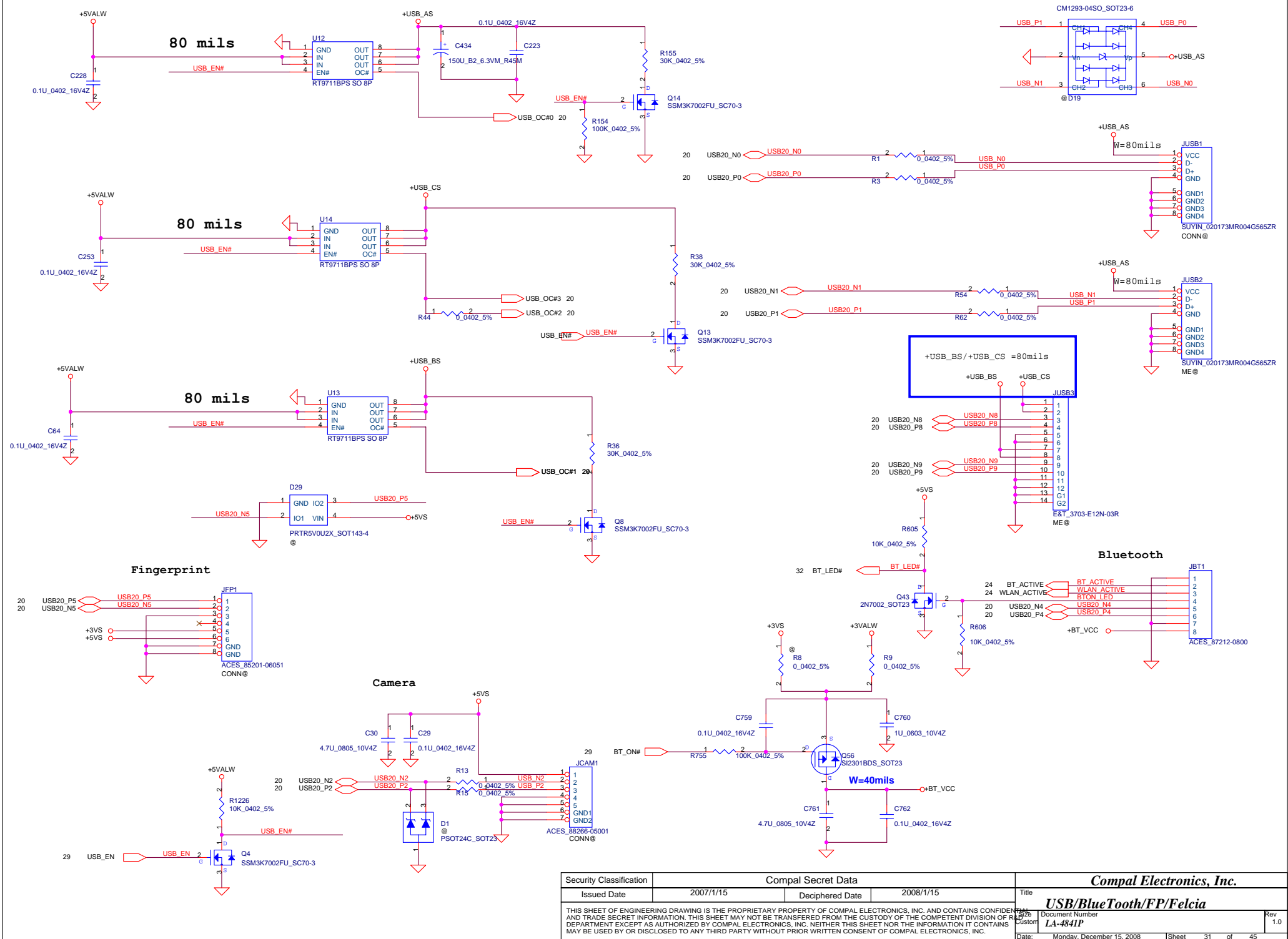
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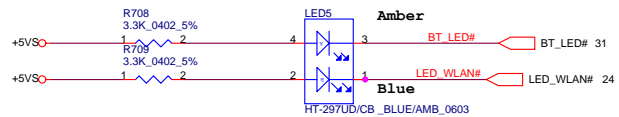
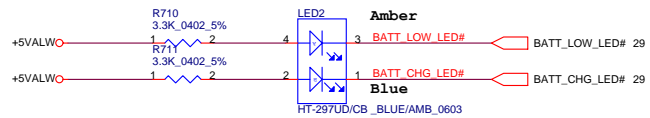
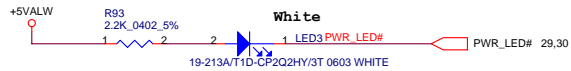


## Touch PAD/B CONN.

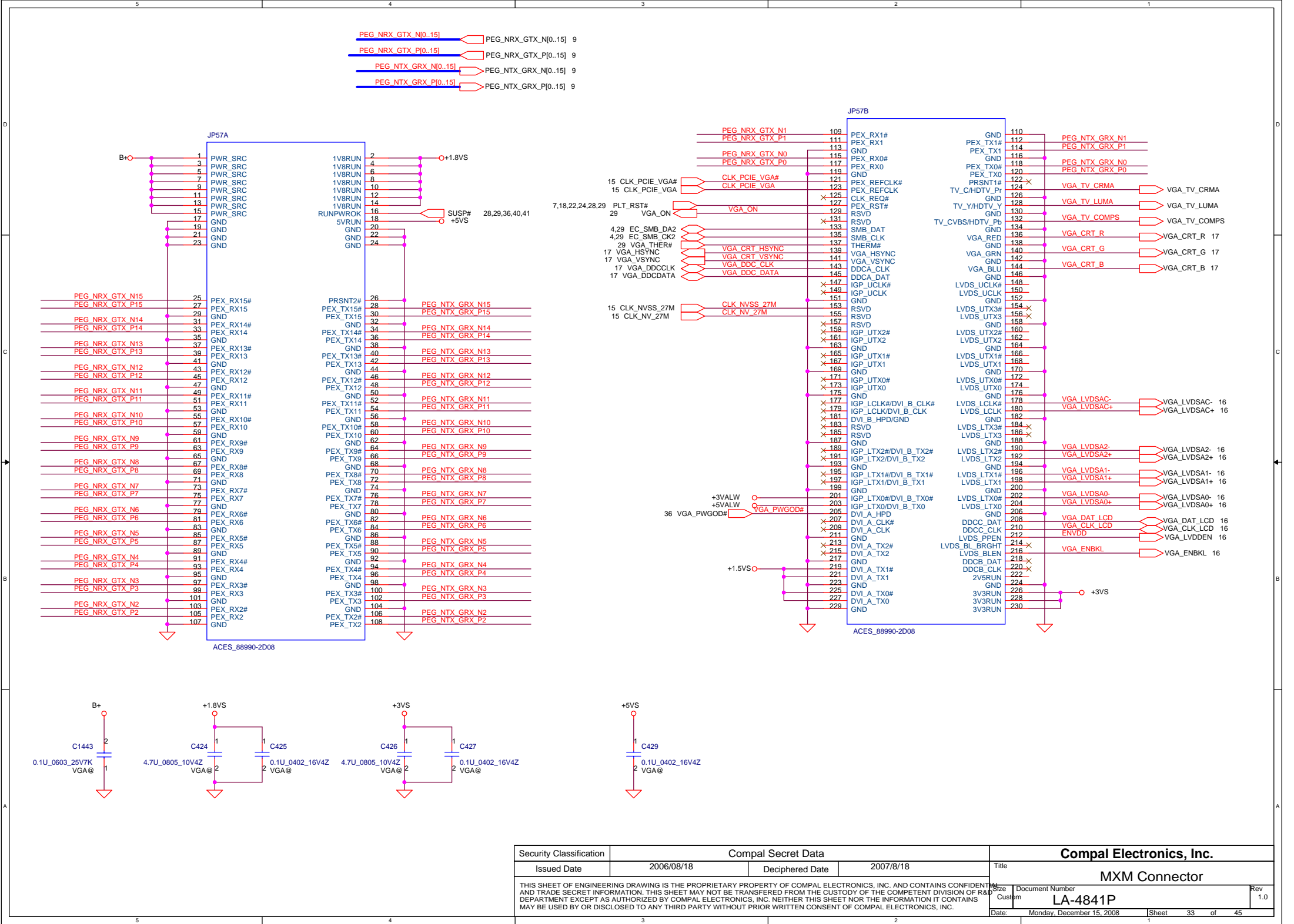


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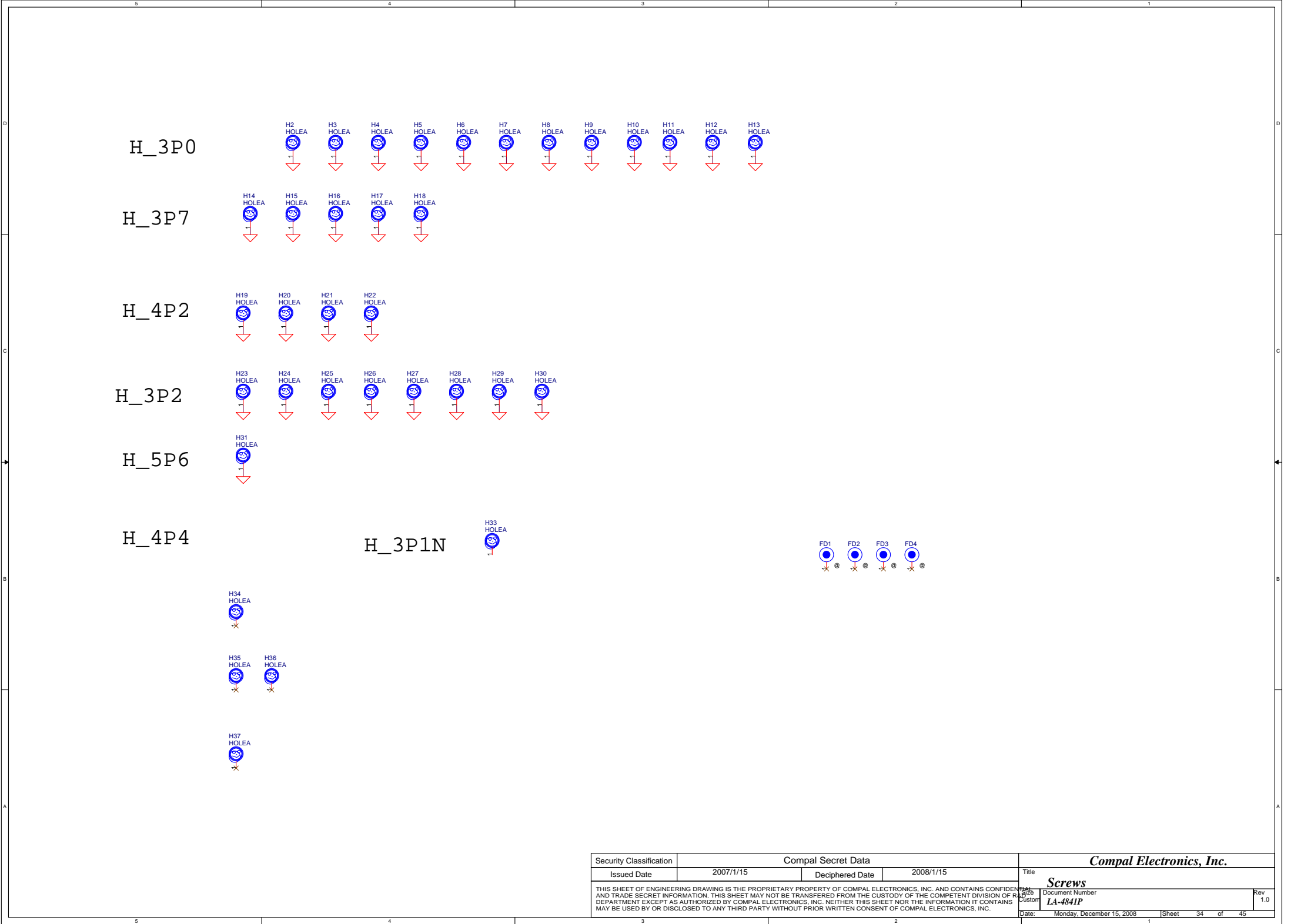




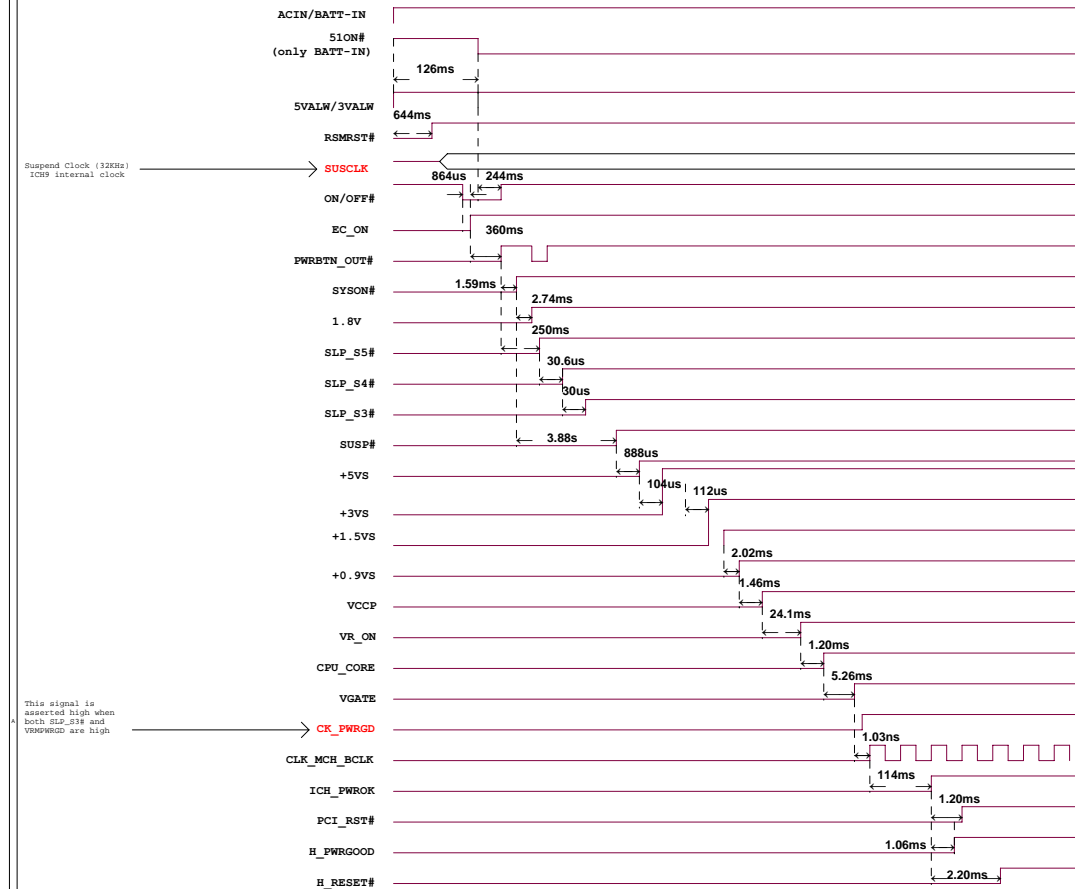
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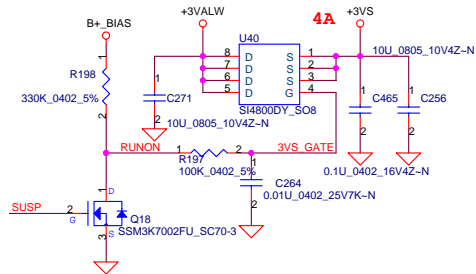
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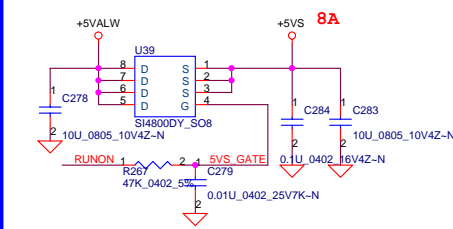
# KAL80 POWER UP SEQUENCE



## +3VALW to +3VS Transfer

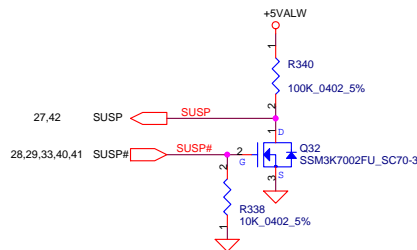
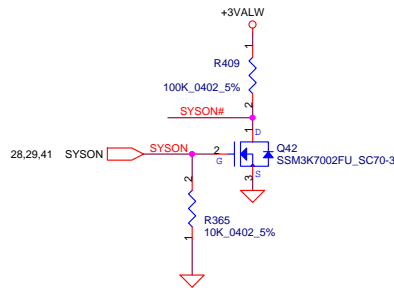
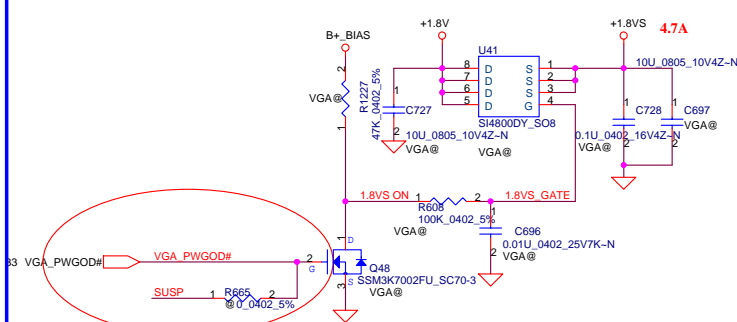


## +5VALW to +5VS Transfer

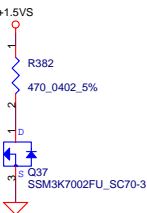
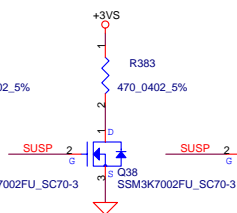
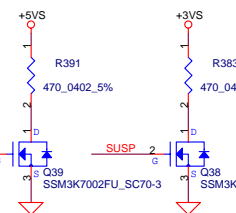
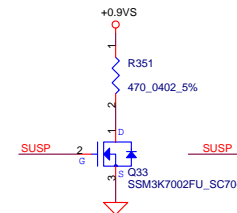
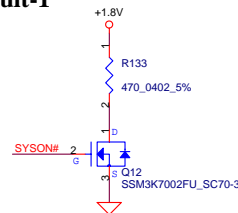


+CPU\_CORE — C211 — 0.1U\_0402\_16V4Z-N — +VCCP

## +1.8V to +1.8VS Transfer



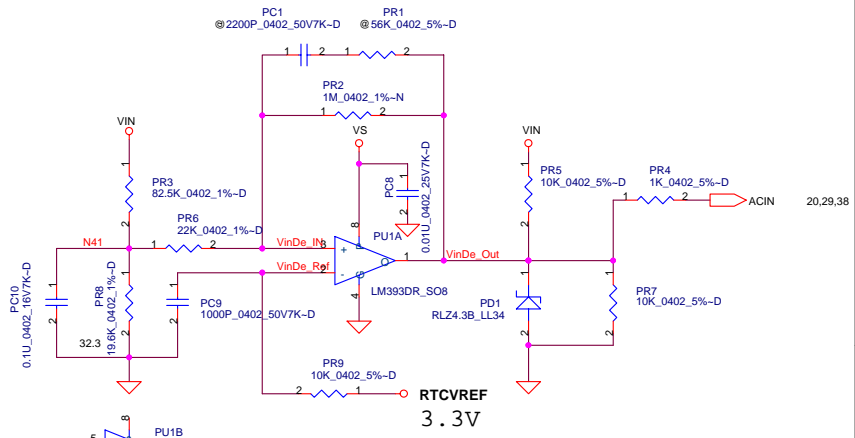
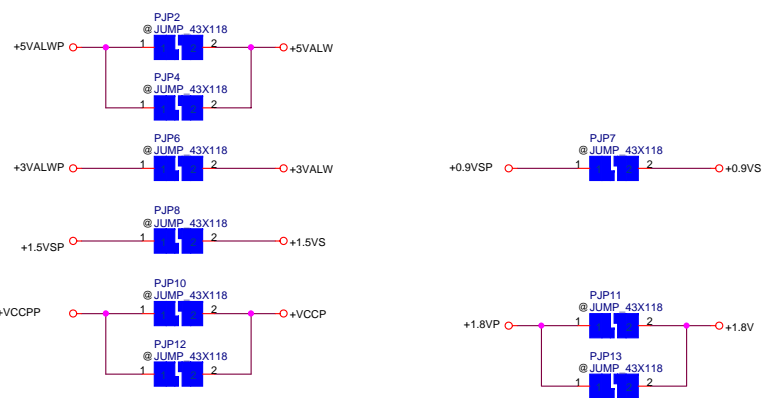
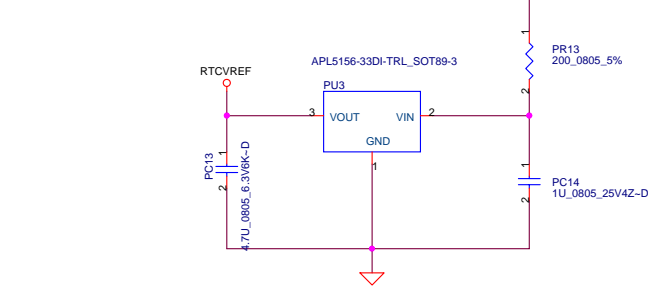
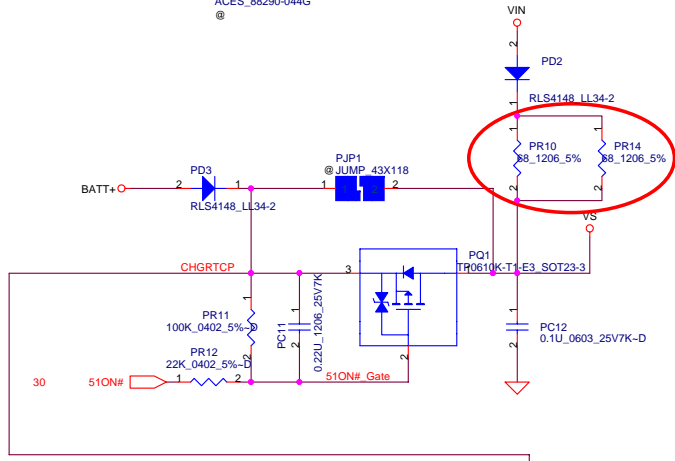
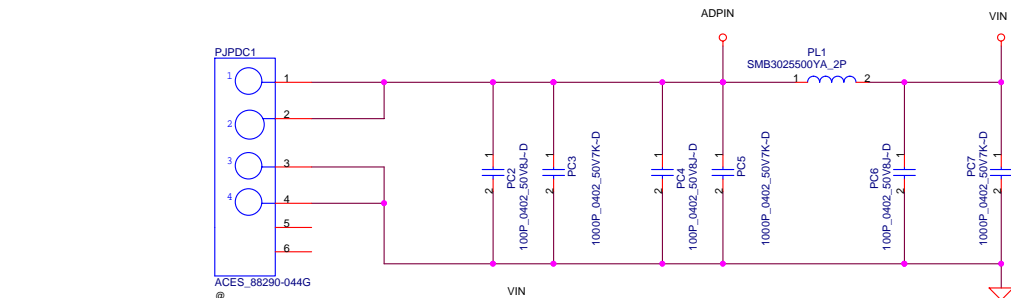
## Discharge circuit-1



SYSON -> SUSP# -> VGA\_ON->VGA\_PWGOD

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	Max.	typ.	Min.
L-->H	18.234	17.841	17.449
H-->L	17.597	17.210	16.813

90W adapter  
 $I_{charge} = (V_{srset}/V_{vdac}) * (0.1/PR222) = 3A$   
 $I_{adapter} = (V_{acset}/V_{vdac}) * (0.1/PR217) = 4.27A$   
 Input OVP : 22.3V  
 Input UVP : 16.98V  
 Fsw : 300KHz

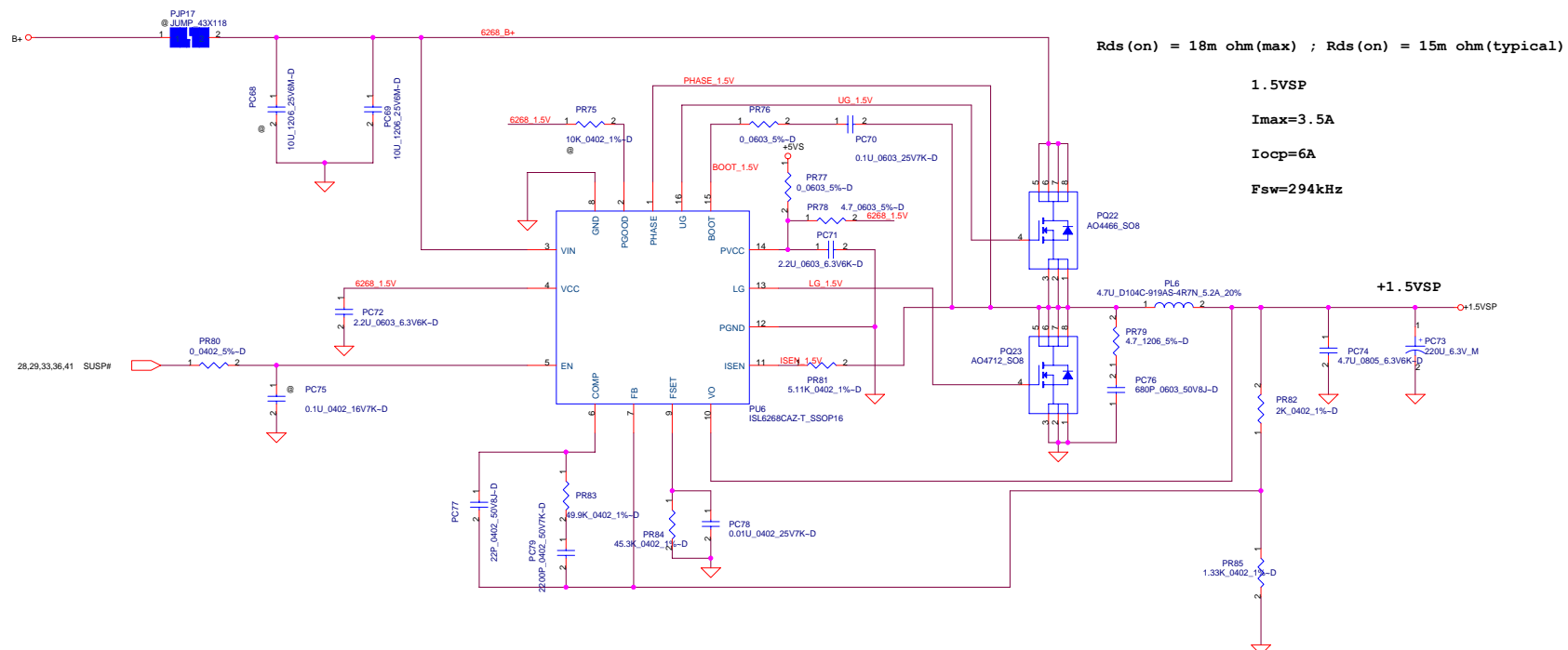
### COIN RTC Battery

CHGVADJ	Battery Voltage/per cell
0V	3V
3.3V	4.2V

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Charger		
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Rds(on) = 18m ohm(max) ; Rds(on) = 15m ohm(typical)

1.5VSP  
Imax=3.5A  
Iocp=6A  
Fsw=294kHz

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