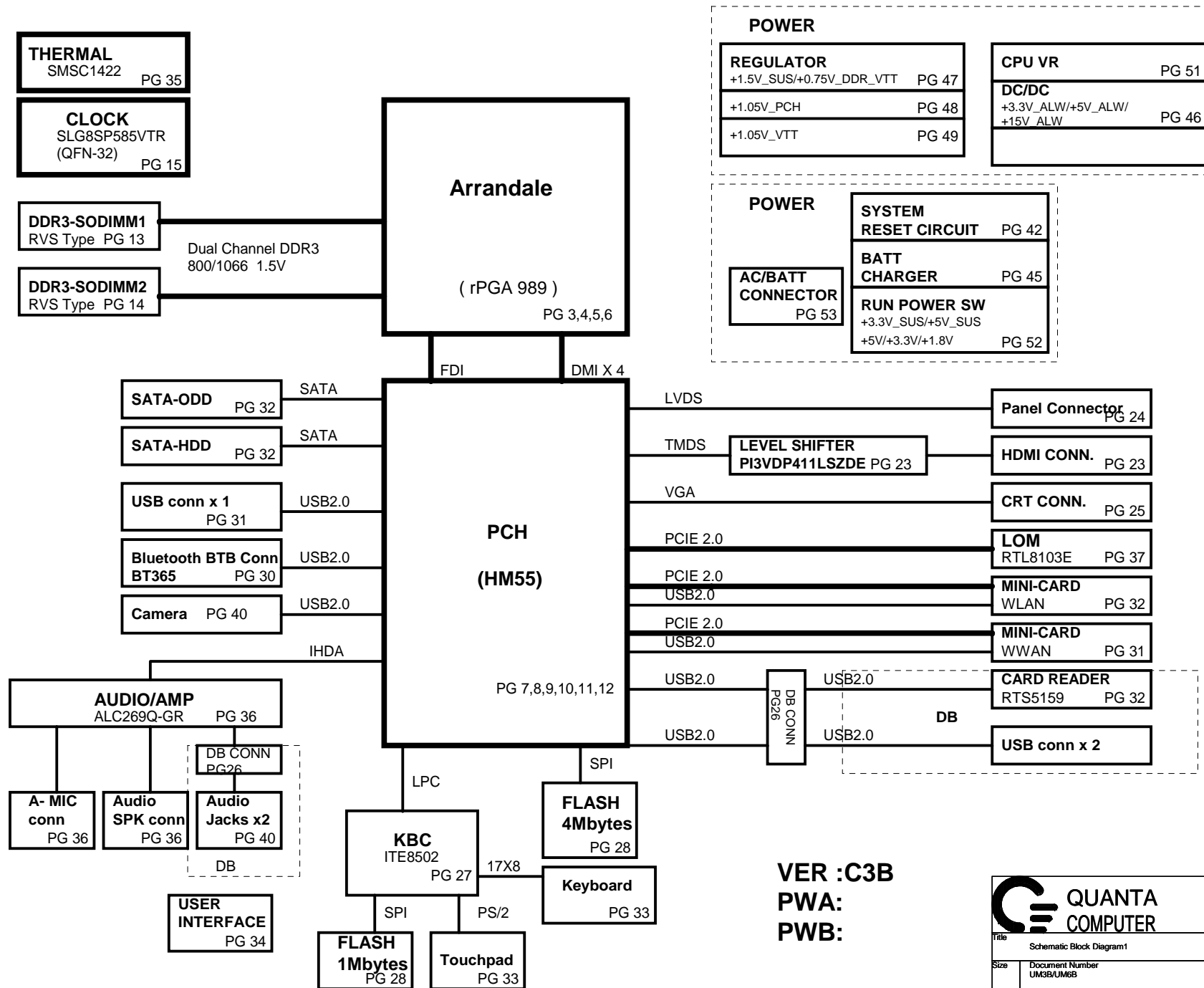


UM3B/UM6B SYSTEM BLOCK DIAGRAM



Title Schematic Block Diagram1

Size Document Number UM3B/UM6B Rev 1C


Date: Wednesday, September 30, 2009 Sheet 1 of 59

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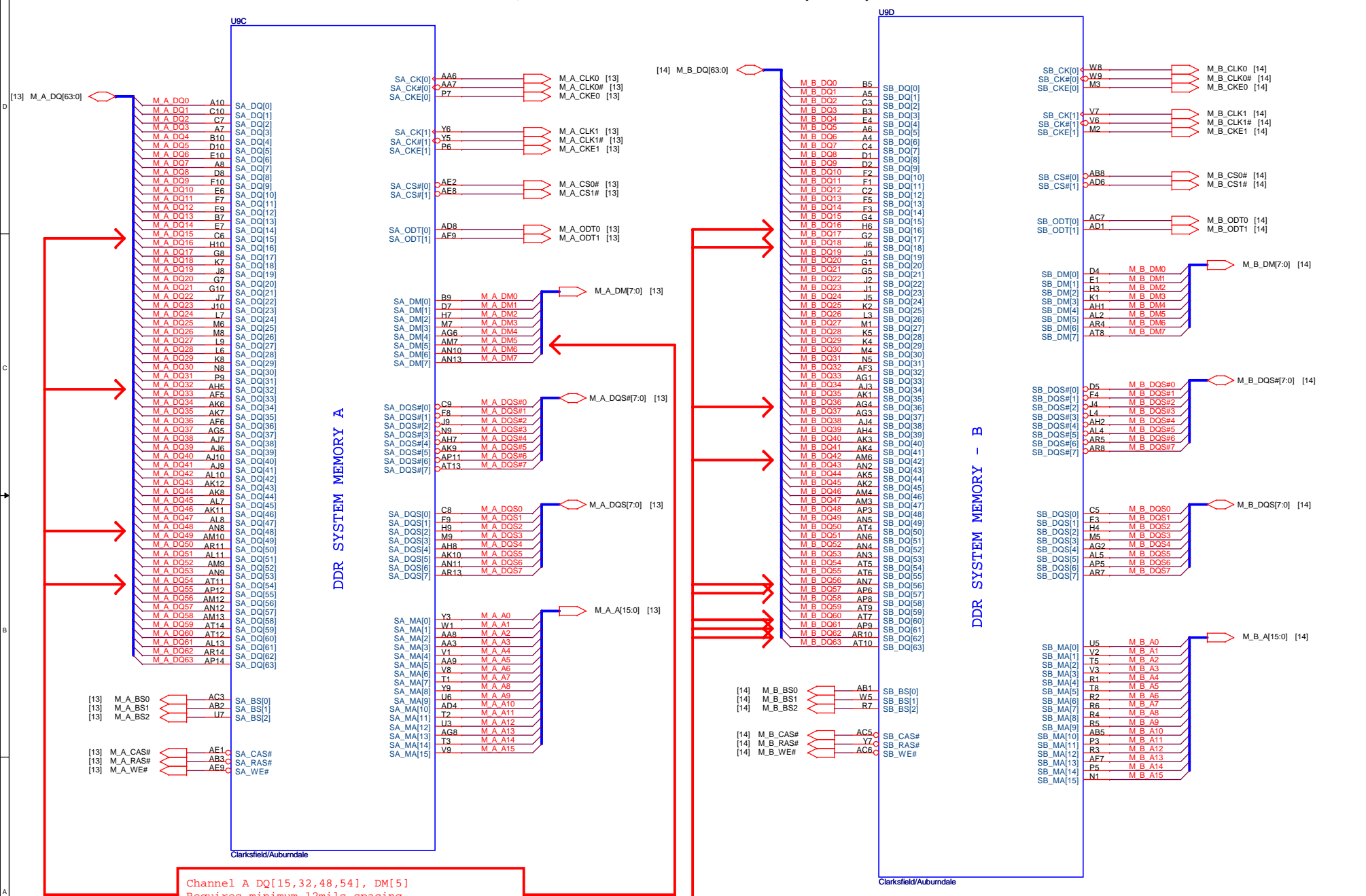
PAGE	DESCRIPTION
1	Schematic Block Diagram
2	Front Page
3-6	Clarksfield/Auburndale
7-12	PCH
13-14	DDRIII SO-DIMM(204P)
15	Clock Generator
16-22	BLANK PAGE
23	HDMI CONN
24	LCD CONN
25	CRT CONN
26	DB CONN
27	SIO (ITE8502)
28	FLASH / RTC
29	MINI-Card (WWAN)
30	MINI-Card (WLAN/WPAN)
31	USB
32	SATA (HDD & CD-ROM)
33	TP / KEYBOARD
34	PWR SWITCH / LED
35	FAN / THERMAL
36	CODEC ALC269
37	LAN(RTL8103M/RJ-45)
38	System Reset Circuit
39	BLANK PAGE
40	1.8V_RUN(RT9018/RT9024)
41	Charger (MAX8731)
42	3V/5V (TPS51427A)
43	1.5_DDR/0.75(TPS51116)
44	1.05V_PCH(TPS51218)
45	1.05_VTT(TPS51218)
46	GFX_VCORE (MAX17028)
47	CPU CORE(MAX17036)
48	Run Power Switch
49	DCin & Batt
50	PAD & SCREW
51	EMI CAP
52	SMBUS BLOCK
53	THERMAL MAP
54	Power Block Diagram
55	Power sequence Block
56	XDP
57	
58	
59	
60	

Power States

POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
+PWR_SRC	10V~+19V	24,30,45,46,47,48,49,50,51	MAIN POWER		S0~S5
+RTC_CELL	+3.0V~+3.3V	08,11,29,30	RTC		S0~S5
+5V_ALW2	+5V	37,46,52,53	LARGE POWER	MAIN POWER	S0~S5
+5V_ALW	+5V	13,33,44,46,47,48,49,50,51,52	LARGE POWER	ALW_ON	S0~S5
+3.3V_ALW	+3.3V	29,30,35,36,37,42,44,45,46,47,51,52,53	8051 POWER	3.3V_ALW_ON	S0~S5
+5V_SUS	+5V	11,33,34,37,51,52	SLP_S5# CTRLD POWER	SUS_ON	
+3.3V_SUS	+3.3V	07,08,09,10,11,13,14,19,24,28,29,37,41,42,44,48,49,50,52	SLP_S5# CTRLD POWER	SUS_ON	
+1.5V_SUS	+1.5V	03,05,13,14,47,50,52	SODIMM POWER	SUS_ON	
+0.75V_DDR_VTT	+0.75V	13,14,47,52	SODIMM POWER	RUN_ON	
+5V_RUN	+5V	11,18,24,25,35,36,38,39,40,51,52	SLP_S3# CTRLD POWER	RUN_ON	
+3.3V_RUN	+3.3V	3,7,8,9,10,11,13,14,15,17,24,25,26,28,29,30,31,32,33,35,37,38,39,40,41,42,46,51,52,60	SLP_S3# CTRLD POWER	RUN_ON	
+1.8V_RUN	+1.8V	05,11,44,52	SDVO POWER	RUN_ON	
+1.05V_VTT	+1.1V	03,05,10,11,49,60	CPU POWER	RUN_ON	
+1.5V_RUN	+1.5V	11,28,31,32,52	Express Card/Min Card	RUN_ON	
+5V_HDD	+5V	35	HDD Power	HDDC_EN	
+1.05V_PCH	+1.05V	08,09,11,15,48	PCH POWER	RUN_ON	
+VCC_CORE	+0.7V~+1.77V	05,51	CPU CORE POWER	IMVP_VR_ON	
+LCDVCC	+3.3V	24	LCD Power	LCDVCC_TST_EN & ENVDD	
+5V_MOD	+5V	35	MOD Power	MODC_EN	

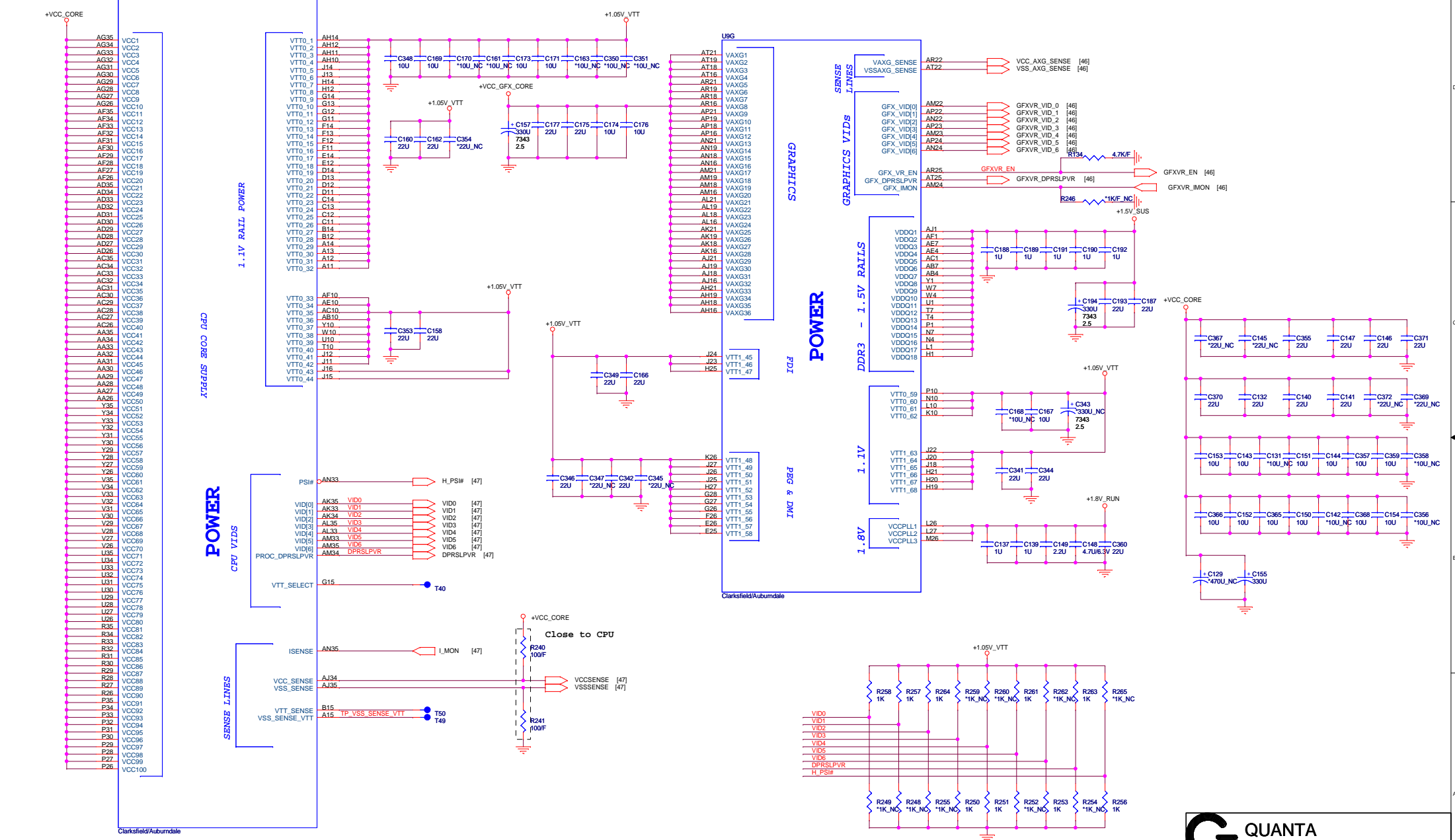
GND PLANE	PAGE	DESCRIPTION
 GND	ALL	

AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)



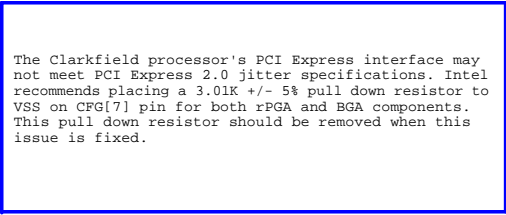
QUANTA
COMPUTER

Title: AUBURND 2/4		
Size: UM3B/UM6B	Document Number: UM3B/UM6B	Rev: 1A
Date: Friday, October 02, 2009	Sheet: 4	of: 59

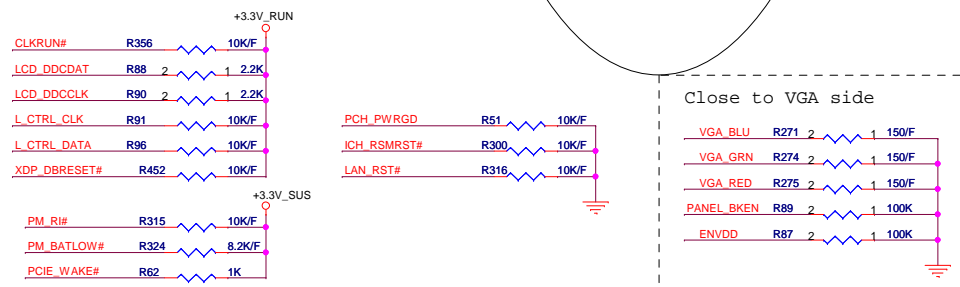
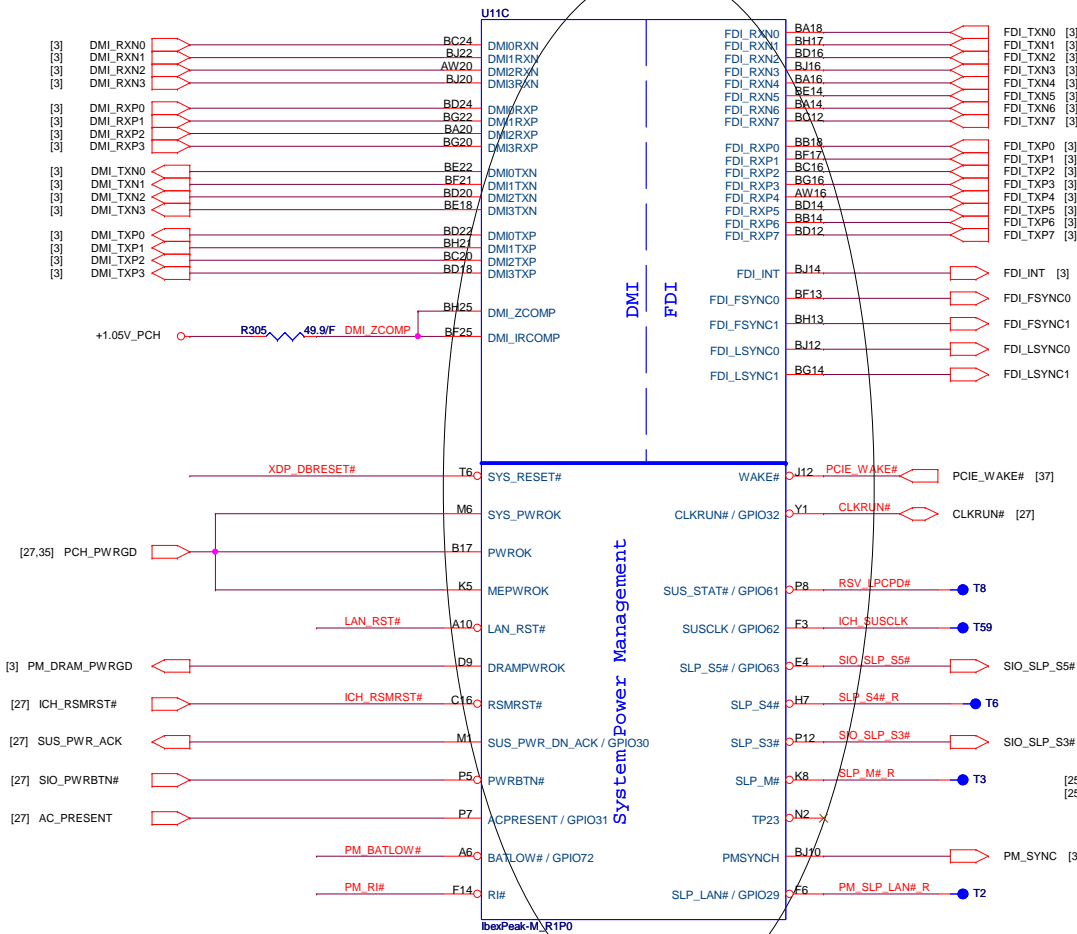


AUBURNDALE/CLARKSFIELD PROCESSOR (POWER)

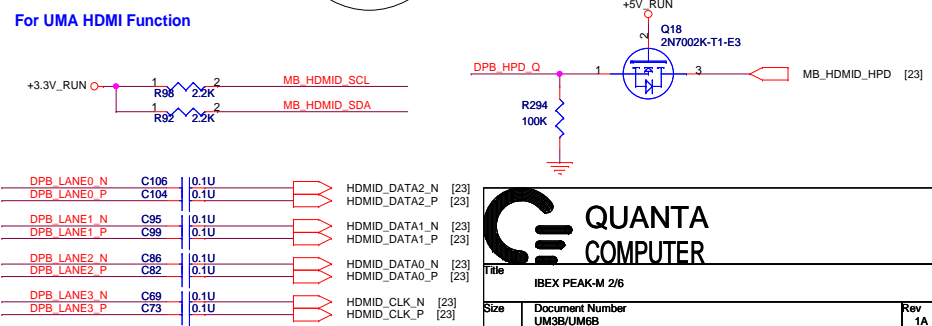
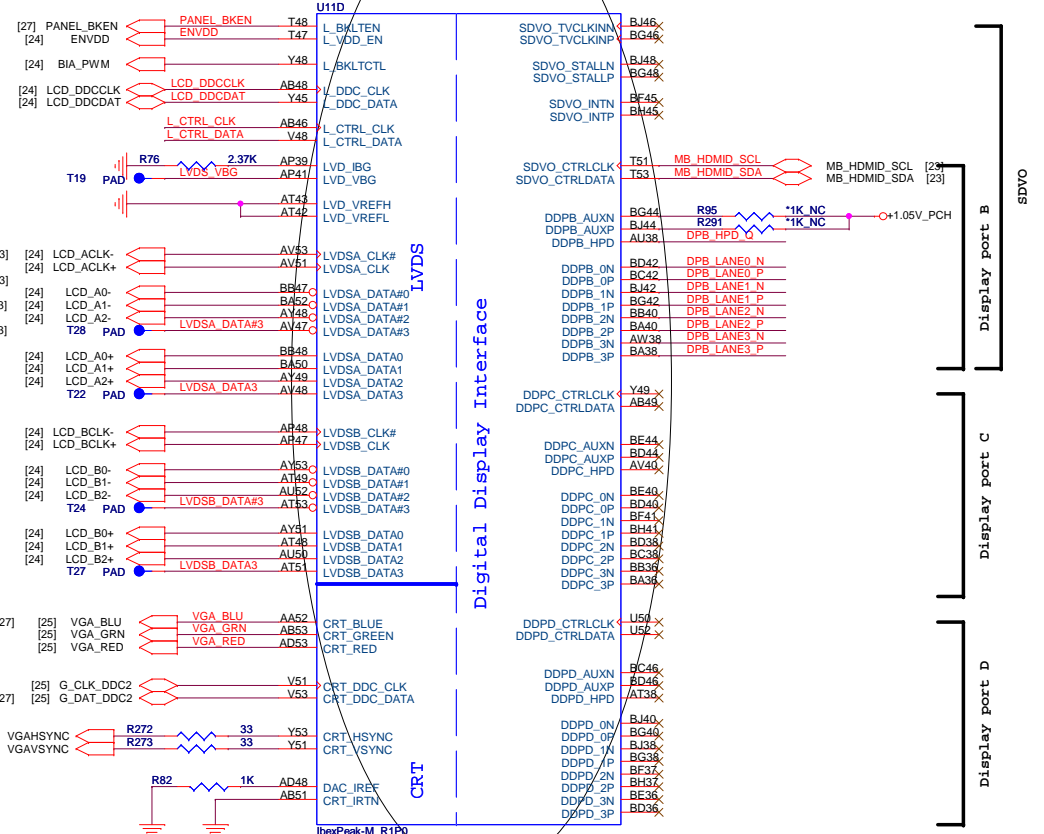
AUBURNDALE/CLARKSFIELD PROCESSOR(RESERVED, CFG)



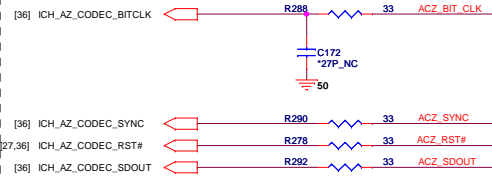
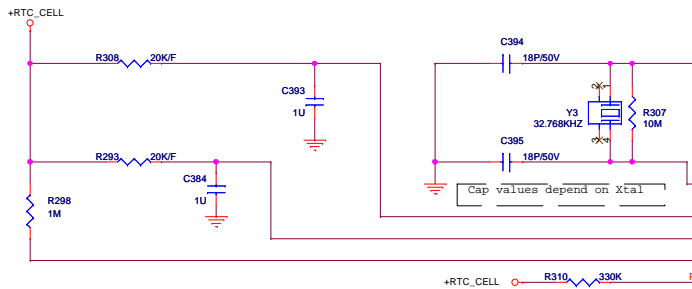
IBEX PEAK-M (DMI,FDI,GPIO)



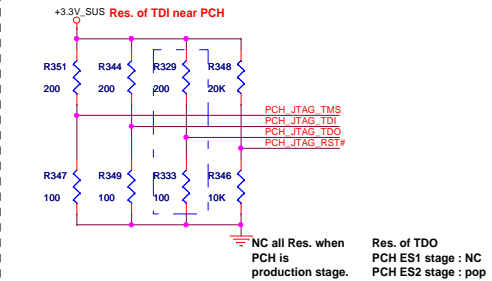
~~IBEX PEAK-M (LVDS,DDI)~~



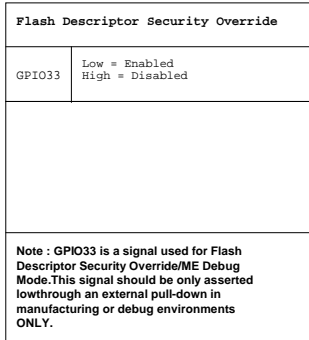
IBEX PEAK-M (HDA,JTAG,SATA)



Place all series terms close to PCH except for SDIN input lines, which should be close to source. Placement of R773, R775, R776 & R777 should equal distance to the T split trace point. Basically, keep the same distance from T for all series termination resistors.

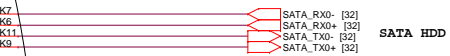
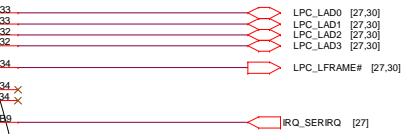
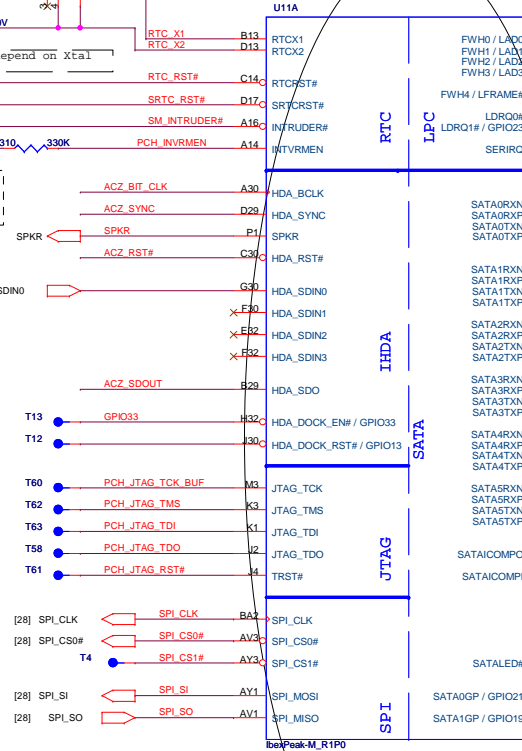


INVRMEN[Internal Voltage Regulator Enable] : This signal enables the internal 1.05 V regulators. This signal must be always pulled-up to VccRTC.



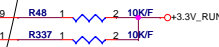
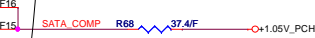
Note : Only pop when PCH is production stage & need "JTAG boundary Scan". Remember to depop XDP side Res.

JTAG Test Pads are need to put on the same side of mother board.

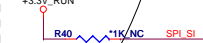


SATA port 2/3 are not support in HM55 . They are only in PM 55

Distance between the PCH and cap on the "P" signal should be identical distance between the PCH and cap on the "N" signal for the same pair.



ITPM ENABLE/DISABLE



TPM Function	
Enable	Mount
Disable	NC (Default)



IBEX PEAK-M 1/6

Size Document Number UM38/UM68

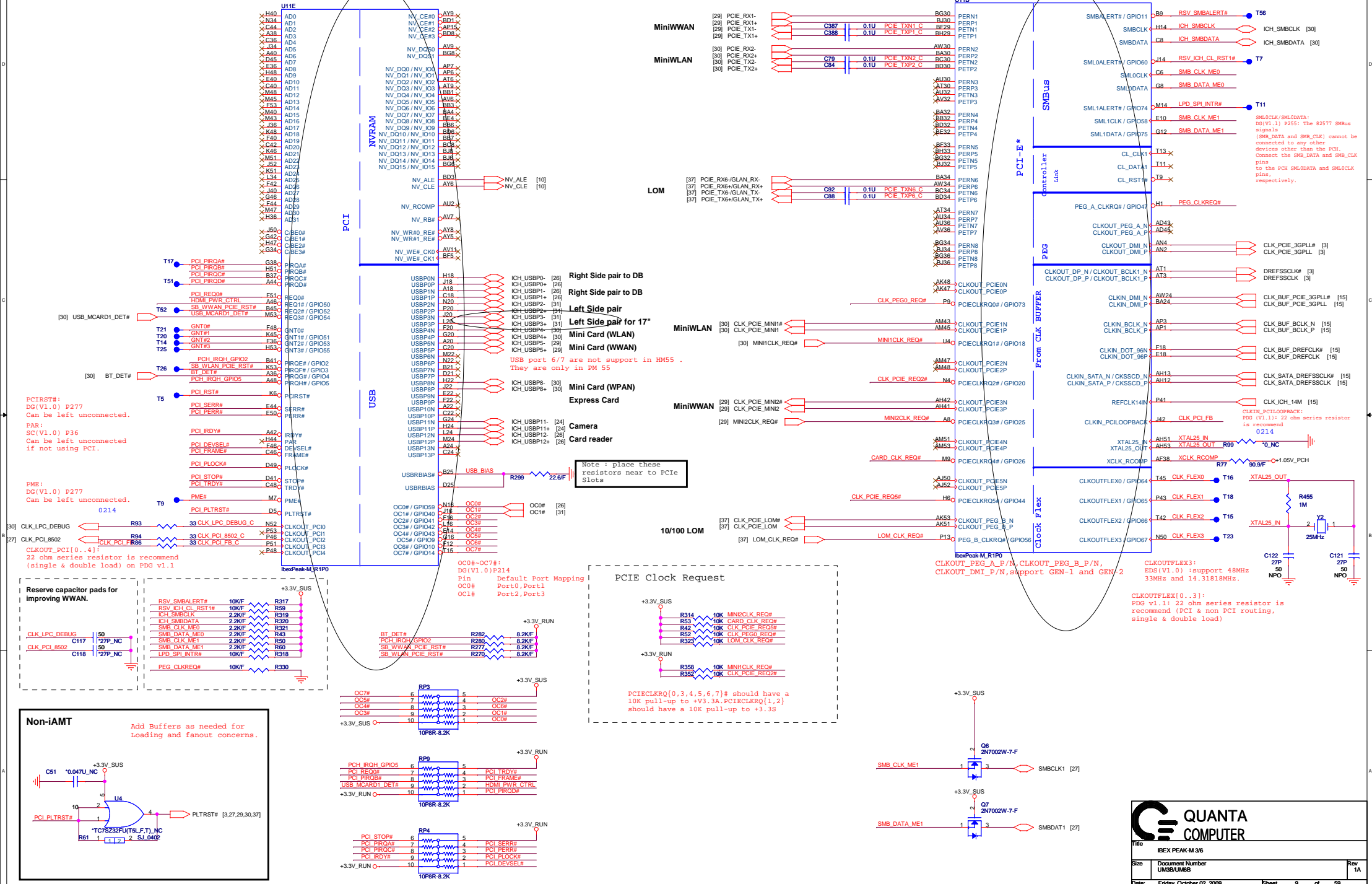
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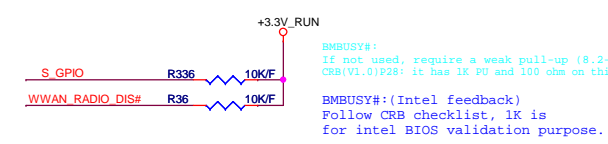
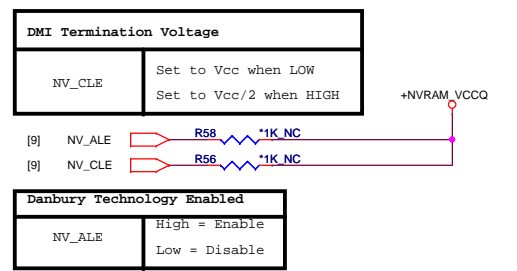
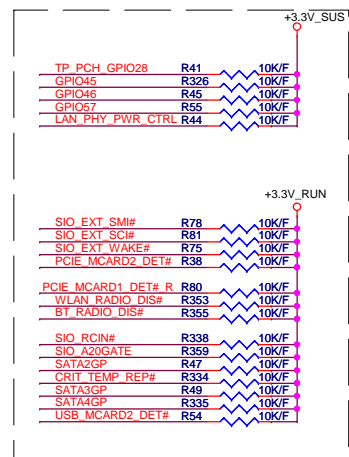
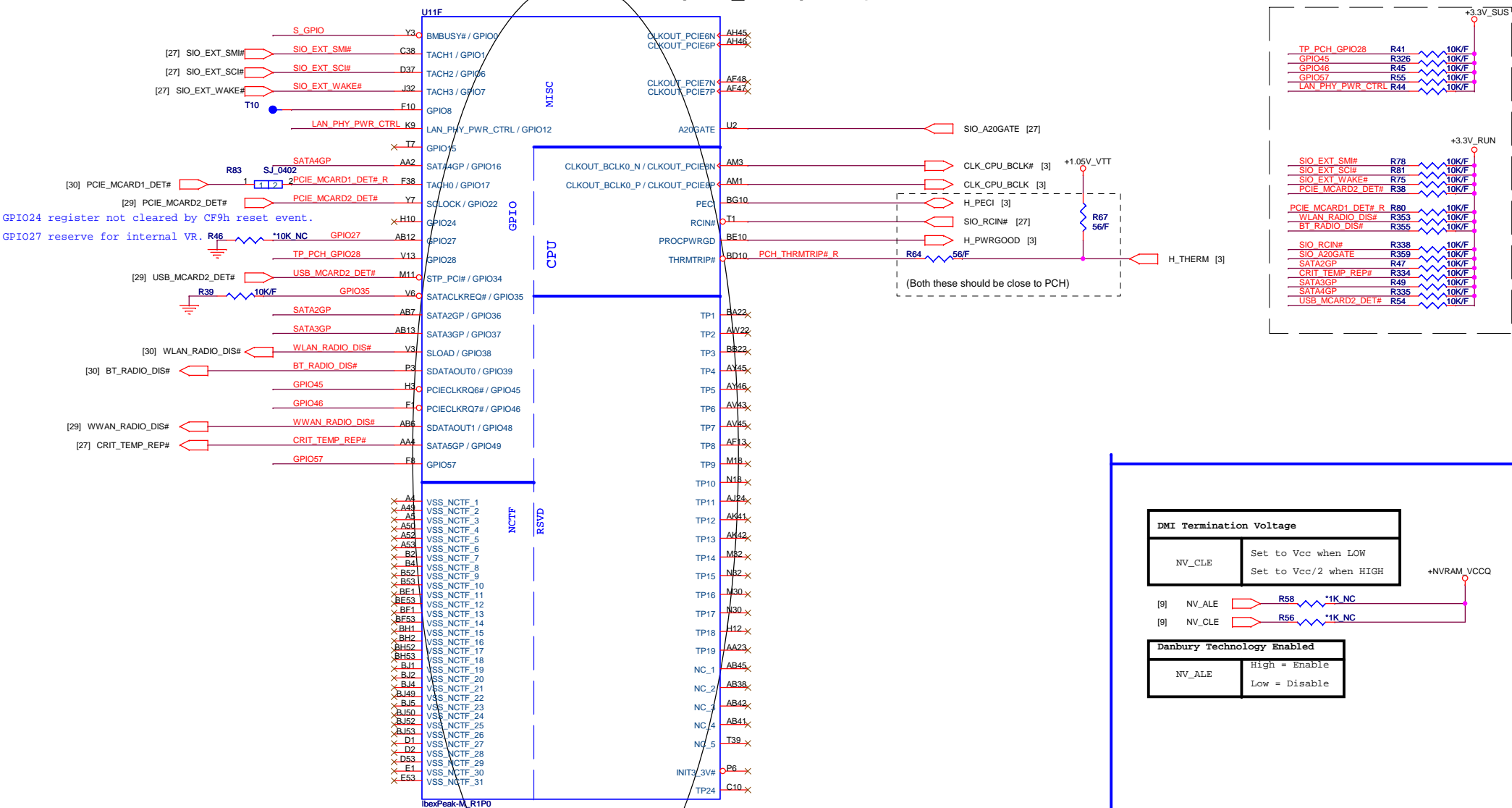
Rev 1A

IBEX PEAK-M (PCI-E, SMBUS, CLK)

U11B

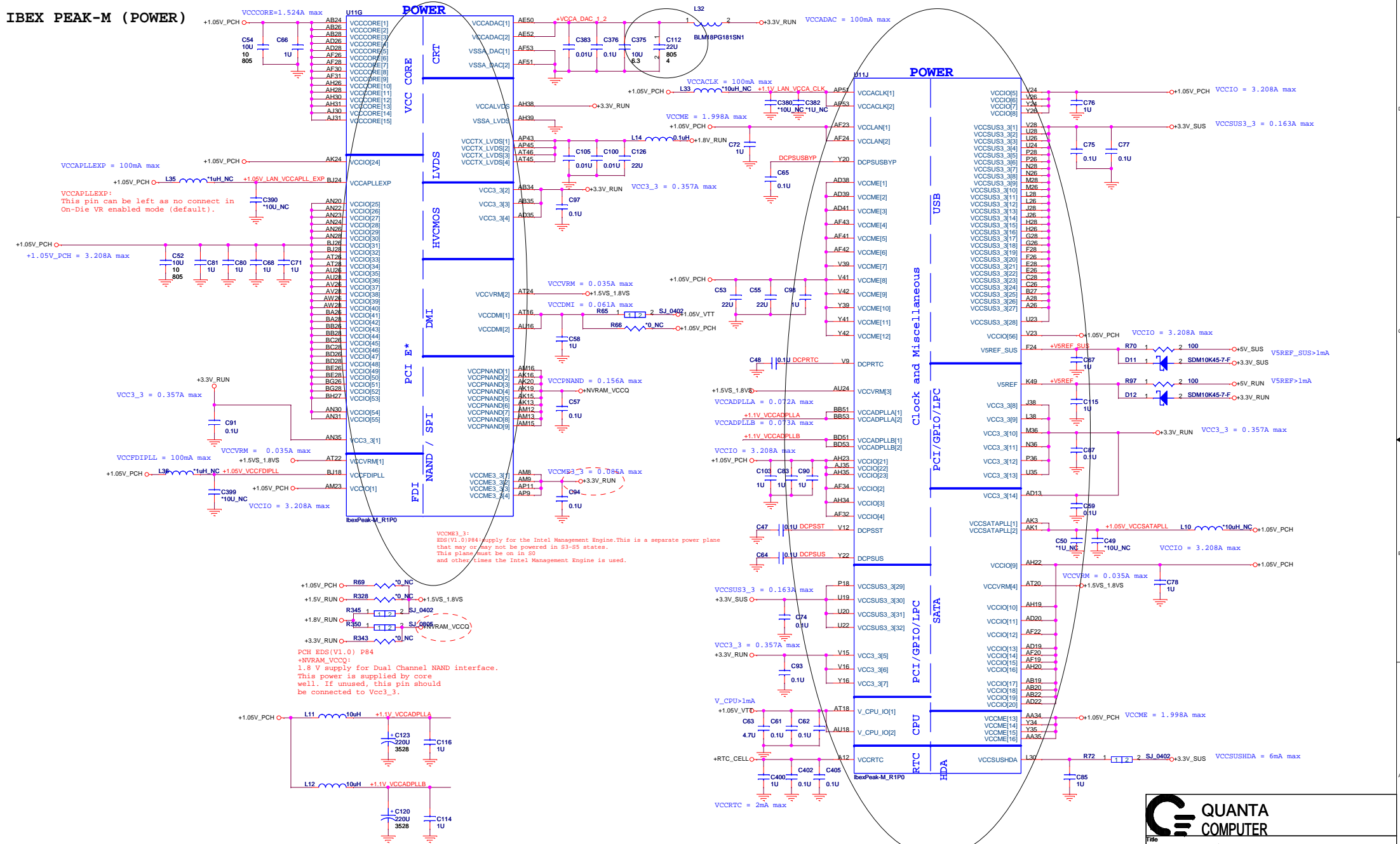


IBEX PEAK-M (GPIO,VSS_NCTF,RSVD)



WWAN_RADIO_DIS#	1-X High = Strong (Default)
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IBEX PEAK-M (POWER)



Title			
IBEX PEAK-M 5/6			
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IBEX PEAK-M (GND)

AB16	VSS[0]
AA19	VSS[1]
AA20	VSS[2]
AA22	VSS[3]
AM15	VSS[4]
AA24	VSS[5]
AA26	VSS[6]
AA28	VSS[7]
AA30	VSS[8]
AA32	VSS[9]
AB17	VSS[10]
AB18	VSS[11]
AB19	VSS[12]
AB20	VSS[13]
AB21	VSS[14]
AB22	VSS[15]
AB23	VSS[16]
AB24	VSS[17]
AB25	VSS[18]
AB26	VSS[19]
AB27	VSS[20]
AB28	VSS[21]
AC2	VSS[22]
AC22	VSS[23]
AD1	VSS[24]
AD12	VSS[25]
AD16	VSS[26]
AD23	VSS[27]
AD30	VSS[28]
AD31	VSS[29]
AD32	VSS[30]
AD34	VSS[31]
AD35	VSS[32]
AD42	VSS[33]
AD46	VSS[34]
AD49	VSS[35]
AD7	VSS[36]
AE2	VSS[37]
AE4	VSS[38]
AF12	VSS[39]
A13	VSS[40]
AH40	VSS[41]
AH4	VSS[42]
AF35	VSS[43]
AF13	VSS[44]
AD34	VSS[45]
AF45	VSS[46]
AF46	VSS[47]
AF49	VSS[48]
AF5	VSS[49]
AF5	VSS[50]
AG2	VSS[51]
AG52	VSS[52]
AH11	VSS[53]
AH15	VSS[54]
AH16	VSS[55]
AH24	VSS[56]
AH32	VSS[57]
AV16	VSS[58]
AH45	VSS[59]
AH47	VSS[60]
AH7	VSS[61]
AV19	VSS[62]
AJ20	VSS[63]
AJ2	VSS[64]
AJ22	VSS[65]
AJ23	VSS[66]
AJ25	VSS[67]
AJ28	VSS[68]
AJ2	VSS[69]
AJ4	VSS[70]
AJ6	VSS[71]
AJ	VSS[72]
AK1	VSS[73]
AM13	VSS[74]
AK26	VSS[75]
AK2	VSS[76]
AK2	VSS[77]
AK2	VSS[78]
AK28	VSS[79]

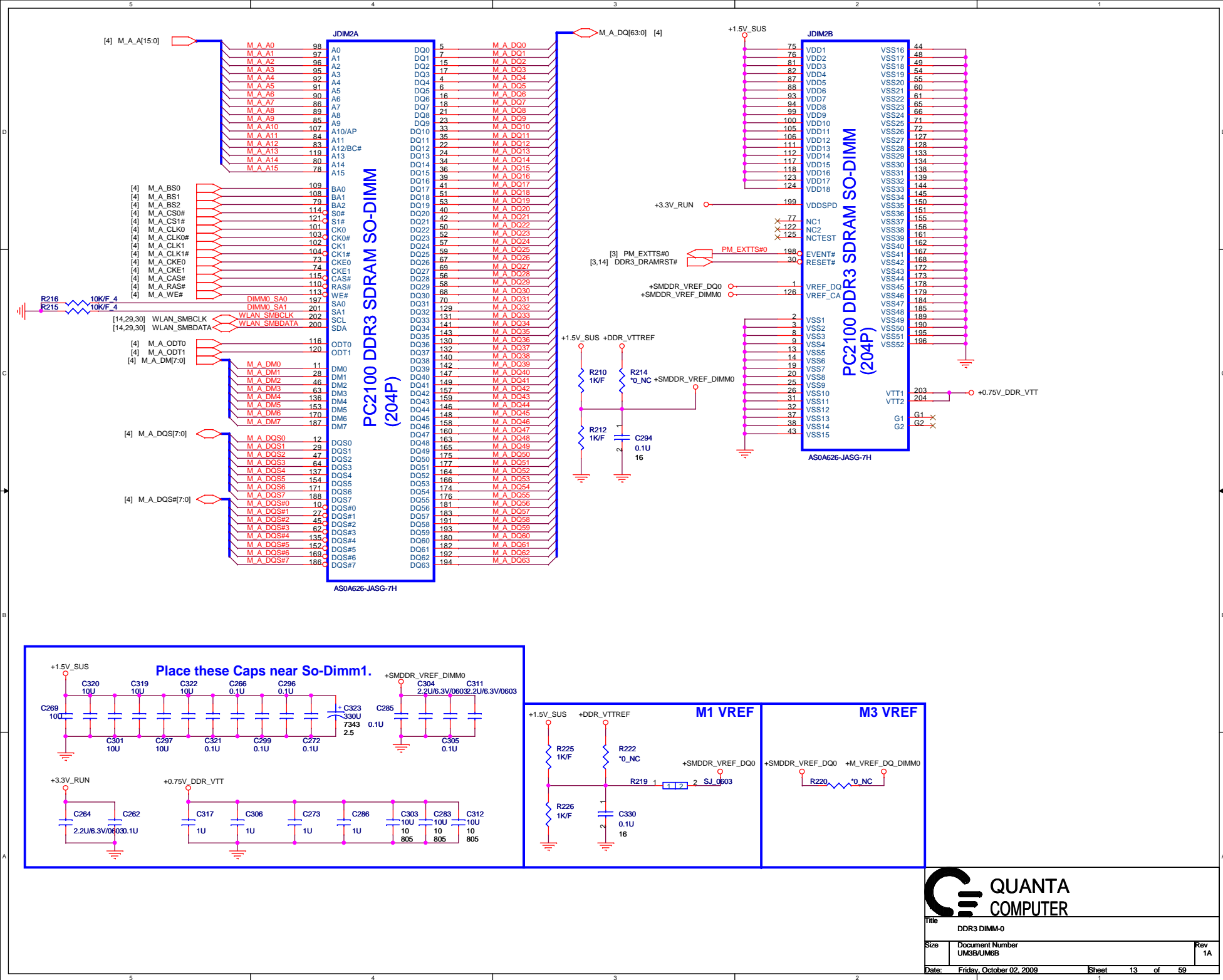
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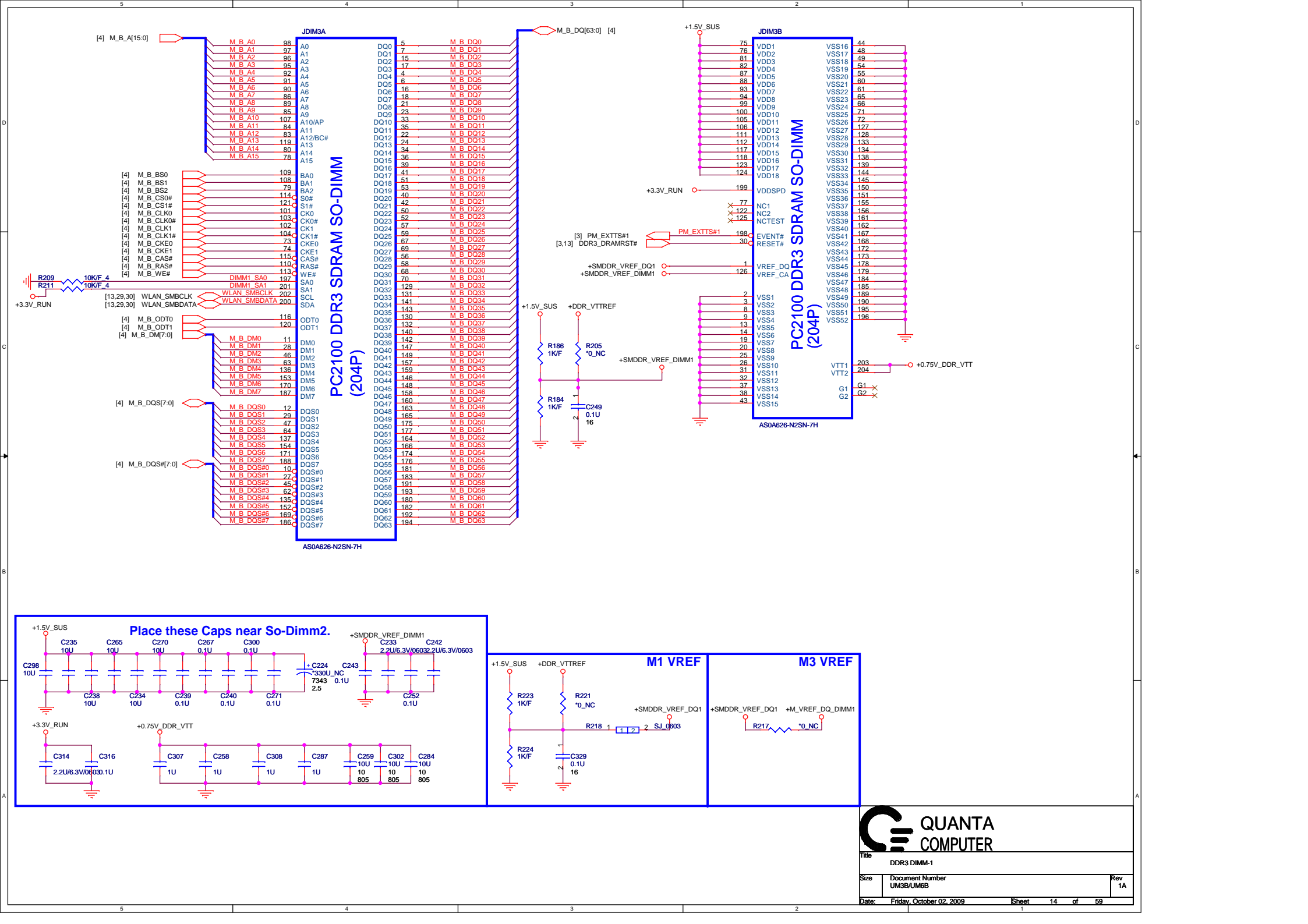
U11	VSS[159]
B11	VSS[160]
B15	VSS[161]
B17	VSS[162]
B2	VSS[163]
BA1	VSS[164]
AC5	VSS[165]
B39	VSS[166]
BA3	VSS[167]
BA7	VSS[168]
B7	VSS[169]
BC12	VSS[170]
BB12	VSS[171]
BB16	VSS[172]
BB20	VSS[173]
BB24	VSS[174]
BB30	VSS[175]
BB34	VSS[176]
BB38	VSS[177]
BB42	VSS[178]
BB46	VSS[179]
BB5	VSS[180]
BC10	VSS[181]
BC14	VSS[182]
BC18	VSS[183]
BC2	VSS[184]
BC22	VSS[185]
BC32	VSS[186]
BC36	VSS[187]
BC40	VSS[188]
BC44	VSS[189]
BC52	VSS[190]
BC6	VSS[191]
BD48	VSS[192]
BD49	VSS[193]
BD5	VSS[194]
BE12	VSS[195]
BE16	VSS[196]
BE20	VSS[197]
BE24	VSS[198]
BE30	VSS[199]
BE34	VSS[200]
BE38	VSS[201]
BE42	VSS[202]
BE46	VSS[203]
BE48	VSS[204]
BE50	VSS[205]
BE6	VSS[206]
BE8	VSS[207]
BE3	VSS[208]
BE49	VSS[209]
BE51	VSS[210]
BE10	VSS[211]
BE24	VSS[212]
BE4	VSS[213]
BE60	VSS[214]
BH11	VSS[215]
BH15	VSS[216]
BH19	VSS[217]
BH23	VSS[218]
BH31	VSS[219]
BH35	VSS[220]
BH39	VSS[221]
BH43	VSS[222]
BH47	VSS[223]
BA12	VSS[224]
BA48	VSS[225]
C50	VSS[226]
D51	VSS[227]
E12	VSS[228]
E16	VSS[229]
E20	VSS[230]
E24	VSS[231]
E30	VSS[232]
E34	VSS[233]
E38	VSS[234]
E42	VSS[235]
E46	VSS[236]
E48	VSS[237]
E8	VSS[238]
E8	VSS[239]
F49	VSS[240]
F5	VSS[241]
G10	VSS[242]
G14	VSS[243]
G18	VSS[244]
G2	VSS[245]
G22	VSS[246]
G32	VSS[247]
G36	VSS[248]
G40	VSS[249]
G44	VSS[250]
G52	VSS[251]
AF39	VSS[252]
H16	VSS[253]
H20	VSS[254]
H30	VSS[255]
H34	VSS[256]
H38	VSS[257]
H42	VSS[258]

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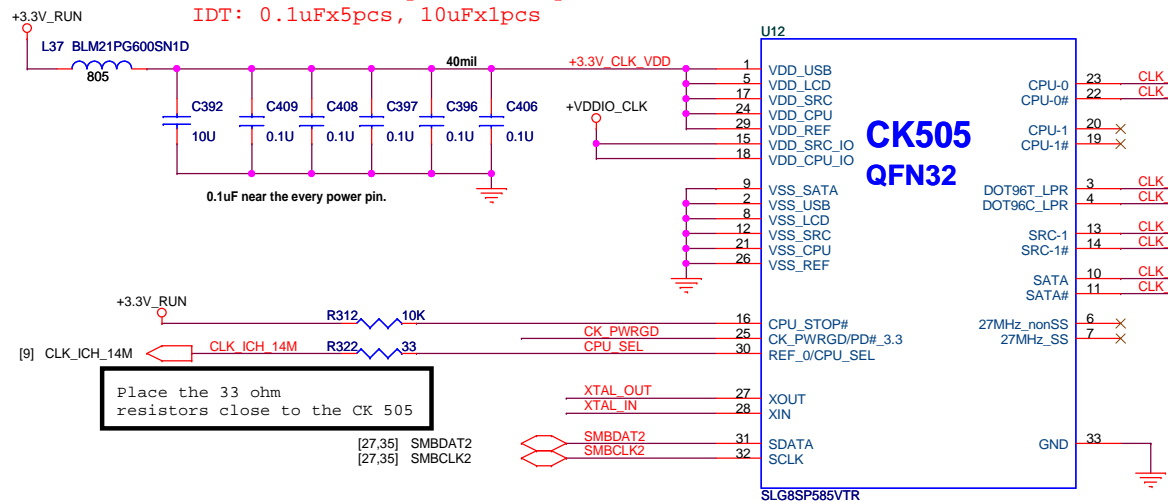
H49	VSS[259]
H5	VSS[260]
J24	VSS[261]
K11	VSS[262]
K43	VSS[263]
K47	VSS[264]
L2	VSS[265]
L14	VSS[266]
L18	VSS[267]
L2	VSS[268]
L22	VSS[269]
L32	VSS[270]
L36	VSS[271]
L40	VSS[272]
L52	VSS[273]
M12	VSS[274]
M16	VSS[275]
M20	VSS[276]
N38	VSS[277]
M34	VSS[278]
N38	VSS[279]
M42	VSS[280]
M46	VSS[281]
M49	VSS[282]
M5	VSS[283]
M6	VSS[284]
N24	VSS[285]
P11	VSS[286]
AD15	VSS[287]
P22	VSS[288]
P30	VSS[289]
P32	VSS[290]
P34	VSS[291]
P42	VSS[292]
P45	VSS[293]
P47	VSS[294]
R2	VSS[295]
R52	VSS[296]
T14	VSS[297]
T41	VSS[298]
T46	VSS[299]
T49	VSS[300]
T5	VSS[301]
T6	VSS[302]
U30	VSS[303]
U32	VSS[304]
U36	VSS[305]
U44	VSS[306]
P38	VSS[307]
V11	VSS[308]
P16	VSS[309]
V19	VSS[310]
V20	VSS[311]
V22	VSS[312]
V30	VSS[313]
V31	VSS[314]
V32	VSS[315]
V34	VSS[316]
V35	VSS[317]
V38	VSS[318]
V43	VSS[319]
V45	VSS[320]
V46	VSS[321]
V47	VSS[322]
V49	VSS[323]
V5	VSS[324]
V7	VSS[325]
V8	VSS[326]
W2	VSS[327]
W52	VSS[328]
Y11	VSS[329]
Y12	VSS[330]
Y15	VSS[331]
Y18	VSS[332]
Y23	VSS[333]
Y28	VSS[334]
Y30	VSS[335]
Y31	VSS[336]
Y32	VSS[337]
Y38	VSS[338]
Y43	VSS[339]
Y46	VSS[340]
Y49	VSS[341]
Y5	VSS[342]
Y6	VSS[343]
Y6	VSS[344]
P24	VSS[345]
T43	VSS[346]
AD51	VSS[347]
AT2	VSS[348]
AT7	VSS[349]
Y47	VSS[350]
AT12	VSS[351]
AT6	VSS[352]
AT13	VSS[353]
AM5	VSS[354]
AK45	VSS[355]
AK39	VSS[356]
AV14	VSS[357]



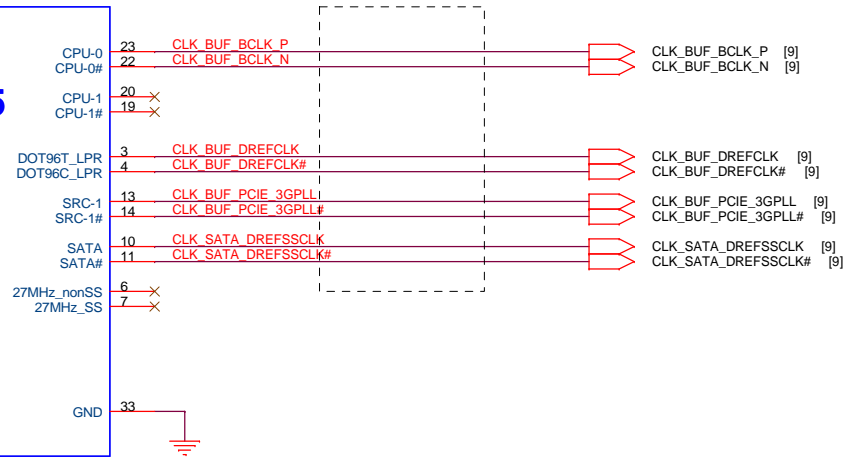




Realtek: 0.1uFx6pcs, 22uFx1pcs
IDT: 0.1uFx5pcs, 10uFx1pcs

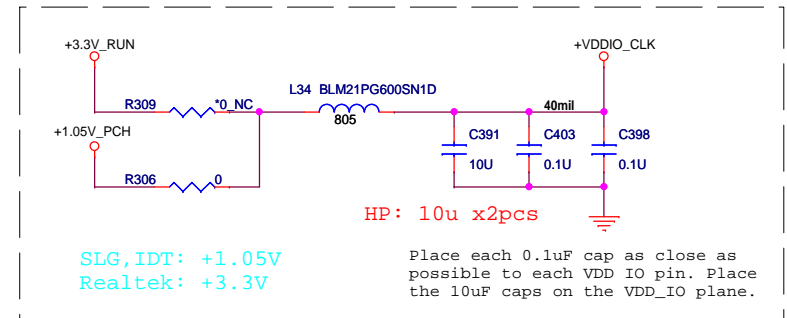


Place within 0.5" of CLKGEN



Place the 33 ohm resistors close to the CK 505

Realtek: 0.1uFx3pcs, 22uFx1pcs
IDT: 0.1uFx2pcs, 10uFx1pcs

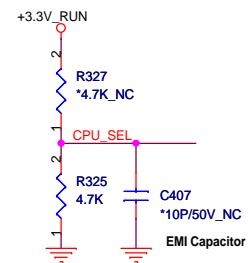
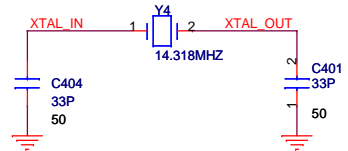
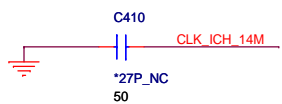


SLG, IDT: +1.05V
Realtek: +3.3V

Place each 0.1uF cap as close as possible to each VDD IO pin. Place the 10uF caps on the VDD_IO plane.

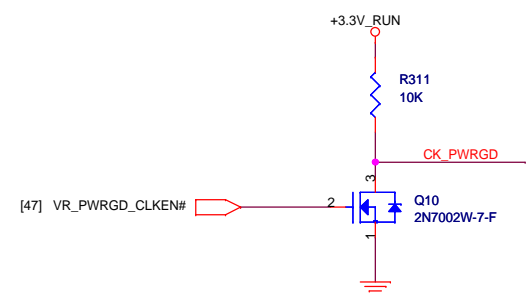
+VDDIO_CLK:
SLG date sheet (V0.2) P15: Min 1.05V, Max 3.465V.
Realtek date sheet (V1.2) P11: Min 1.05V, Max 3.3V.
IDT date sheet (V0.7) P10: Min 0.9975V, Max 3.465V.

Add capacitor pads for improving WWAN.




PIN 30	CPU_0	CPU_1
0 (default)	133MHz	133MHz
1 (0.7V-1.5V)	100MHz	100MHz

CPU_SEL:
SLG date sheet (V0.2) P15:
High Voltage: Min 0.7V, Max 1.5V.
Low Voltage: Min Vss-0.3V, Max 0.35V.
Realtek date sheet (V1.2) P11:
High Voltage: Min 0.7V, Max 1.5V.
Low Voltage: Min Vss-0.3V, Max 0.35V.
IDT date sheet (V0.7) P10:
High Voltage: Min 0.7V, Max 1.5V.
Low Voltage: Min Vss-0.3V, Max 0.35V.




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 QUANTA COMPUTER		
Title VGA-M92-XT (PCIe)		
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 QUANTA COMPUTER		
Title: VGA-M82-XT (PCIe)		
Size:	Document Number: UM3B/UM6B	Rev: 1A
Date: Wednesday, September 30, 2009 Sheet 19 of 59		

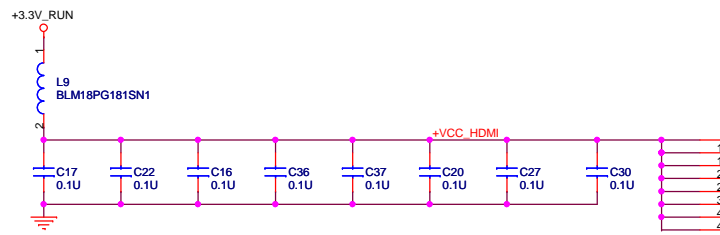
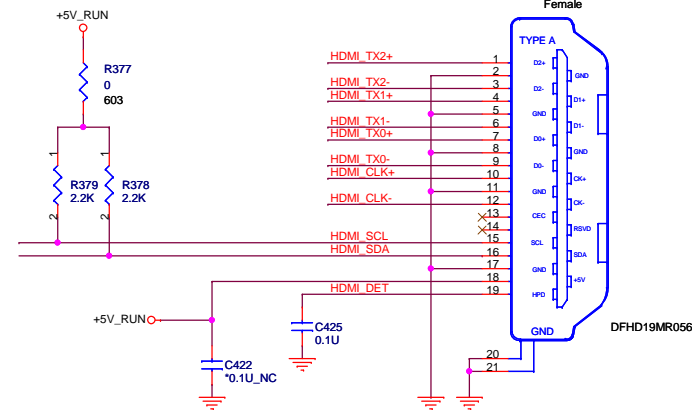
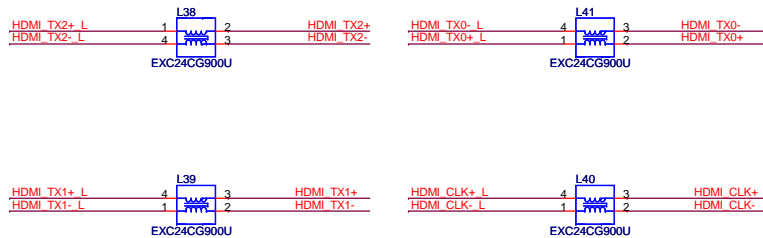
**BLANK PAGE FOR PAGE
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NUMBER SAME AS DISCRETE**

 QUANTA COMPUTER		
Title VGA-M92-XT (PCIe)		
Size	Document Number UM3B/UM6B	Rev 1A
Date	Wednesday, September 30, 2009	Sheet 21 of 59

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NUMBER SAME AS DISCRETE**

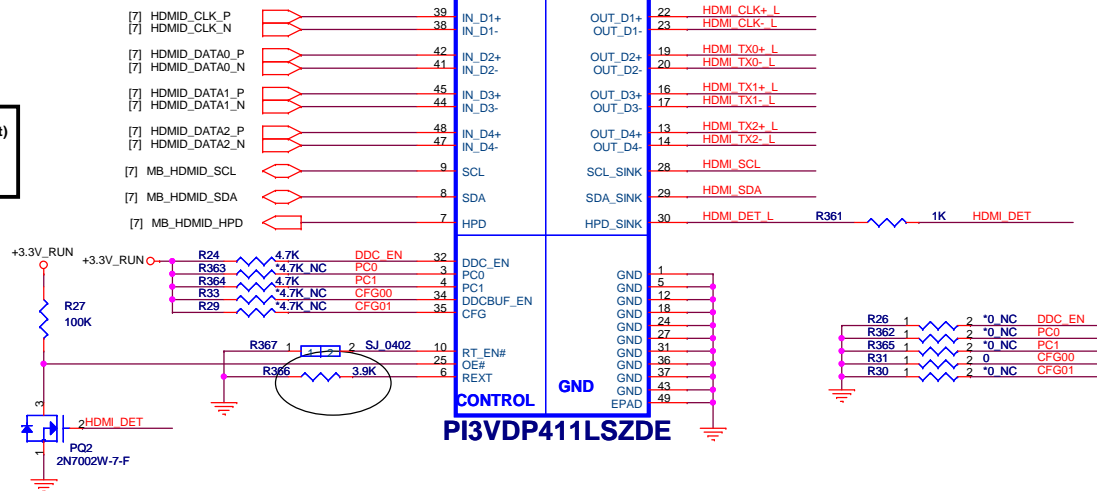
 QUANTA COMPUTER		
Title VGA-M92-XT (PCIe)		
Size	Document Number UM3B/UM6B	Rev 1A
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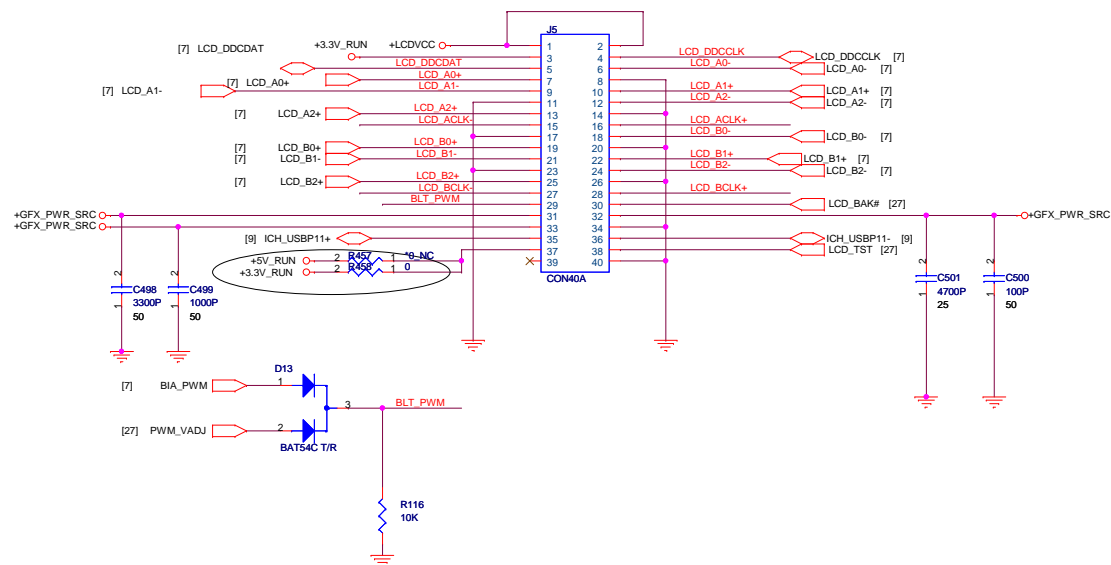
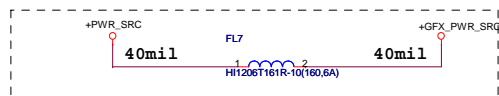
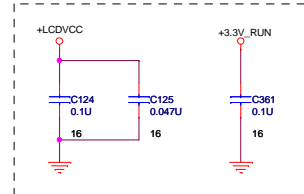
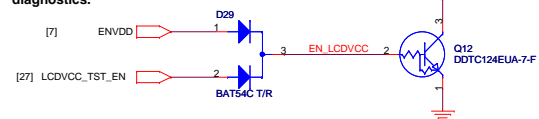
EQUALIZATION SETTING
 PC1:PC0=0:0 8dB
 PC1:PC0=0:1 4dB Recommended
 PC1:PC0=1:0 12dB
 PC1:PC0=1:1 0dB

SCL2/SDA2 Low-level input/output Voltage
 CFG01:CFG00=0:0 VIL:<0.4V VOL:0.6V (Default)
 CGF01:CGF00=0:1 VIL:<0.36V VOL:0.55V
 CGF01:CGF00=1:0 VIL:<0.44V VOL:0.65V
 CGF01:CGF00=1:1 VIL:<0.36V VOL:0.6V

HDMI_PWR_CTRL
 0 is Enable
 1 is Disable

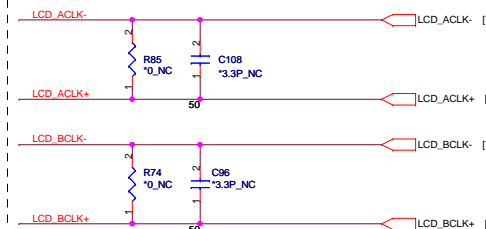


Support the new imbedded diagnostics.

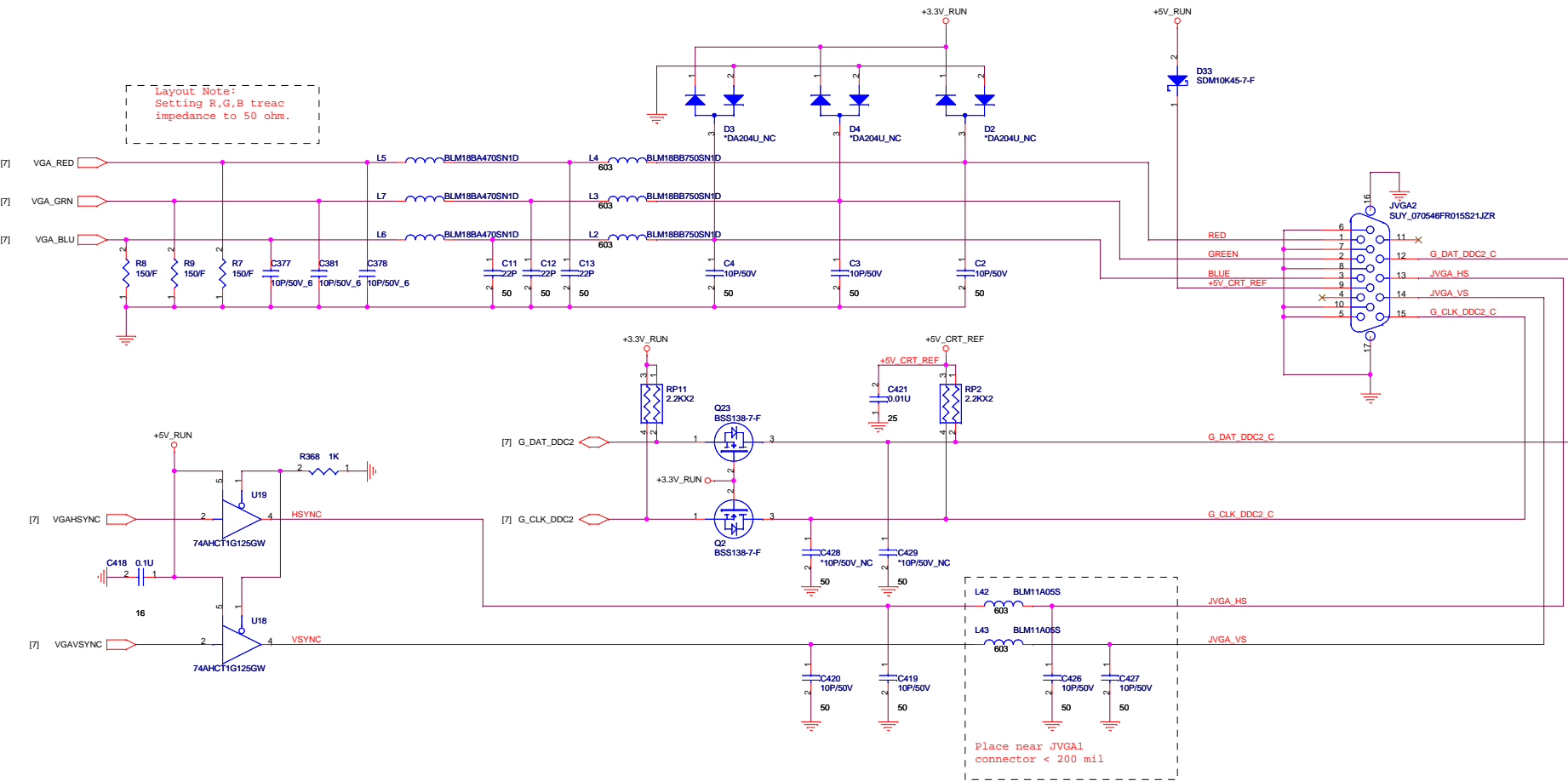


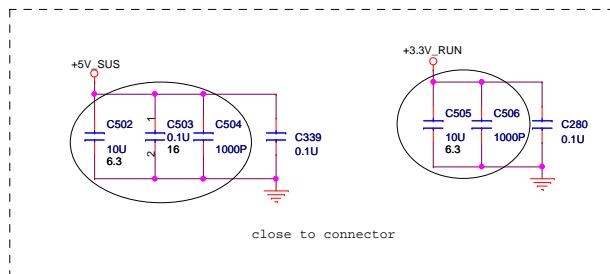
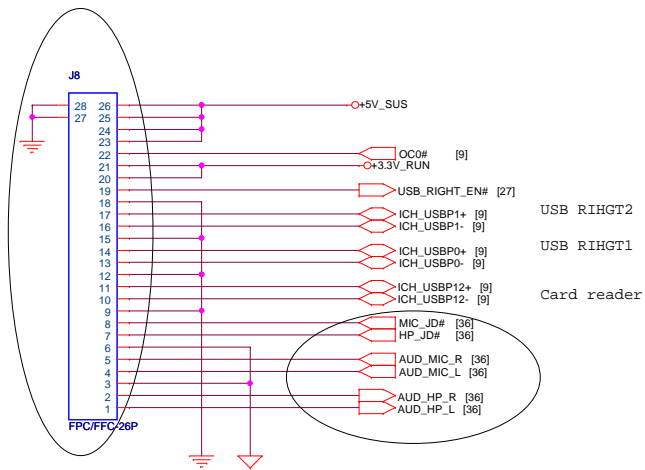
Shunt capacitors on LVDS for improving WWAN.

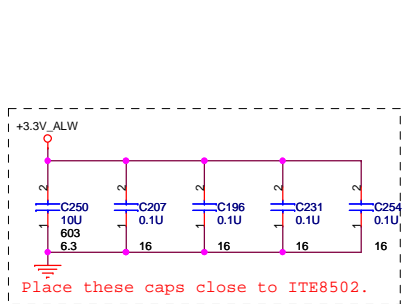
LCD B0-	C109	1	2	*3.3P_NC	50	LCD B0+
LCD B1-	C107	1	2	*3.3P_NC	50	LCD B1+
LCD B2-	C101	1	2	*3.3P_NC	50	LCD B2+
LCD A0-	C119	1	2	*3.3P_NC	50	LCD A0+
LCD A1-	C113	1	2	*3.3P_NC	50	LCD A1+
LCD A2-	C111	1	2	*3.3P_NC	50	LCD A2+



Layout Note:
Setting R,G,B treac
impedance to 50 ohm.

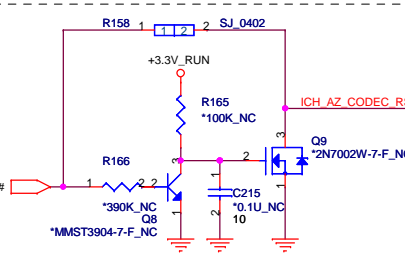
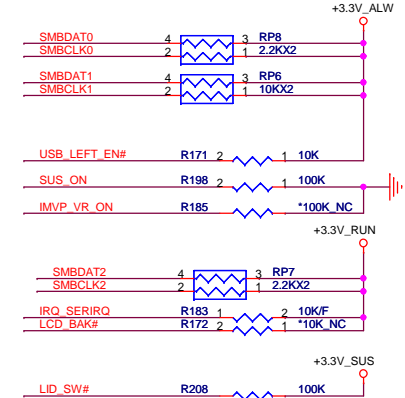
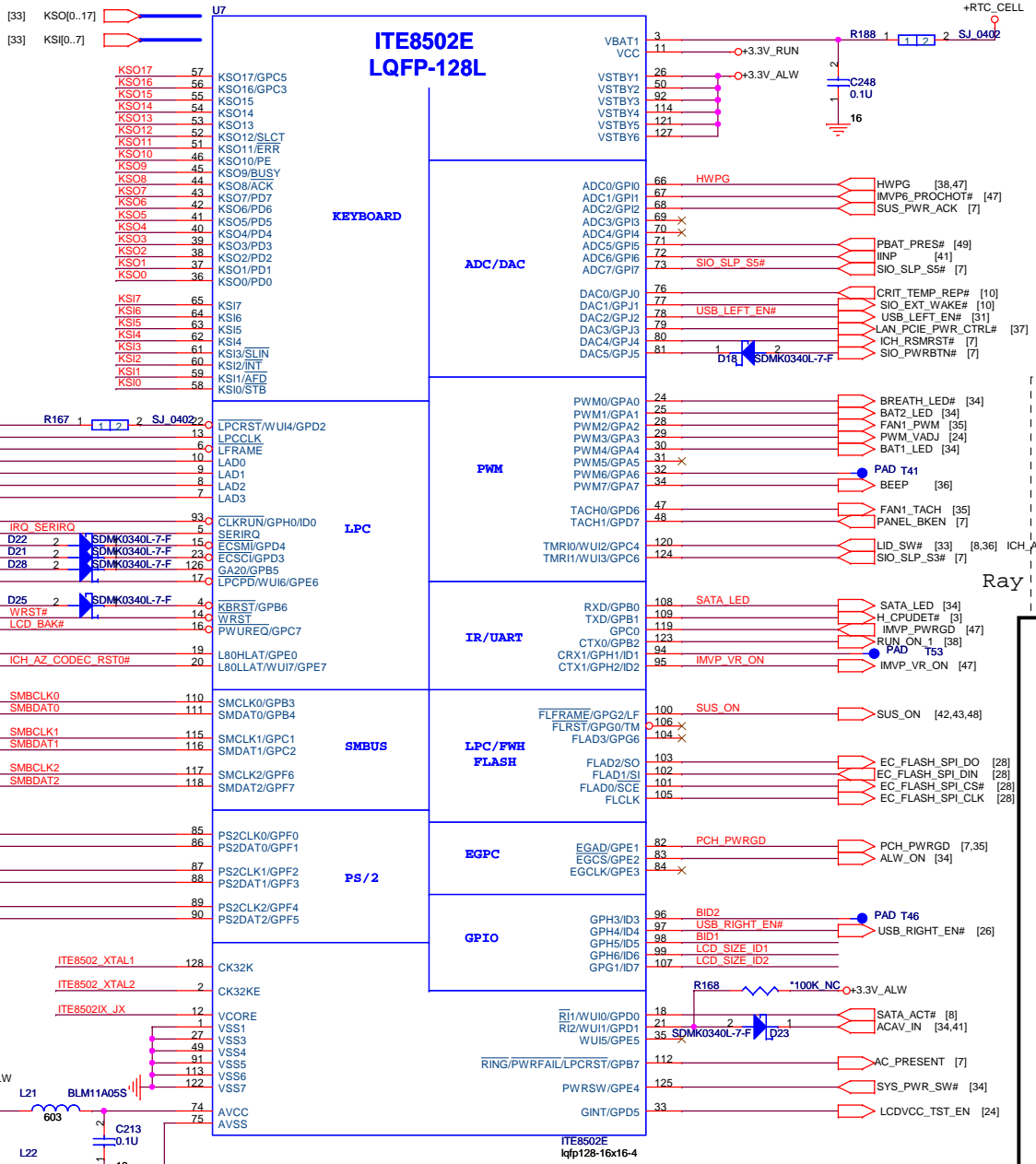
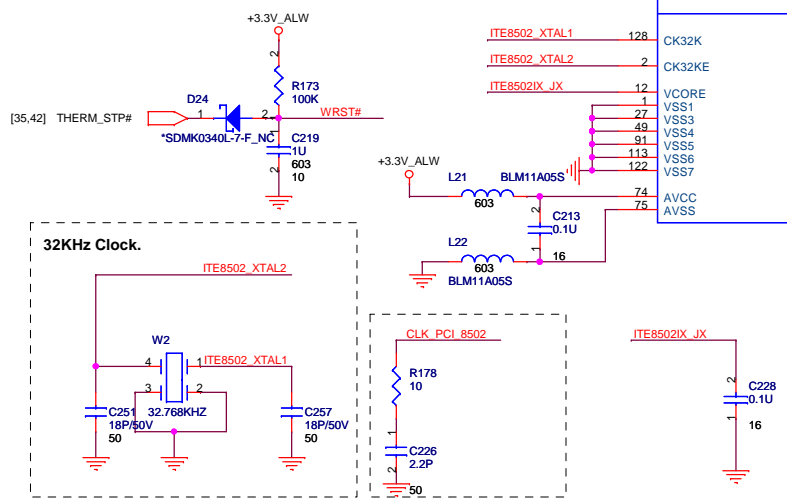




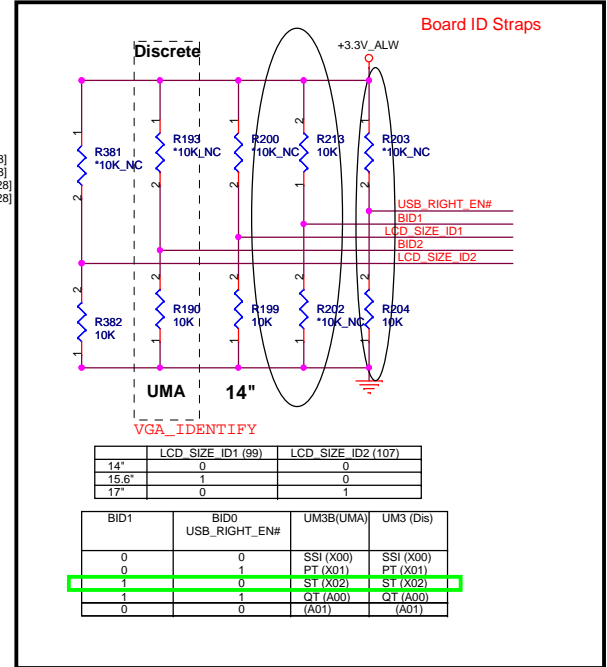



CLK, LCD and Thermal

Charge and BAT



Ray 8/12



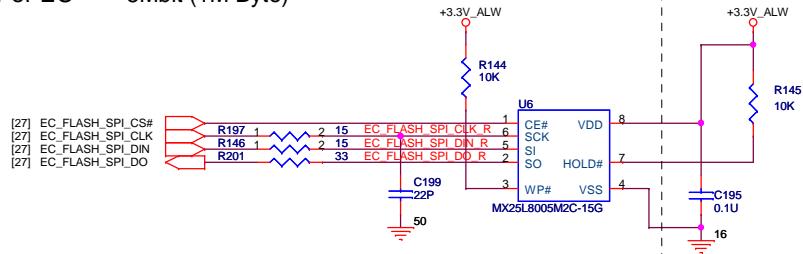
**QUANTA
COMPUTER**

Title: I/O Controller IT8502

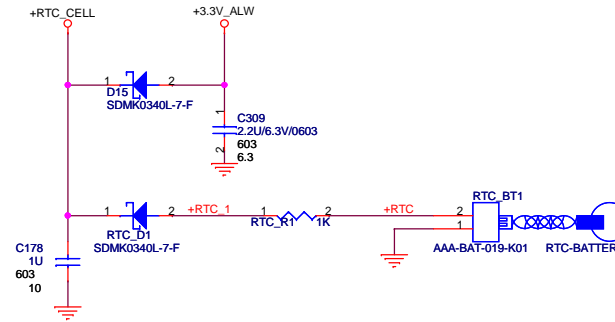
Size	Document Number	Rev
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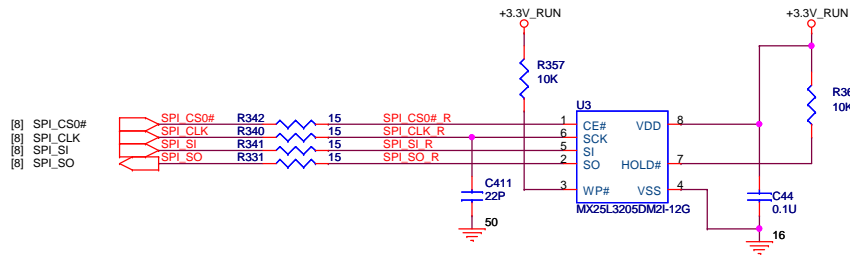
For EC 8Mbit (1M Byte)



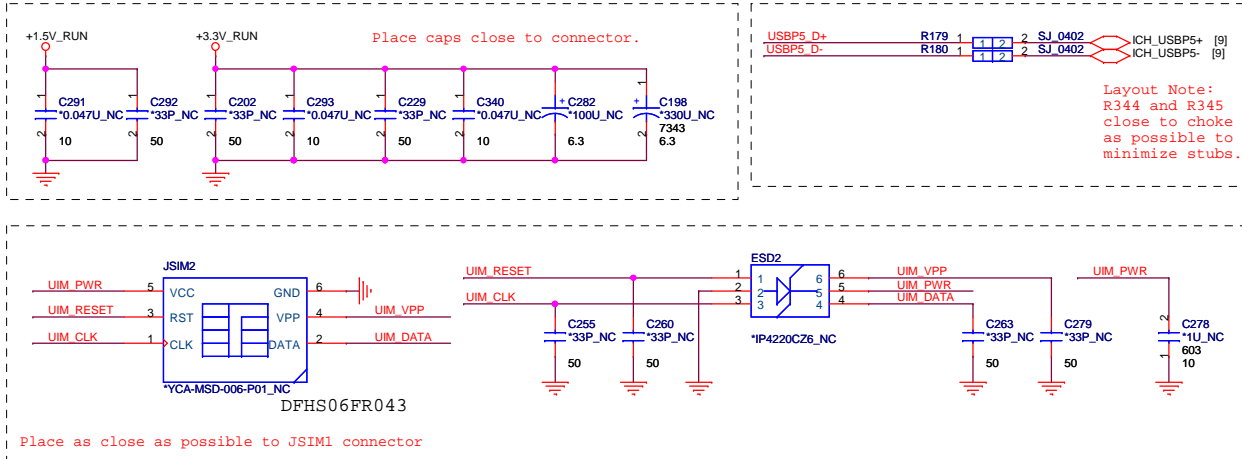
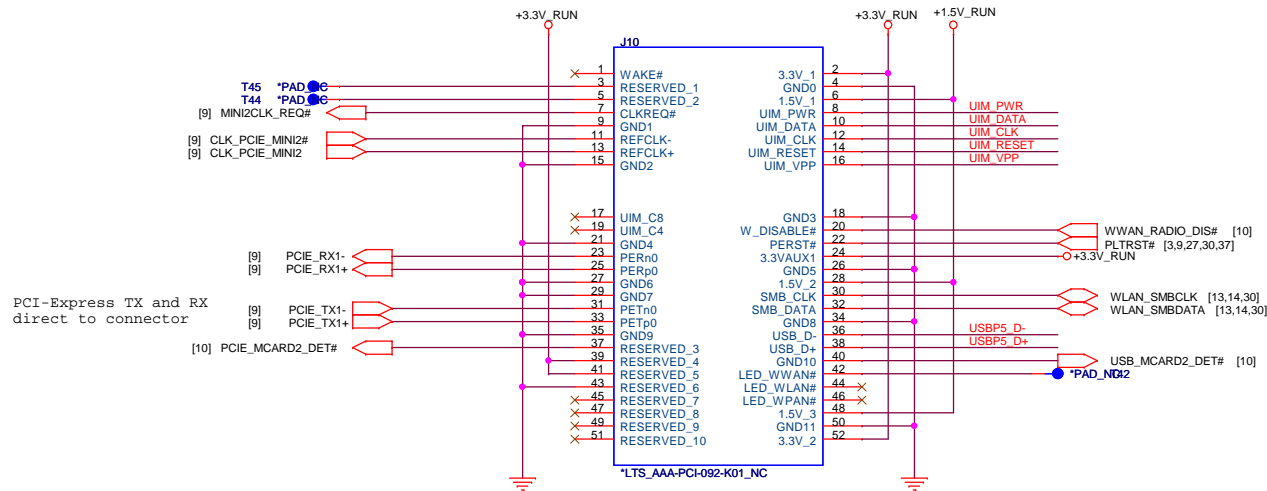
RTC BATTERY



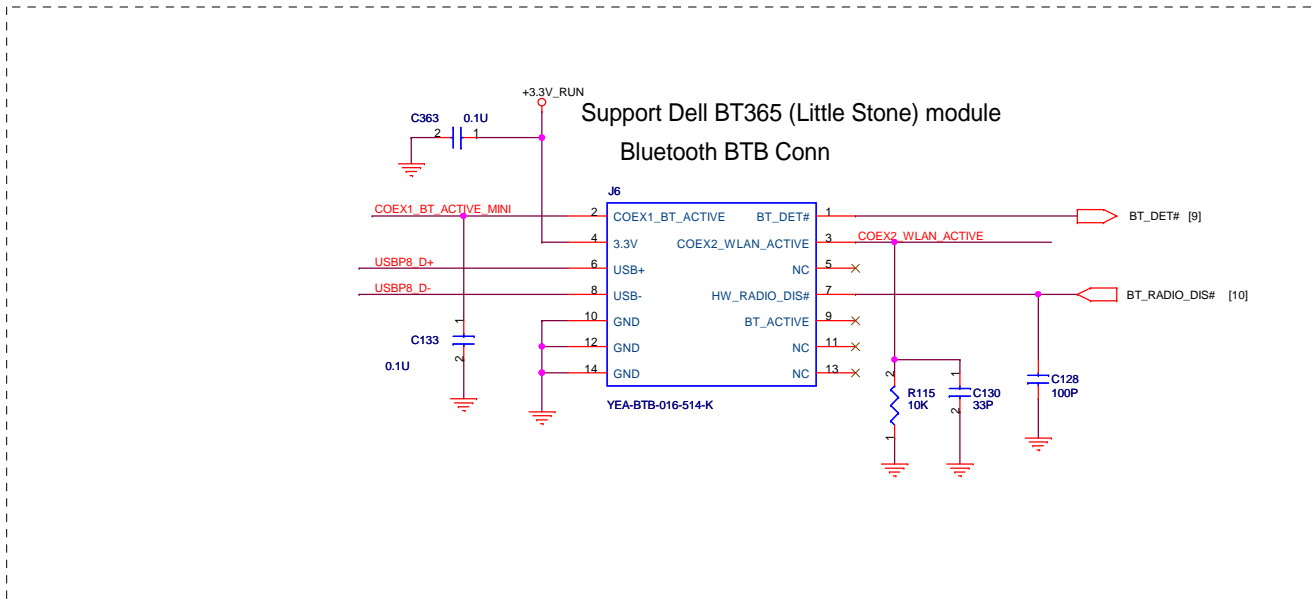
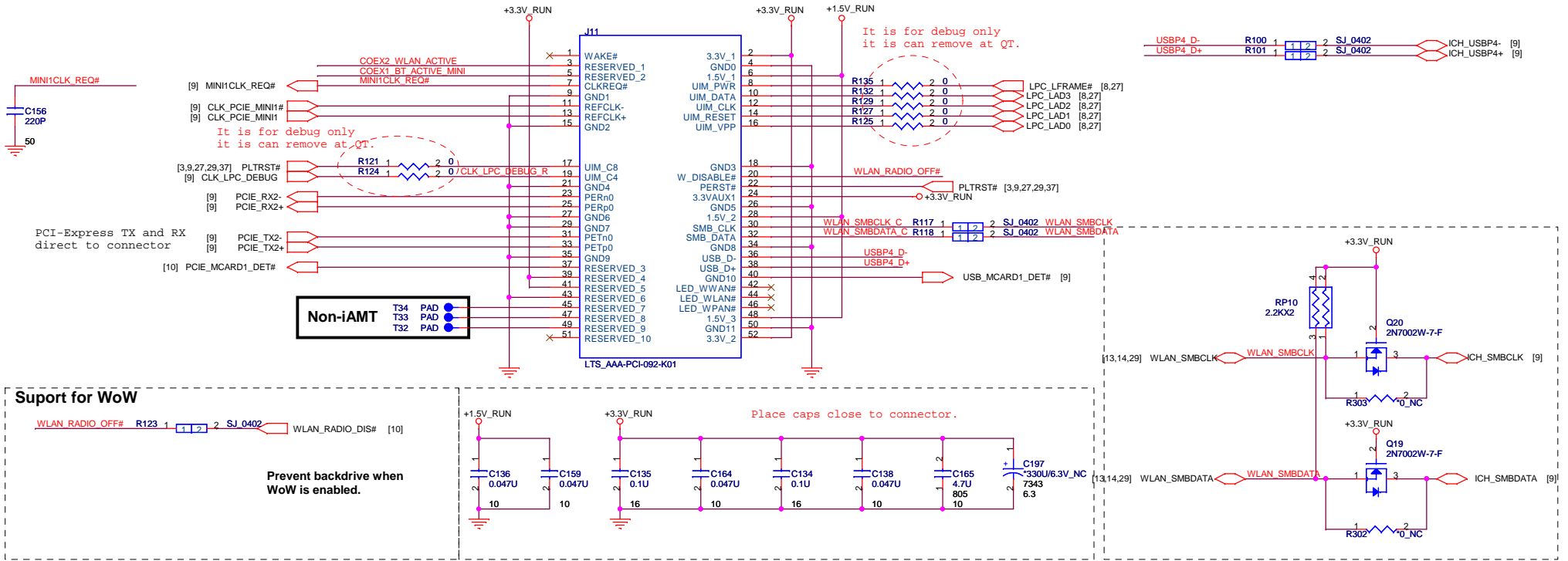
For PCH 32Mbit (4M Byte)

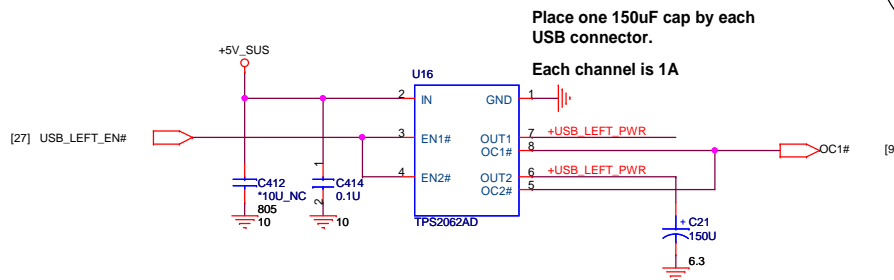
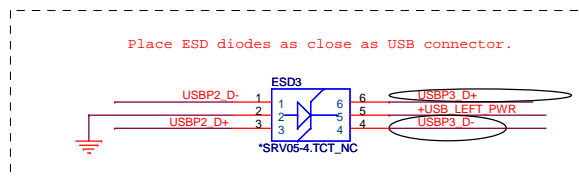
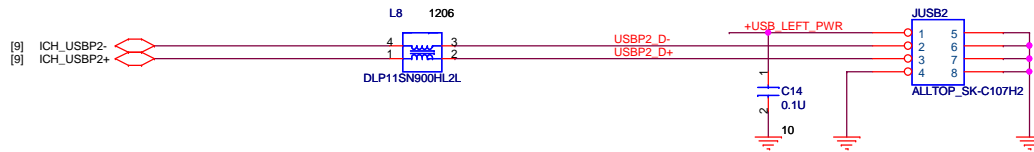


MiniCard WWAN connector



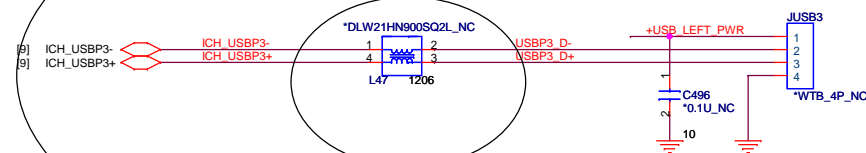
MiniCard WLAN connector





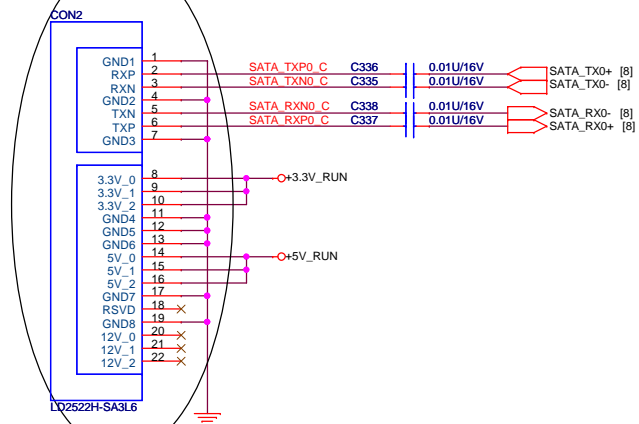
REV FOR 17"

Add L47 ,C496 , JUSB3 for UM5

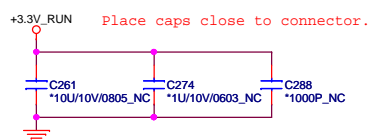


EDISON 8/10

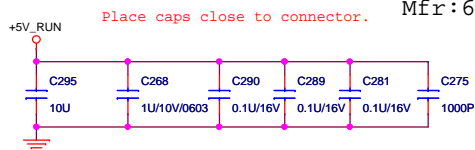
HDD Connector.



UM5與UM3/6不同，只差在高度，footprint沒變

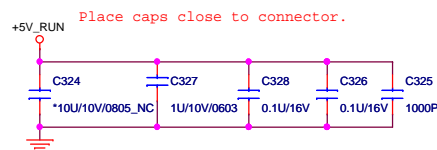
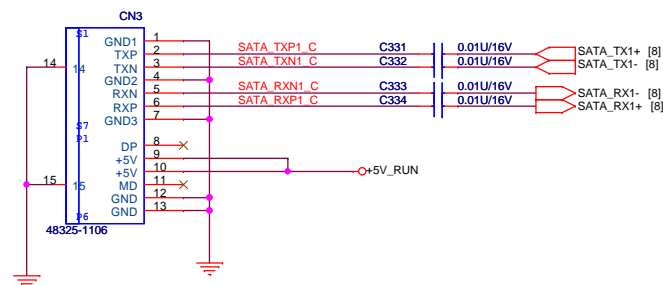


UM5/UM5B
PN:DFHS22FR137
Mfr:67492-1224



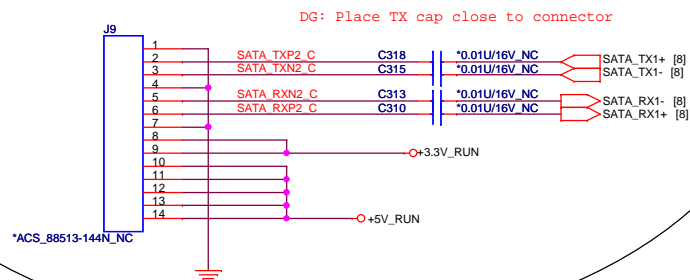
UM3/UM3B/UM6/UM6B
PN:
Mfr:67492-1730

ODD Connector



Place caps close to connector.

REV FOR 15.6"



DG: Place TX cap close to connector

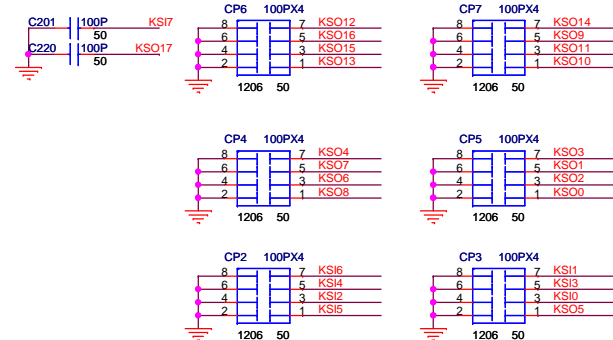
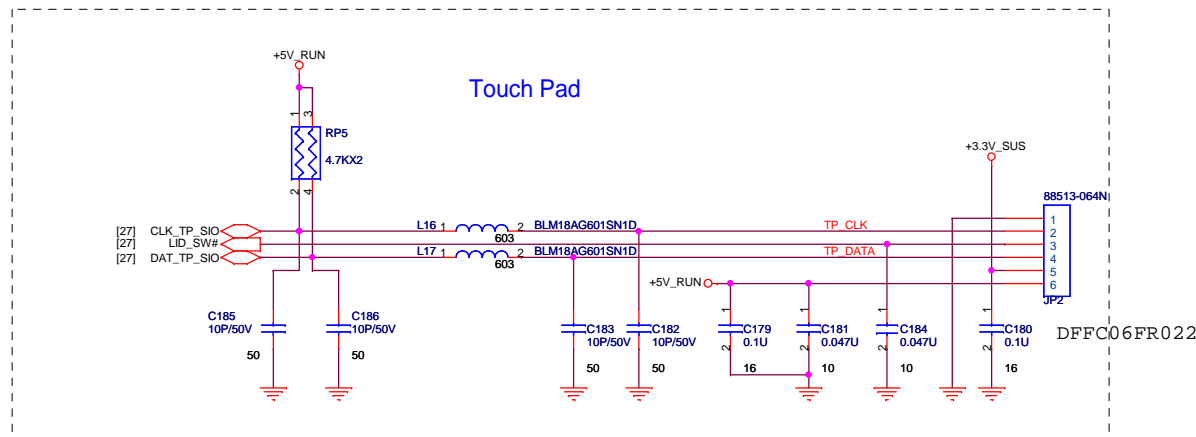
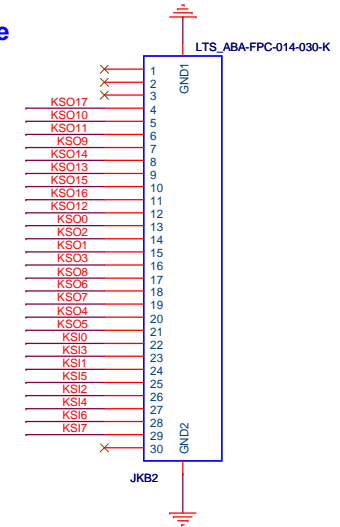


Title		
SATA (HDD&CD_ROM)		
Size	Document Number	Rev
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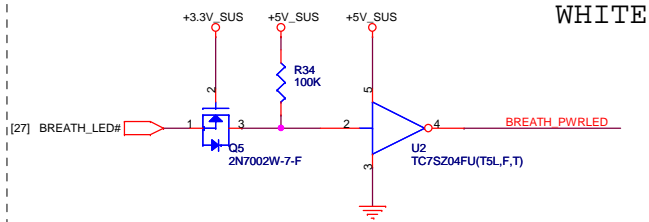
KEYBOARD CONNECTOR

Top side

[27] KSO[0..17]
[27] KSI[0..7]

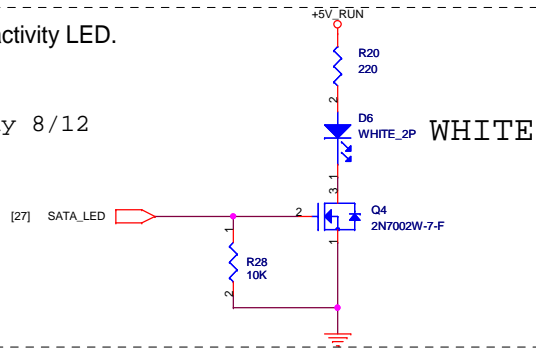


Power



HDD activity LED.

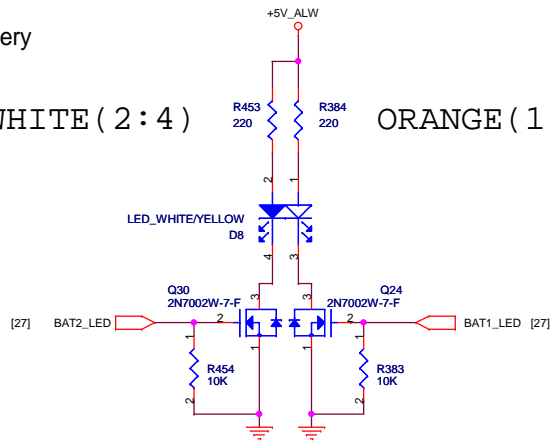
Ray 8/12



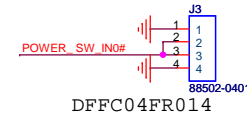
Battery

WHITE (2:4)

ORANGE (1:3)

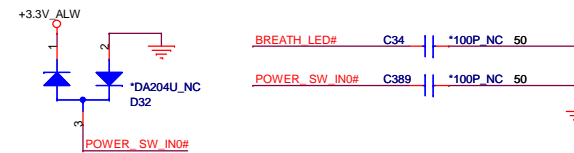
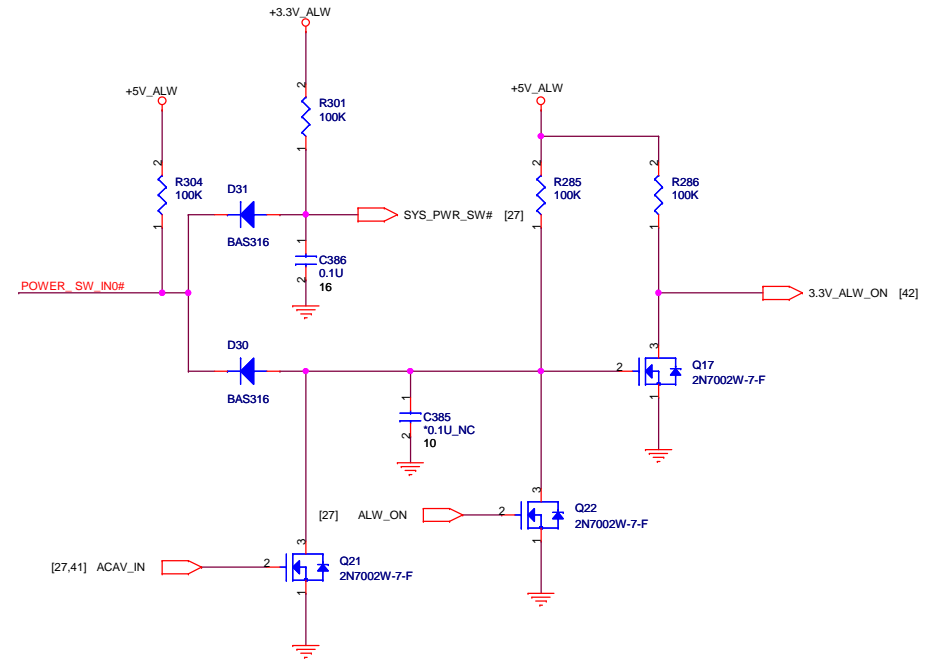


Power button Cable



PIN2,3 connect to POWER_SW_IN0#

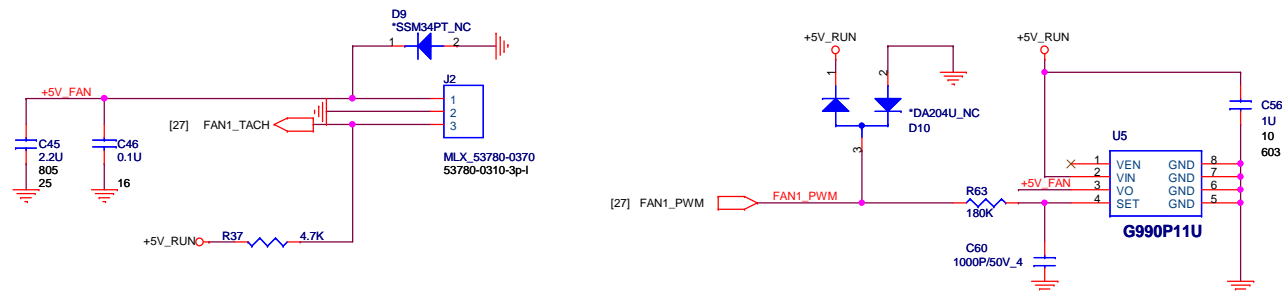
3VALW ON POWER LOGIC



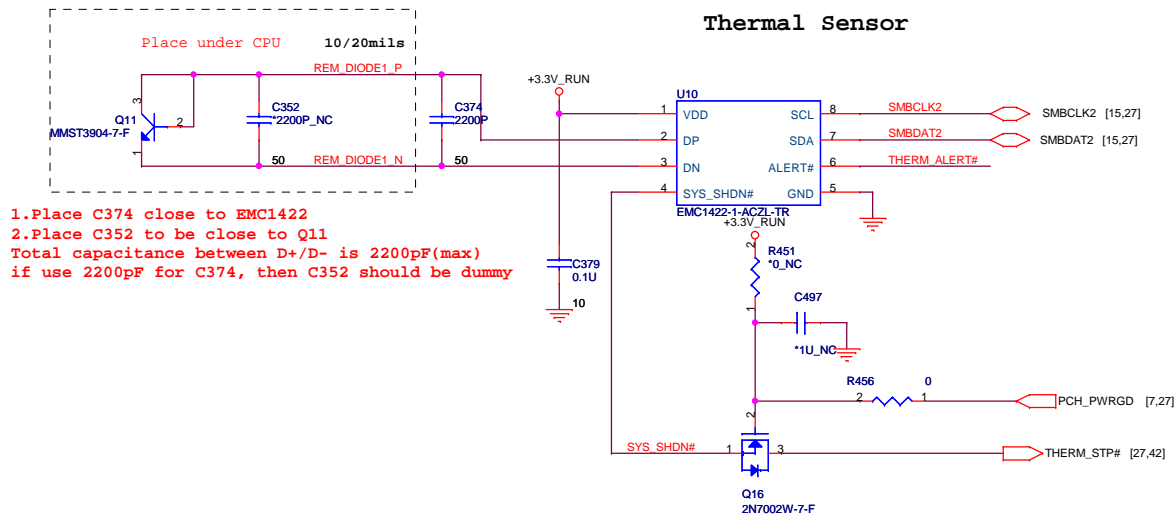
Title		
SWITCH, KEYBOARD & LED&Touch Screen Module		
Size	Document Number	Rev
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FAN CONTROL

6/23 COPY FROM RM6

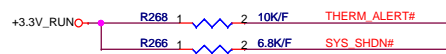


Thermal Sensor

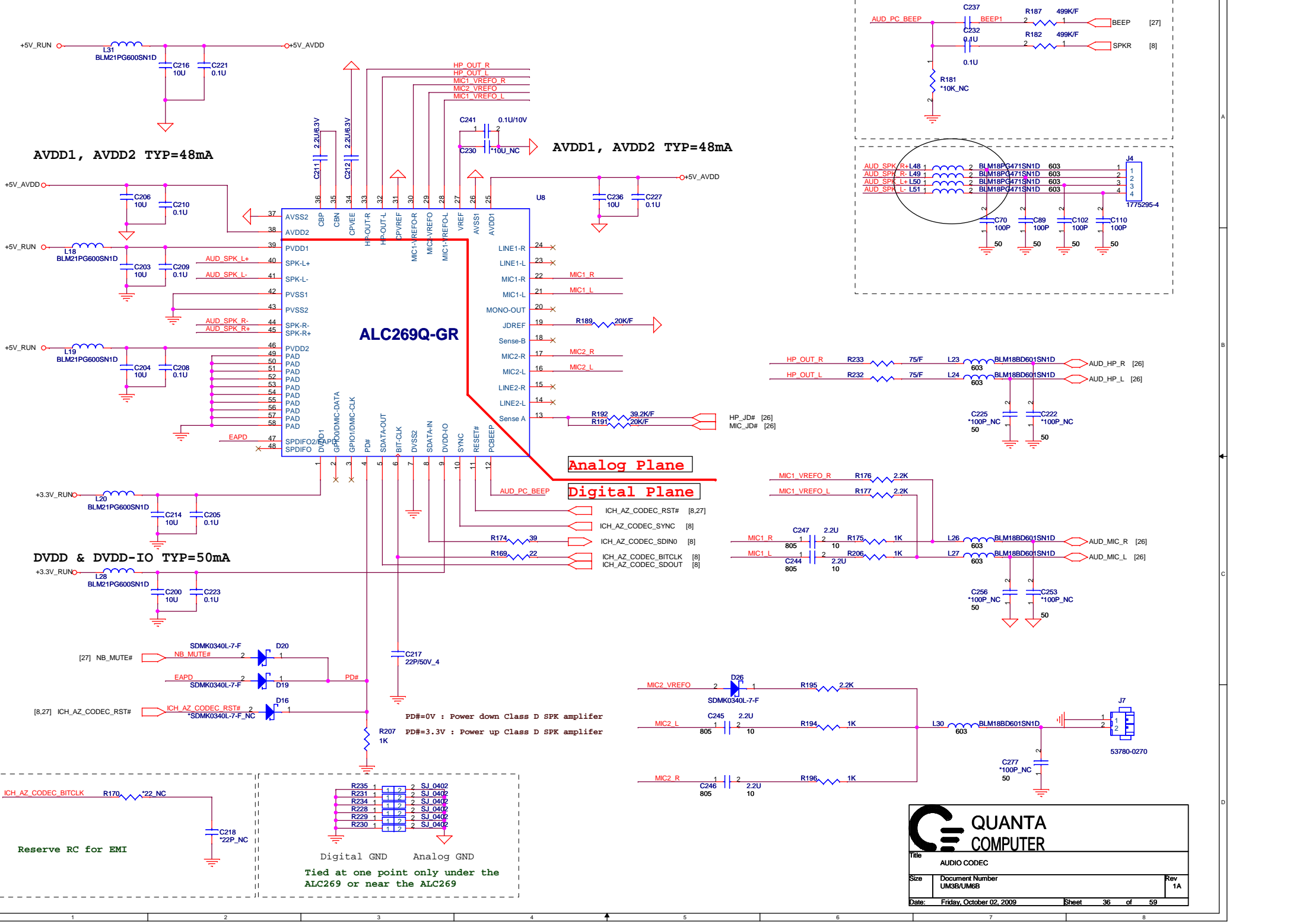



1. Place C374 close to EMC1422
 2. Place C352 to be close to Q11
- Total capacitance between D+/D- is 2200pF(max)
if use 2200pF for C374, then C352 should be dummy

OTP 85 degree C

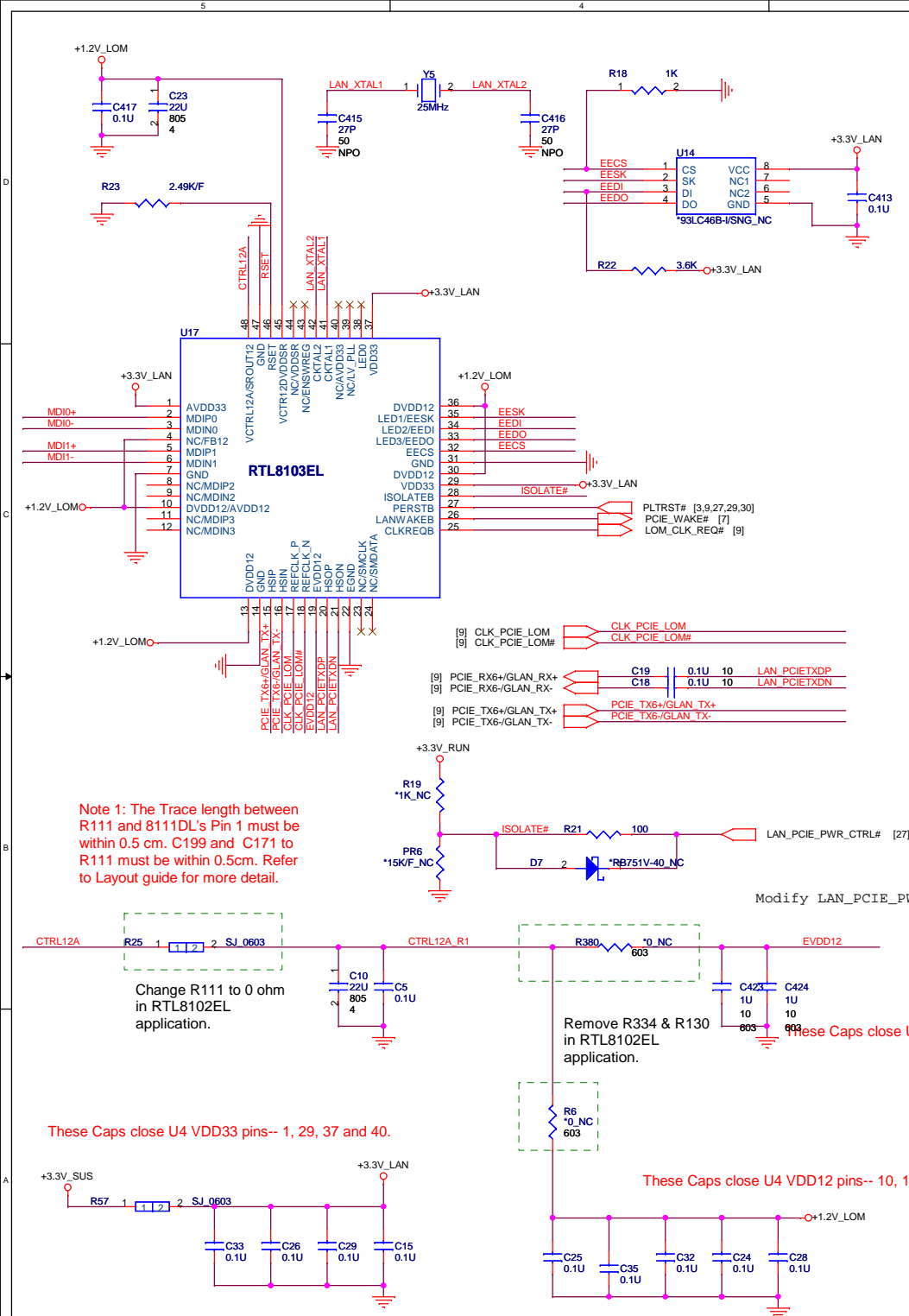


Title		
FAN & THERMAL		
Size	Document Number	Rev
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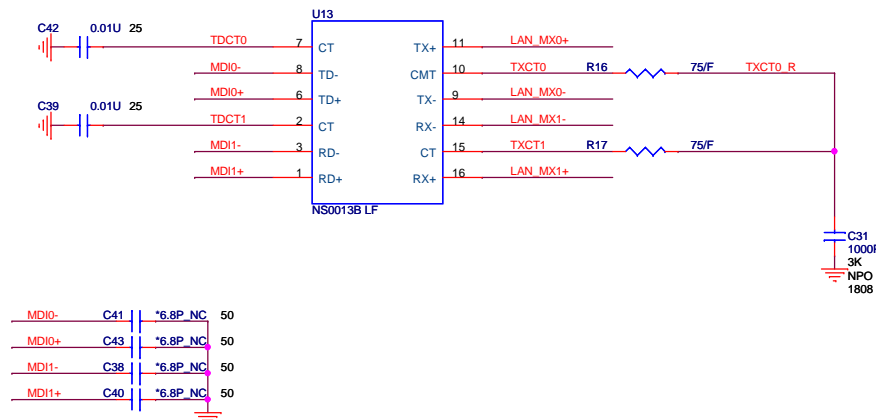


**QUANTA
COMPUTER**

Title AUDIO CODEC		
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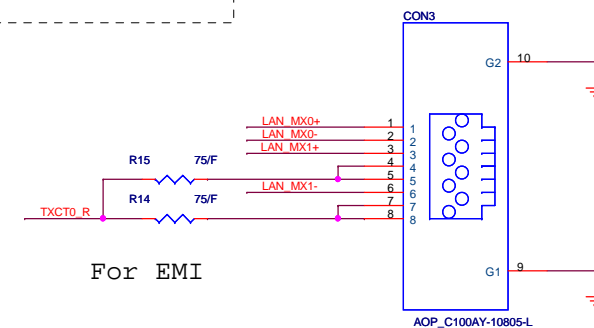


TRANSFORMER

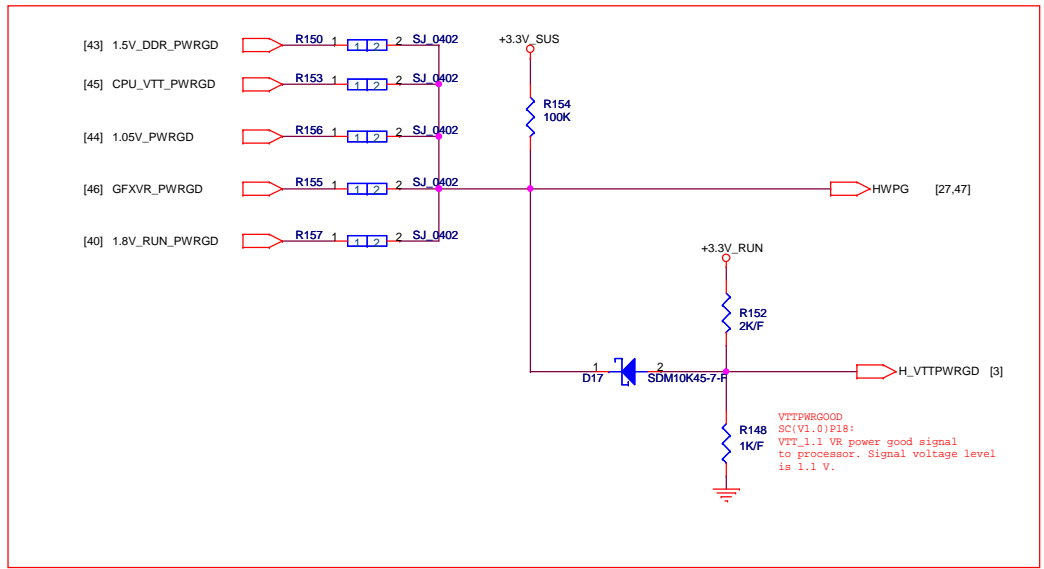


ISOLATEB
 Datasheet(V1.4)P5:
 Used to isolate the RTL8111DL
 from the PCI-E bus. RTL8111DL will not drive
 its PCI-E outputs(excluding LANWAKEB)
 and will not sample its PCI-E input
 as long as the isolate pin is asserted.
 Realtek feed back:
 進入S3,S4,S5要
 拉low 離開S3,S4,S5要拉high for WOL support

RJ-45 Connector COPY FROM UM2




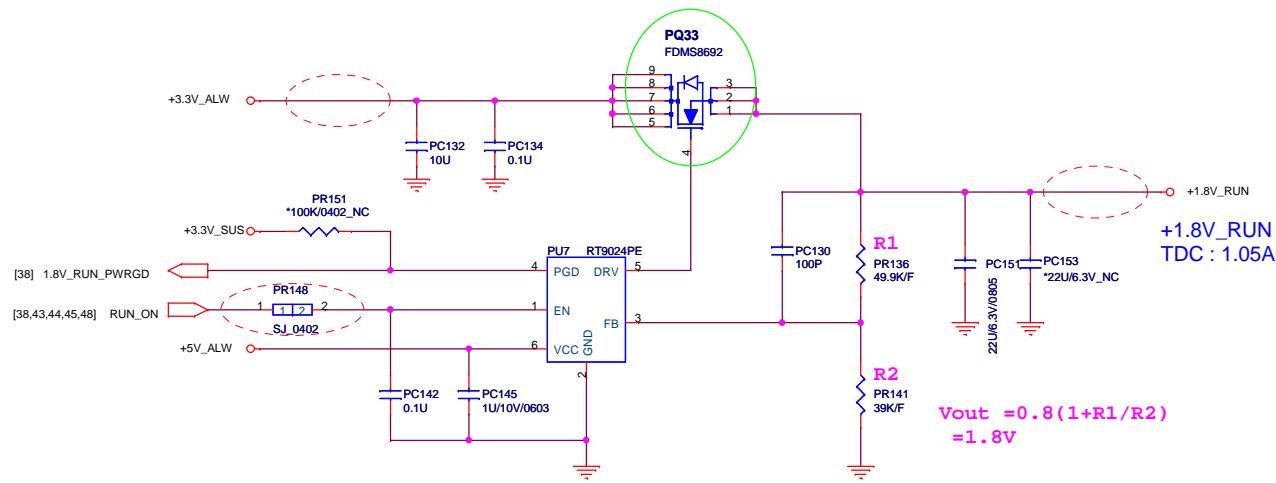
Title			LAN
Size	Document Number	Rev	
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[27] RUN_ON_1 R32 1 1 2 SJ_0402 RUN_ON [40,43,44,45,48]

	1	2	3	4	5
A					
B					
C					
D					

 QUANTA COMPUTER		
Title Battery Selector		
Size	Document Number UMGB/UMGB	Rev 1A
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+1.8V_RUN for CPU and PCH 1.8V

09/08: remove PJP11 and PJP18, change PR148 from 0 ohm to shot jump

Title		
+1.8V_RUN_GFX (RT9024PE) & +1.8V_RUN(RT9018B)		
Size	Document Number UMGB/UMGB	Rev 1A
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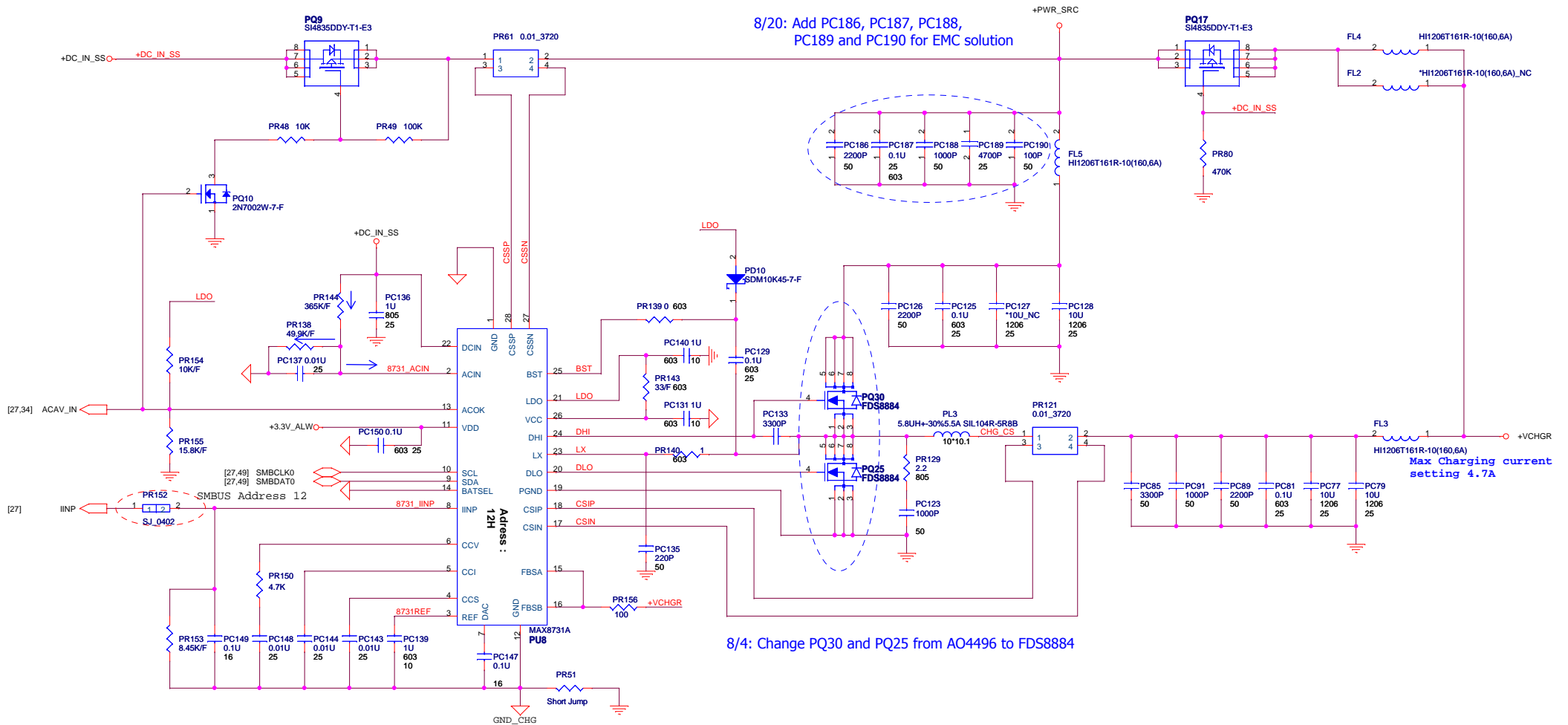
Continuous current : 13A
Rds(on) : 18mohm

Continuous current : 13A
Rds(on) : 18mohm

8/20: Add PC186, PC187, PC188,
PC189 and PC190 for EMC solution

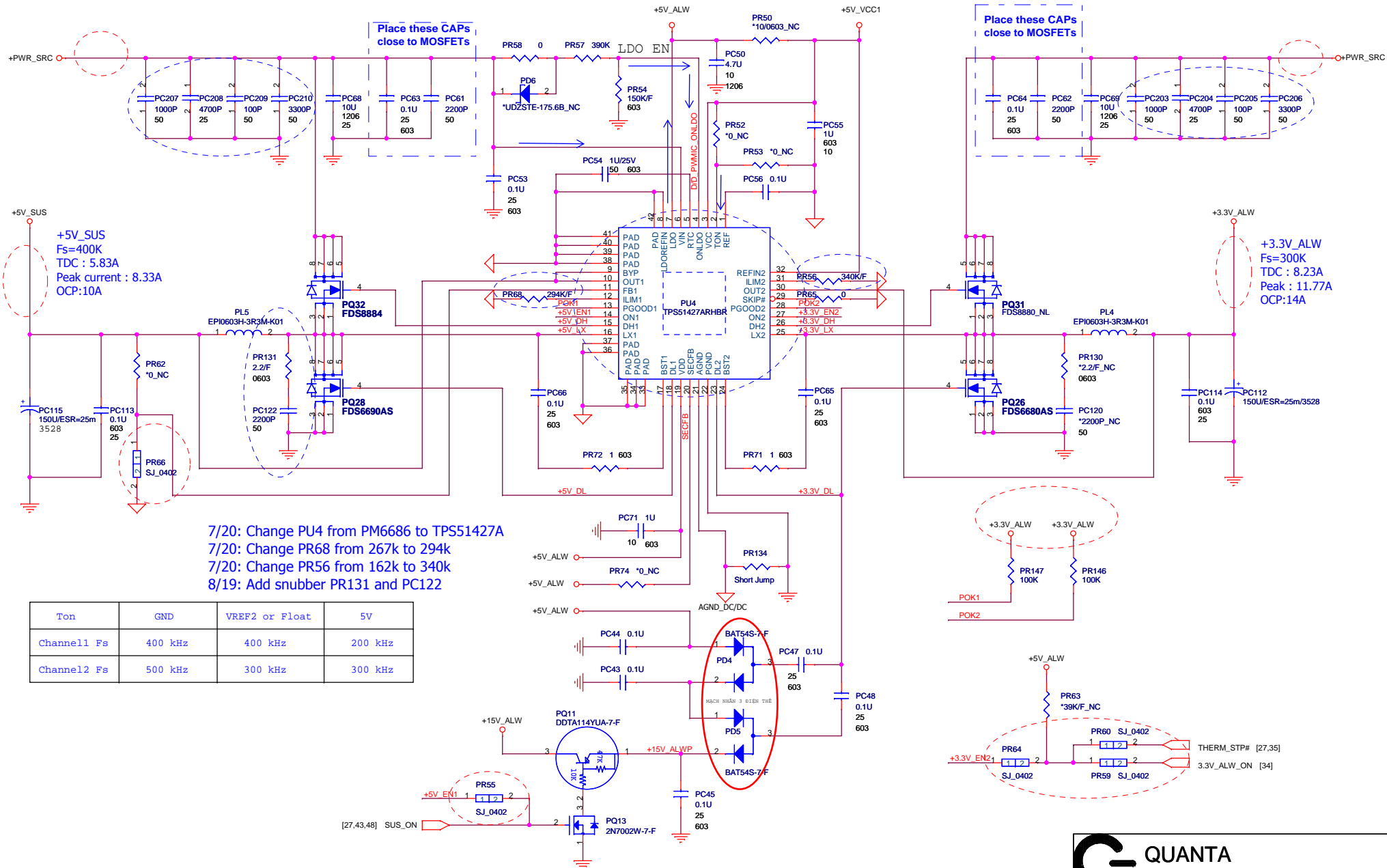
8/4: Change PQ30 and PQ25 from AO4496 to FDS8884

09/08: change PR152 from 0 ohm to shot jump

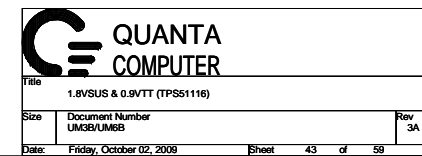


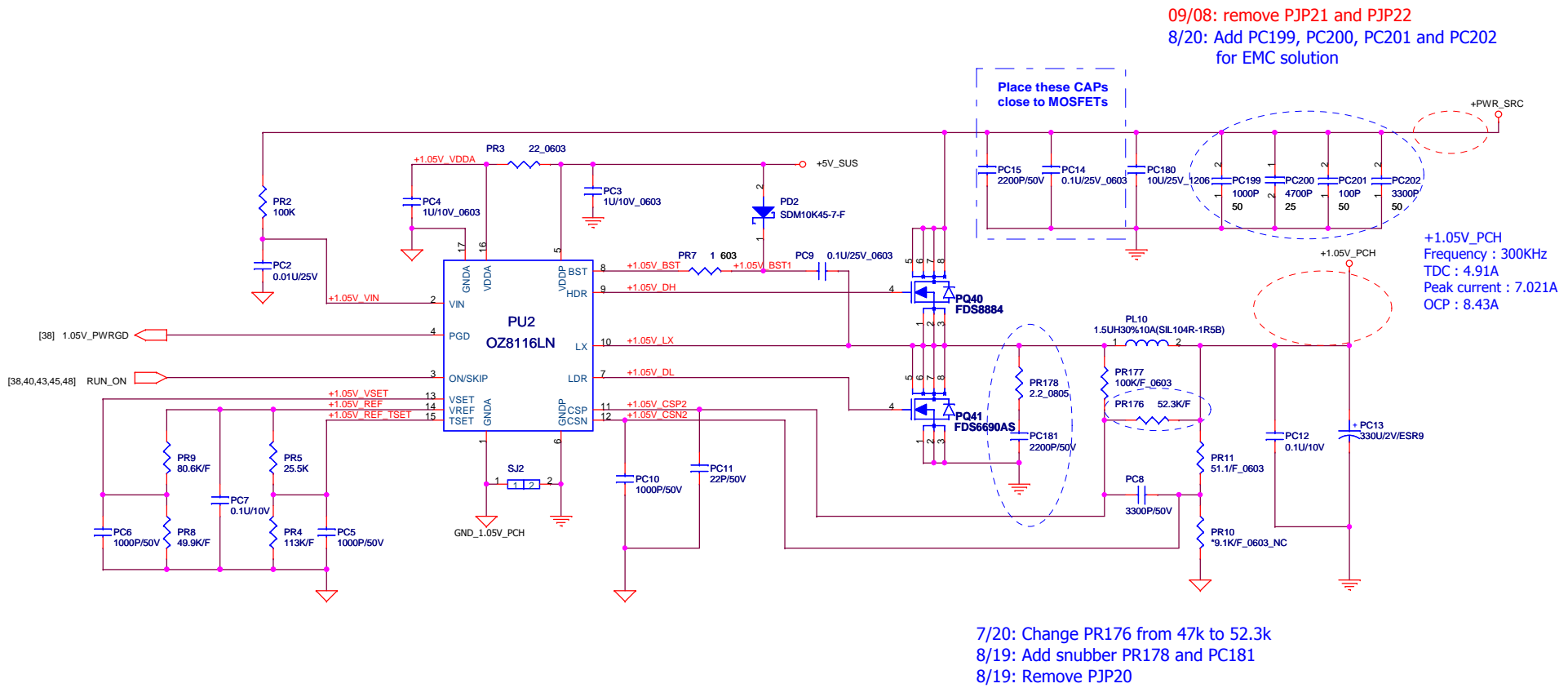
09/08: change PR59, PR60, PR64, PR66, PR55 from 0 ohm to shot jump
 09/08: remove PJP8, PJP9, PJP12 and PJP15
 8/20: Add PC207, PC208, PC209 and PC210
 for EMC solution

8/20: Add PC203, PC204, PC205 and PC206
 for EMC solution



8/20: Add PC211, PC212, PC213 and PC214
for EMC solution

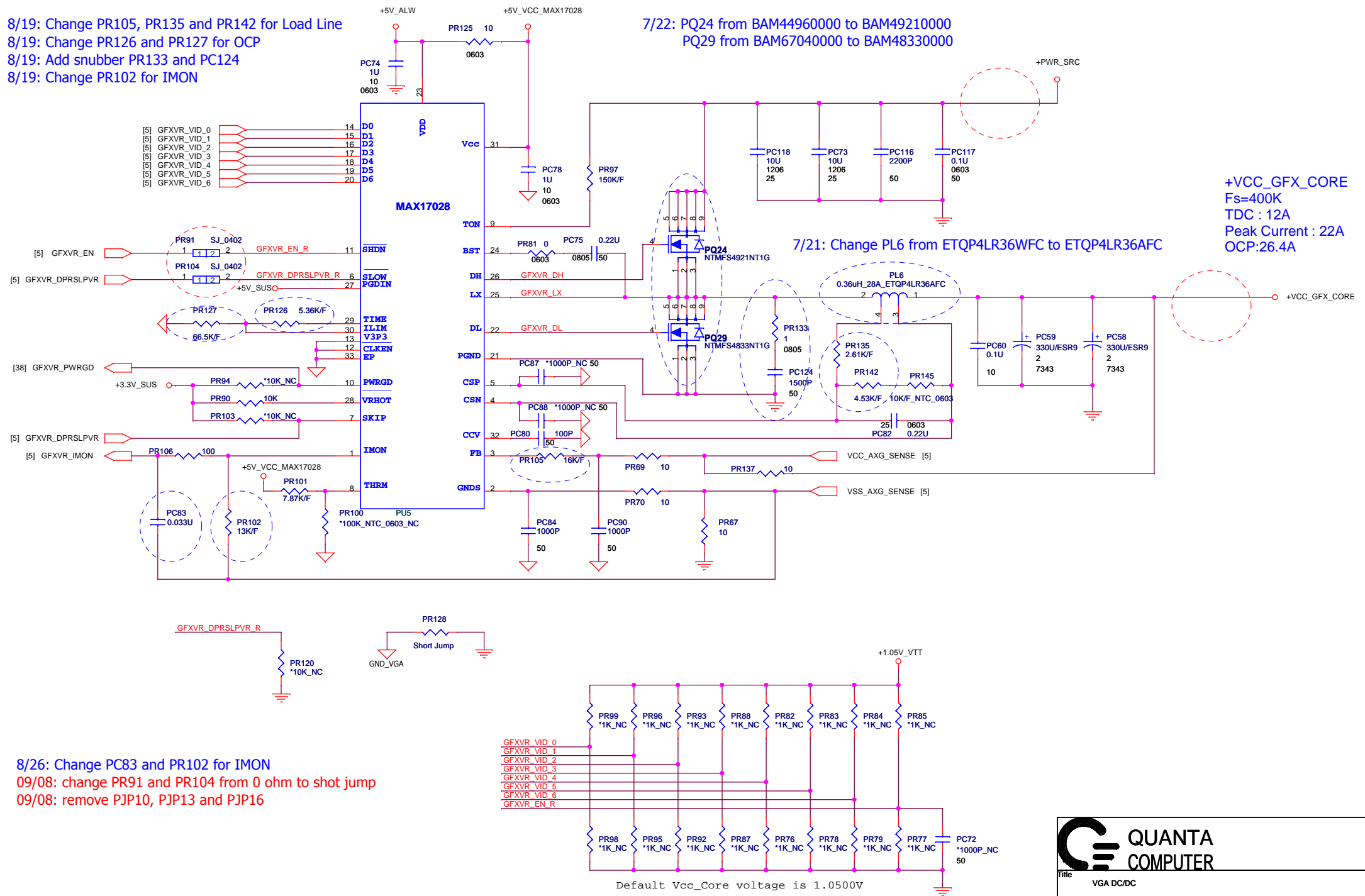




Title		
<Title>		
Size	Document Number	Rev
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8/19: Change PR105, PR135 and PR142 for Load Line
8/19: Change PR126 and PR127 for OCP
8/19: Add snubber PR133 and PC124
8/19: Change PR102 for IMON

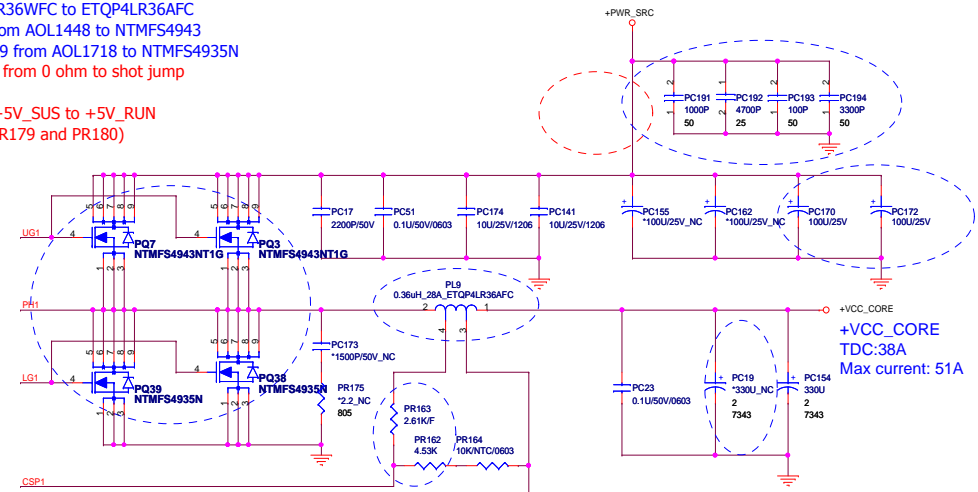
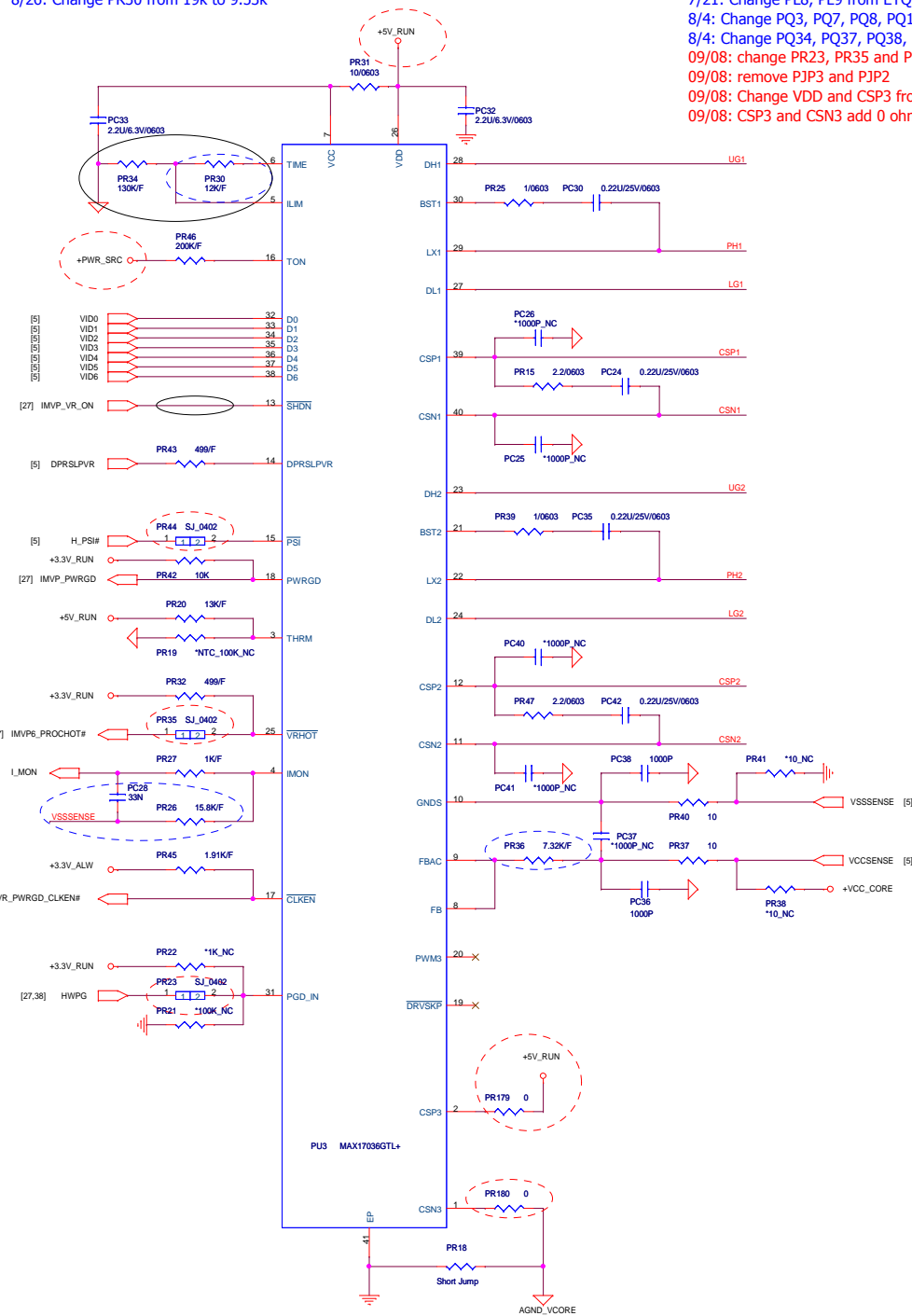
7/22: PQ24 from BAM44960000 to BAM49210000
PQ29 from BAM67040000 to BAM48330000



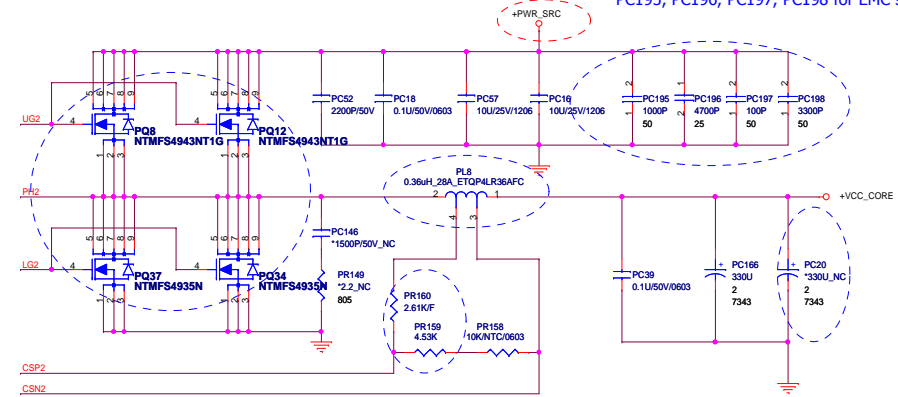
8/26: Change PC83 and PR102 for IMON
09/08: change PR91 and PR104 from 0 ohm to shot jump
09/08: remove PJP10, PJP13 and PJP16

8/26: Change PR30 from 19k to 9.53k

7/21: Change PL8, PL9 from ETQP4LR36WFC to ETQP4LR36AFC
8/4: Change PQ3, PQ7, PQ8, PQ12 from AOL1448 to NTMFS4943
8/4: Change PQ34, PQ37, PQ38, PQ39 from AOL1718 to NTMFS4935N
09/08: change PR23, PR35 and PR44 from 0 ohm to shot jump
09/08: remove PJP3 and PJP2
09/08: Change VDD and CSP3 from +5V_SUS to +5V_RUN
09/08: CSP3 and CSN3 add 0 ohm (PR179 and PR180)



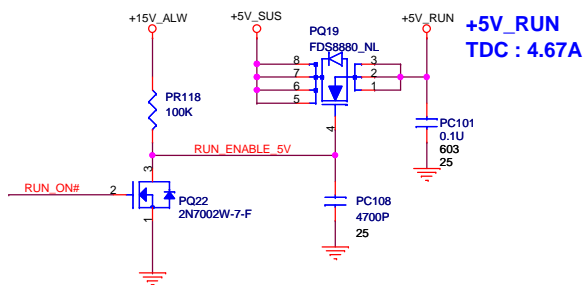
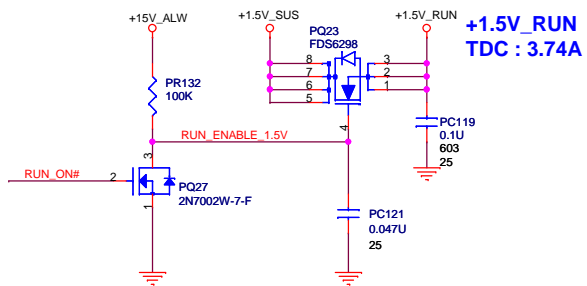
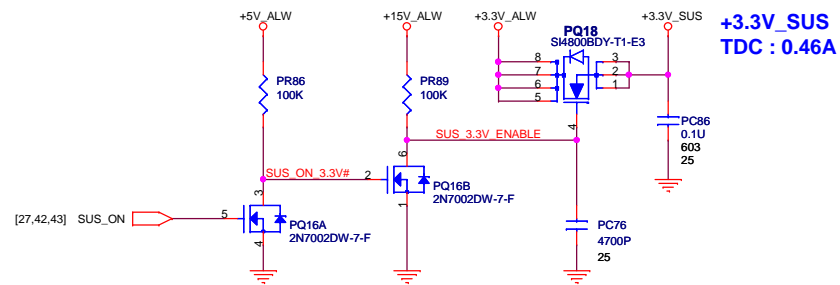
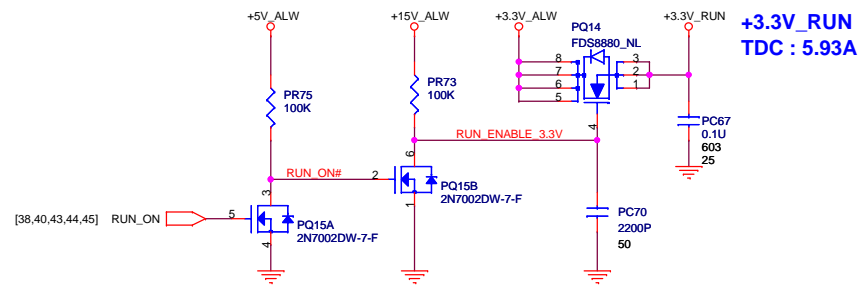
8/20: Add PC191, PC192, PC193, PC194, PC195, PC196, PC197, PC198 for EMC solution



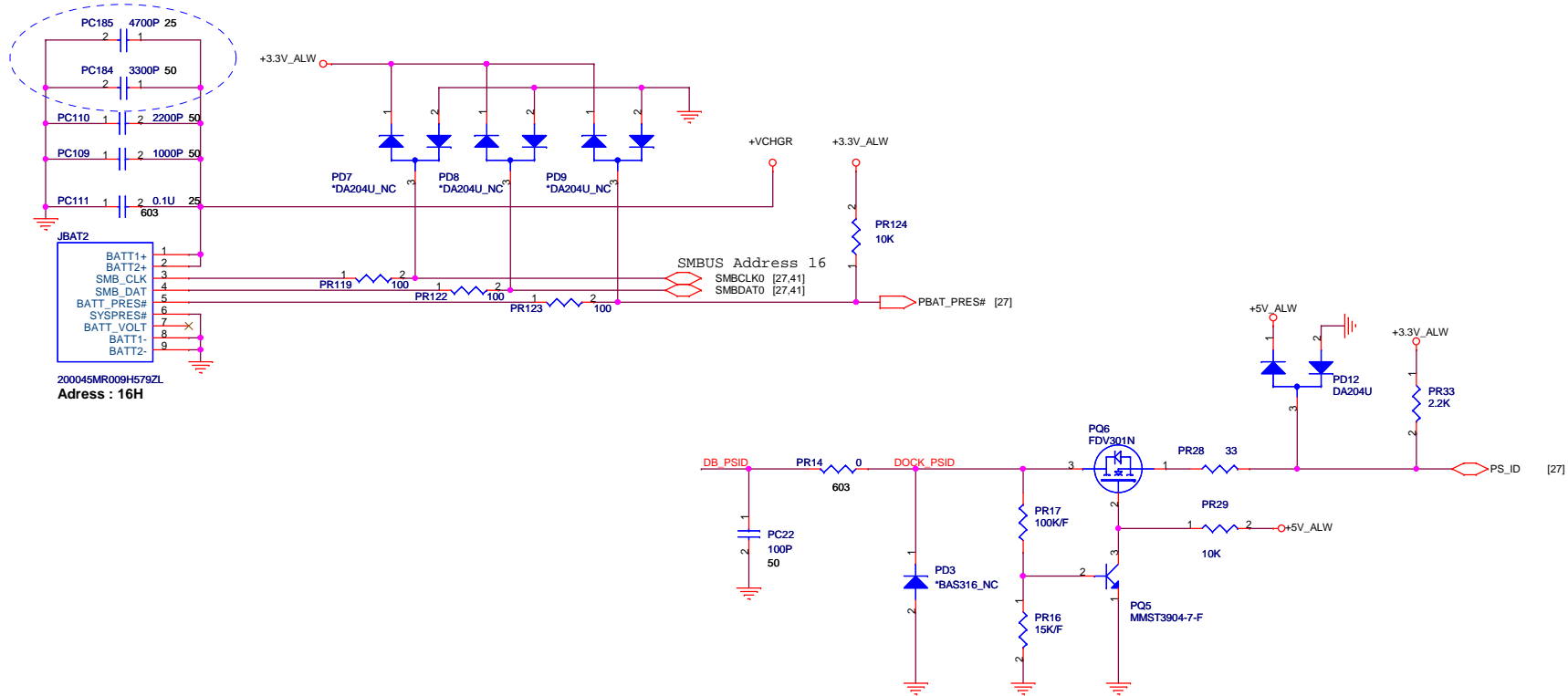
8/13: NC output cap PC19 and PC20

8/13: Change for Load Line and IMON regulator
change PR26 from 9.53k to 15.8k
change PR36 from 6.8k to 7.32k
change PR159, PR162 from 3.4k to 4.53k
change PR160, PR163 from 1.8k to 2.61k
change PC28 from 0.1uF to 33nF

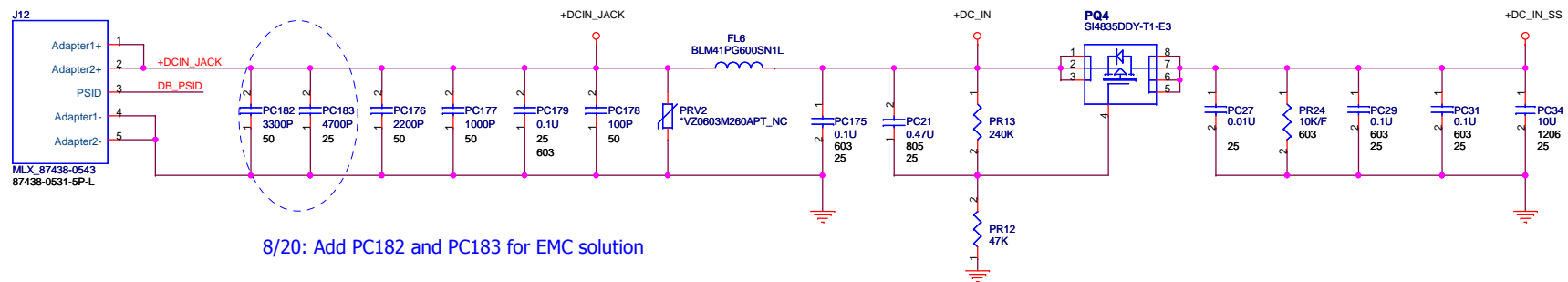
Title			
CPU core (MAX17036)			
Size	Document Number		Rev
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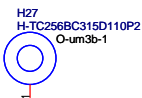
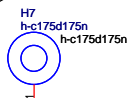
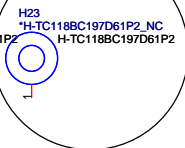
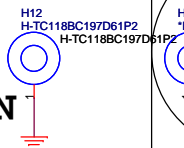
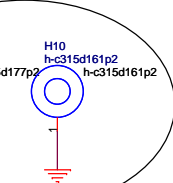
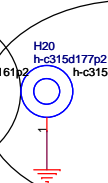
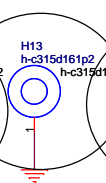
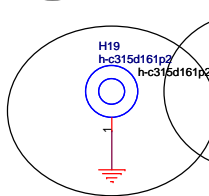
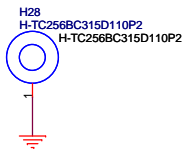
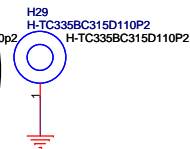
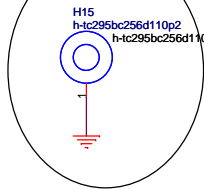
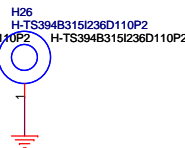
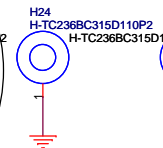
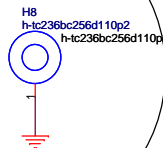
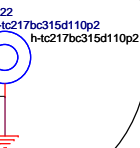
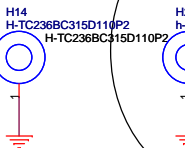
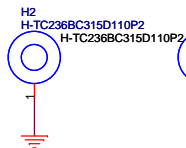
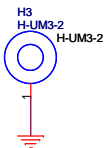
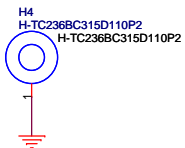
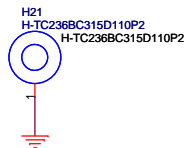
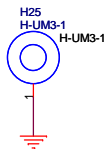
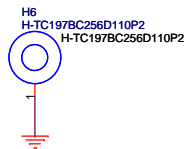
8/20: Add PC184 and PC185 for EMC solution



ZM1

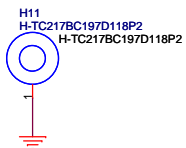
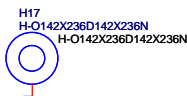


8/20: Add PC182 and PC183 for EMC solution

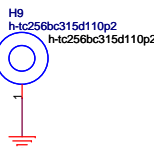
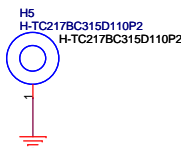


WLAN

BOT
QPN: FBFM8001010




BT TOP
QPN: FBCW4003010



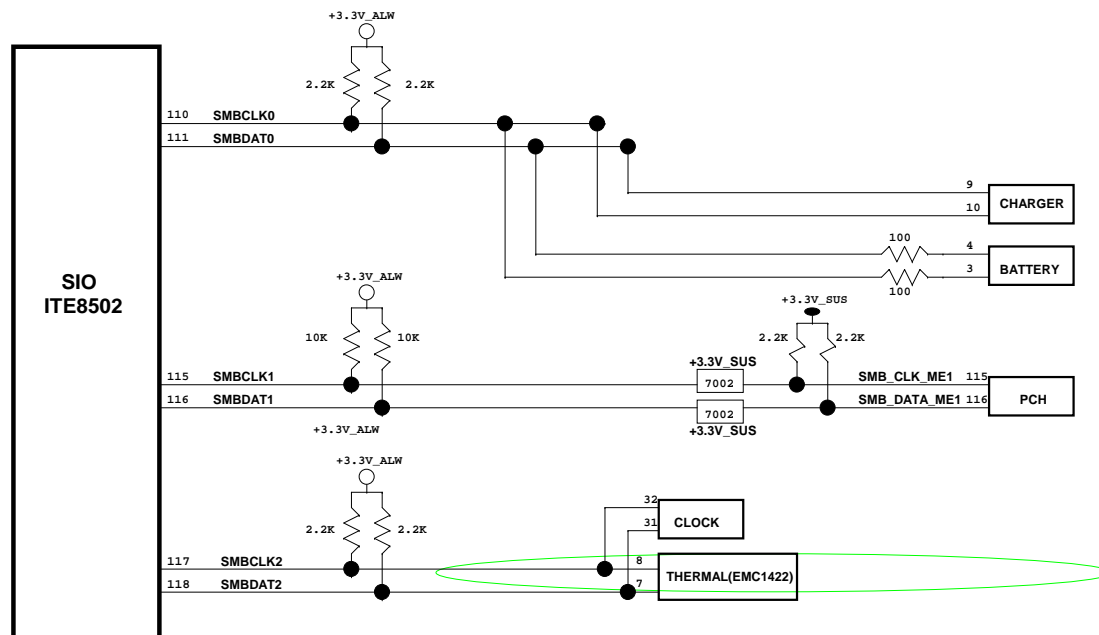
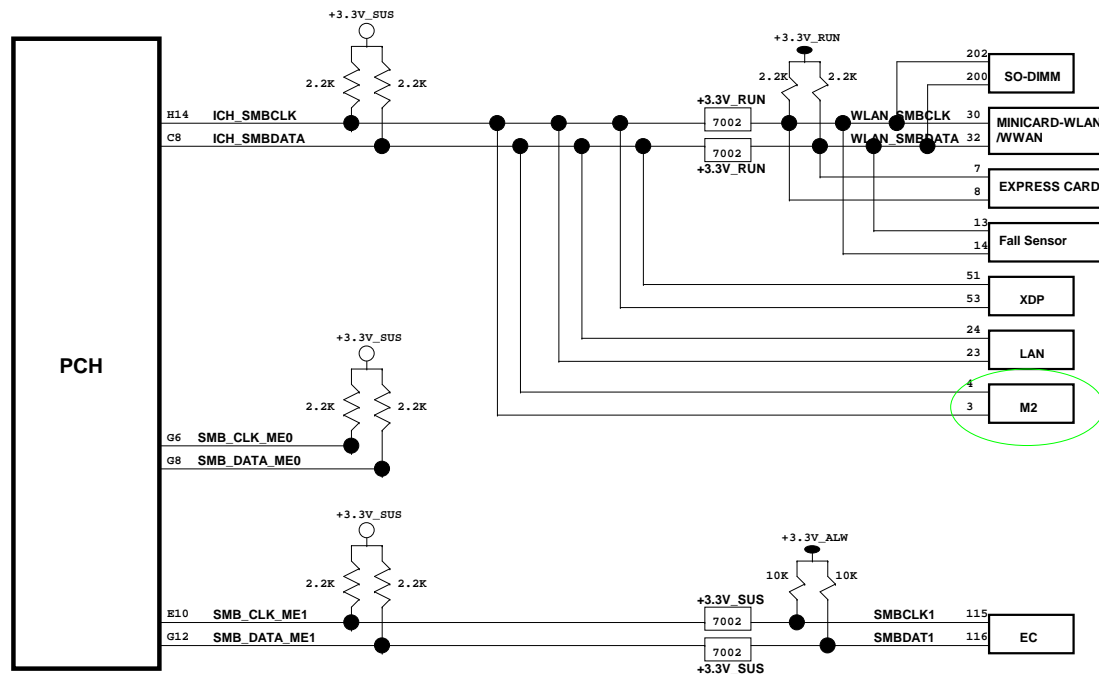
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Size	Document Number	Rev	
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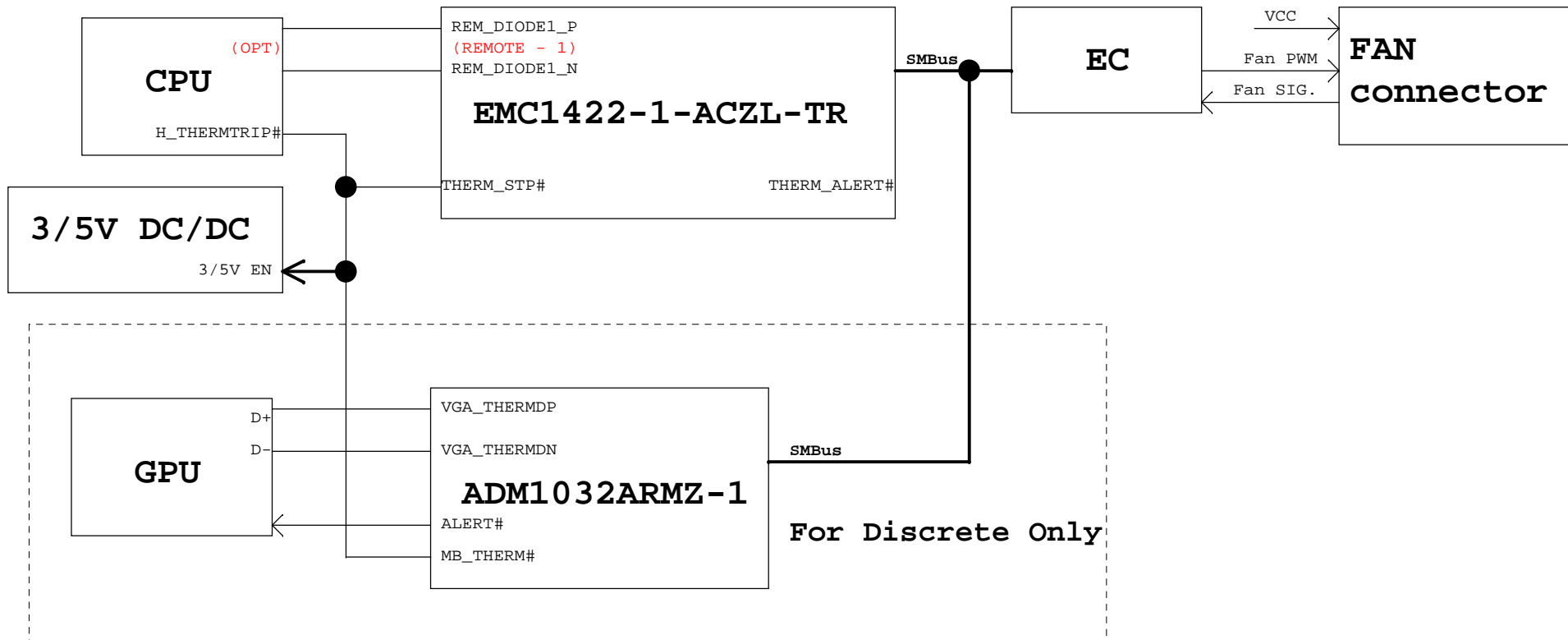
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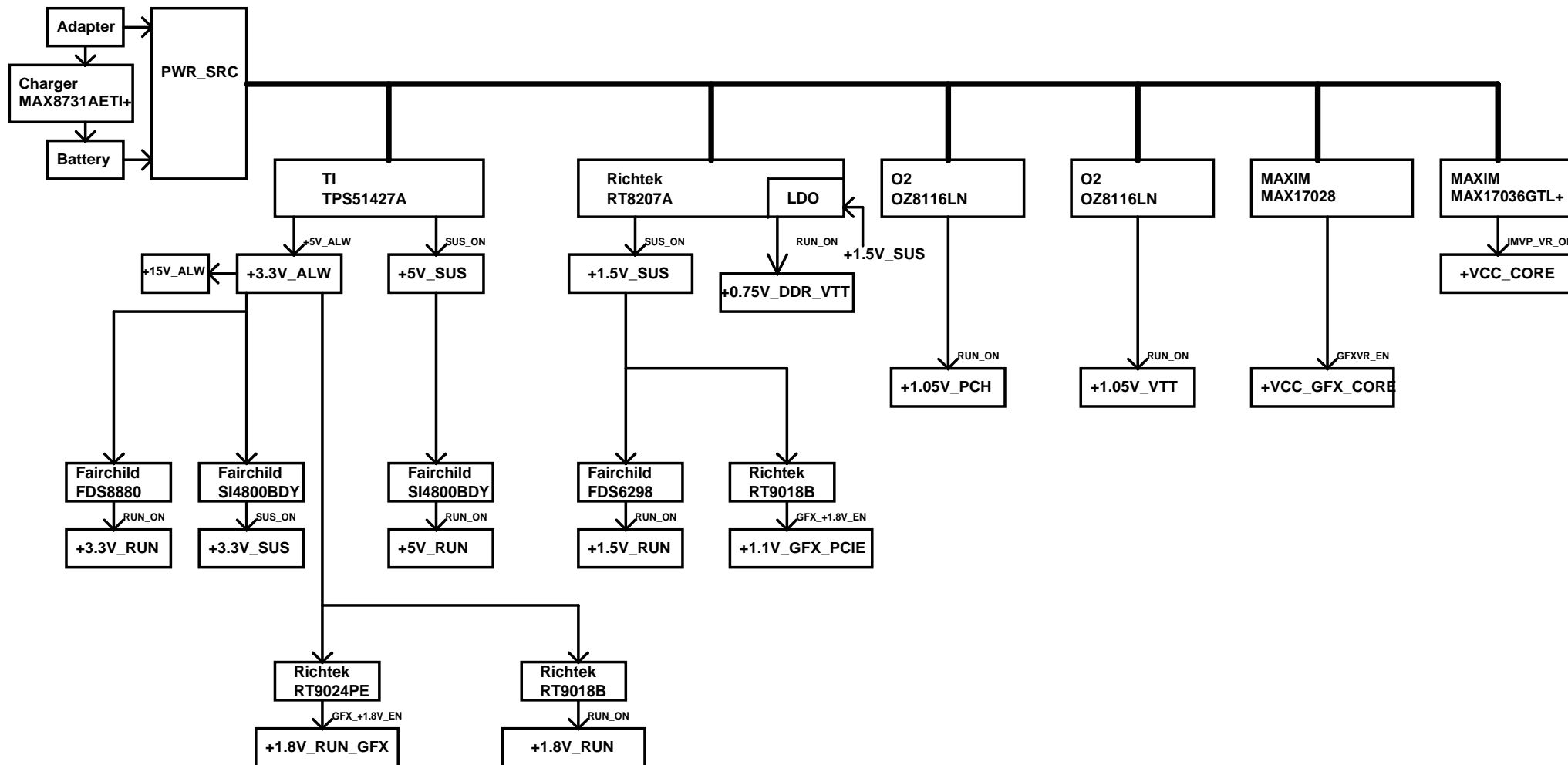


QUANTA
COMPUTER

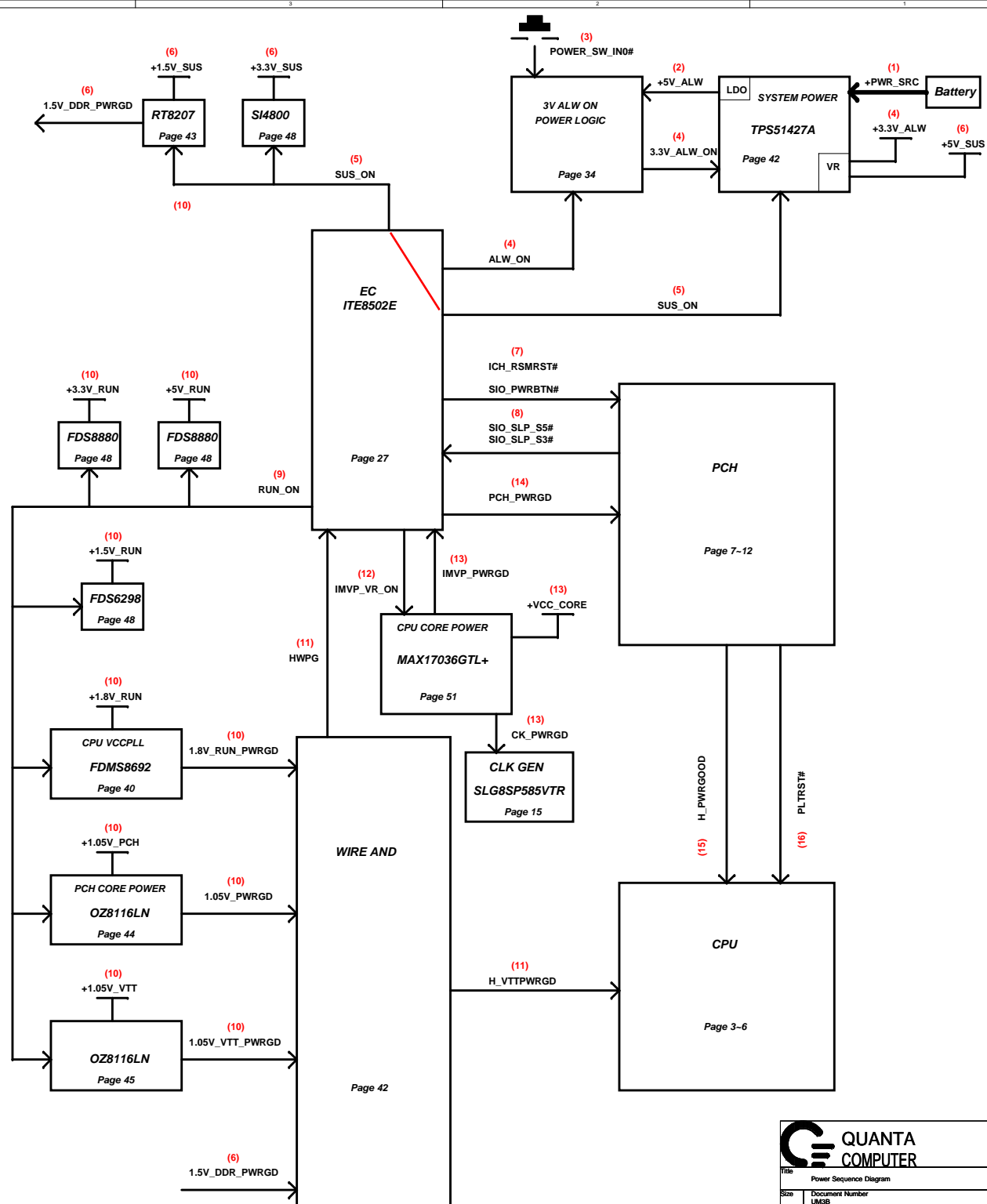
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EMI CAP		
Size	Document Number	Rev
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UM3 Power Design Block Diagram 2009/07/28



1	2	3	4	5	6	7	8
A							
B							
C							
D							
1	2	3	4	5	6	7	8



QUANTA
COMPUTER

Title		
SMBUS BLOCK		
Size	Document Number	Rev
	UM3B/UM6B	1A
Date:	Wednesday, September 30, 2009	Sheet 56 of 59