TAHITI-INTEGRATED

POWER DC/DC

- CPU VR — PG 42
- RUN POWER SW — PG 46
- DC/DC +3V_SRC +5VSUS — PG 45
- DRAM Power 1.8V, 0.9V — PG 43
- 1.5VSUS, 1.05V — PG 44
- AC/BATT CONNECTOR — PG 47
- BATT SELECTOR — PG 40
- BATT CHARGER — PG 41

Dothan
(478 Micro-FCPGA)
PG 3,4

4X133MHZ

CLOCKS — PG 15

SYSTEM RESET CKT — PG 39

Alviso-GM
1257 PCBGA
PG 5,6,7,8,9

- DDR2-SODIMM1 — PG 13,14 — 400/533 MHZ DDR II
- DDR2-SODIMM2 — PG 13,14 — 400/533 MHZ DDR II

sDVO — **SI1362** PG 16

- LVDS — **Panel Connector** PG 17
- DVI
- TVOUT — **S-Video** PG 18
- VGA — **CRT** PG 18

DMI interface

ICH6-M
609 BGA
PG 10,11,12

- USB2.0 (P5,P7) — **2 Rear Ports** PG 32
- USB2.0 (P4,P6) — **2 right Side** PG 32
- USB2.0 (P1)

- PATA — **PATA - HDD** PG 19
- SATA — **88SA8040** PG 19
- ATA 66/100 — **Internal Media Bay CD-ROM** PG 19
- USB2.0 1 port(P0)
- AC97

33MHz PCI

- **LAN (100/10) BCM4401** PG 35
- **1394 CONN** PG 22
- **CARDBUS** PCI4515 PG 21
- **PCMCIA CON.** PG 21
- **MINI-PCI** Wireless LAN PG 23

DOCKING CONNECTOR PG 38

- **E-Switch** PI3L110Q PG 36
- **I/O Board CONN** PG 32

LPC

- USB2.0 (P2) — **Bluetooth** PG 30
- **AUDIO** PG 31,32
- **MDC** PG 24
- **S/PDIF to DOCK** PG 38
- **Audio Jacks** PG 34
- **RJ11 to DOCK** PG 38
- **Tip Ring** PG 24

SIO(Macallan 3)
256 Pins LBGA
PG 25,26

DOCK LPC

X-Bus

PS/2

- **IrDA** PG 37
- **Keyboard** PG 26
- **Serial** PG 27
- **Parallel** PG 28
- **Touchpad** PG 30
- **Flash** PG 29
- **SWTICH & LED & IO CONN** PG 32
- **FAN & THERMAL** PG 31

QUANTA COMPUTER

Title	Schematic Block Diagram1	
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INDEX

Pg#	Description	DNI LIST
1	Schematic Block Diagram 1	
2	Front Page	
3-4	Dothan	
5-9	Alviso	
10-12	ICH6	
13-14	DDRII SO-DIMM(200P)	
15	Clock Generator	
16	SI1362	
17	LCD Conn. & SSP	
18	CRT & TV Conn.	
19	SATA & IDE Conn.	
20	PAD & Screw Hole	
21	TI PIC4510	
22	CB/1394 CONN	
23	Mini PCI Conn.	
24	MDC Conn.	
25-26	SIO (LPC47N354)	
27	Parallel Port	
28	Serial Port	
29	Flash ROM	
30	Touch Pad CONN.& Bluetooth CONN	
31	Switch Board Conn. & LED & IO Board	
32	FAN & Thermal	
33-34	Audio CODEC (STAC9751) & Phone Jack	
35-36	LAN Interface	
37	FIR	
38	MISCELLANEA	
39	Docking Conn.	
40	SYSTEM RESET/POWER GOOD	
41-42	Battery Selector & Charger	
43	CPU Power	
44	1.8VSUS/0.9V	
45	1.5V/1.05V	
46	D/D Power	
47	RUN Power Switch	
48	RUN POWER SW	

Power & Ground

Label	Pg#	Description	Control Signal
DC_IN+		AC ADAPTER (20V)	
PBATT+		MAIN BATTERY + (10~17V)	
PWR_SRC		MAIN POWER (10~20V)	
VHCORE		CPU CORE POWER (1.25/1.15V)	RUNPWROK
1.05V		AGTL+ POWER (1.05V) I/O	RUNPWROK
+3VRUN		SLP_S3# CTRLD POWER	RUN_ON
+3VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VALW		8051 POWER (5V)	
+5VRUN		SLP_S3# CTRLD POWER	RUN_ON
+5VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VHDD		HDD POWER (5V)	HDDC_EN#
+5VMOD		MODULE POWER (5V)	MODC_EN#
STRB#/5V		EXTERNAL FDD POWER (5V)	FDD/LPT#
+5VRUN		FAN POWER (5V)	FAN_OFF/ON#
VDDA		AUDIO ANALOG POWER (5V)	RUN_ON
1_8VSUS		RESUME WELL IN ICH	
1_8VRUN		SLP_S3# CTRLD POWER	
+3VALW		8051 POWER (3V)	
V1_5RUN		ALVISO POWER Non-CPU I/O	
GND	ALL PAGES	DIGITAL GROUND	
		COMBO CONN GND	

QUANTA COMPUTER

Title	Index, DNI, Power & Ground	
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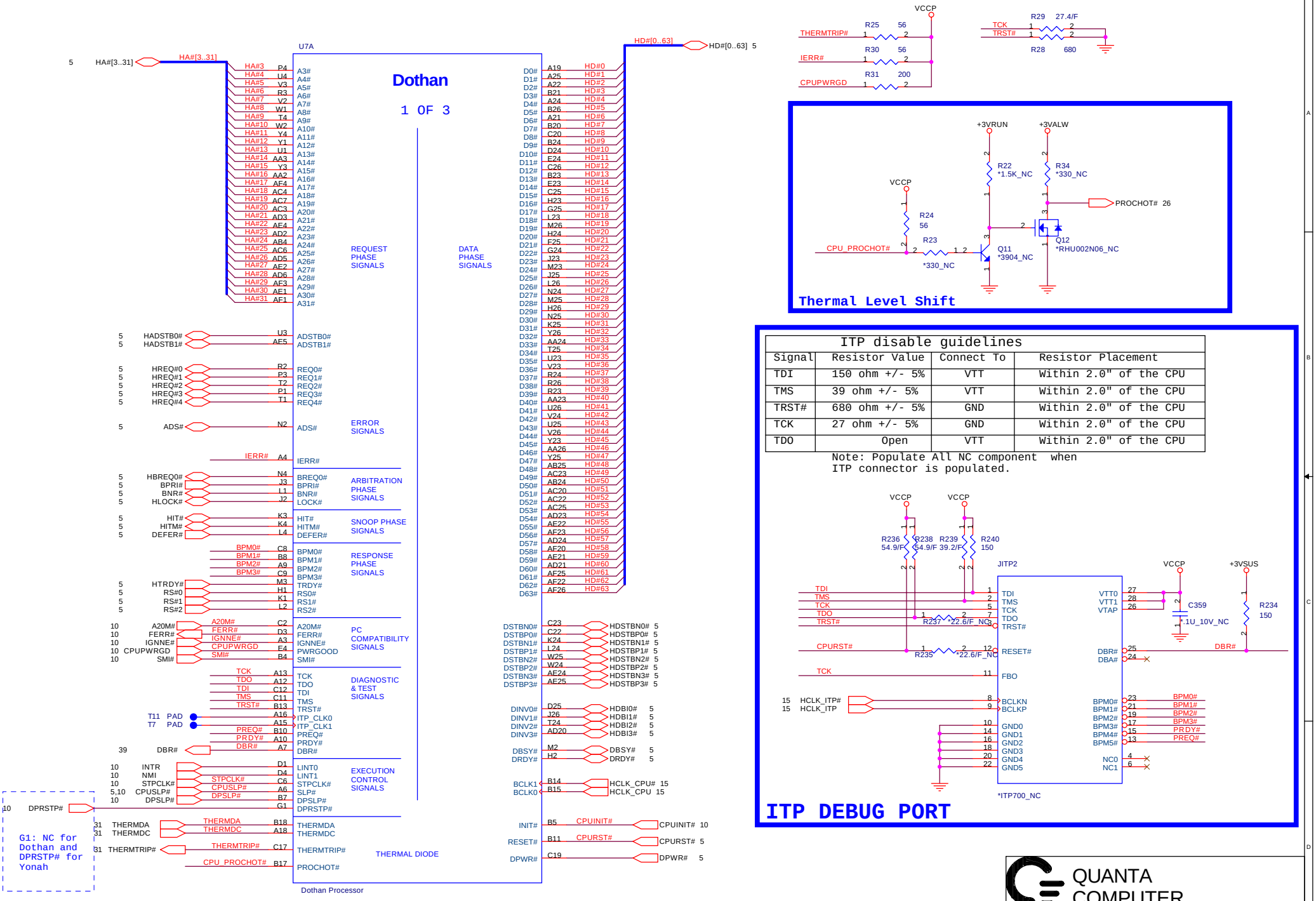

ITP disable guidelines			
Signal	Resistor Value	Connect To	Resistor Placement
TDI	150 ohm +/- 5%	VTT	Within 2.0" of the CPU
TMS	39 ohm +/- 5%	VTT	Within 2.0" of the CPU
TRST#	680 ohm +/- 5%	GND	Within 2.0" of the CPU
TCK	27 ohm +/- 5%	GND	Within 2.0" of the CPU
TDO	Open	VTT	Within 2.0" of the CPU

Note: Populate All NC component when ITP connector is populated.

QUANTA COMPUTER

Title	Dothan Processor (HOST)	
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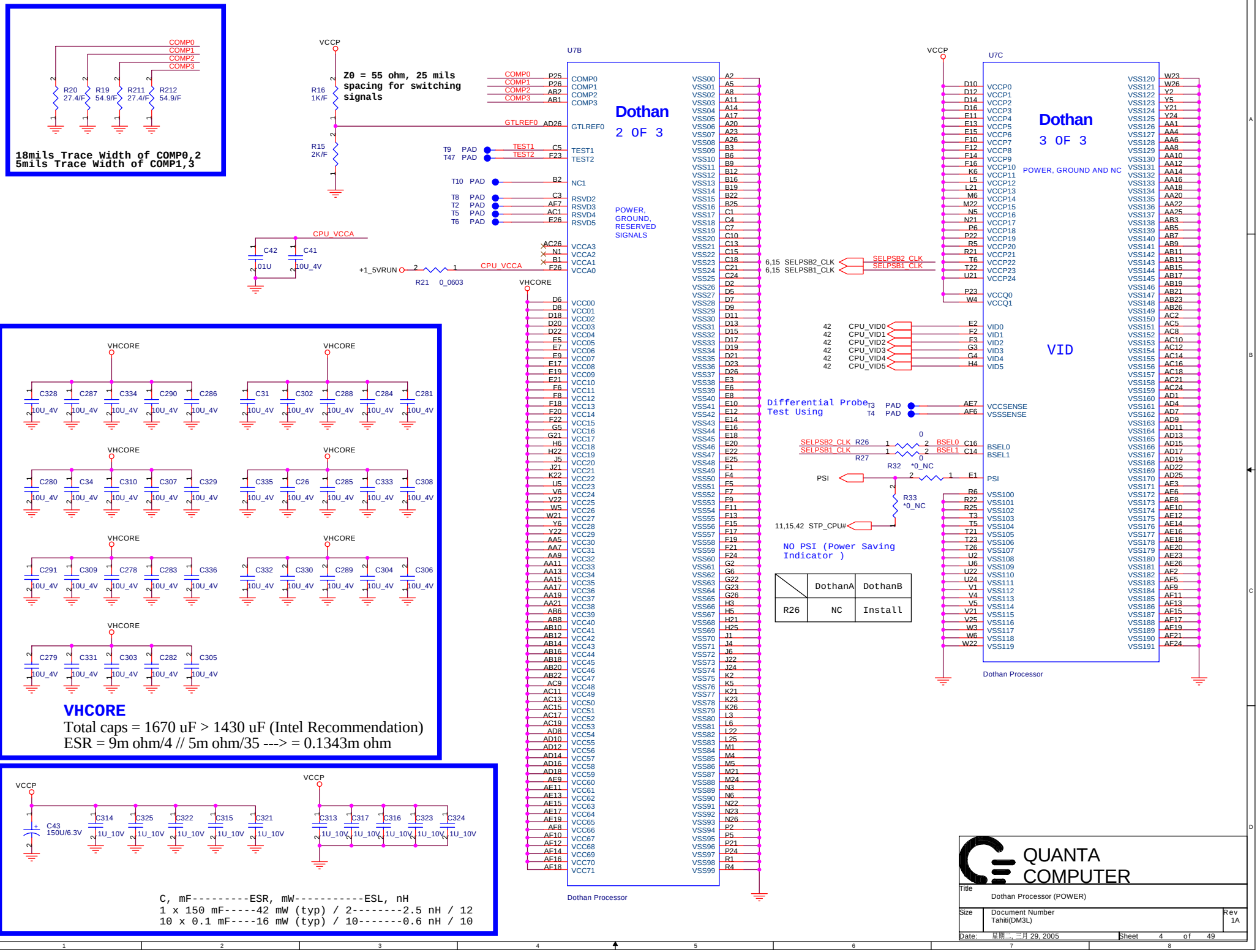
Date: 星期二, 三月 29, 2005	Sheet 3 of 49	


COMP0
COMP1
COMP2
COMP3
R20 27.4/F
R19 54.9/F
R211 27.4/F
R212 54.9/F
18mils Trace Width of COMP0,2
5mils Trace Width of COMP1,3
VCCP
R16 1K/F
R15 2K/F
Z0 = 55 ohm, 25 mils
spacing for switching
signals
GTLREF0
T9 PAD
T47 PAD
TEST1
TEST2
T10 PAD
NC1
T8 PAD
T2 PAD
T5 PAD
T6 PAD
RSVD2
RSVD3
RSVD4
RSVD5
CPU_VCCA
C42 01U
C41 10U_4V
+1_5VRUN
R21 0_0603
VCCA3
VCCA2
VCCA1
VCCA0
VHCORE
U7B
Dothan
2 OF 3
POWER, GROUND, RESERVED SIGNALS
Dothan Processor
U7C
Dothan
3 OF 3
POWER, GROUND AND NC
VID
Differential Probe Test Using
T3 PAD
T4 PAD
VCCSENSE
VSSSENSE
SELPSB2_CLK R26
SELPSB1_CLK
R27
BSEL0
BSEL1
PSI
R32 *0_NC
R33 *0_NC
11,15,42 STP_CPU#
NO PSI (Power Saving Indicator)
DothanA
DothanB
R26 NC Install
Dothan Processor
C328 C287 C334 C290 C286
C31 C302 C288 C284 C281
C280 C34 C310 C307 C329
C335 C26 C285 C333 C308
C291 C309 C278 C283 C336
C332 C330 C289 C304 C306
C279 C331 C303 C282 C305
10U_4V
VHCORE
Total caps = 1670 uF > 1430 uF (Intel Recommendation)
ESR = 9m ohm/4 // 5m ohm/35 ---> = 0.1343m ohm
C43 150U/6.3V
C314 C325 C322 C315 C321
C313 C317 C316 C323 C324
1U_10V
C, mF---------ESR, mW----------ESL, nH
1 x 150 mF-----42 mW (typ) / 2--------2.5 nH / 12
10 x 0.1 mF----16 mW (typ) / 10-------0.6 nH / 10
QUANTA COMPUTER
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Dothan Processor (POWER)
Size
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Tahiti(DM3L)
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1A
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HXRCOMP
R44
24.9/F

One pulled-down resistor per pin. Trace should be 10-mil wide with 20-mil spacing.

VCCP
R43
54.9/F
HXSCOMP

VCCP
R48
221/F
HXSWING
R50
100/F
C72
.1U_10V

Signal voltage level = 0.3125*VCCP. C1a=0.1 µF. C1b=0.1 µF.Trace should be 10-mil wide with 20-mil spacing..

HYRCOMP
R63
24.9/F

One pulled-down resistor per pin. Trace should be 10-mil wide with 20-mil spacing.

VCCP
R51
54.9/F
HYSCOMP

VCCP
R53
221/F
HYSWING
R56
100/F
C82
.1U_10V

Signal voltage level = 0.3125*VCCP. C1a=0.1 µF. C1b=0.1 µF.Trace should be 10-mil wide with 20-mil spacing..

Concern about Trace Length and Width

U11A

HD#[0..63] HD#0 HD#1 HD#2 HD#3 HD#4 HD#5 HD#6 HD#7 HD#8 HD#9 HD#10 HD#11 HD#12 HD#13 HD#14 HD#15 HD#16 HD#17 HD#18 HD#19 HD#20 HD#21 HD#22 HD#23 HD#24 HD#25 HD#26 HD#27 HD#28 HD#29 HD#30 HD#31 HD#32 HD#33 HD#34 HD#35 HD#36 HD#37 HD#38 HD#39 HD#40 HD#41 HD#42 HD#43 HD#44 HD#45 HD#46 HD#47 HD#48 HD#49 HD#50 HD#51 HD#52 HD#53 HD#54 HD#55 HD#56 HD#57 HD#58 HD#59 HD#60 HD#61 HD#62 HD#63

HXRCOMP HXSCOMP HXSWING HYRCOMP HYSCOMP HYSWING

HOST

HA#[3..31] HA3# HA4# HA5# HA6# HA7# HA8# HA9# HA10# HA11# HA12# HA13# HA14# HA15# HA16# HA17# HA18# HA19# HA20# HA21# HA22# HA23# HA24# HA25# HA26# HA27# HA28# HA29# HA30# HA31#

HADS# HADSTB0# HADSTB1# HVREF HBNR# HBPRI# BREQ0# HCPURST#

HCLKINN HCLKINP

HDBSY# HDEFER# HDINV#0 HDINV#1 HDINV#2 HDINV#3 HDPWR# HDRDY# HDSTBN0# HDSTBN1# HDSTBN2# HDSTBN3# HDSTBP0# HDSTBP1# HDSTBP2# HDSTBP3# HEDRDY# HHIT# HHITM# HLOCK# HPCREQ# HREQ0# HREQ1# HREQ2# HREQ3# HREQ4# HRS0# HRS1# HRS2# HCPUSLP# HTRDY#

ALVISO

HA#[3..31] HA#[3..31] 3

ADS# 3
HADSTB0# 3
HADSTB1# 3
BNR# 3
BPRI# 3
HBREQ0# 3
CPURST# 3

HCLK_MCH# 15
HCLK_MCH 15

DBSY# 3
DEFER# 3
HDBI0# 3
HDBI1# 3
HDBI2# 3
HDBI3# 3
DPWR# 3
DRDY# 3
HDSTBN0# 3
HDSTBN1# 3
HDSTBN2# 3
HDSTBN3# 3
HDSTBP0# 3
HDSTBP1# 3
HDSTBP2# 3
HDSTBP3# 3

T77 PAD
HIT# 3
HITM# 3
HLOCK# 3
T15 PAD

HREQ#0 3
HREQ#1 3
HREQ#2 3
HREQ#3 3
HREQ#4 3
RS#0 3
RS#1 3
RS#2 3

HCPUSLP# GMCH
HTRDY# 3

R37
0
CPUSLP# 3,10

Do not install R37 for Dothan-A and install for Dothan-B

VCCP
R284
100/F
HVREF
C381
.1U_10V
R276
200/F

Signal voltage level = 2/3 of VCCP. One 0.1 µF decoupling capacitor should be placed 100 mils or less from GMCH pin.

Concern about HVREF Trace Length & Width

QUANTA COMPUTER

Title	Alviso (Host)	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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CFG[2:0]
001=FSB533
010=FSB800
101=FSB400

CFG6
R254
2.2K
Low=DDR2
High=DDR1

VCCP
R286
10K

U11C

DMI
DMIRXN0
DMIRXN1
DMIRXN2
DMIRXN3
DMIRXP0
DMIRXP1
DMIRXP2
DMIRXP3
DMITXN0
DMITXN1
DMITXN2
DMITXN3
DMITXP0
DMITXP1
DMITXP2
DMITXP3

DDR MUXING
SM_CK0
SM_CK1
SM_CK2
SM_CK3
SM_CK4
SM_CK5
SM_CK0#
SM_CK1#
SM_CK2#
SM_CK3#
SM_CK4#
SM_CK5#
SM_CKE0
SM_CKE1
SM_CKE2
SM_CKE3
SM_CS0#
SM_CS1#
SM_CS2#
SM_CS3#
SM_OCDCOMP0
SM_OCDCOMP1
SM_ODT0
SM_ODT1
SM_ODT2
SM_ODT3
SMRCOMPN
SMRCOMPP
SMVREF0
SMVREF1
SMXSLEWIN
SMXSLEWOUT
SMYSLEWIN
SMYSLEWOUT

CFG/RSVD
PM
LCK
NC

ALVISO

SMDDR_VREF
C714 1U_10V
C715 1U_10V
Place these 2 Caps near Alviso SMVREF pins.

R314 *40.2/F_NC
R315 *40.2/F_NC

+1_8VSUS
R326 80.6/F
M_RCOMPN
M_RCOMPP
R318 80.6/F
On-Die OCD driver compensation (DDR2 only)

R273 *2.2K_NC
Low=DMIx2
High=DMIx4

R269 *1K_NC
R271 *1K_NC
+2.5VRUN

R49 56

PM_BMBUSY# 11
PM_EXTTS#0
PM_EXTTS#1
THERMTRIP_GMCH# 31
IMVP_PWRGD 11,39,42
PLTRST# 10,11,16,19,25
PLTRST# R
R312 100

REFCLK/SSCLK
DOT96#
DOT96
DOT96# 15
DOT96 15
DREFSSCLK# R
DREFSSCLK R

RP2
DREFSSCLK# R
DREFSSCLK R
DREFSSCLK# 17
DREFSSCLK 17
*4P2R-S-0_NC

RP12
DOT100#_SS 15
DOT100_SS 15
4P2R-S-0

Populate RP2 if using CK-SSCD.
Populate Rp12 if using CK410M

U11F

MISC
SDVOCTRL_DATA
SDVOCTRL_CLK
GCLKN
GCLKP

TV
TVDAC_A
TVDAC_B
TVDAC_C
TV_REFSET
TV_IRTNA
TV_IRTNB
TV_IRTNC
R277 4.99K/F
R241 150/F
R248 150/F
R243 150/F

VGA
DDCCLK
DDCDATA
BLUE
BLUE#
GREEN
GREEN#
RED
RED#
VSYNC
HSYNC
REFSET
R267 *150/F_NC
R251 *150/F_NC
R242 *150/F_NC
R275 39
R274 39
R285 255/F

LVDS
LBKLT_CTRL
LBKLT_EN
LCTLA_CLK
LCTLB_DATA
LDDC_CLK
LDDC_DATA
LVDD_EN
LIBG
LVBG
LVREFH
LVREFL
R247 1.5K/F
LACLKN
LACLKP
LBCLKN
LBCLKP
LADATAN0
LADATAN1
LADATAN2
LADATAP0
LADATAP1
LADATAP2
LBDATAN0
LBDATAN1
LBDATAN2
LBDATAP0
LBDATAP1
LBDATAP2

PCI-EXPRESS GRAPHICS
EXP_COMPI
EXP_ICOMPO
R268 24.9/F
VCC3G_PCIE
VCC3G_PCIE_R
SDVOB_INT- 16
SDVOB_INT+ 16
SDVOB_R- C
SDVOB_G- C
SDVOB_B- C
SDVOB_CLK- C
SDVOB_R+ C
SDVOB_G+ C
SDVOB_B+ C
SDVOB_CLK+ C

ALVISO

+2.5VRUN
R282 10K
R283 10K
PM_EXTTS#0
PM_EXTTS#1
For Memory throttling
External Thermal Sensor Input

SDVO Output
C67 .1U_10V
C49 .1U_10V
C69 .1U_10V
C51 .1U_10V
C66 .1U_10V
C48 .1U_10V
C68 .1U_10V
C50 .1U_10V

QUANTA COMPUTER

Title	Alviso (VGA,DMI)		
Size: Custom	Document Number: Tahiti(DM3L)		Rev: 1A
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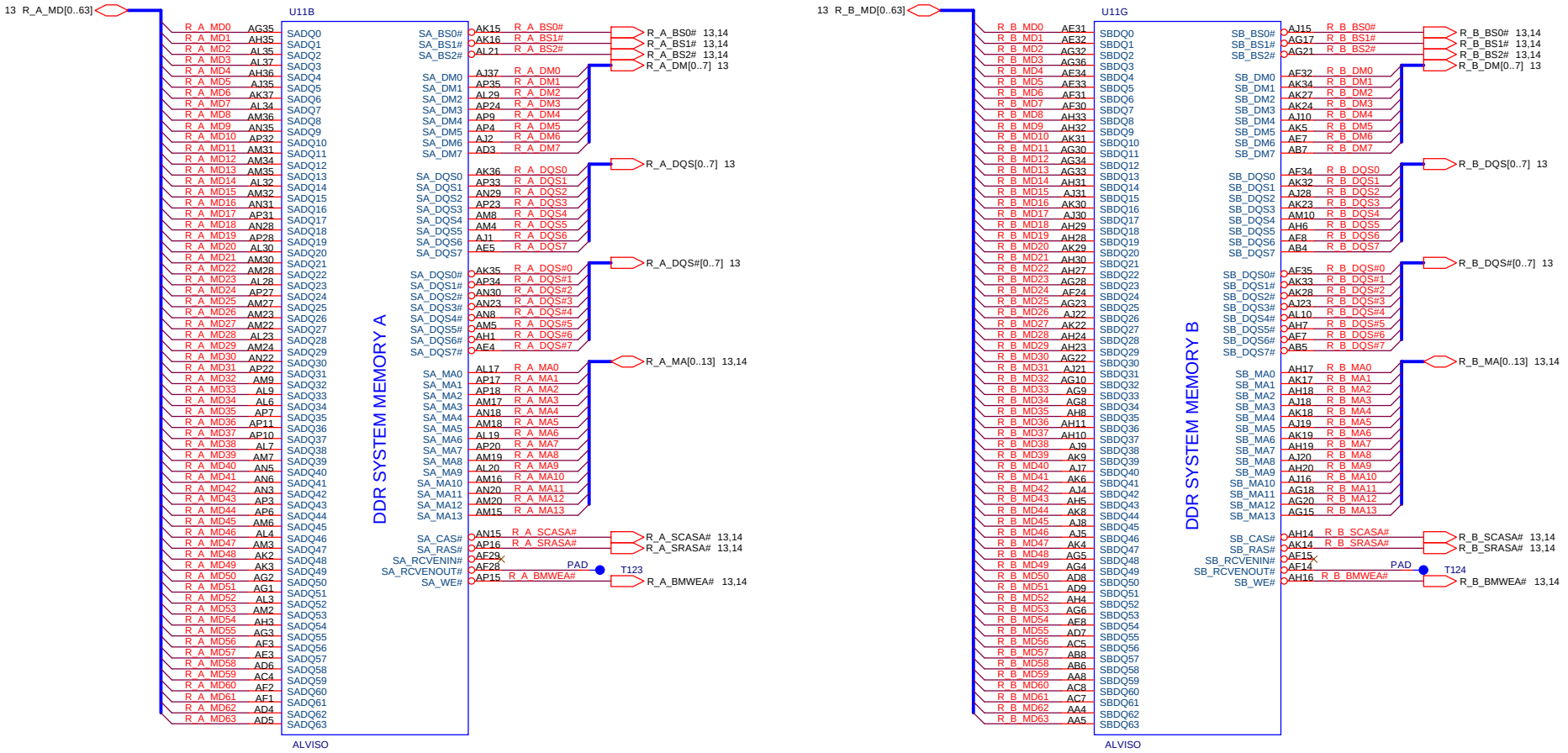

QUANTA COMPUTER

Title	Alviso (DDR)	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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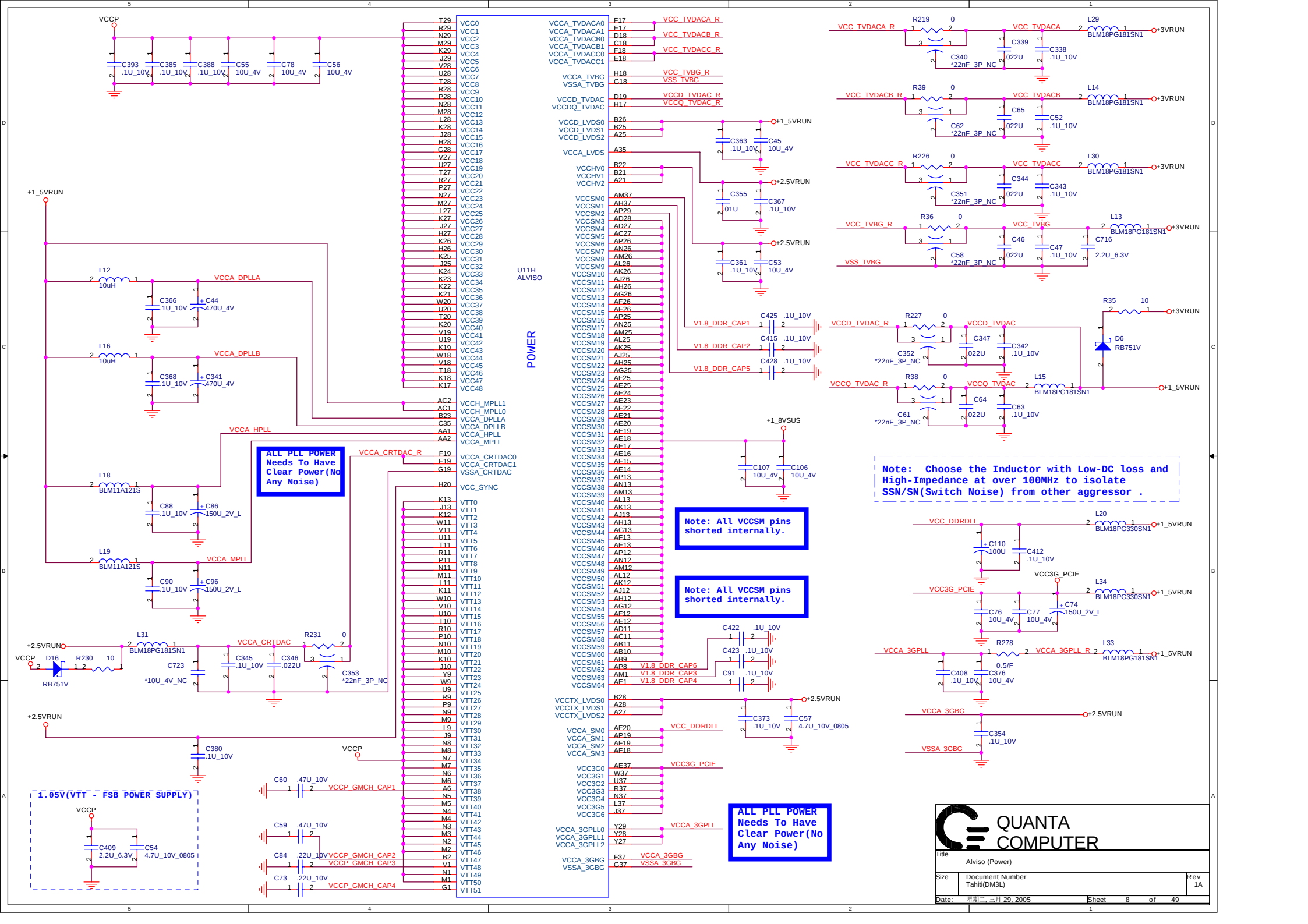
VCCP
C393 .1U_10V
C385 .1U_10V
C388 .1U_10V
C55 10U_4V
C78 10U_4V
C56 10U_4V
+1_5VRUN
L12 10uH
VCCA_DPLLA
C366 .1U_10V
C44 470U_4V
L16 10uH
VCCA_DPLLB
C368 .1U_10V
C341 470U_4V
VCCA_HPLL
L18 BLM11A121S
C88 .1U_10V
C86 150U_2V_L
L19 BLM11A121S
VCCA_MPLL
C90 .1U_10V
C96 150U_2V_L
+2.5VRUN
L31 BLM18PG181SN1
VCCP
D16 RB751V
R230 10
VCCA_CRTDAC
C723 *10U_4V_NC
C345 .1U_10V
C346 .022U
R231 0
C353 *22nF_3P_NC
+2.5VRUN
C380 .1U_10V
1.05V(VTT - FSB POWER SUPPLY)
VCCP
C409 2.2U_6.3V
C54 4.7U_10V_0805
VCCP
C60 .47U_10V
VCCP_GMCH_CAP1
C59 .47U_10V
C84 .22U_10V
VCCP_GMCH_CAP2
VCCP_GMCH_CAP3
C73 .22U_10V
VCCP_GMCH_CAP4
T29 R29 N29 M29 K29 J29 V28 U28 T28 R28 P28 N28 M28 L28 K28 J28 H28 G28 V27 U27 T27 R27 P27 N27 M27 L27 K27 J27 H27 K26 H26 K25 J25 K24 K23 K22 K21 W20 U20 T20 K20 V19 U19 K19 W18 V18 T18 K18 K17
VCC0 VCC1 VCC2 VCC3 VCC4 VCC5 VCC6 VCC7 VCC8 VCC9 VCC10 VCC11 VCC12 VCC13 VCC14 VCC15 VCC16 VCC17 VCC18 VCC19 VCC20 VCC21 VCC22 VCC23 VCC24 VCC25 VCC26 VCC27 VCC28 VCC29 VCC30 VCC31 VCC32 VCC33 VCC34 VCC35 VCC36 VCC37 VCC38 VCC39 VCC40 VCC41 VCC42 VCC43 VCC44 VCC45 VCC46 VCC47 VCC48
AC2 AC1 B23 C35 AA1 AA2
VCCH_MPLL1 VCCH_MPLL0 VCCA_DPLLA VCCA_DPLLB VCCA_HPLL VCCA_MPLL
F19 E19 G19
VCCA_CRTDAC0 VCCA_CRTDAC1 VSSA_CRTDAC
H20
VCC_SYNC
K13 J13 K12 W11 V11 U11 T11 R11 P11 N11 M11 L11 K11 W10 V10 U10 T10 R10 P10 N10 M10 K10 J10 Y9 W9 U9 R9 P9 N9 M9 L9 J9 N8 M8 N7 M7 N6 M6 A6 N5 M5 N4 M4 N3 M3 N2 M2 B2 V1 N1 M1 G1
VTT0 VTT1 VTT2 VTT3 VTT4 VTT5 VTT6 VTT7 VTT8 VTT9 VTT10 VTT11 VTT12 VTT13 VTT14 VTT15 VTT16 VTT17 VTT18 VTT19 VTT20 VTT21 VTT22 VTT23 VTT24 VTT25 VTT26 VTT27 VTT28 VTT29 VTT30 VTT31 VTT32 VTT33 VTT34 VTT35 VTT36 VTT37 VTT38 VTT39 VTT40 VTT41 VTT42 VTT43 VTT44 VTT45 VTT46 VTT47 VTT48 VTT49 VTT50 VTT51
U11H ALVISO
POWER
VCCA_TVDACA0 VCCA_TVDACA1 VCCA_TVDACB0 VCCA_TVDACB1 VCCA_TVDACC0 VCCA_TVDACC1
F17 E17 D18 C18 F18 E18
VCC_TVDACA_R
VCC_TVDACB_R
VCC_TVDACC_R
VCCA_TVBG VSSA_TVBG
H18 G18
VCC_TVBG_R VSS_TVBG
VCCD_TVDAC VCCDQ_TVDAC
D19 H17
VCCD_TVDAC_R VCCQ_TVDAC_R
VCCD_LVDS0 VCCD_LVDS1 VCCD_LVDS2
B26 B25 A25
+1_5VRUN
C363 .1U_10V
C45 10U_4V
VCCA_LVDS
A35
VCCHV0 VCCHV1 VCCHV2
B22 B21 A21
+2.5VRUN
C355 .01U
C367 .1U_10V
+2.5VRUN
C361 .1U_10V
C53 10U_4V
VCCSM0 VCCSM1 VCCSM2 VCCSM3 VCCSM4 VCCSM5 VCCSM6 VCCSM7 VCCSM8 VCCSM9 VCCSM10 VCCSM11 VCCSM12 VCCSM13 VCCSM14 VCCSM15 VCCSM16 VCCSM17 VCCSM18 VCCSM19 VCCSM20 VCCSM21 VCCSM22 VCCSM23 VCCSM24 VCCSM25 VCCSM26 VCCSM27 VCCSM28 VCCSM29 VCCSM30 VCCSM31 VCCSM32 VCCSM33 VCCSM34 VCCSM35 VCCSM36 VCCSM37 VCCSM38 VCCSM39 VCCSM40 VCCSM41 VCCSM42 VCCSM43 VCCSM44 VCCSM45 VCCSM46 VCCSM47 VCCSM48 VCCSM49 VCCSM50 VCCSM51 VCCSM52 VCCSM53 VCCSM54 VCCSM55 VCCSM56 VCCSM57 VCCSM58 VCCSM59 VCCSM60 VCCSM61 VCCSM62 VCCSM63 VCCSM64
AM37 AH37 AP29 AD28 AD27 AC27 AP26 AN26 AM26 AL26 AK26 AJ26 AH26 AG26 AE26 AE26 AP25 AN25 AM25 AL25 AK25 AJ25 AH25 AG25 AF25 AE25 AE24 AE23 AE22 AE21 AE20 AE19 AE18 AE17 AE16 AE15 AE14 AP13 AN13 AM13 AL13 AK13 AJ13 AH13 AG13 AF13 AE13 AP12 AN12 AM12 AL12 AK12 AJ12 AH12 AG12 AF12 AE12 AD11 AC11 AB11 AB10 AB9 AP8 AM1 AE1
V1.8_DDR_CAP1
V1.8_DDR_CAP2
V1.8_DDR_CAP5
C425 .1U_10V
C415 .1U_10V
C428 .1U_10V
V1.8_DDR_CAP6
V1.8_DDR_CAP3
V1.8_DDR_CAP4
C422 .1U_10V
C423 .1U_10V
C91 .1U_10V
+1_8VSUS
C107 10U_4V
C106 10U_4V
Note: All VCCSM pins shorted internally.
Note: All VCCSM pins shorted internally.
VCCTX_LVDS0 VCCTX_LVDS1 VCCTX_LVDS2
B28 A28 A27
+2.5VRUN
C373 .1U_10V
C57 4.7U_10V_0805
VCCA_SM0 VCCA_SM1 VCCA_SM2 VCCA_SM3
AF20 AP19 AF19 AF18
VCC_DDRDLL
VCC3G0 VCC3G1 VCC3G2 VCC3G3 VCC3G4 VCC3G5 VCC3G6
AE37 W37 U37 R37 N37 L37 J37
VCC3G_PCIE
VCCA_3GPLL0 VCCA_3GPLL1 VCCA_3GPLL2
Y29 Y28 Y27
VCCA_3GPLL
VCCA_3GBG VSSA_3GBG
F37 G37
VCCA_3GBG VSSA_3GBG
ALL PLL POWER Needs To Have Clear Power(No Any Noise)
ALL PLL POWER Needs To Have Clear Power(No Any Noise)
R219 0
VCC_TVDACA_R
VCC_TVDACA
C339 .022U
C338 .1U_10V
C340 *22nF_3P_NC
L29 BLM18PG181SN1
+3VRUN
R39 0
VCC_TVDACB_R
VCC_TVDACB
C65 .022U
C52 .1U_10V
C62 *22nF_3P_NC
L14 BLM18PG181SN1
+3VRUN
R226 0
VCC_TVDACC_R
VCC_TVDACC
C344 .022U
C343 .1U_10V
C351 *22nF_3P_NC
L30 BLM18PG181SN1
+3VRUN
R36 0
VCC_TVBG_R
VCC_TVBG
C46 .022U
C47 .1U_10V
C58 *22nF_3P_NC
VSS_TVBG
L13 BLM18PG181SN1
+3VRUN
C716 2.2U_6.3V
R35 10
+3VRUN
R227 0
VCCD_TVDAC_R
VCCD_TVDAC
C347 .022U
C342 .1U_10V
C352 *22nF_3P_NC
D6 RB751V
R38 0
VCCQ_TVDAC_R
VCCQ_TVDAC
C64 .022U
C63 .1U_10V
C61 *22nF_3P_NC
L15 BLM18PG181SN1
+1_5VRUN
Note: Choose the Inductor with Low-DC loss and High-Impedance at over 100MHz to isolate SSN/SN(Switch Noise) from other aggressor .
VCC_DDRDLL
L20 BLM18PG330SN1
+1_5VRUN
C110 100U
C412 .1U_10V
VCC3G_PCIE
L34 BLM18PG330SN1
+1_5VRUN
C76 10U_4V
C77 10U_4V
C74 150U_2V_L
R278
VCCA_3GPLL
VCCA_3GPLL_R
L33 BLM18PG181SN1
+1_5VRUN
C408 .1U_10V
C376 0.5/F 10U_4V
VCCA_3GBG
+2.5VRUN
C354 .1U_10V
VSSA_3GBG
QUANTA COMPUTER
Title
Alviso (Power)
Size
Document Number
Tahiti(DM3L)
Rev
1A
Date: 星期二, 三月 29, 2005
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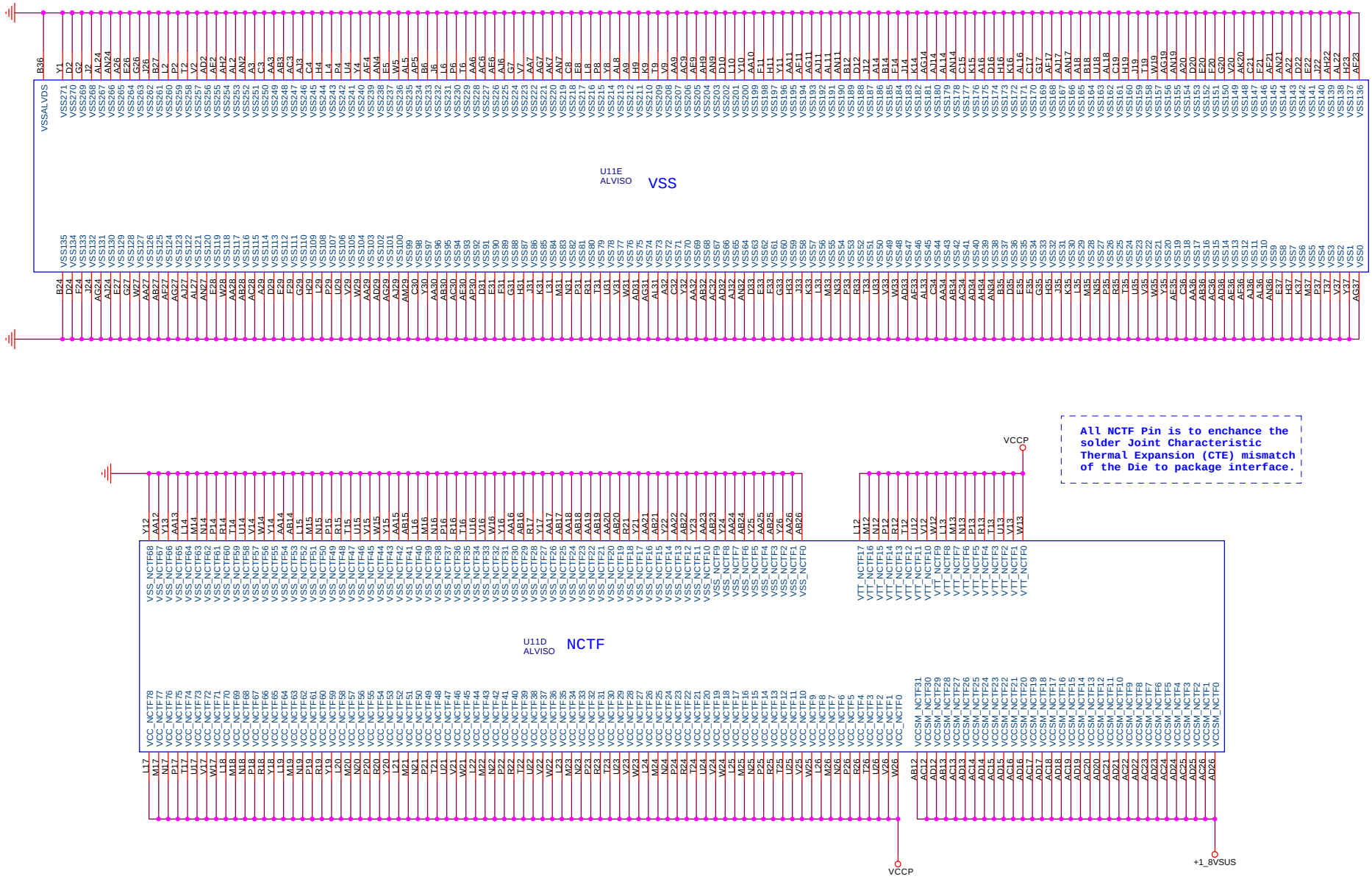

QUANTA COMPUTER

Title	Alviso (VSS,NCTF)	
Size	Document Number Tahiti(DM3L)	Rev 1A
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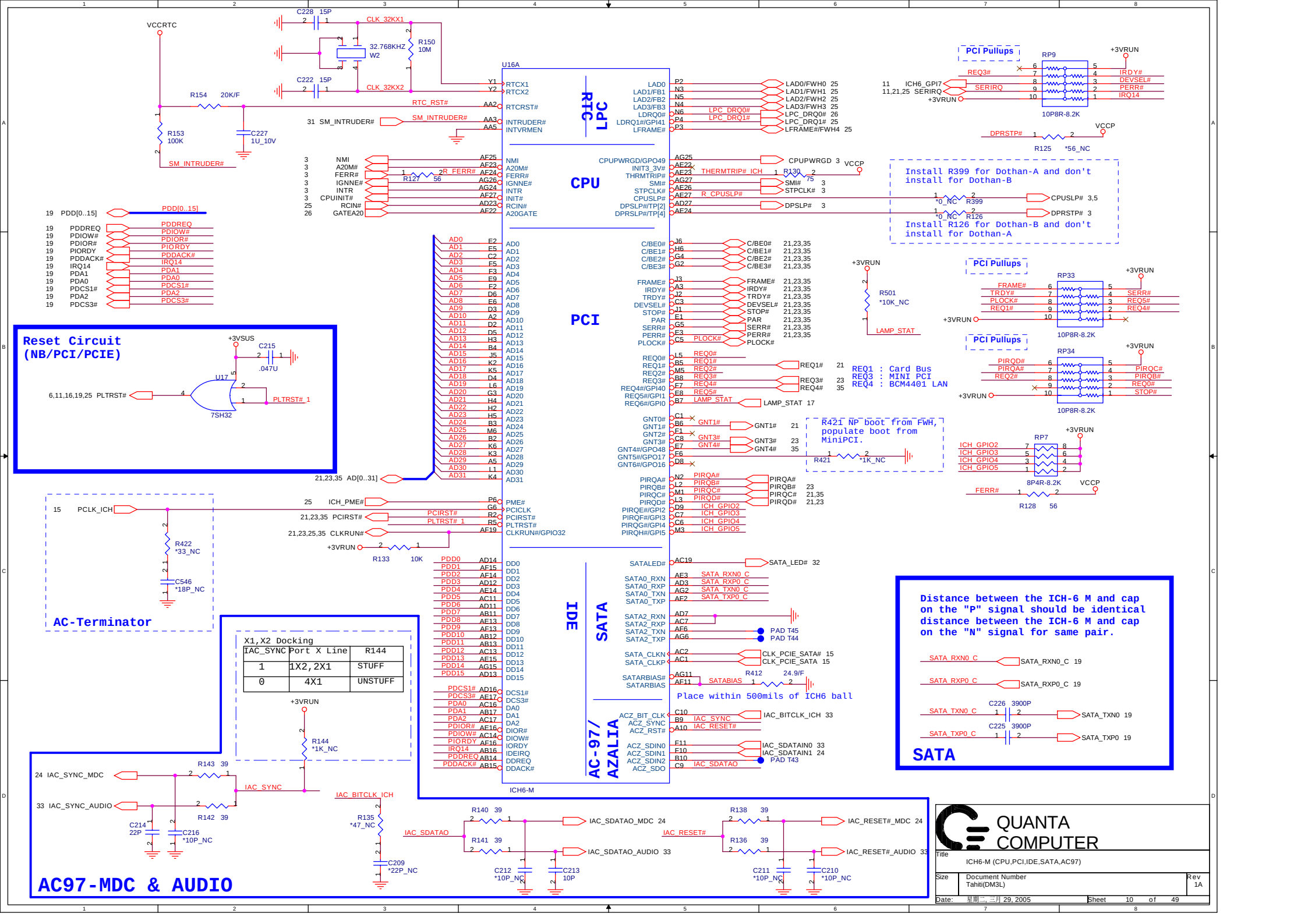
U16A
RTC
LPC
CPU
PCI
IDE
SATA
AC-97/ AZALIA
ICH6-M
Reset Circuit (NB/PCI/PCIE)
AC-Terminator
X1,X2 Docking
IAC_SYNC | Port X Line | R144
1 | 1X2,2X1 | STUFF
0 | 4X1 | UNSTUFF
AC97-MDC & AUDIO
PCI Pullups
Install R399 for Dothan-A and don't install for Dothan-B
Install R126 for Dothan-B and don't install for Dothan-A
REQ1 : Card Bus
REQ3 : MINI PCI
REQ4 : BCM4401 LAN
R421 NP boot from FWH, populate boot from MiniPCI.
Place within 500mils of ICH6 ball
Distance between the ICH-6 M and cap on the "P" signal should be identical distance between the ICH-6 M and cap on the "N" signal for same pair.
SATA
QUANTA COMPUTER
Title
ICH6-M (CPU,PCI,IDE,SATA,AC97)
Size
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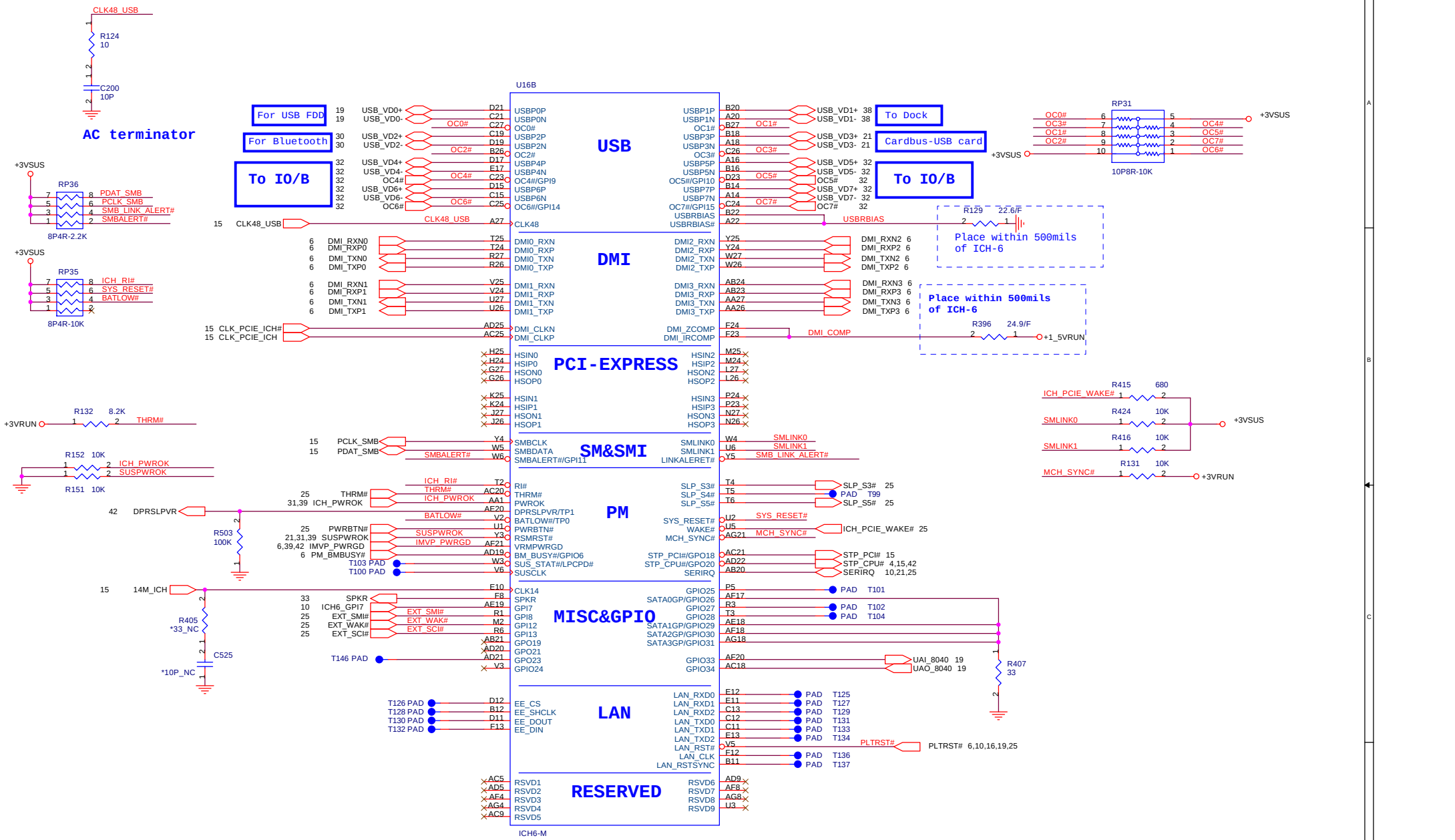
AC terminator
For USB FDD
For Bluetooth
To IO/B
To Dock
Cardbus-USB card
To IO/B
Place within 500mils of ICH-6
Place within 500mils of ICH-6
U16B
USB
DMI
PCI-EXPRESS
SM&SMI
PM
MISC&GPIO
LAN
RESERVED
ICH6-M
QUANTA COMPUTER
Title
ICH6-M (USB,DMI,LPC)
Size
Document Number
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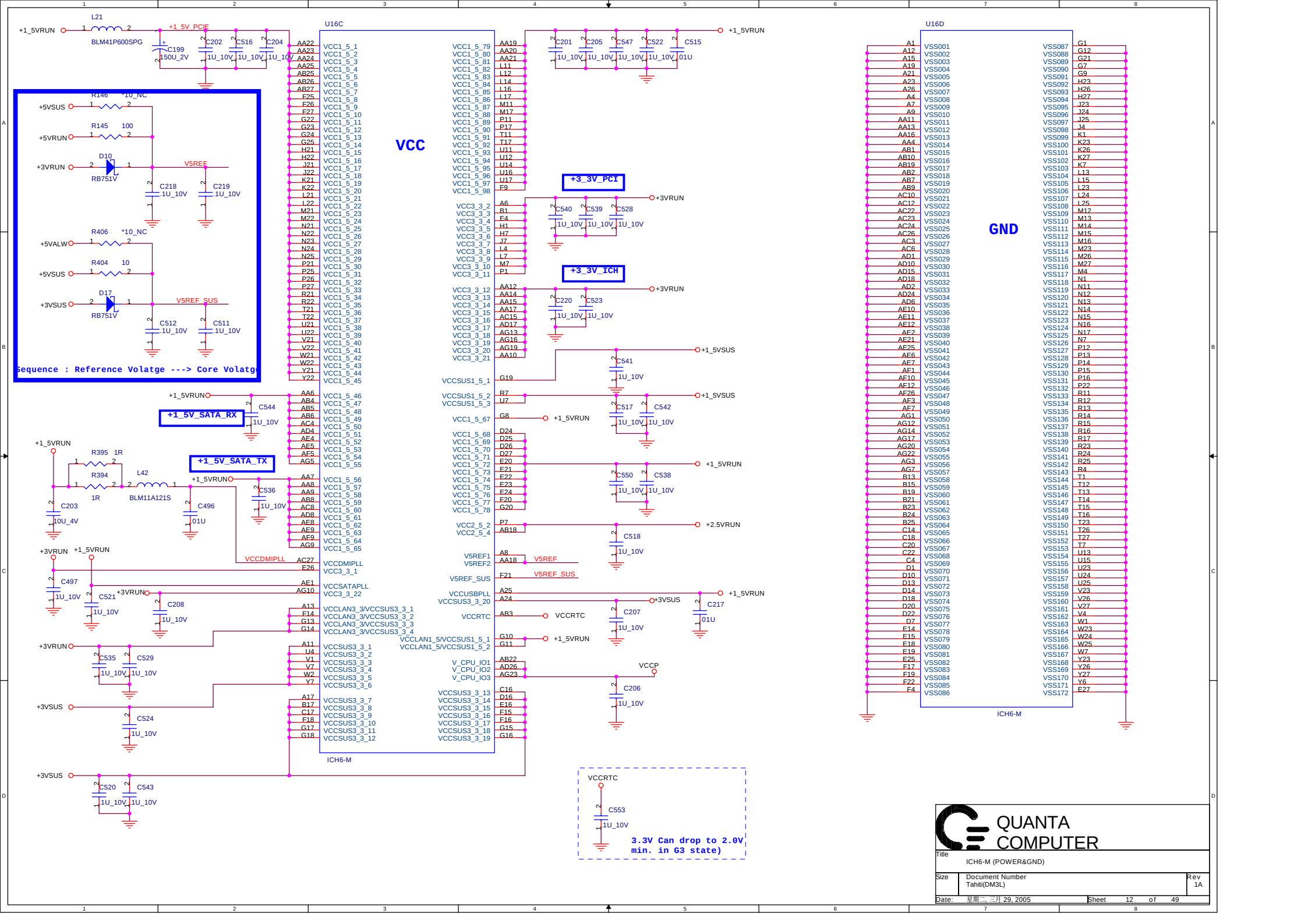
U16C
VCC
ICH6-M
U16D
GND
ICH6-M
+1_5VRUN
+1_5V_PCIE
BLM41P600SPG
C199 150U_2V
C202 1U_10V
C516 1U_10V
C204 1U_10V
R146 *10_NC
R145 100
D10 RB751V
V5REF
C218 .1U_10V
C219 .1U_10V
R406 *10_NC
R404 10
D17 RB751V
V5REF_SUS
C512 .1U_10V
C511 .1U_10V
Sequence : Reference Volatge ---> Core Volatge
+1_5V_SATA_RX
C544 1U_10V
+1_5V_SATA_TX
R395 1R
R394 1R
L42 BLM11A121S
C203 10U_4V
C496 .01U
C536 1U_10V
VCCDMIPLL
C497 1U_10V
C521 1U_10V
C208 1U_10V
C535 1U_10V
C529 1U_10V
C524 1U_10V
C520 1U_10V
C543 1U_10V
+3_3V_PCI
C540 1U_10V
C539 1U_10V
C528 1U_10V
+3_3V_ICH
C220 1U_10V
C523 1U_10V
C541 1U_10V
C517 1U_10V
C542 1U_10V
C550 1U_10V
C538 1U_10V
C518 1U_10V
V5REF
V5REF_SUS
C207 1U_10V
C217 .01U
VCCRTC
C206 1U_10V
C553 1U_10V
3.3V Can drop to 2.0V min. in G3 state)
C201 1U_10V
C205 1U_10V
C547 1U_10V
C522 1U_10V
C515 .01U
+3VRUN
+1_5VSUS
+2.5VRUN
+3VSUS
VCCP
QUANTA COMPUTER
Title
ICH6-M (POWER&GND)
Size
Document Number
Tahiti(DM3L)
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1A
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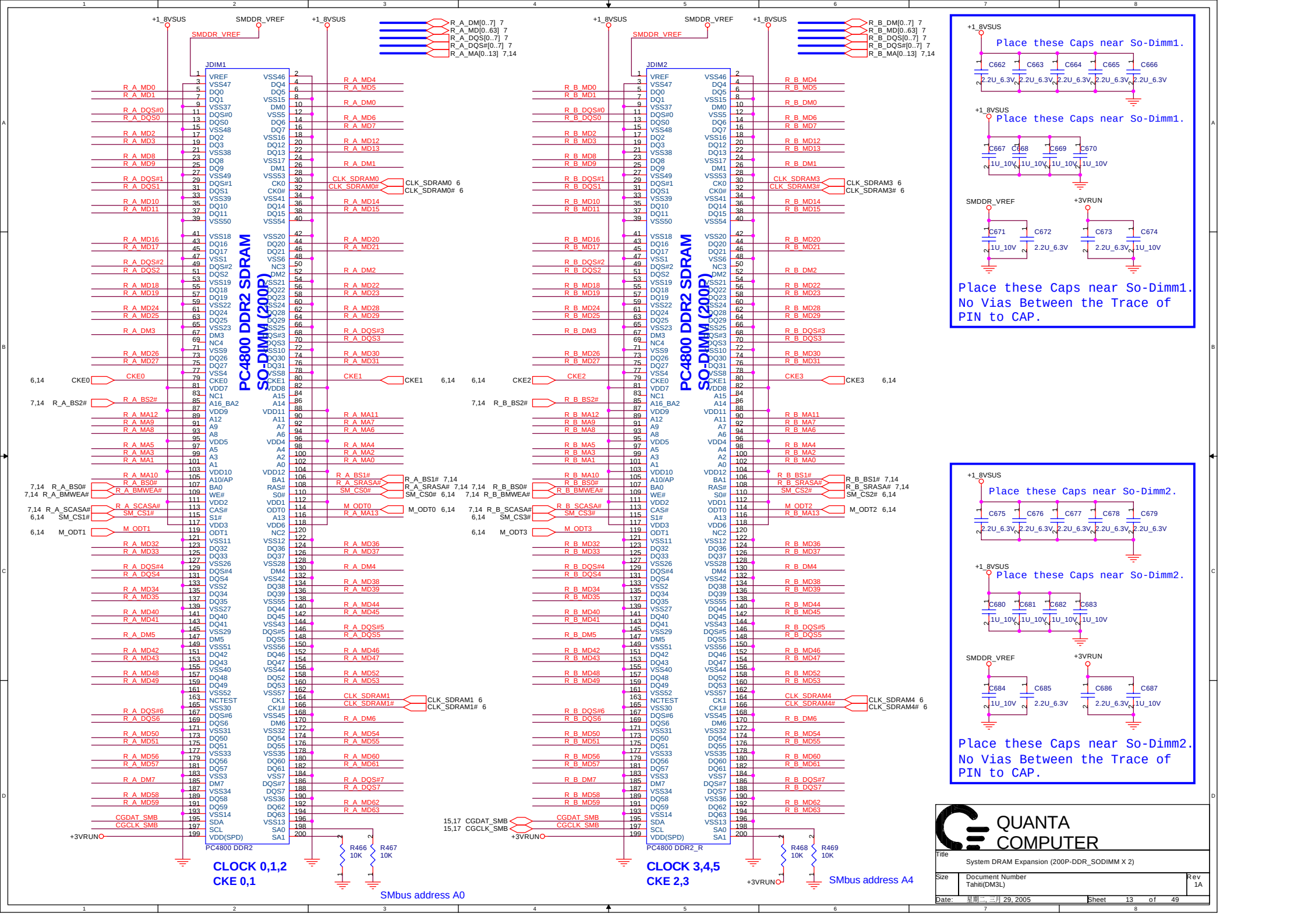
JDIM1
JDIM2
PC4800 DDR2 SDRAM SO-DIMM (200P)
PC4800 DDR2
PC4800 DDR2_R
+1_8VSUS
SMDDR_VREF
+3VRUN
R_A_DM[0..7] 7
R_A_MD[0..63] 7
R_A_DQS[0..7] 7
R_A_DQS#[0..7] 7
R_A_MA[0..13] 7,14
R_B_DM[0..7] 7
R_B_MD[0..63] 7
R_B_DQS[0..7] 7
R_B_DQS#[0..7] 7
R_B_MA[0..13] 7,14
CLOCK 0,1,2
CKE 0,1
SMbus address A0
CLOCK 3,4,5
CKE 2,3
SMbus address A4
R466 10K
R467 10K
R468 10K
R469 10K
Place these Caps near So-Dimm1.
C662 C663 C664 C665 C666 2.2U_6.3V
C667 C668 C669 C670 1U_10V
C671 C672 1U_10V 2.2U_6.3V
C673 C674 2.2U_6.3V 1U_10V
Place these Caps near So-Dimm1.
No Vias Between the Trace of PIN to CAP.
Place these Caps near So-Dimm2.
C675 C676 C677 C678 C679 2.2U_6.3V
C680 C681 C682 C683 1U_10V
C684 C685 1U_10V 2.2U_6.3V
C686 C687 2.2U_6.3V 1U_10V
Place these Caps near So-Dimm2.
No Vias Between the Trace of PIN to CAP.
QUANTA COMPUTER
Title
System DRAM Expansion (200P-DDR_SODIMM X 2)
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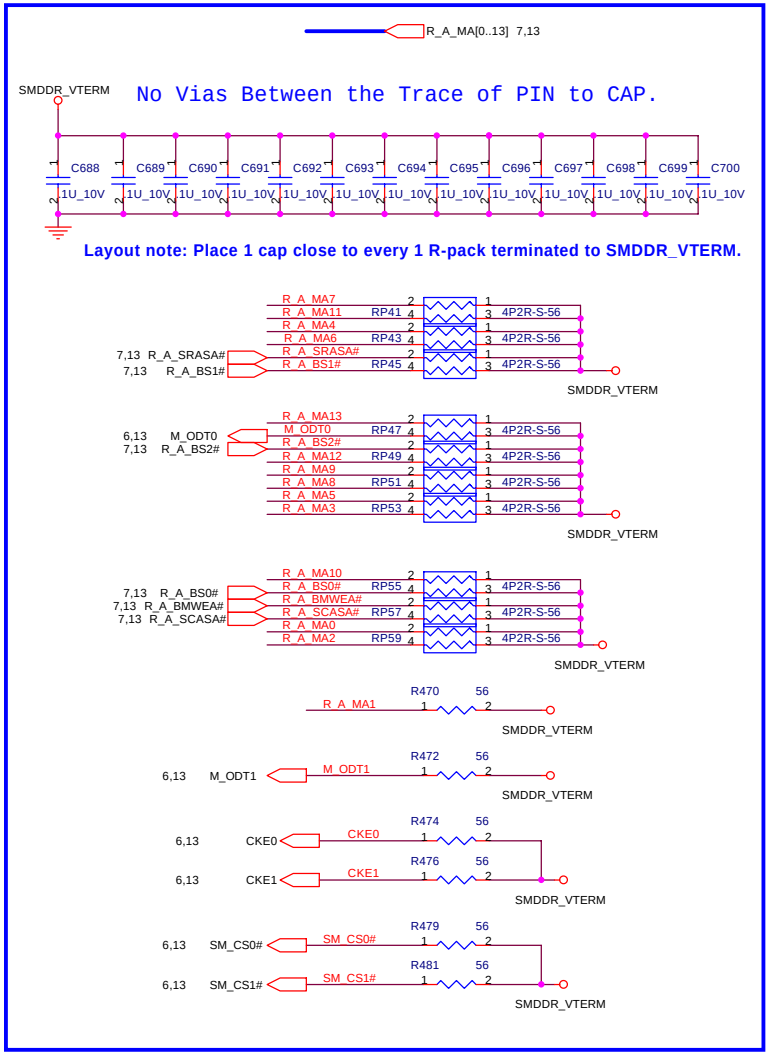
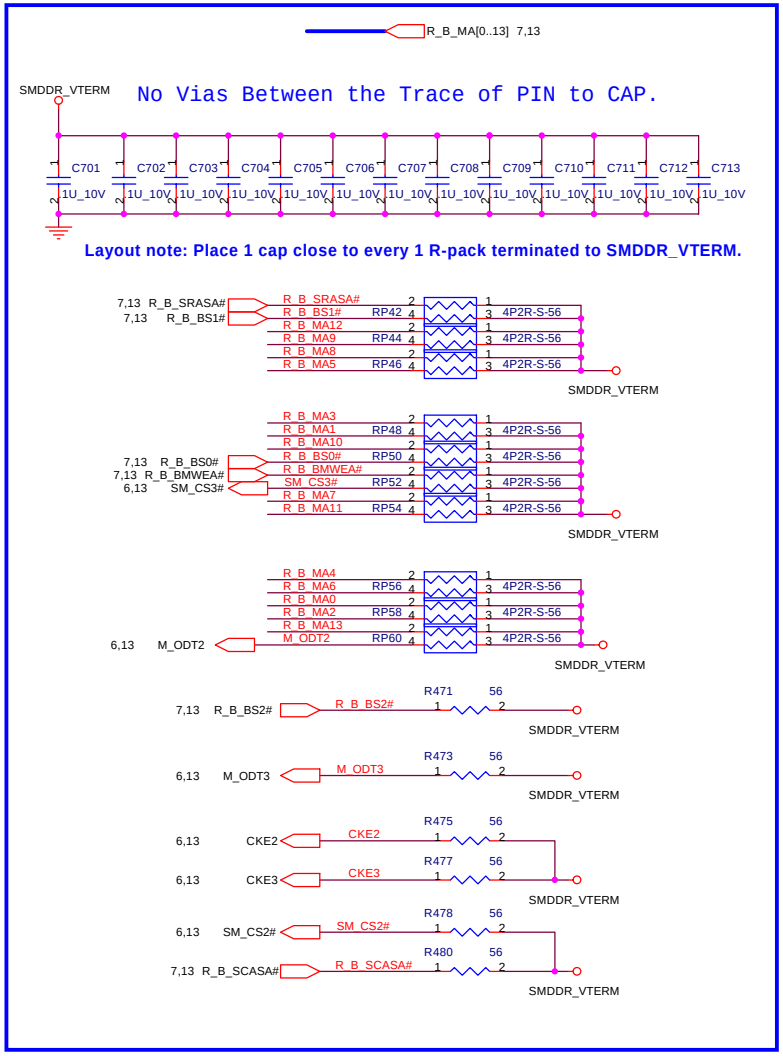
DDRII DUAL CHANNEL A,B.

DDRII A CHANNEL



DDRII B CHANNEL



QUANTA COMPUTER

Title	DDR RES.ARRAY	
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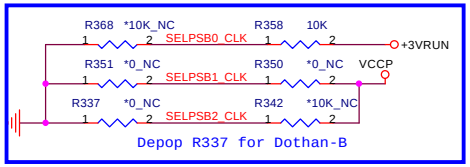
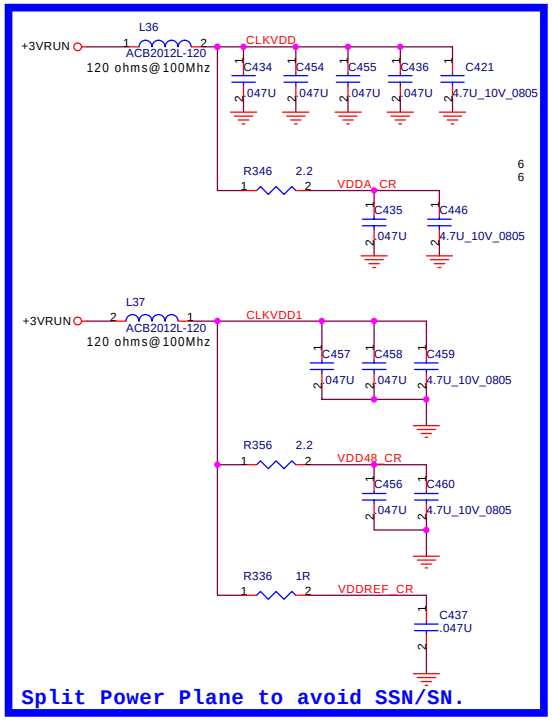
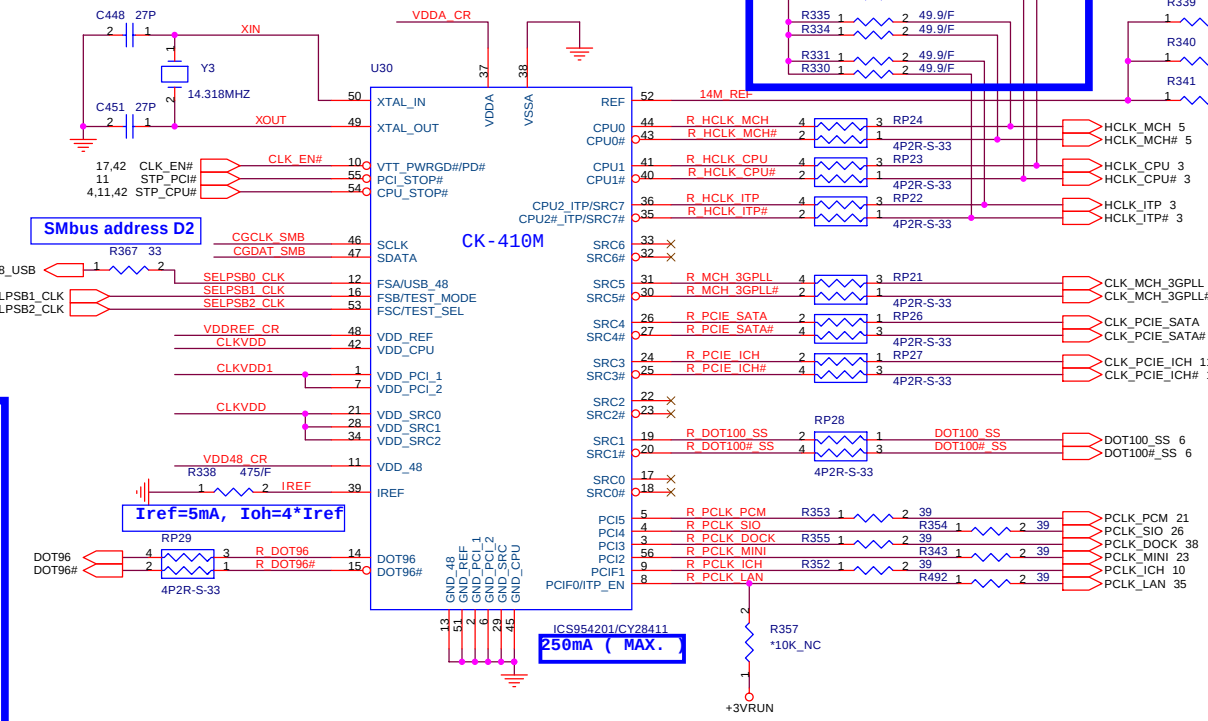
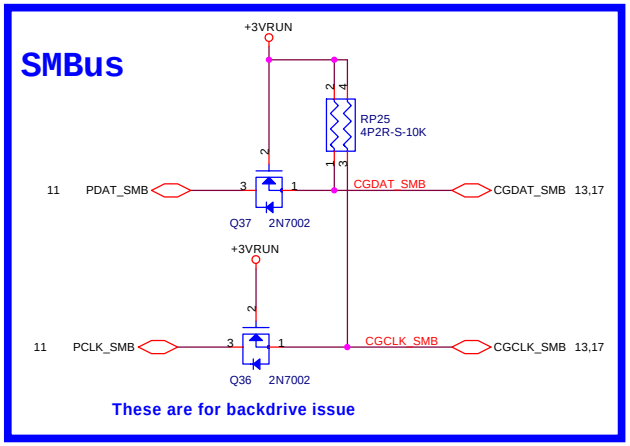
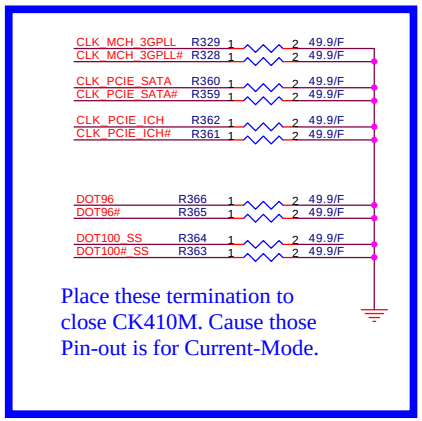
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33





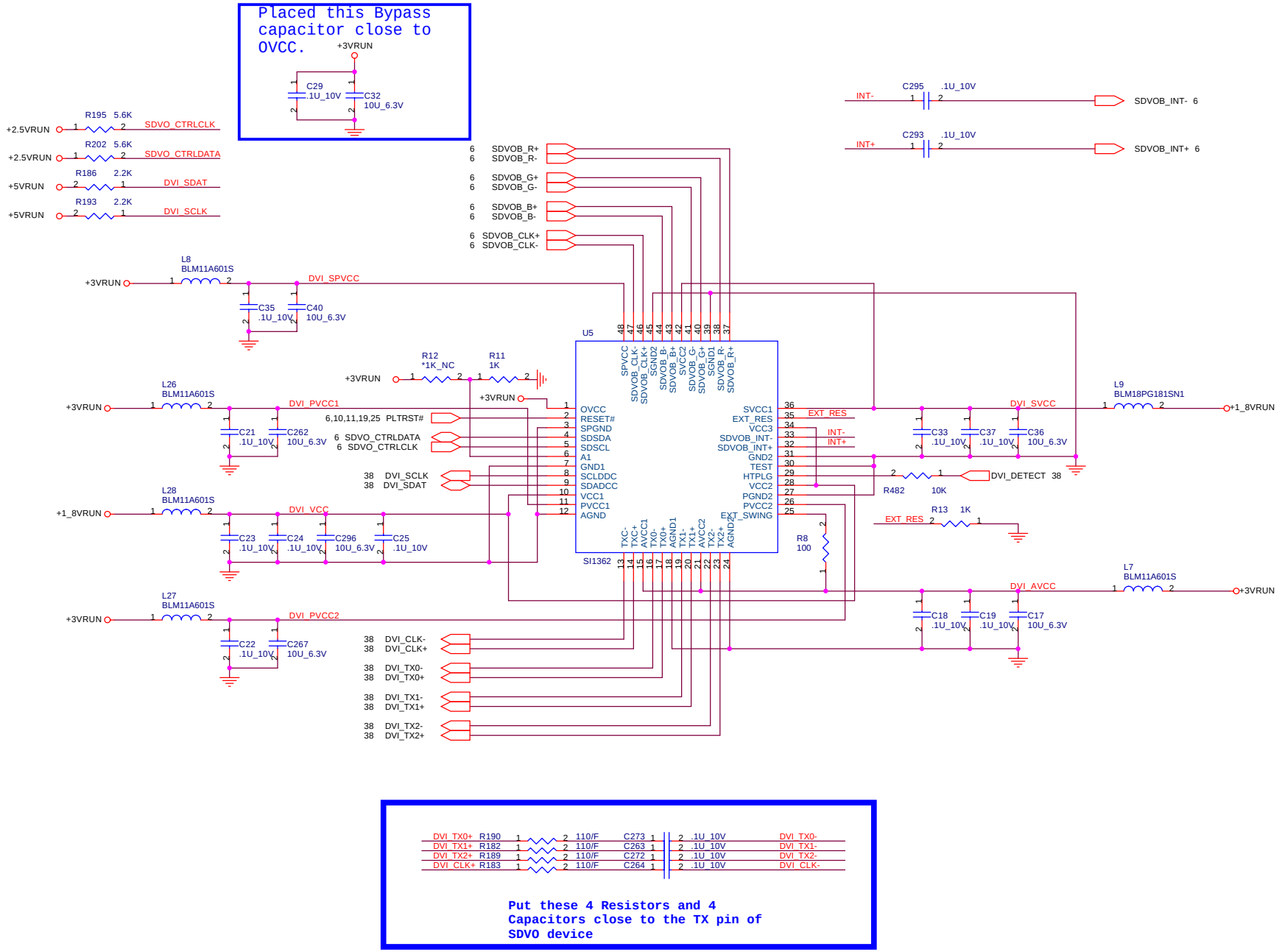




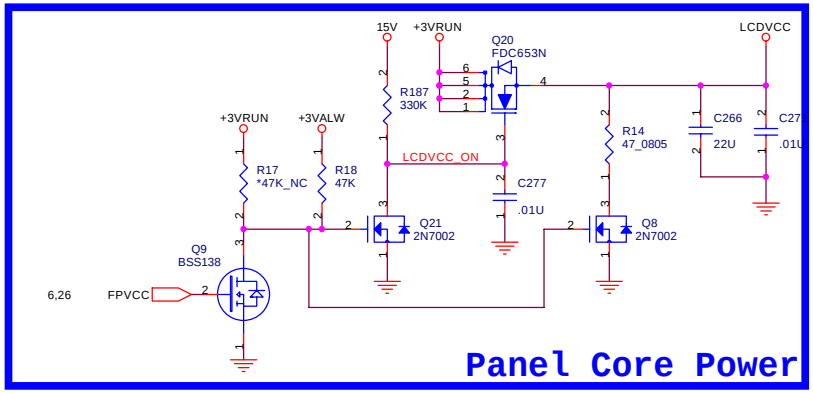


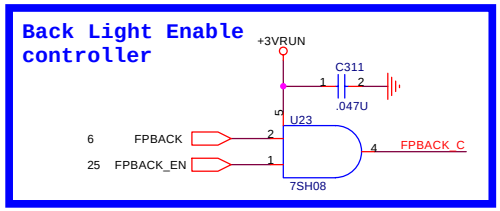
QUANTA COMPUTER

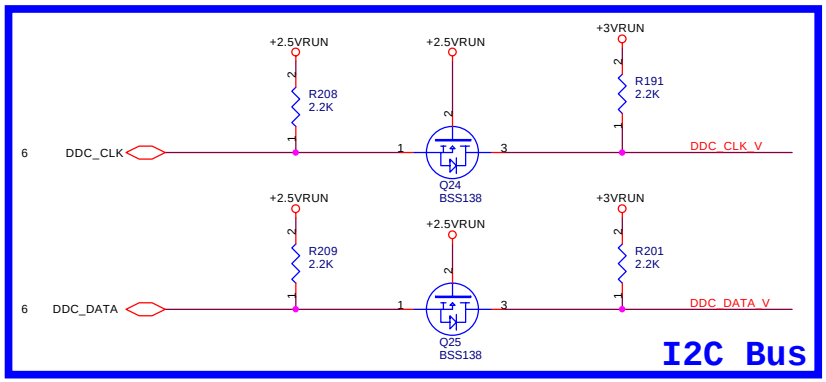
Title	CLOCK GENERATOR	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
Date: 星期二, 三月 29, 2005	Sheet 15 of 49	

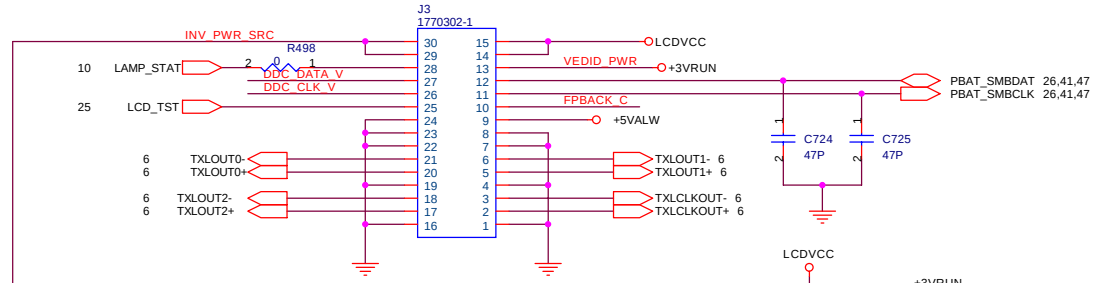


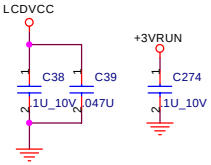
QUANTA COMPUTER		
Title	SIL 1362 DVI	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
Date: 星期三, 三月 29, 2005	Sheet 16 of 49	

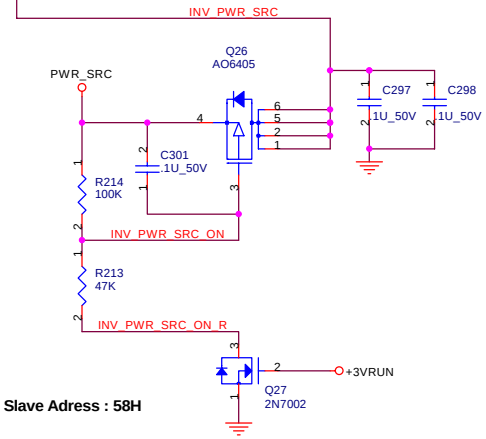




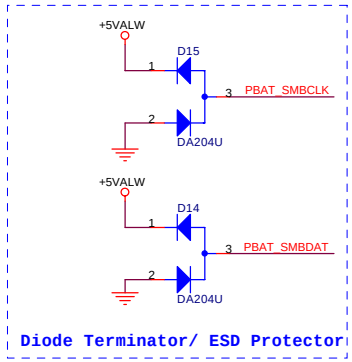


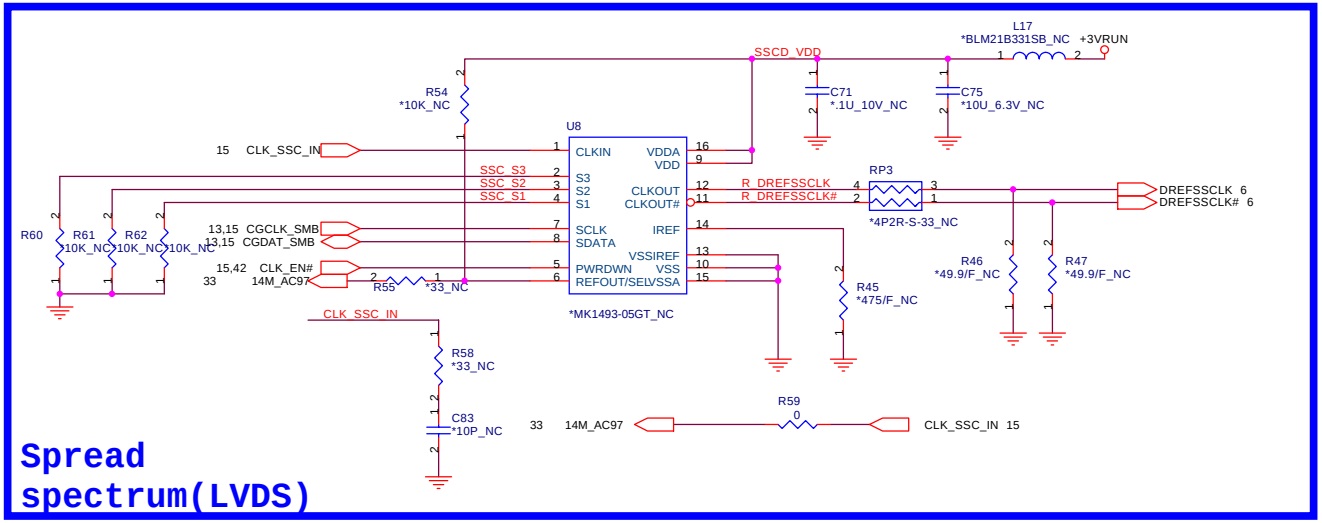






Slave Adress : 58H





QUANTA COMPUTER

Title	LCD CONN&CK-SSCD	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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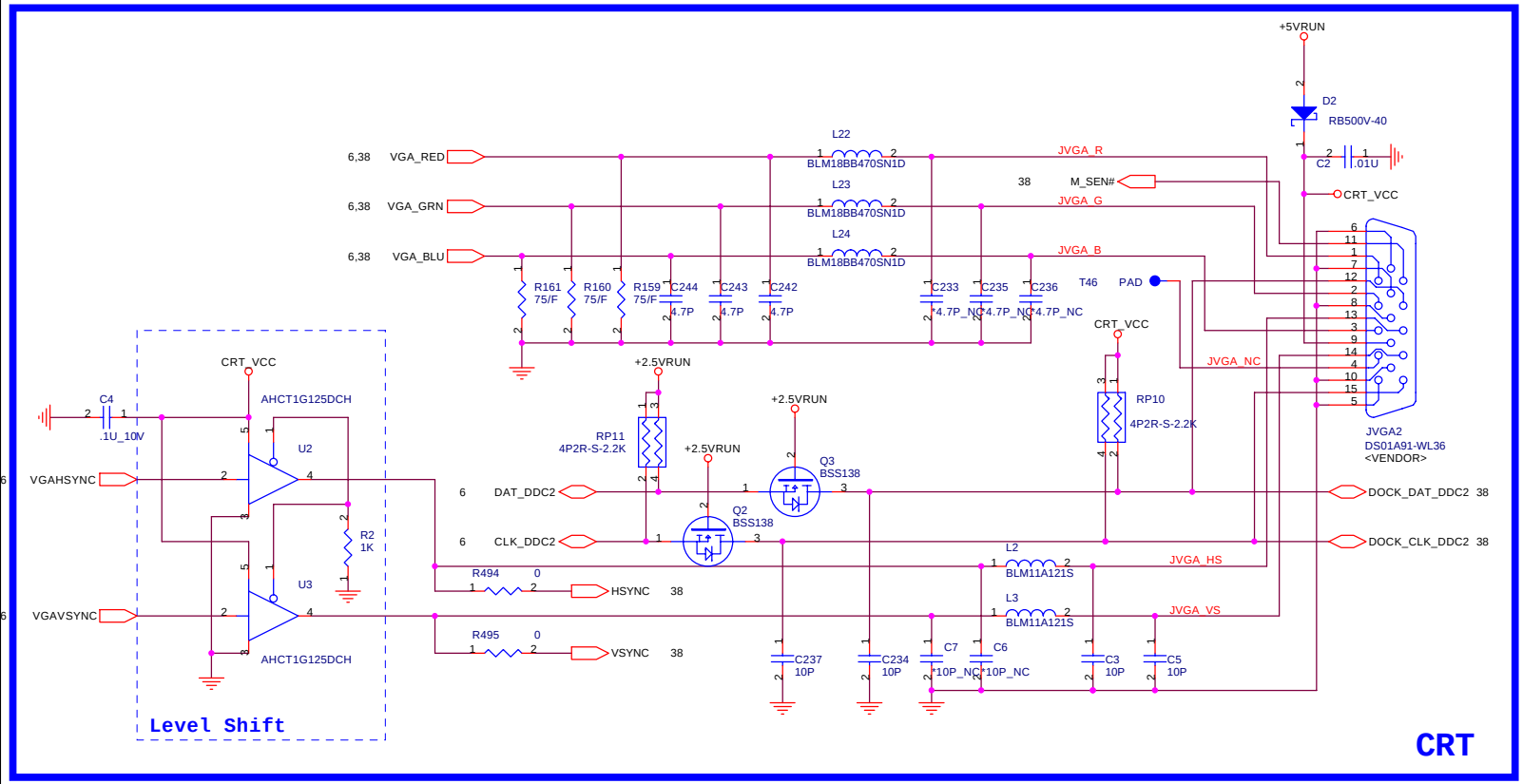

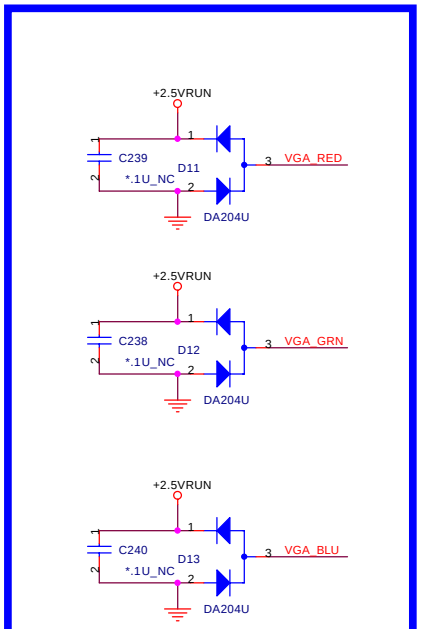

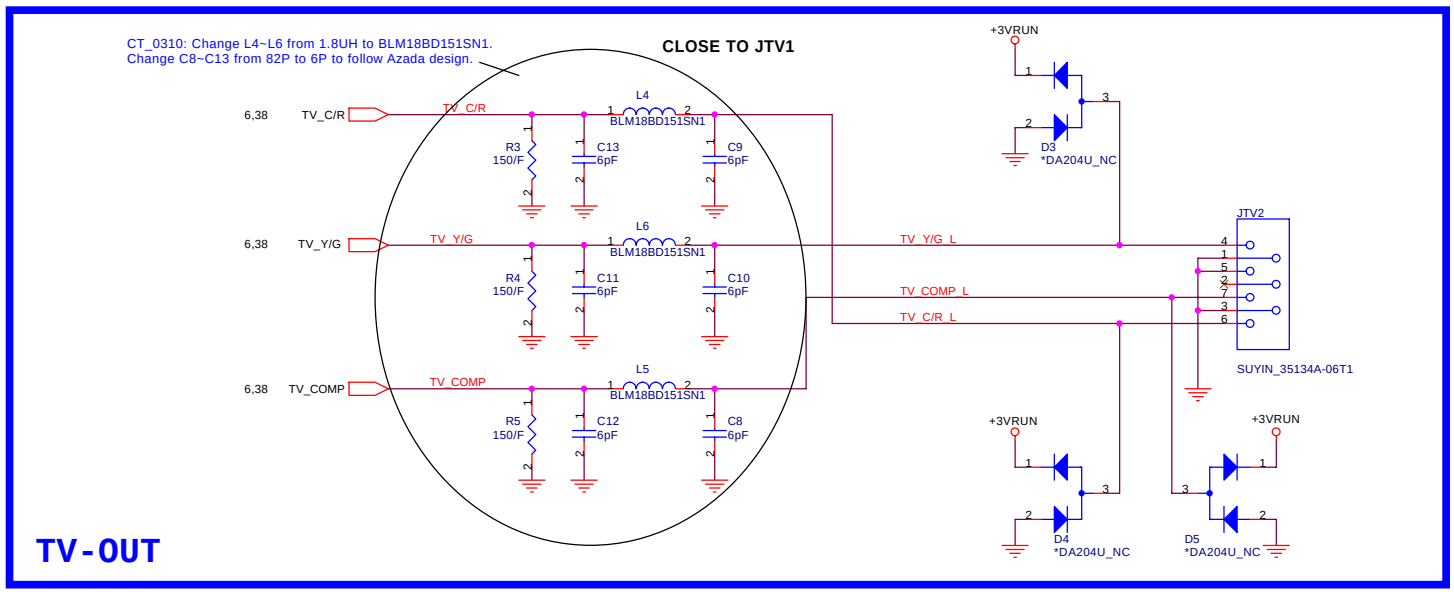

QUANTA COMPUTER

Title	CRT&TV CONN	
Size	Document Number Tahiti(DM3L)	Rev 1A
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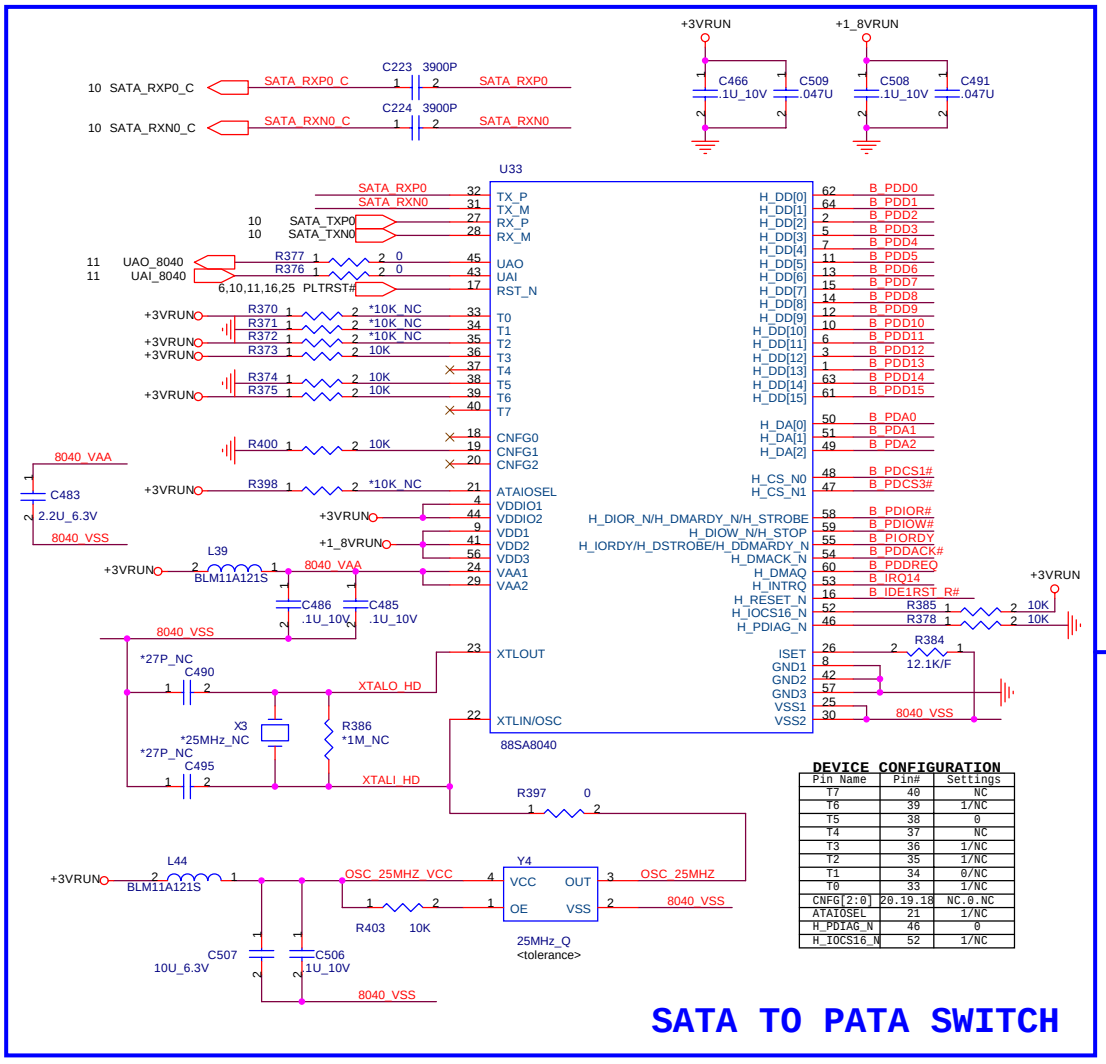
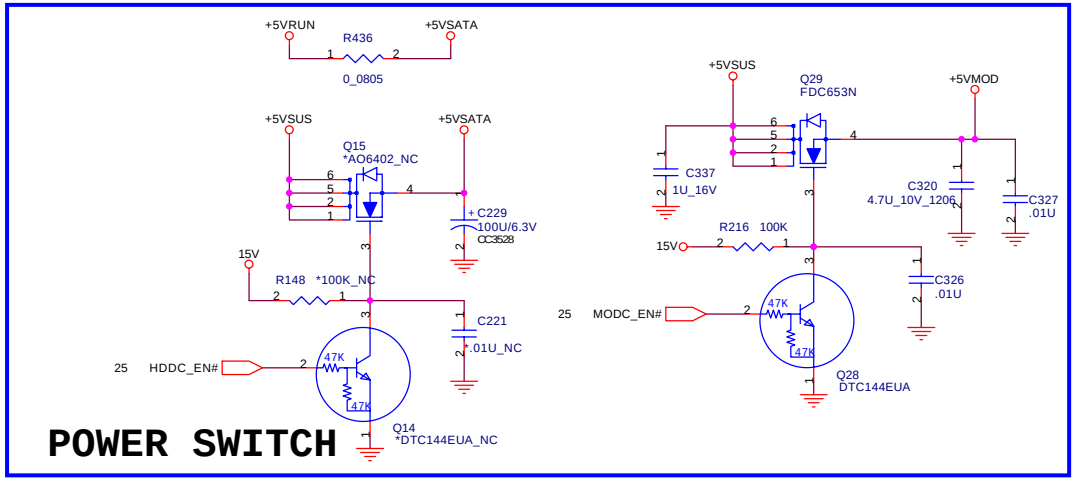
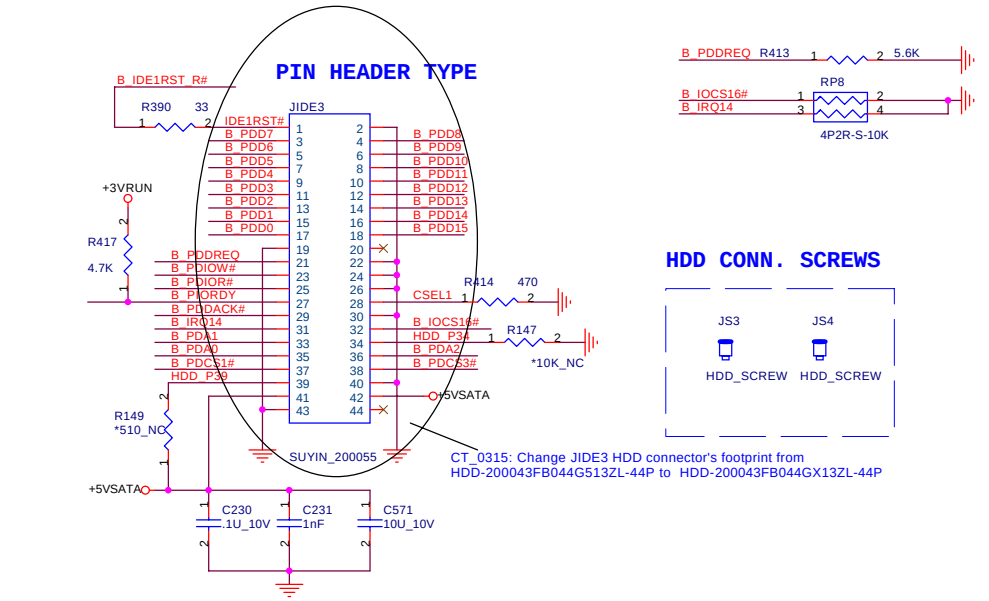
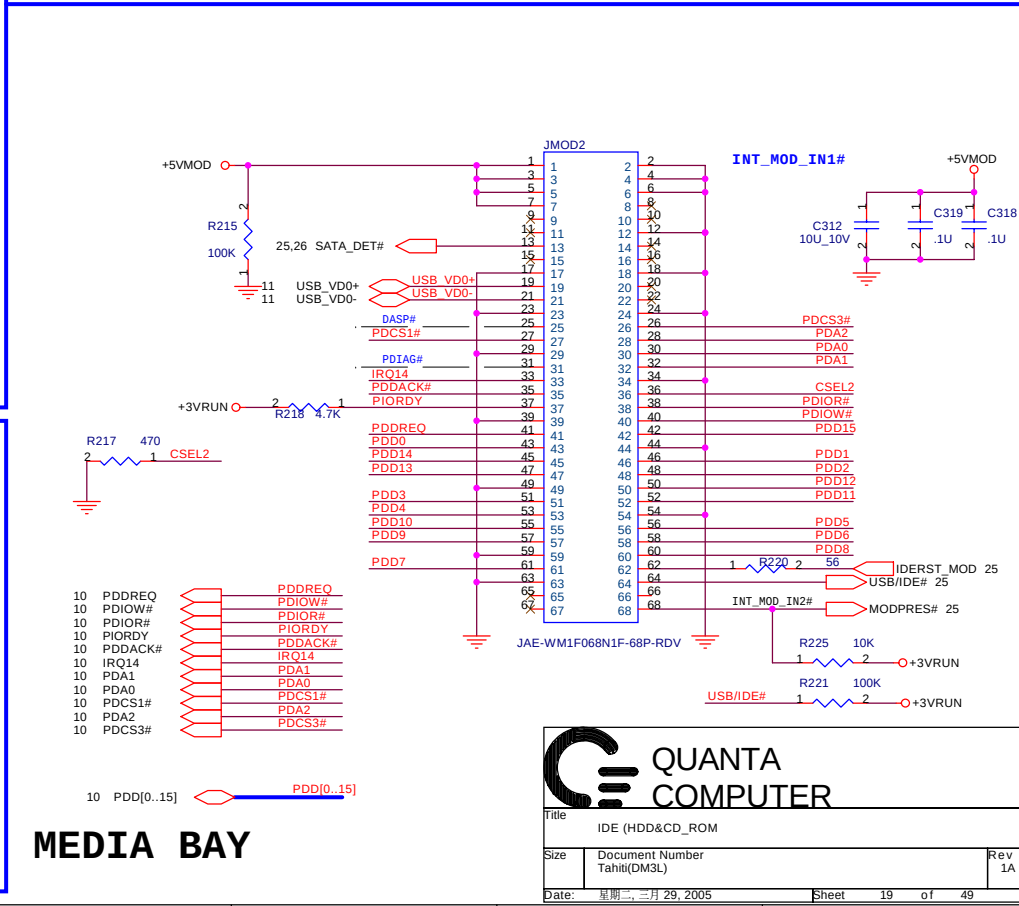

SATA TO PATA SWITCH
U33
88SA8040
DEVICE CONFIGURATION
Pin_Name | Pin# | Settings
T7 | 40 | NC
T6 | 39 | 1/NC
T5 | 38 | 0
T4 | 37 | NC
T3 | 36 | 1/NC
T2 | 35 | 1/NC
T1 | 34 | 0/NC
T0 | 33 | 1/NC
CNFG[2:0] | 20.19.18 | NC.0.NC
ATAIOSEL | 21 | 1/NC
H_PDIAG_N | 46 | 0
H_IOCS16_N | 52 | 1/NC


POWER SWITCH
Q14
*DTC144EUA_NC
Q15
*AO6402_NC
Q28
DTC144EUA
Q29
FDC653N


HDD CONN
PIN HEADER TYPE
JIDE3
SUYIN_200055
HDD CONN. SCREWS
JS3
JS4
HDD_SCREW
HDD_SCREW
CT_0315: Change JIDE3 HDD connector's footprint from
HDD-200043FB044G513ZL-44P to HDD-200043FB044GX13ZL-44P


MEDIA BAY
JMOD2
JAE-WM1F068N1F-68P-RDV
INT_MOD_IN1#
QUANTA COMPUTER
Title
IDE (HDD&CD_ROM
Size
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PV9 PAD138X98 GND
PV19 PAD197X98 GND
PV20 PAD197X98 GND
PV3 PAD197X98 GND
PV21 PAD138X98 GND
PV22 PAD138X98 GND
H16 h-c315d126p2-4
H18 h-c315d126p2-4
H23 h-c315d126p2-4
H17 h-c315d126p2-4
H21 h-c315d126p2-4
+3VSUS
C465 2200P
C531 2200P
C116 2200P
C111 2200P
+3VRUN
Stitching caps
PV12 PAD197X98 GND
PV16 PAD197X98 GND
PV15 PAD197X98 GND
H2 h-c315d126p2-4
H22 h-c315d126p2-4
H19 h-c315d126p2-4
H14 H-C315I166D126P2
PV7 PAD197X98 GND
PV13 PAD197X98 GND
PV14 PAD197X98 GND
PV2 PAD197X98 GND
PV8 PAD197X98 GND
PV23 *PAD138X98_NC GND
+5VSUS
C14 2200P
+3VRUN
Stitching caps
H8 h-c236d126p2-4
PV5 PAD138X98 GND
PV17 PAD138X98 GND
PV4 PAD197X98 GND
CT_0314:Change PV17 from PAD197x98 to PAD138x98 small size.
H13 h-c236d110p2-4
H12 h-c236d110p2-4
H20 h-c315d118p2-4
H15 h-c354d126p2
H3 h-tc177bc63d63pt
H5 h-c315d102p2
H6 h-c315d102p2
H11 h-c315d102p2
H10 h-c315d102p2
JS2
H24 h-tc236bc118d63p2
MDC_NUT
JS2 is mounted on H24(BOT).
H7 h-c236d126p2-4
H4 h-c256d126p2-4

QUANTA COMPUTER

Title	SCREW PAD	
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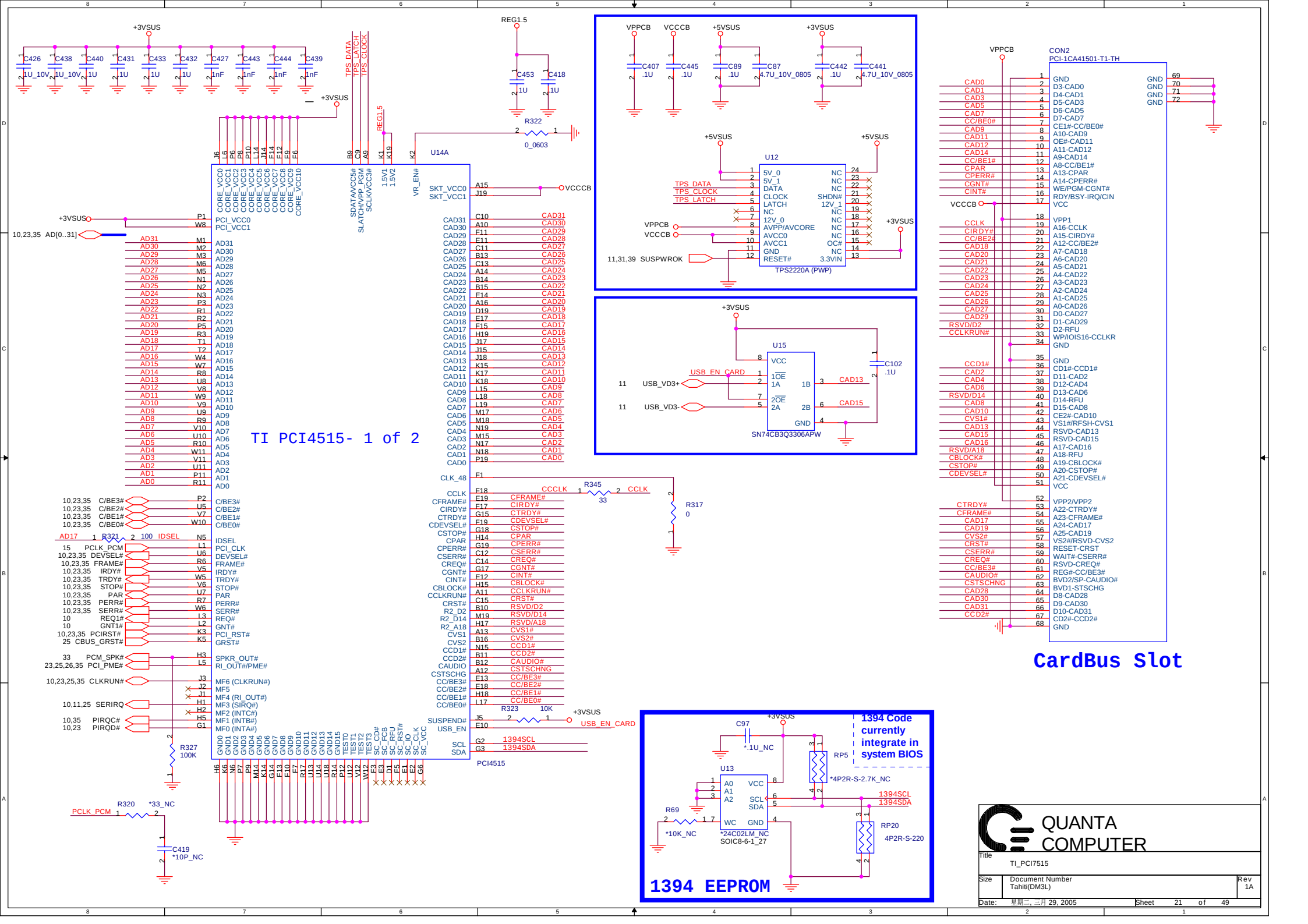
TI PCI4515- 1 of 2
PCI4515
U14A
CardBus Slot
CON2
PCI-1CA41501-T1-TH
1394 EEPROM
1394 Code currently integrate in system BIOS
TPS2220A (PWP)
SN74CB3Q3306APW
*24C02LM_NC
SOIC8-6-1_27
QUANTA COMPUTER
TI_PCI7515
Tahiti(DM3L)
Rev 1A
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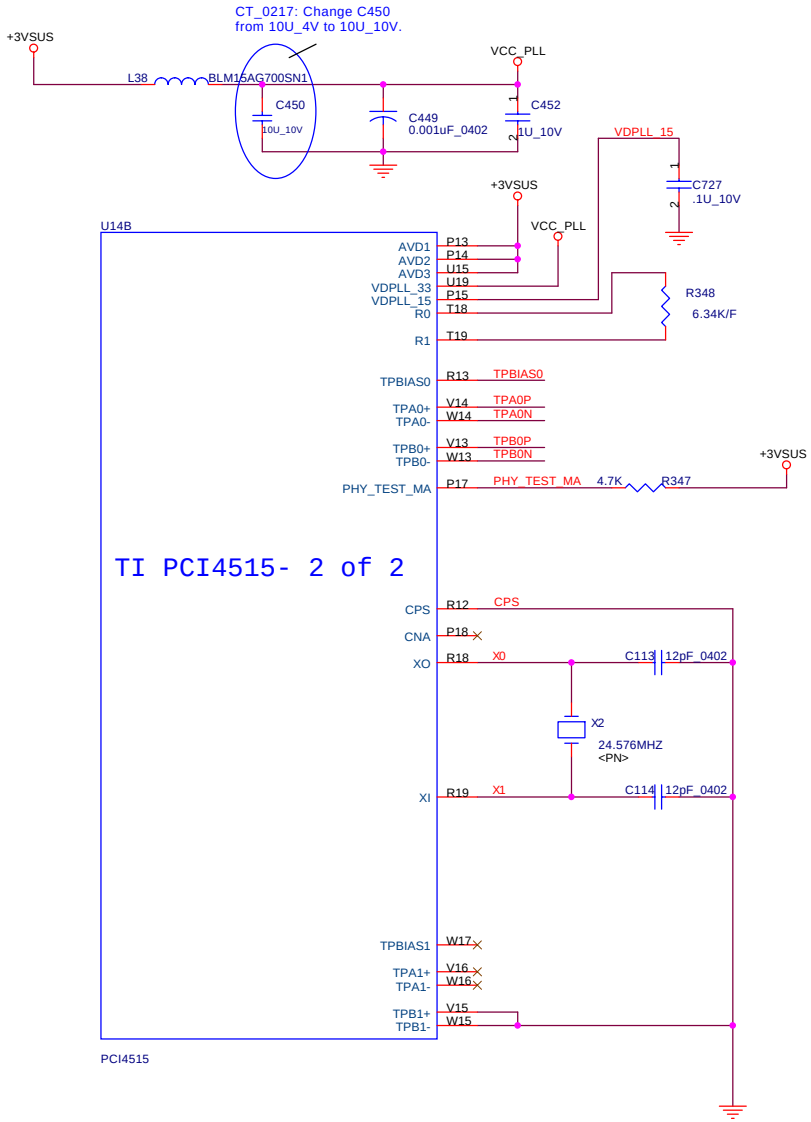

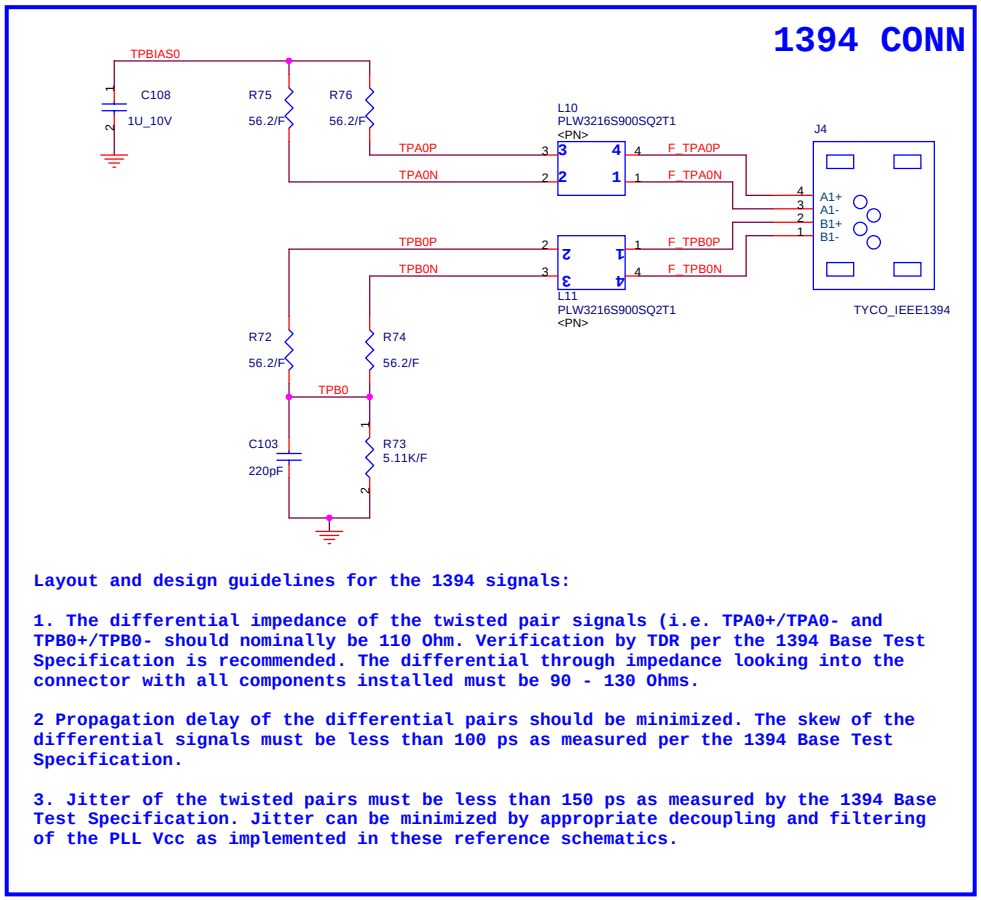

QUANTA COMPUTER

Title	TI_PCI7611_1394 & CONN	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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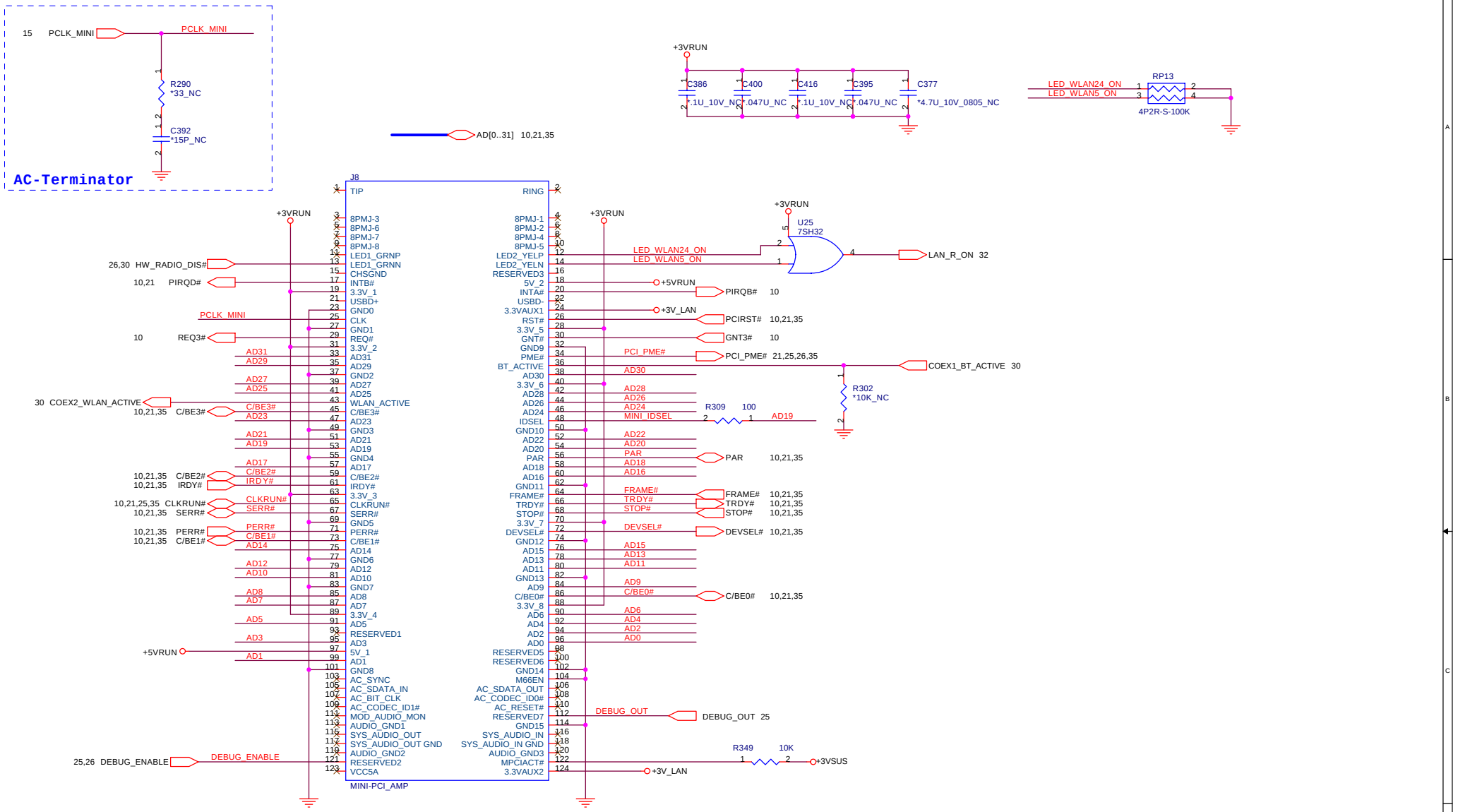
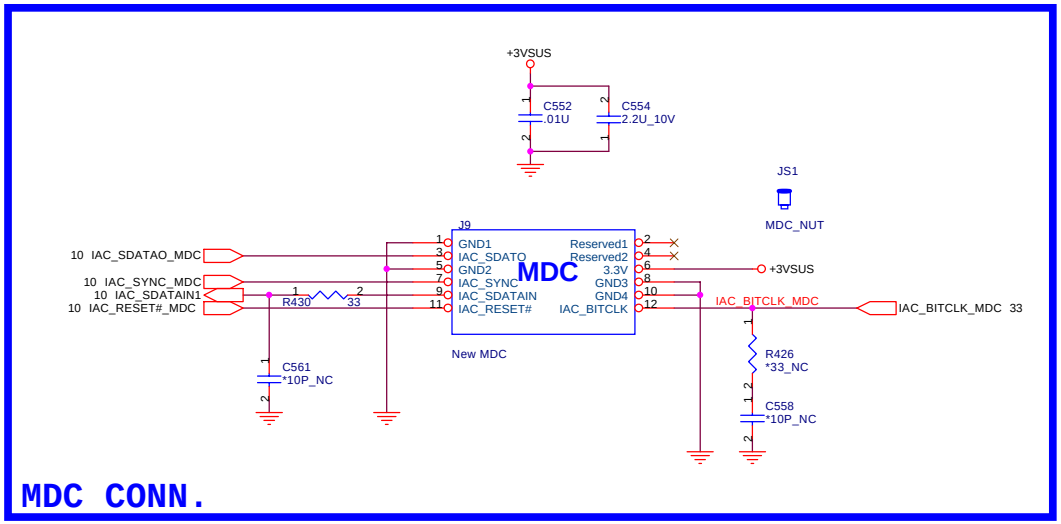
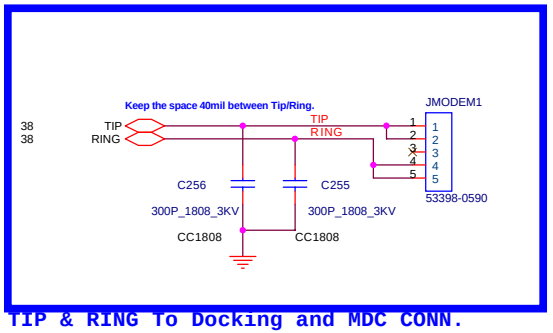


	QUANTA COMPUTER		
Title	MINI-PCI &MDC		
Size	Document Number: Tahiti(DM3L)		Rev: 1A
Date:	星期二, 三月 29, 2005	Sheet 23 of 49	



MDC CONN.



TIP & RING To Docking and MDC CONN.

QUANTA COMPUTER

Title	MDC CONN.	
Size	Document Number Tahiti(DM3L)	Rev 1A
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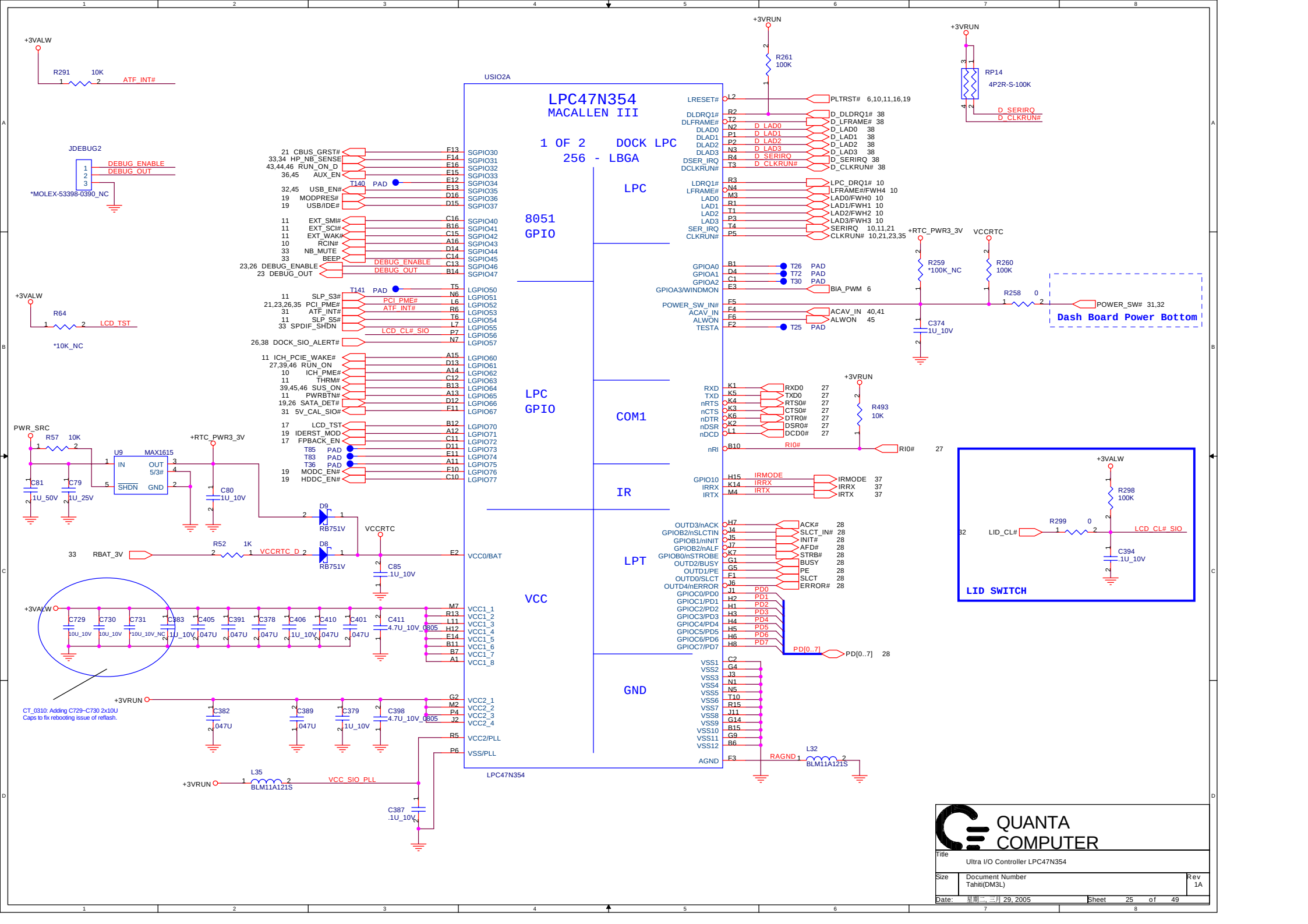
LPC47N354
MACALLEN III
1 OF 2 DOCK LPC
256 - LBGA
USIO2A
LPC
8051 GPIO
LPC GPIO
COM1
IR
LPT
VCC
GND
LPC47N354
JDEBUG2
DEBUG_ENABLE
DEBUG_OUT
*MOLEX-53398-0390_NC
R291 10K
ATF_INT#
R64
LCD_TST
*10K_NC
PWR_SRC
R57 10K
U9 MAX1615
C81 1U_50V
C79 1U_25V
C80 1U_10V
+RTC_PWR3_V
D9 RB751V
D8 RB751V
VCCRTC
R52 1K
VCCRTC_D
RBAT_3V
C85 .1U_10V
C729 C730 C731 C383 C405 C391 C378 C406 C410 C401 C411 4.7U_10V_0805
CT_0310: Adding C729~C730 2x10U
Caps to fix rebooting issue of reflash.
C382 C389 C379 C398 4.7U_10V_0805
L35 BLM11A121S
VCC_SIO_PLL
C387 .1U_10V
R261 100K
RP14 4P2R-S-100K
D_SERIRQ
D_CLKRUN#
R259 *100K_NC
R260 100K
R258 0
C374 1U_10V
POWER_SW#
Dash Board Power Bottom
R493 10K
RI0#
R298 100K
R299 0
LID_CL#
LCD_CL# SIO
C394 .1U_10V
LID SWITCH
L32 BLM11A121S
RAGND
PD[0..7]
QUANTA COMPUTER
Title
Ultra I/O Controller LPC47N354
Size
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Tahiti(DM3L)
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1A
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LPC47N354 MACALLEN III 2 OF 2 256 - LBGA

USIO2B

Board ID

BID3	BID2	BID1	BID0	Board Revision
0				
0	0	0	0	PROTO1
0	0	0	1	PROTO1.5
0	0	1	0	PROTO2.0
0	0	1	1	PROTO3.0
0	1	0	0	QT
0	1	0	1	RAMP1

CT_0217: Pop R296,R280,R292 10K, Depop R301,R287,R289 10K.

Keyboard CONN

JKB2

JAE_FK1S030W52

100P CAPS CLOSE TO JKB1

Title	Ultra I/O Controller LPC47N254(GPIO/KB/MISC/FLASH)
Size	Document Number: Tahiti(DM3L)
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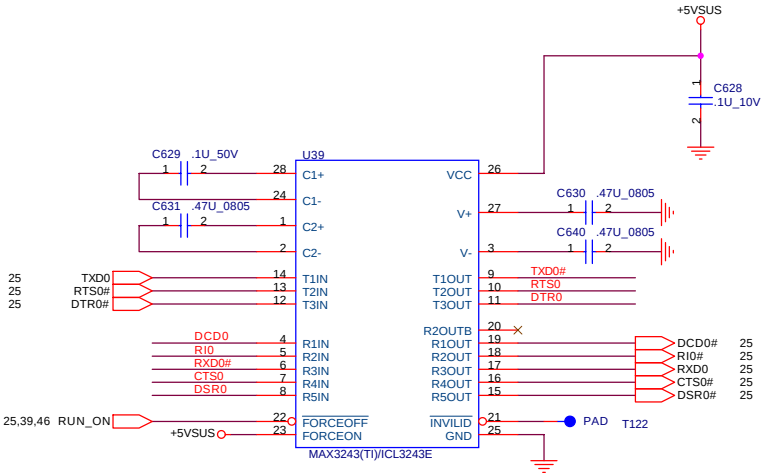
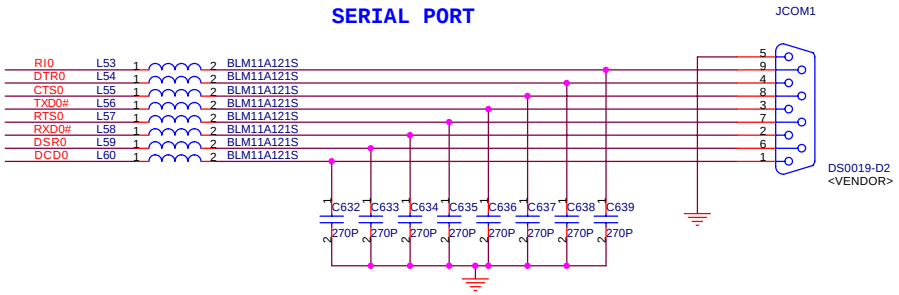
QUANTA COMPUTER



If MAX3243 pin 22 tied to RUN_ON,then it can not support Ring Out

SERIAL PORT



Place them close to serial port

QUANTA COMPUTER

Title	SERIAL PORT & USB	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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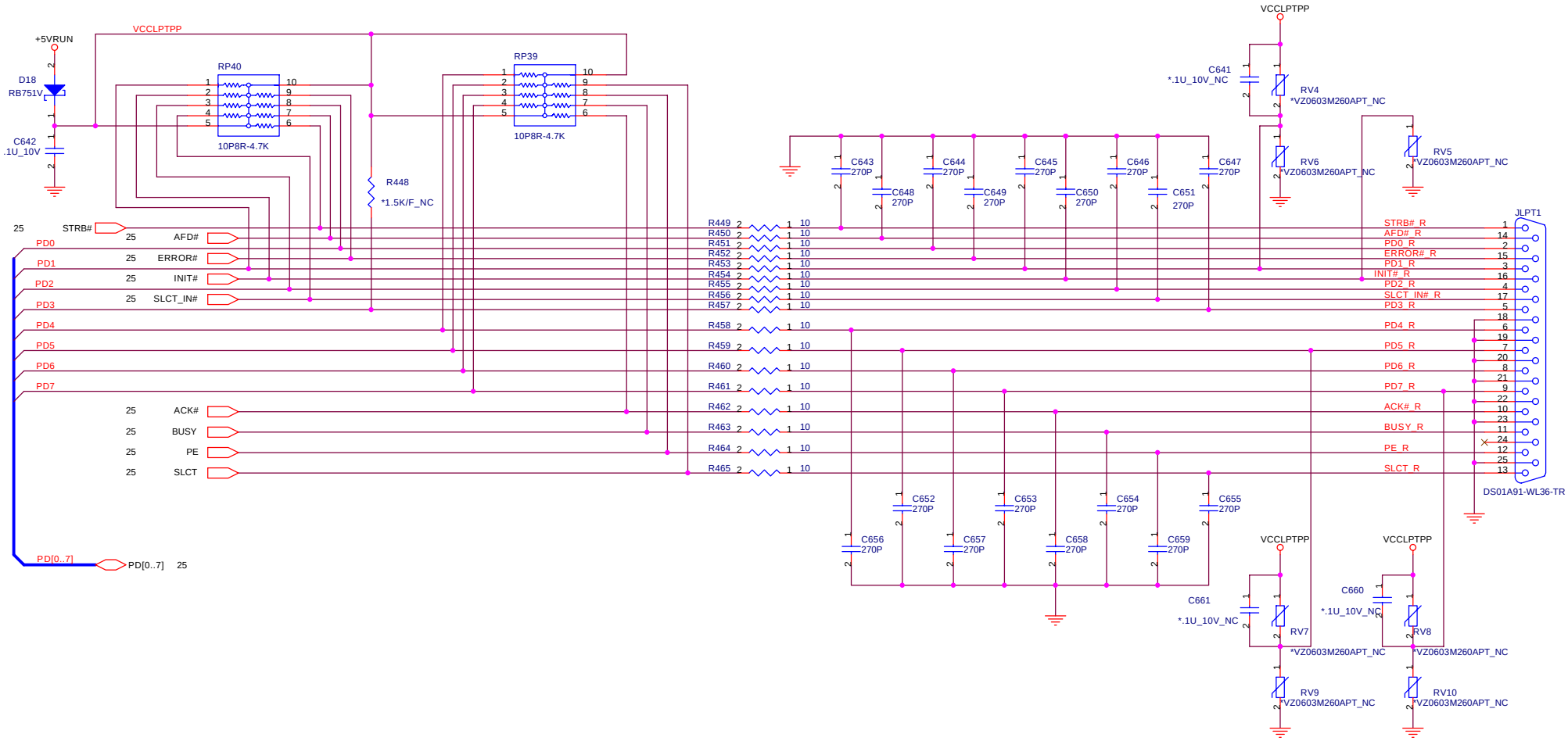

QUANTA COMPUTER

Title	PARALLEL CONN.	
Size	Document Number Tahiti(DM3L)	Rev 1A
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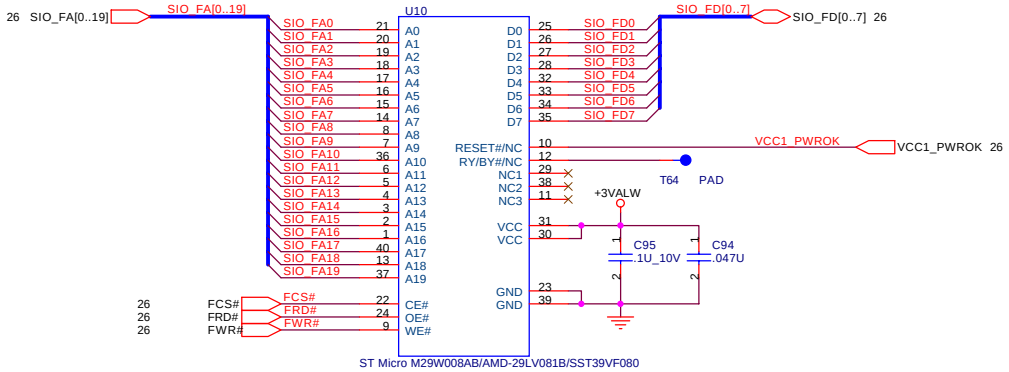
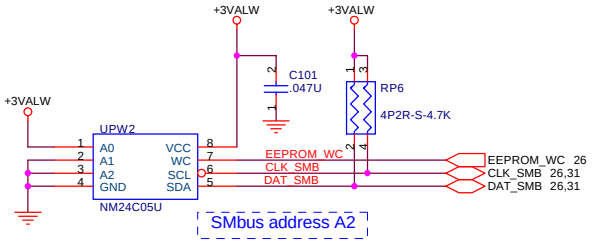
BIOS FLASH MEMORY

8Mbit (1M Byte),ISN'T PLCC TYPE



AMD :Pin 10 is RESET# ; Pin12 is RY/BY#
SST :Pin10,12 are NC

1.AMD-29LV081B require MAX 500nS Tready for it's hardware reset.And MAX6326_UR29 has >100mS reset timing.So we can tie it's reset# pin to +3VALW directly.
2.SIO has internal 20 mS delay of VCC1_PWROK



User Password

QUANTA COMPUTER

Title	FLASH	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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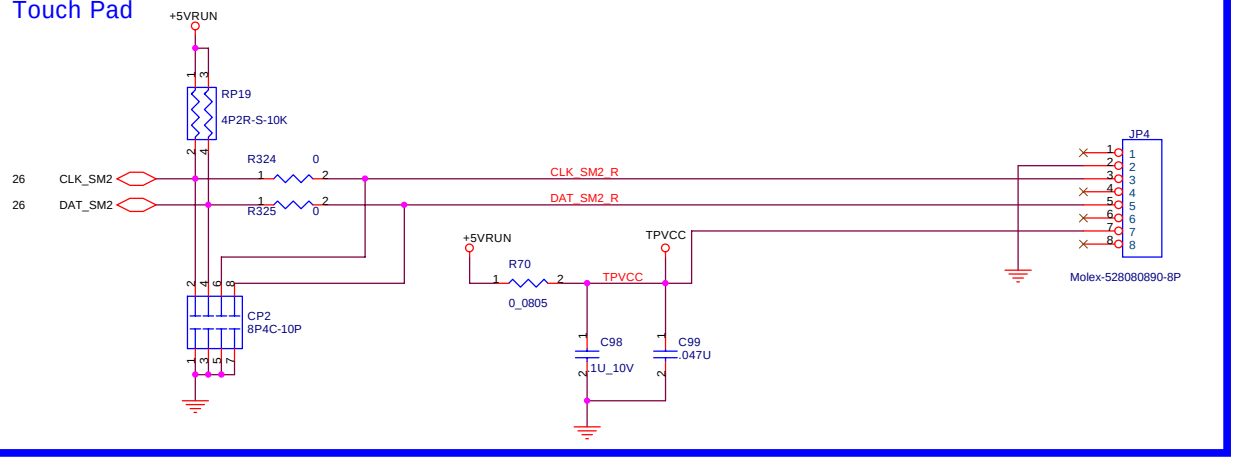
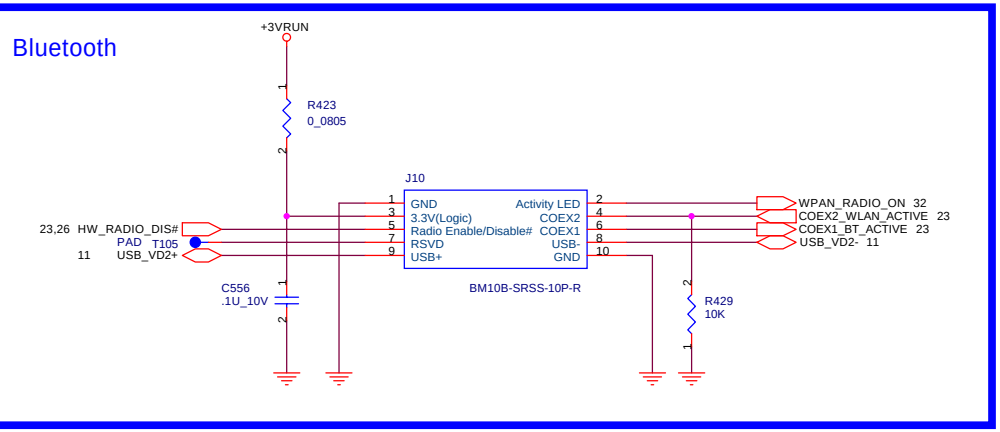
Touch Pad



Bluetooth



QUANTA COMPUTER

Title	TOUCH PAD & BULE TOOTH	
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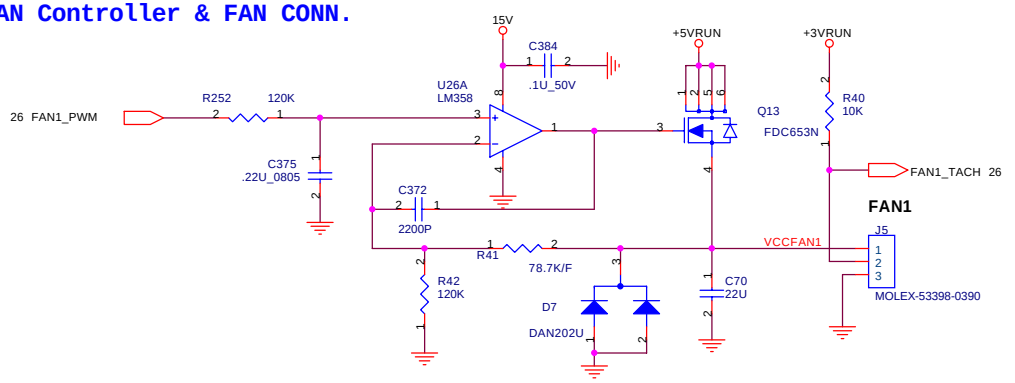
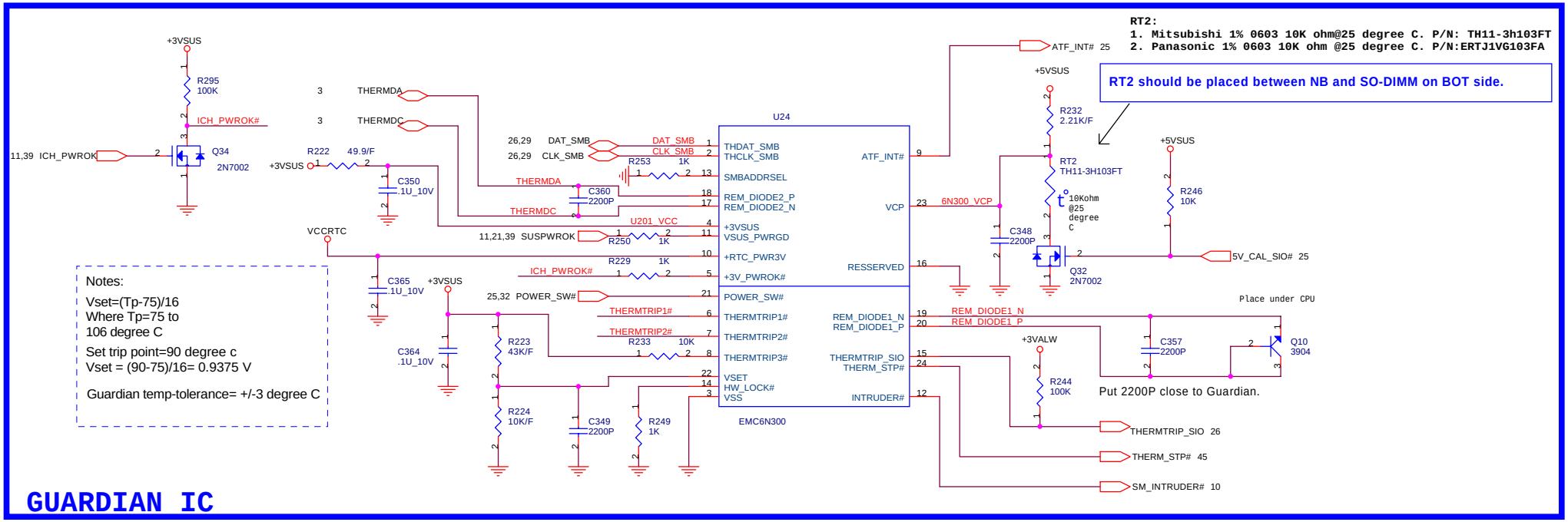
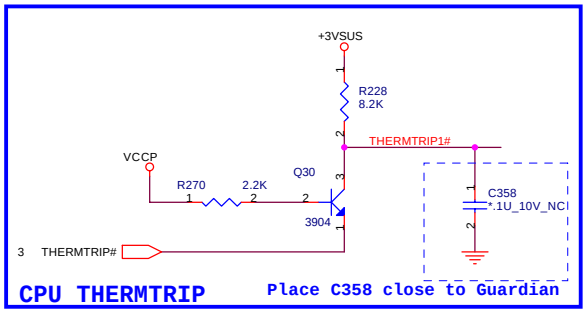
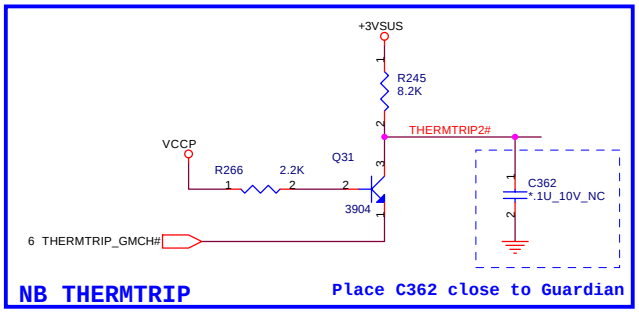
FAN Controller & FAN CONN.



GUARDIAN IC

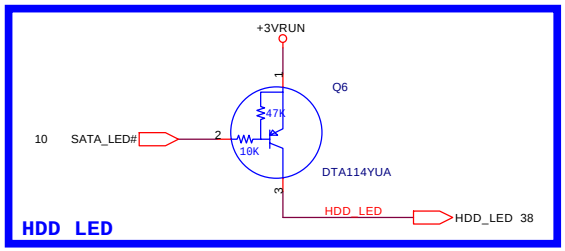


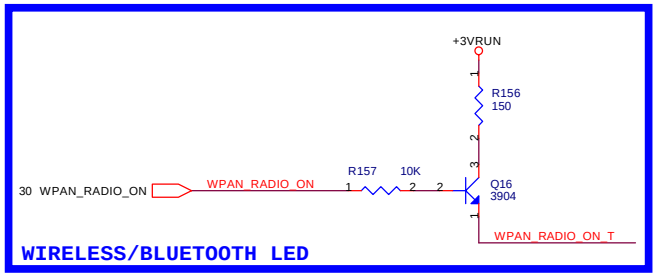


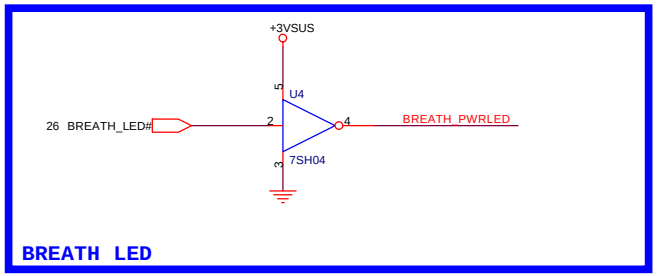


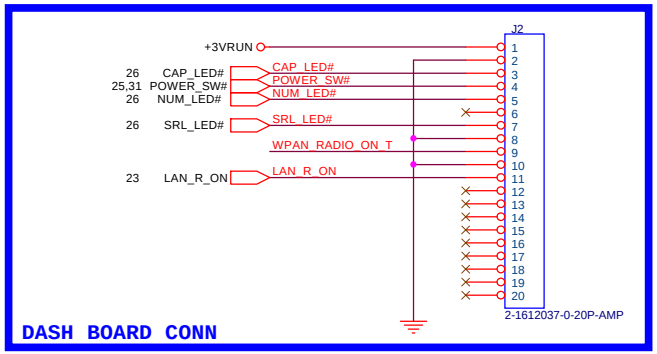
QUANTA COMPUTER

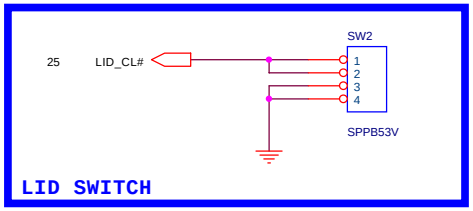
Title	FAN & THERMAL	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
Date: 星期二, 三月 29, 2005	Sheet 31 of 49	

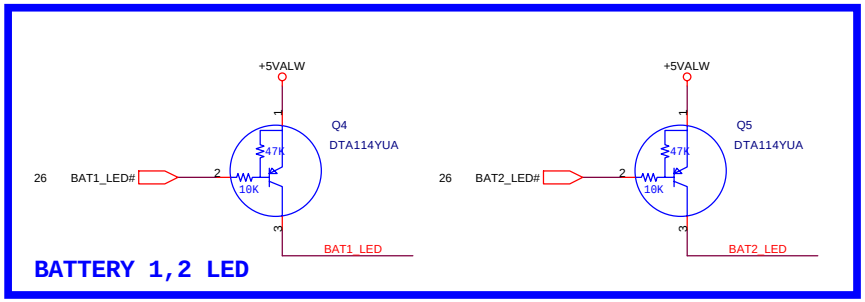


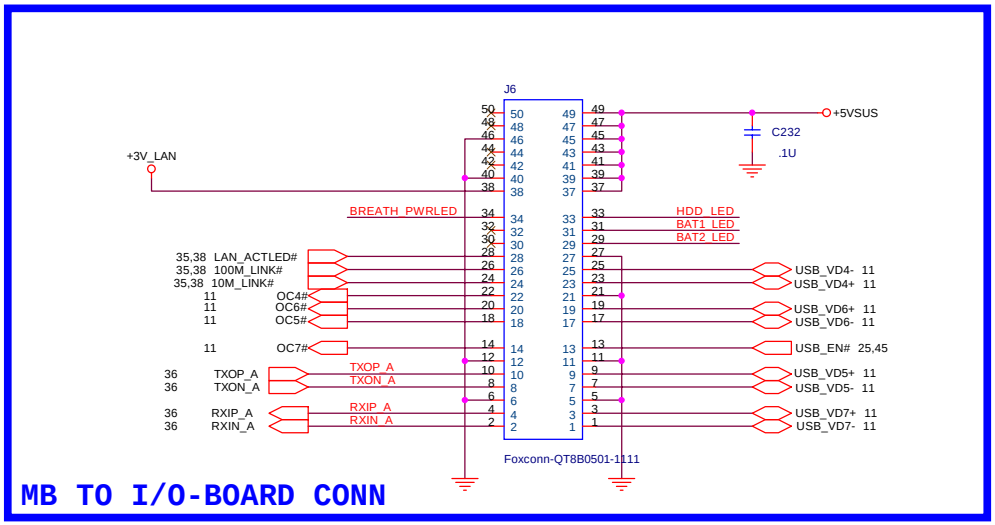






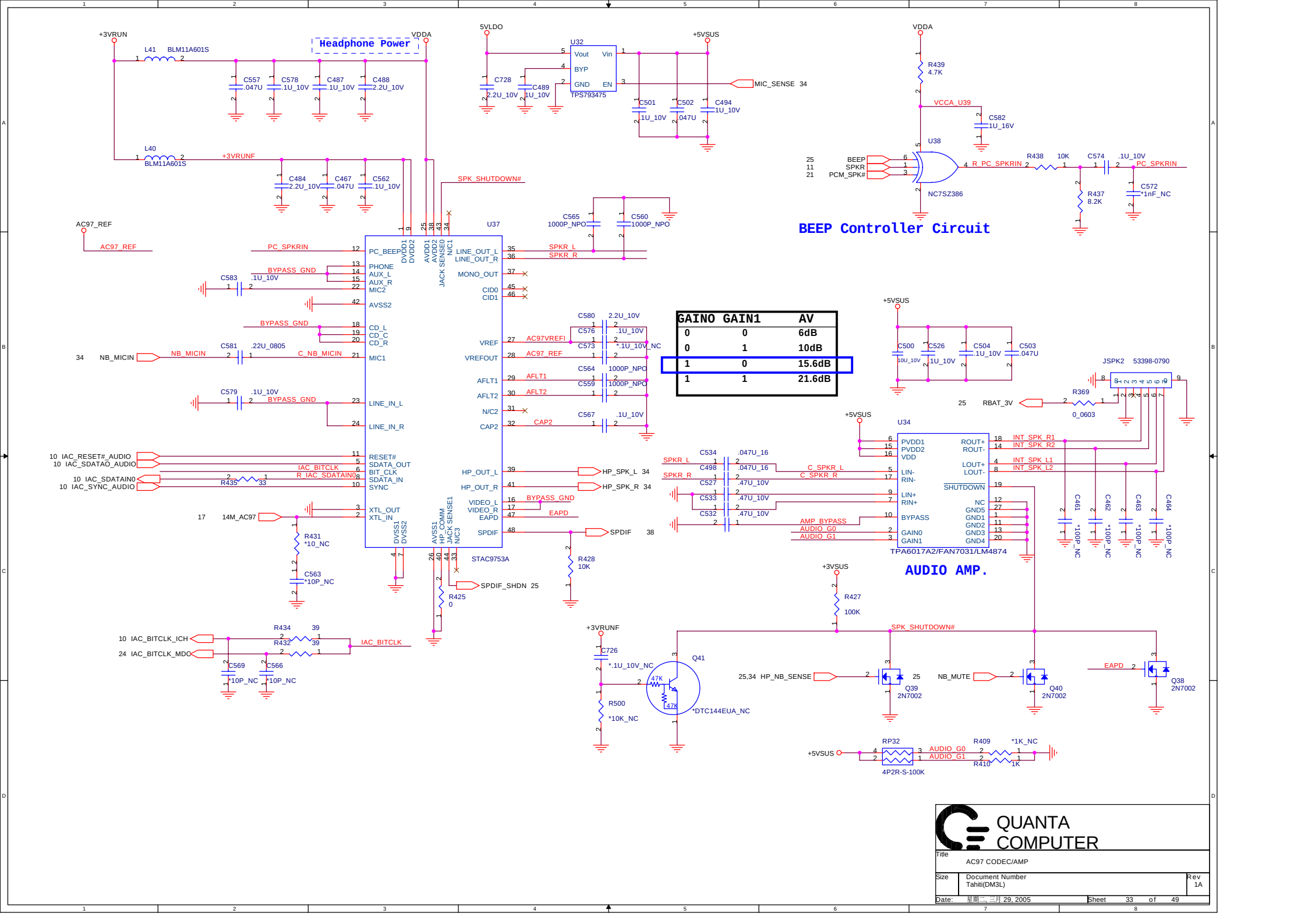






QUANTA COMPUTER

Title	SWITCH & LED	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
Date: 星期三, 三月 29, 2005	Sheet 32 of 49	


Headphone Power
BEEP Controller Circuit
AUDIO AMP.
TPA6017A2/FAN7031/LM4874
STAC9753A
TPS793475
NC7SZ386
GAINO GAIN1 AV
0 0 6dB
0 1 10dB
1 0 15.6dB
1 1 21.6dB
QUANTA COMPUTER
Title
AC97 CODEC/AMP
Size
Document Number
Tahiti(DM3L)
Rev
1A
Date: 星期二, 三月 29, 2005
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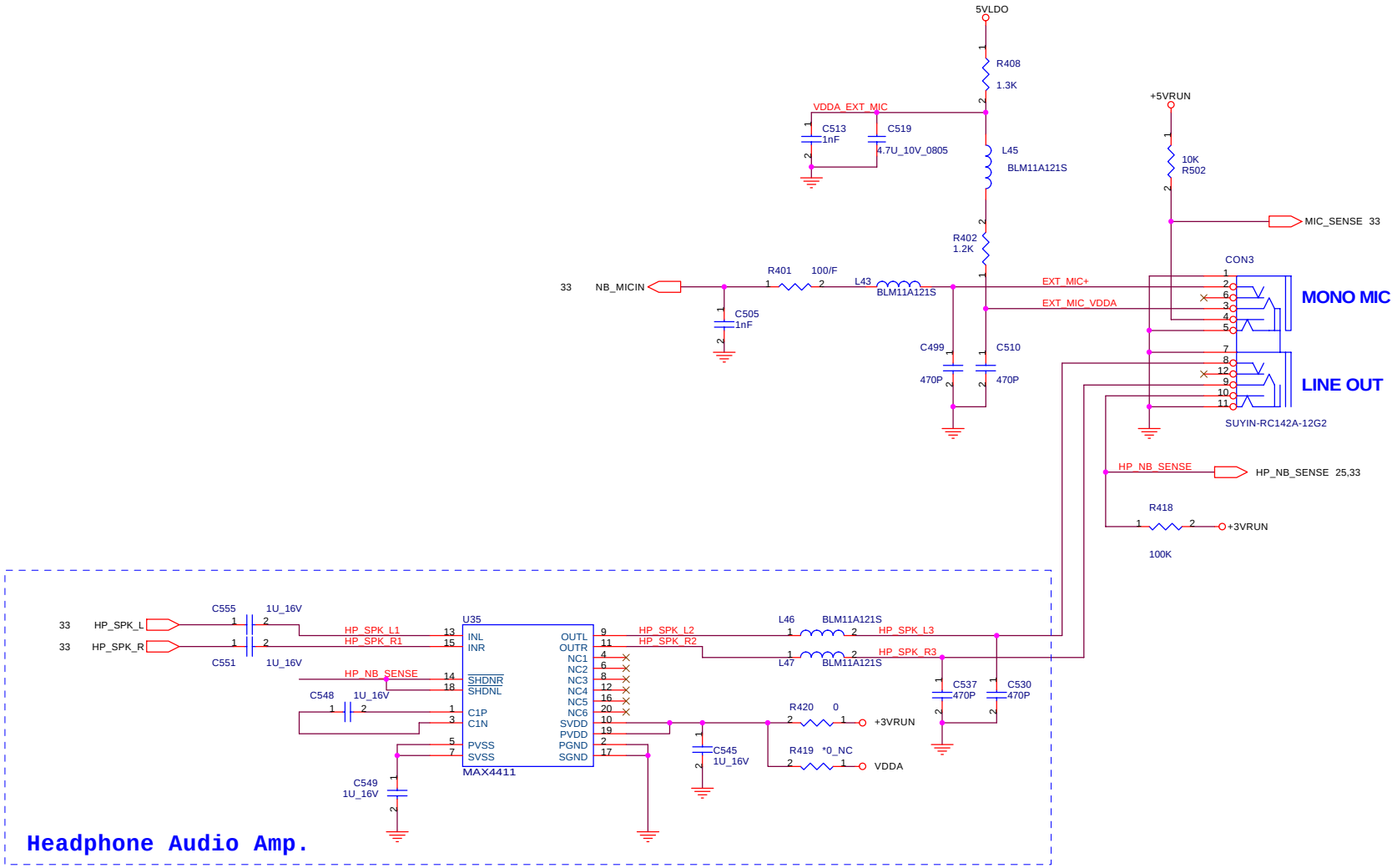

QUANTA COMPUTER

Title	AUDIO HEADPHONE CONN	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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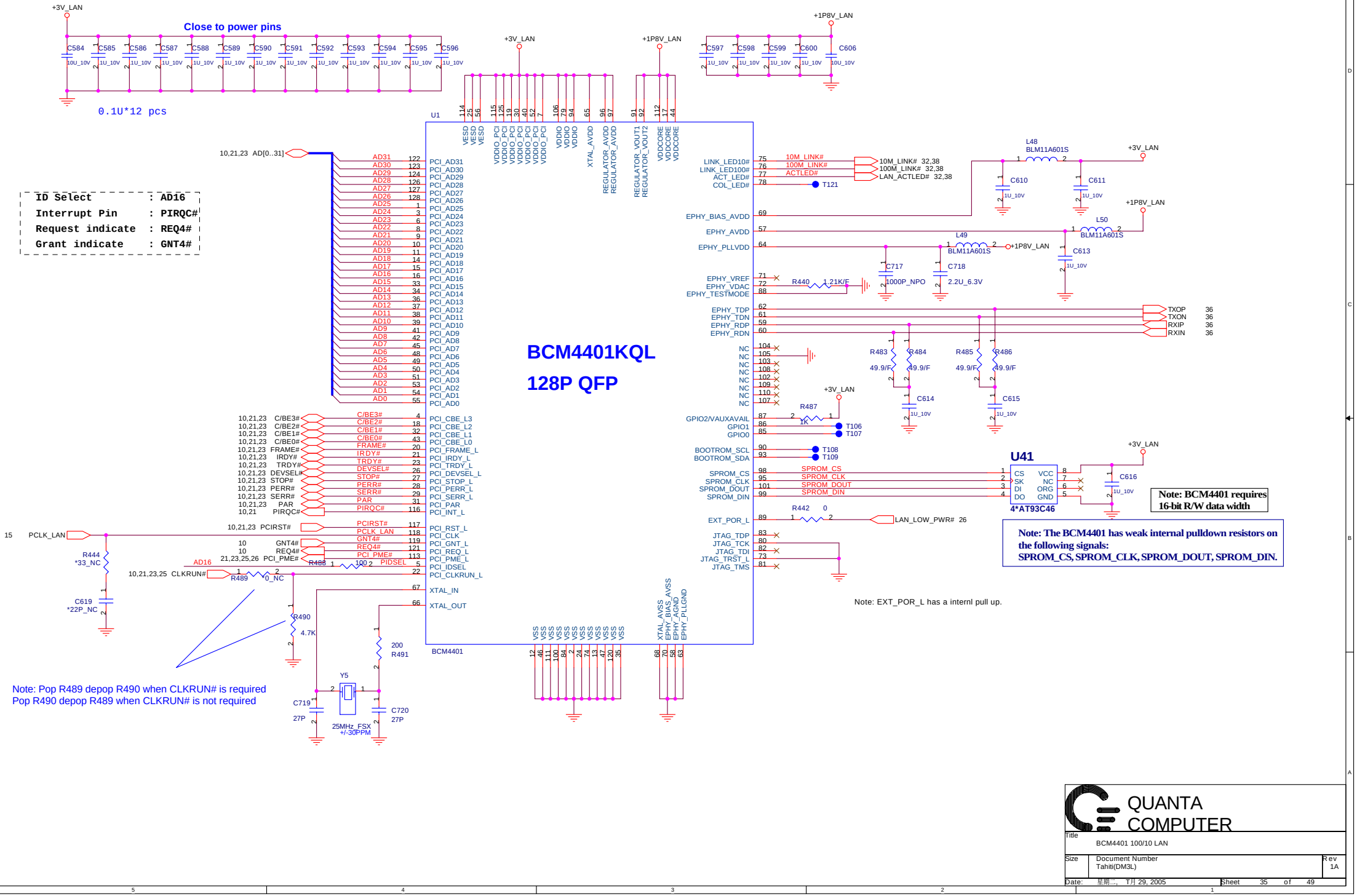
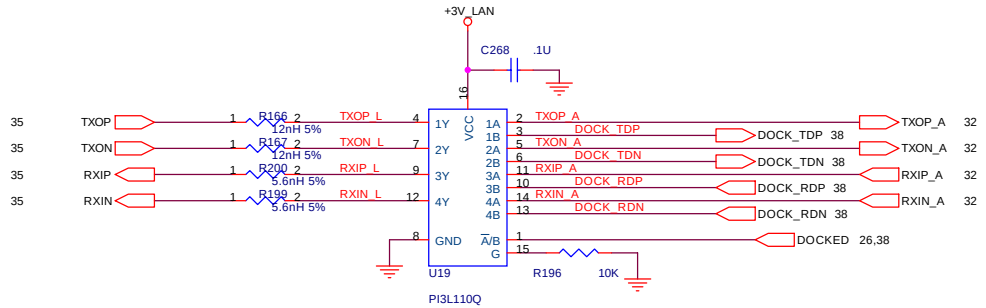
LAN-BCM4401KFB(10/100M)

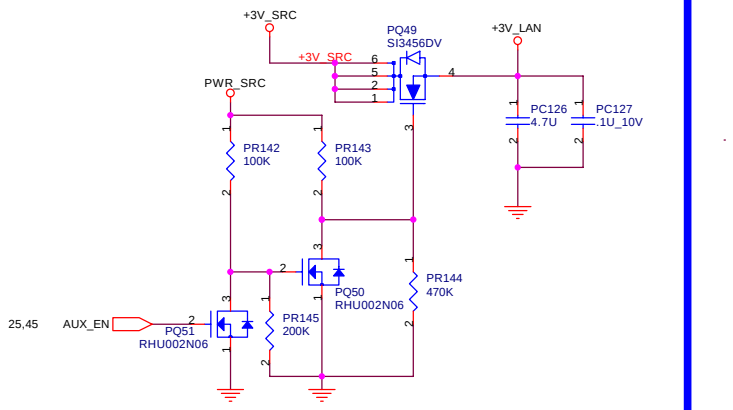
BCM4401--10/100



Title	BCM4401 100/10 LAN		
Size	Document Number Tahiti(DM3L)		Rev 1A
Date: 星期二, 7月 29, 2005		Sheet 35 of 49	



10/100LAN_E-SWITCH



LAN POWER

QUANTA COMPUTER

Title	LAN SWITCH/LAN POWER	
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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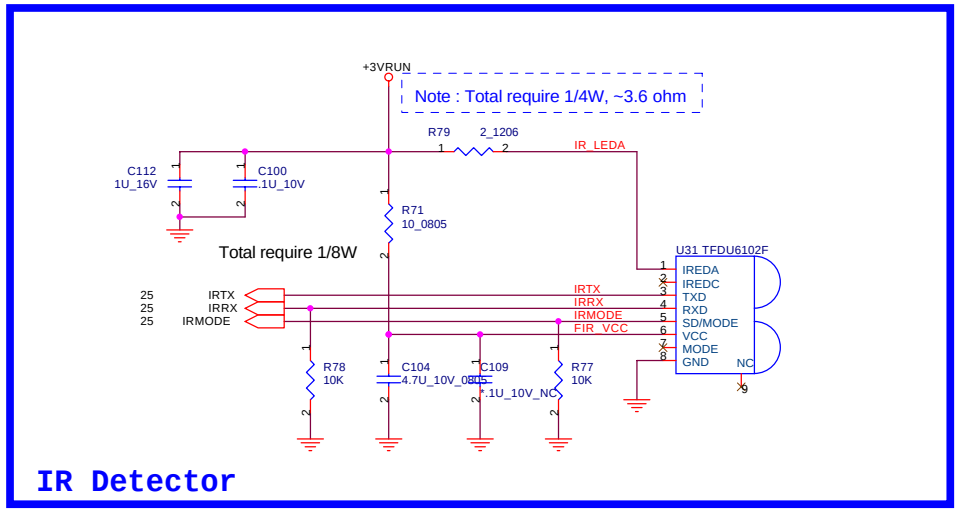

QUANTA COMPUTER		
Title: FIR		
Size	Document Number: Tahiti(DM3L)	Rev: 1A
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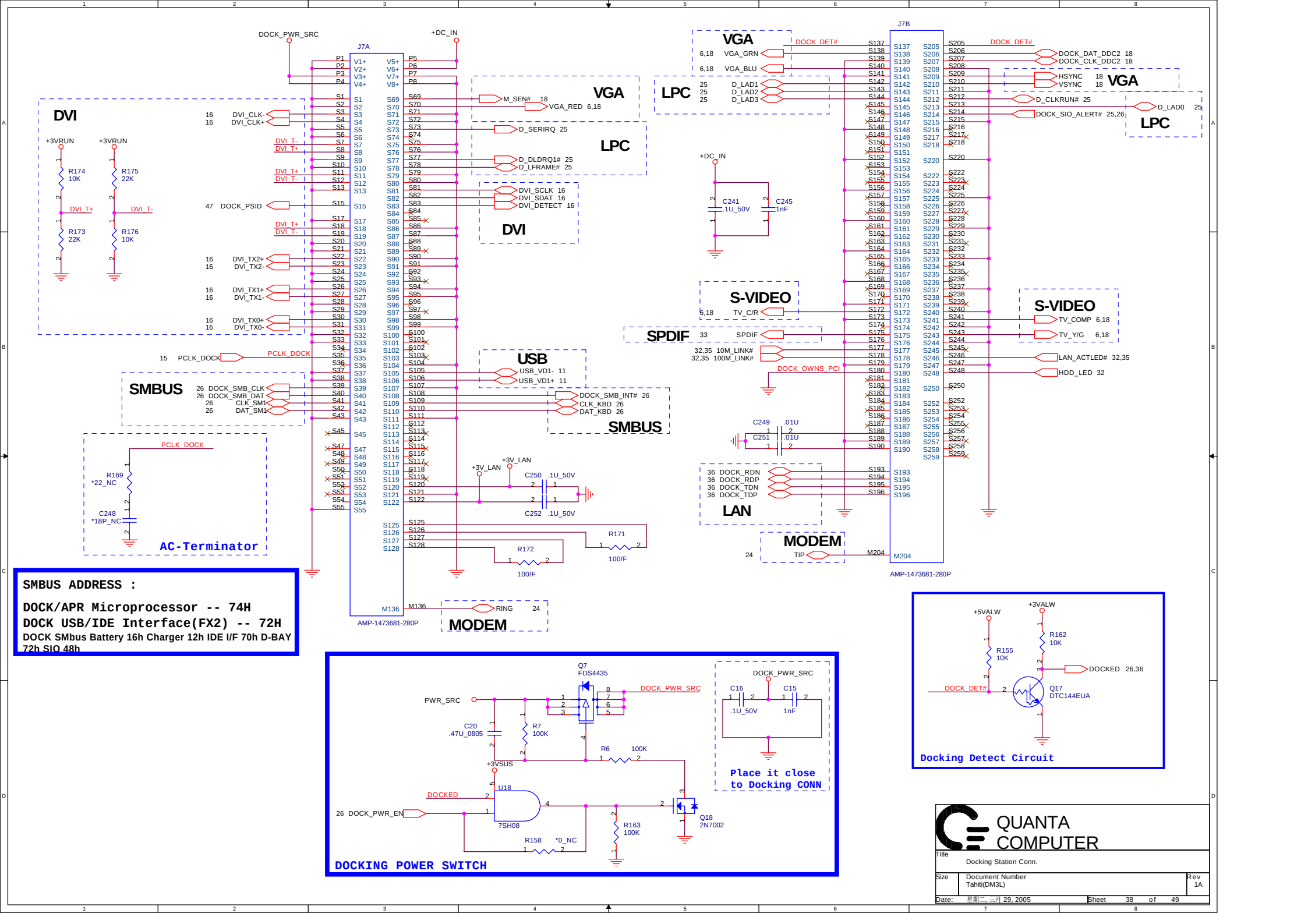
DOCK_PWR_SRC
+DC_IN
J7A
AMP-1473681-280P
DVI
VGA
LPC
S-VIDEO
SPDIF
USB
SMBUS
MODEM
LAN
J7B
AMP-1473681-280P
SMBUS ADDRESS :
DOCK/APR Microprocessor -- 74H
DOCK USB/IDE Interface(FX2) -- 72H
DOCK SMbus Battery 16h Charger 12h IDE I/F 70h D-BAY 72h SIO 48h
AC-Terminator
DOCKING POWER SWITCH
Place it close to Docking CONN
Docking Detect Circuit
QUANTA COMPUTER
Title
Docking Station Conn.
Size
Document Number
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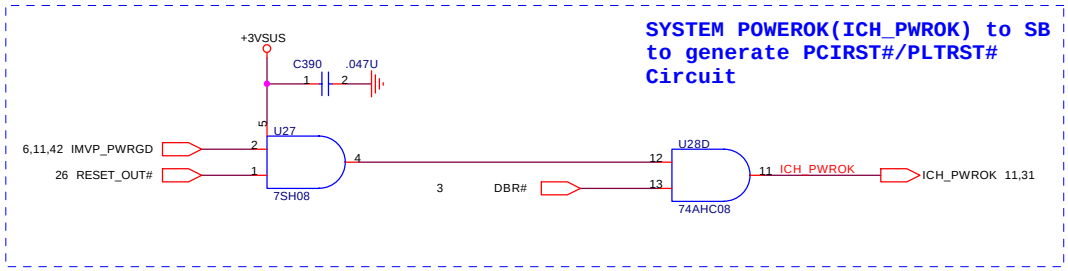

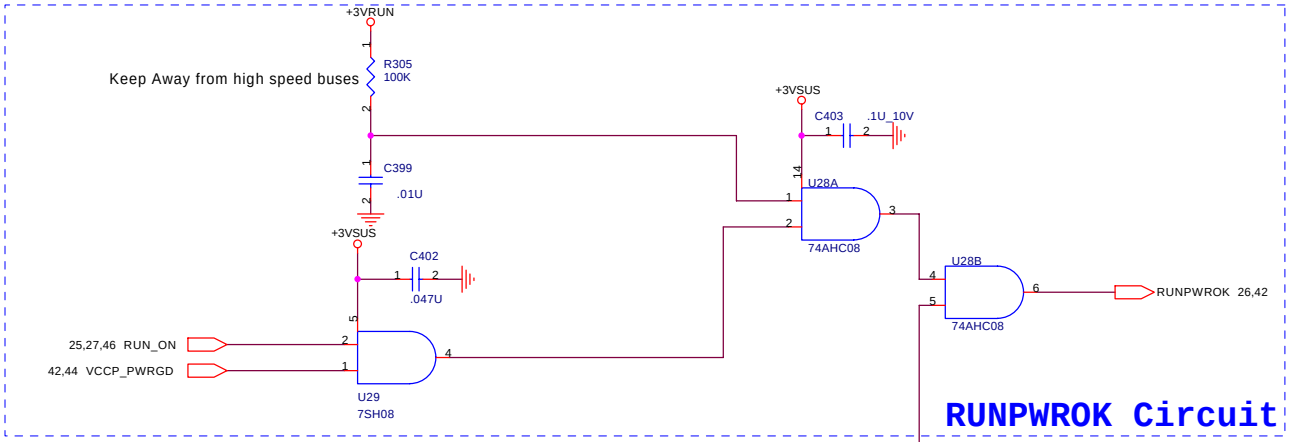

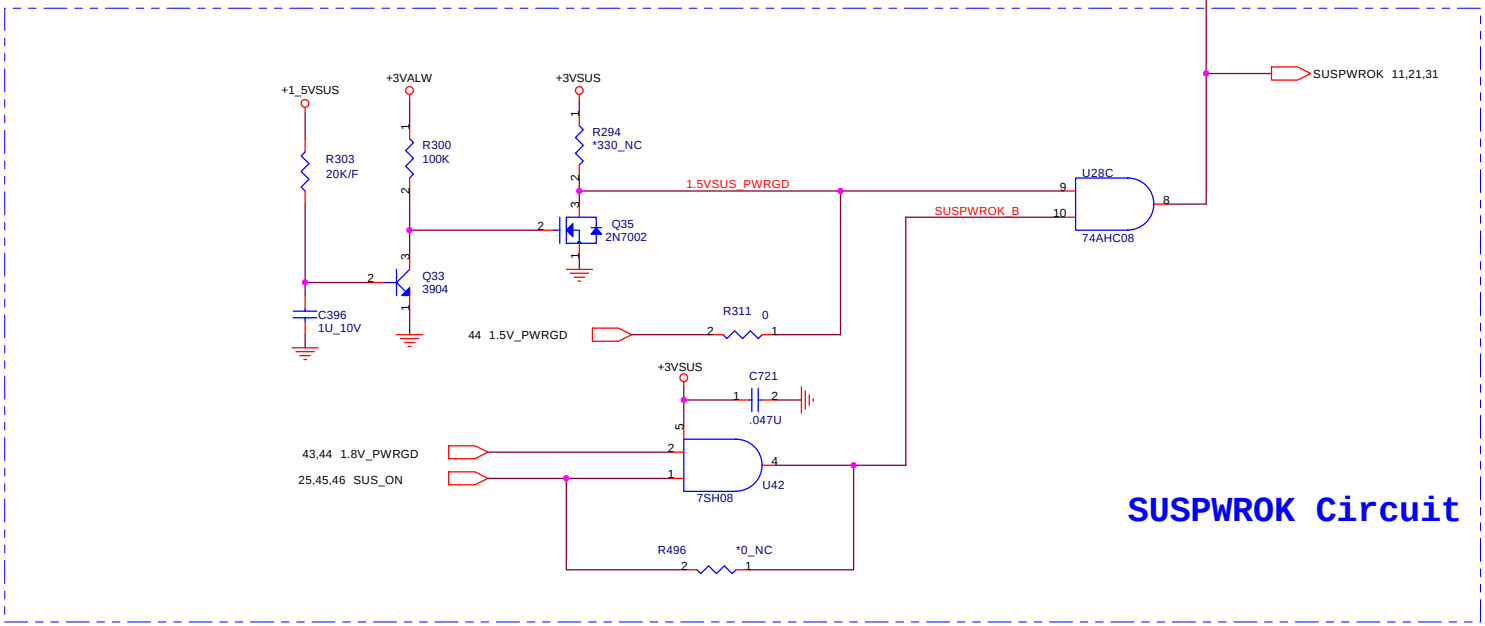

QUANTA COMPUTER

Title	SYSTEM RESET/POWER GOOD	
Size	Document Number Tahiti(DM3L)	Rev 1A
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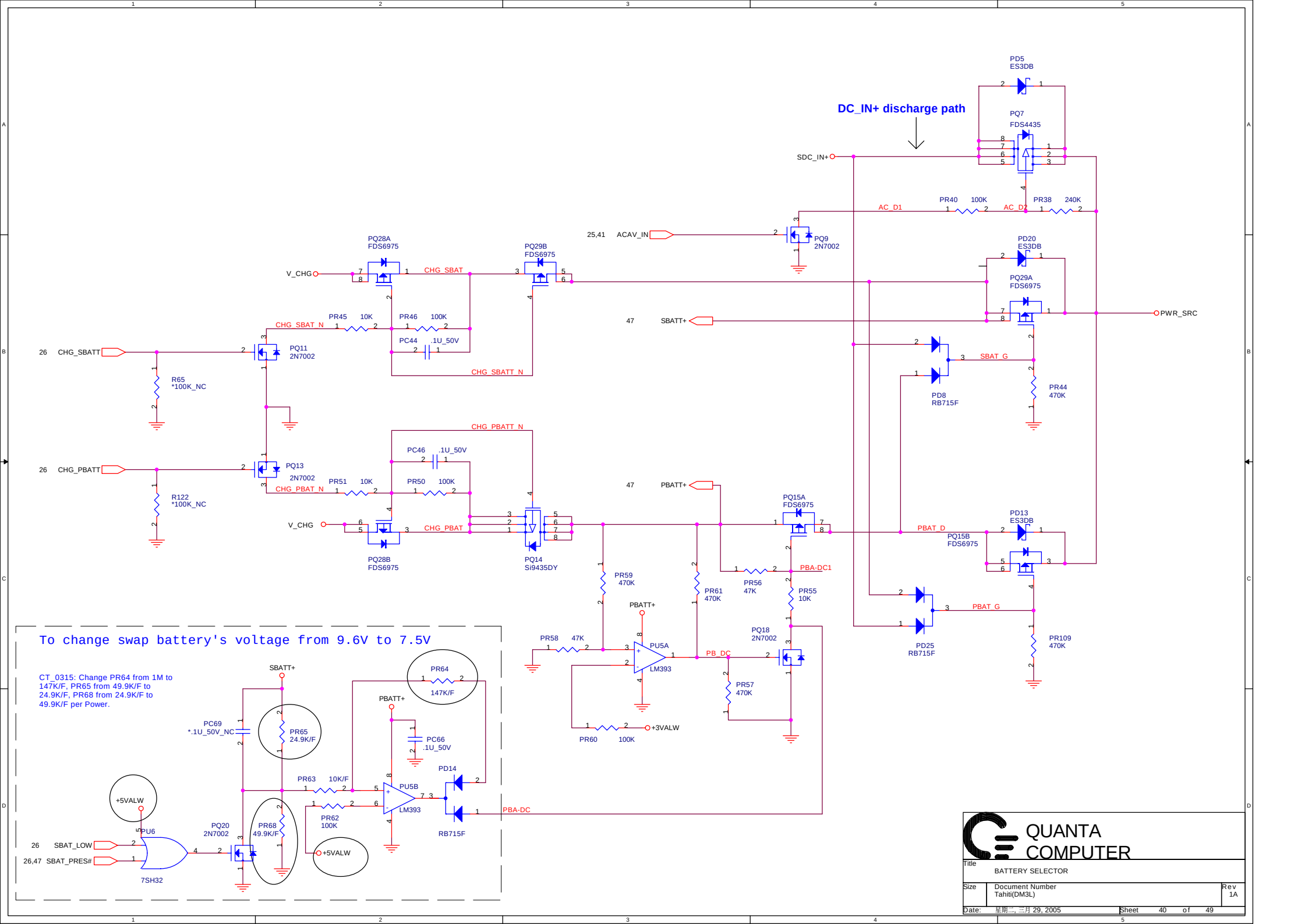

Title	BATTERY SELECTOR	
Size	Document Number Tahiti(DM3L)	Rev 1A
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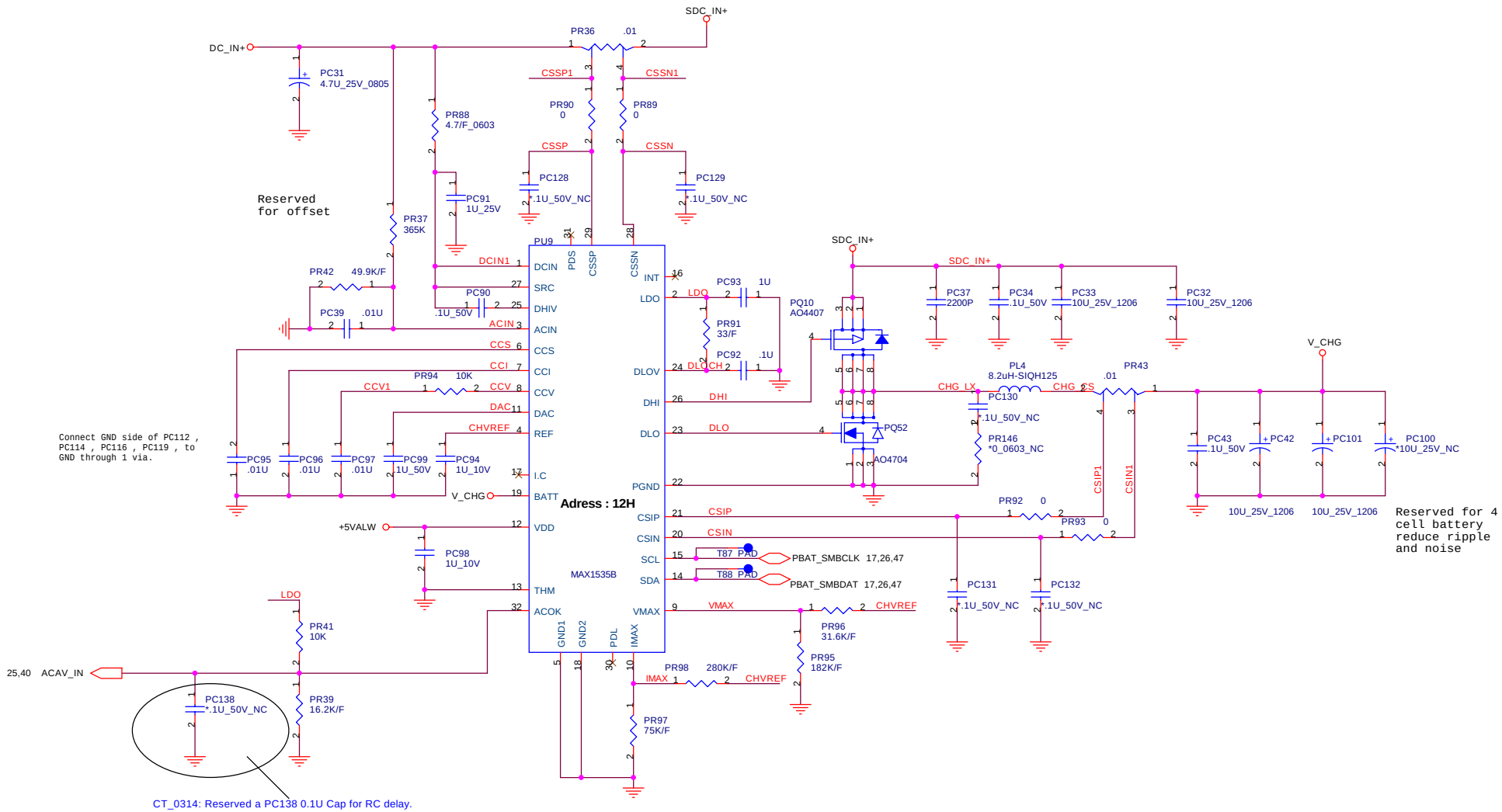
QUANTA COMPUTER



Title	Battery Charger	
Size	Document Number Tahiti(DM3L)	Rev 1A
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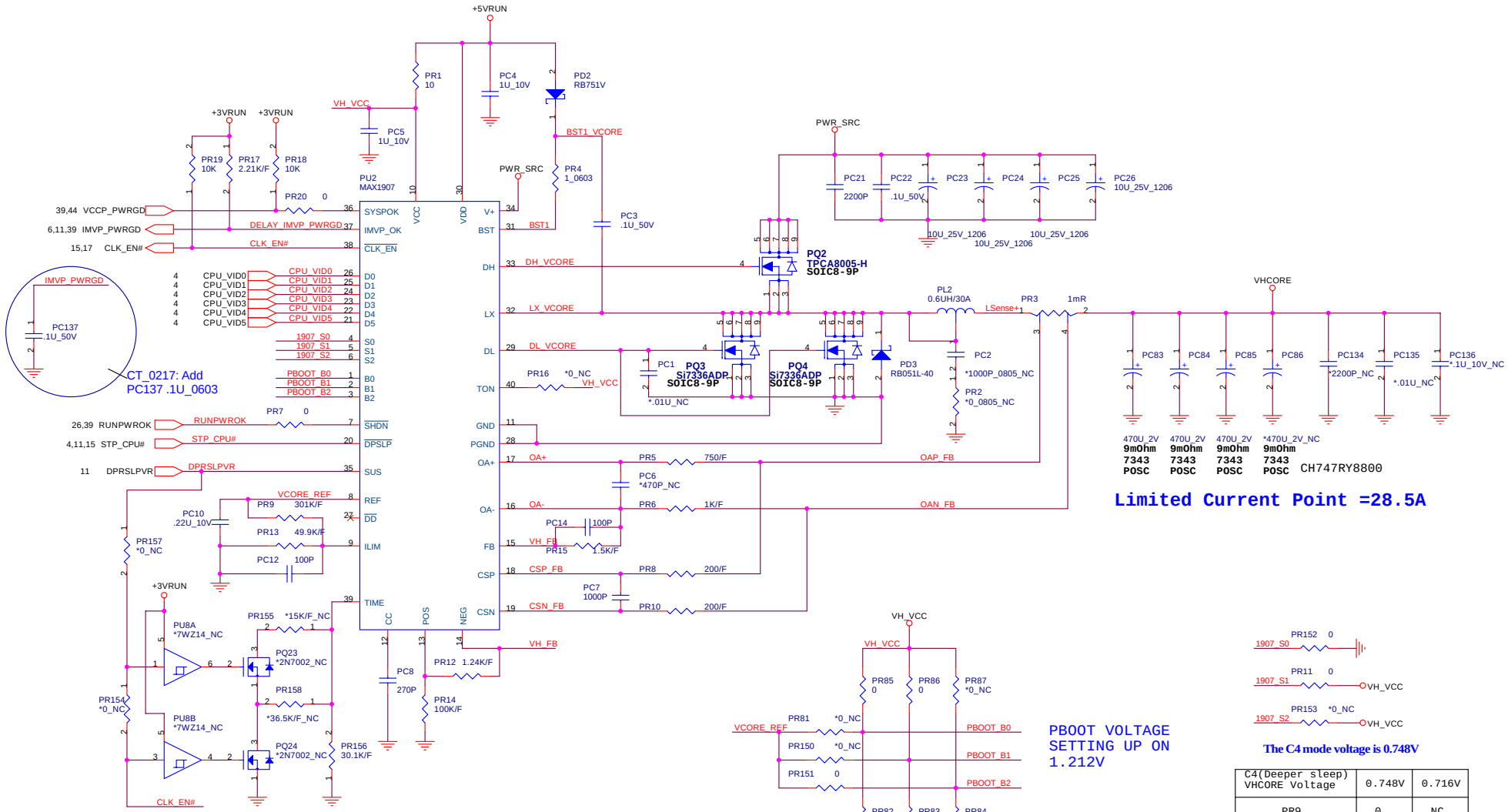

C4(Deeper sleep) VHCORE Voltage	0.748V	0.716V
PR9	0	NC
PR11	0	0
PR13	NC	NC

D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V
1	0	0	0	0	1	1.180V
1	0	0	0	1	0	1.164V
1	0	0	0	1	1	1.148V
1	0	0	1	0	0	1.132V
1	0	0	1	0	1	1.116V
1	0	0	1	1	0	1.100V
1	0	0	1	1	1	1.084V
1	0	1	0	0	0	1.068V
1	0	1	0	0	1	1.052V
1	0	1	0	1	0	1.036V
1	0	1	0	1	1	1.020V
1	0	1	1	0	0	1.004V
1	0	1	1	0	1	0.988V
1	0	1	1	1	0	0.972V
1	0	1	1	1	1	0.956V
1	1	0	0	0	0	0.940V
1	1	0	0	0	1	0.924V
1	1	0	0	1	0	0.908V
1	1	0	0	1	1	0.892V
1	1	0	1	0	0	0.876V
1	1	0	1	0	1	0.860V
1	1	0	1	1	0	0.844V
1	1	0	1	1	1	0.828V
1	1	1	0	0	0	0.812V
1	1	1	0	0	1	0.796V
1	1	1	0	1	0	0.780V
1	1	1	0	1	1	0.764V
1	1	1	1	0	0	0.748V
1	1	1	1	0	1	0.732V
1	1	1	1	1	0	0.716V
1	1	1	1	1	1	0.700V

D5	D4	D3	D2	D1	D0	Output
0	0	0	0	0	0	1.708V
0	0	0	0	0	1	1.692V
0	0	0	0	1	0	1.676V
0	0	0	0	1	1	1.660V
0	0	0	1	0	0	1.644V
0	0	0	1	0	1	1.628V
0	0	0	1	1	0	1.612V
0	0	0	1	1	1	1.596V
0	0	1	0	0	0	1.580V
0	0	1	0	0	1	1.564V
0	0	1	0	1	0	1.548V
0	0	1	0	1	1	1.532V
0	0	1	1	0	0	1.516V
0	0	1	1	0	1	1.500V
0	0	1	1	1	0	1.484V
0	0	1	1	1	1	1.468V
0	1	0	0	0	0	1.452V
0	1	0	0	0	1	1.436V
0	1	0	0	1	0	1.420V
0	1	0	0	1	1	1.404V
0	1	0	1	0	0	1.388V
0	1	0	1	0	1	1.372V
0	1	0	1	1	0	1.356V
0	1	0	1	1	1	1.340V
0	1	1	0	0	0	1.324V
0	1	1	0	0	1	1.308V
0	1	1	0	1	0	1.292V
0	1	1	0	1	1	1.276V
0	1	1	1	0	0	1.260V
0	1	1	1	0	1	1.244V
0	1	1	1	1	0	1.228V
0	1	1	1	1	1	1.212V

QUANTA COMPUTER

Title	CPU POWER	
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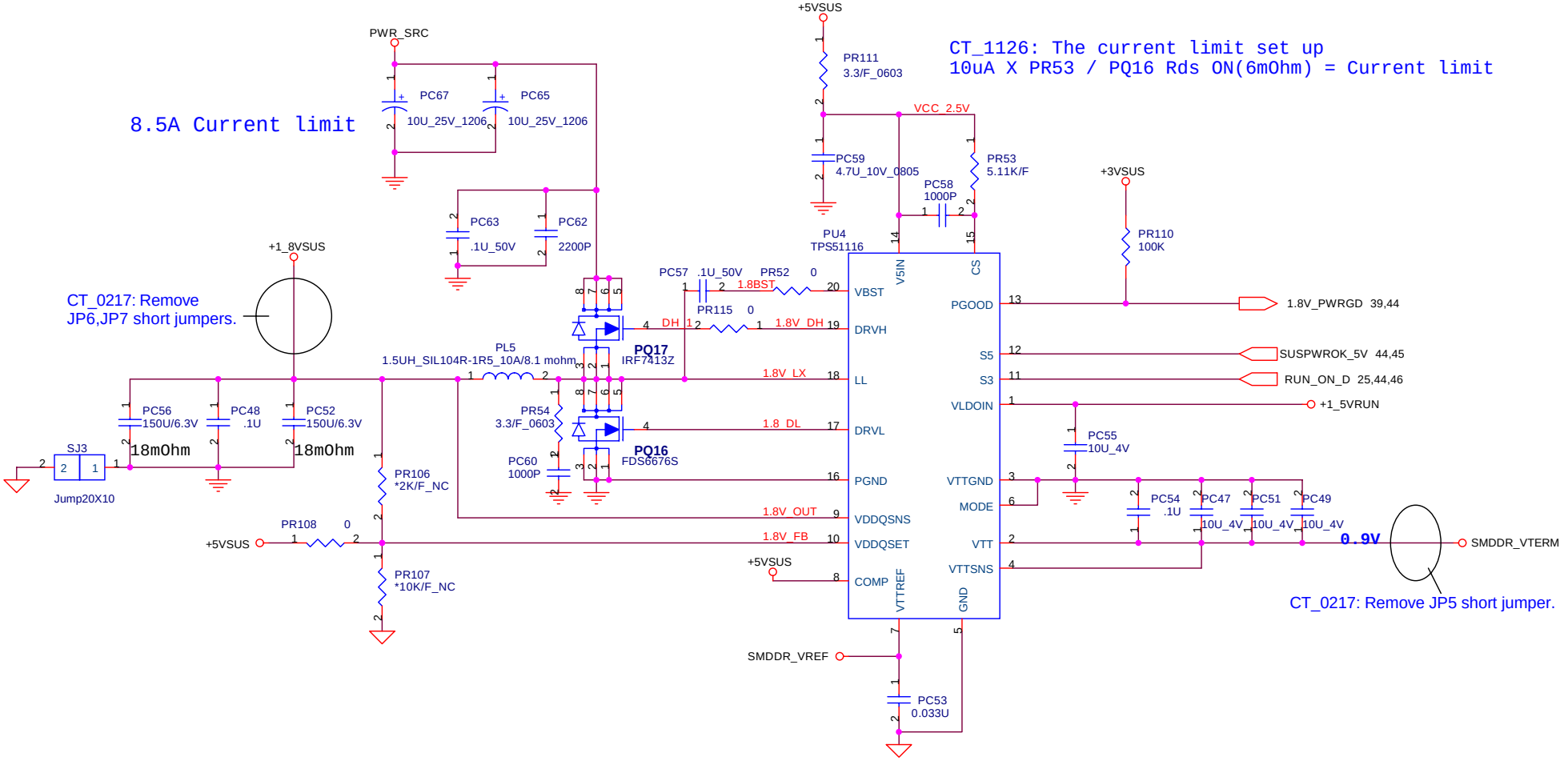
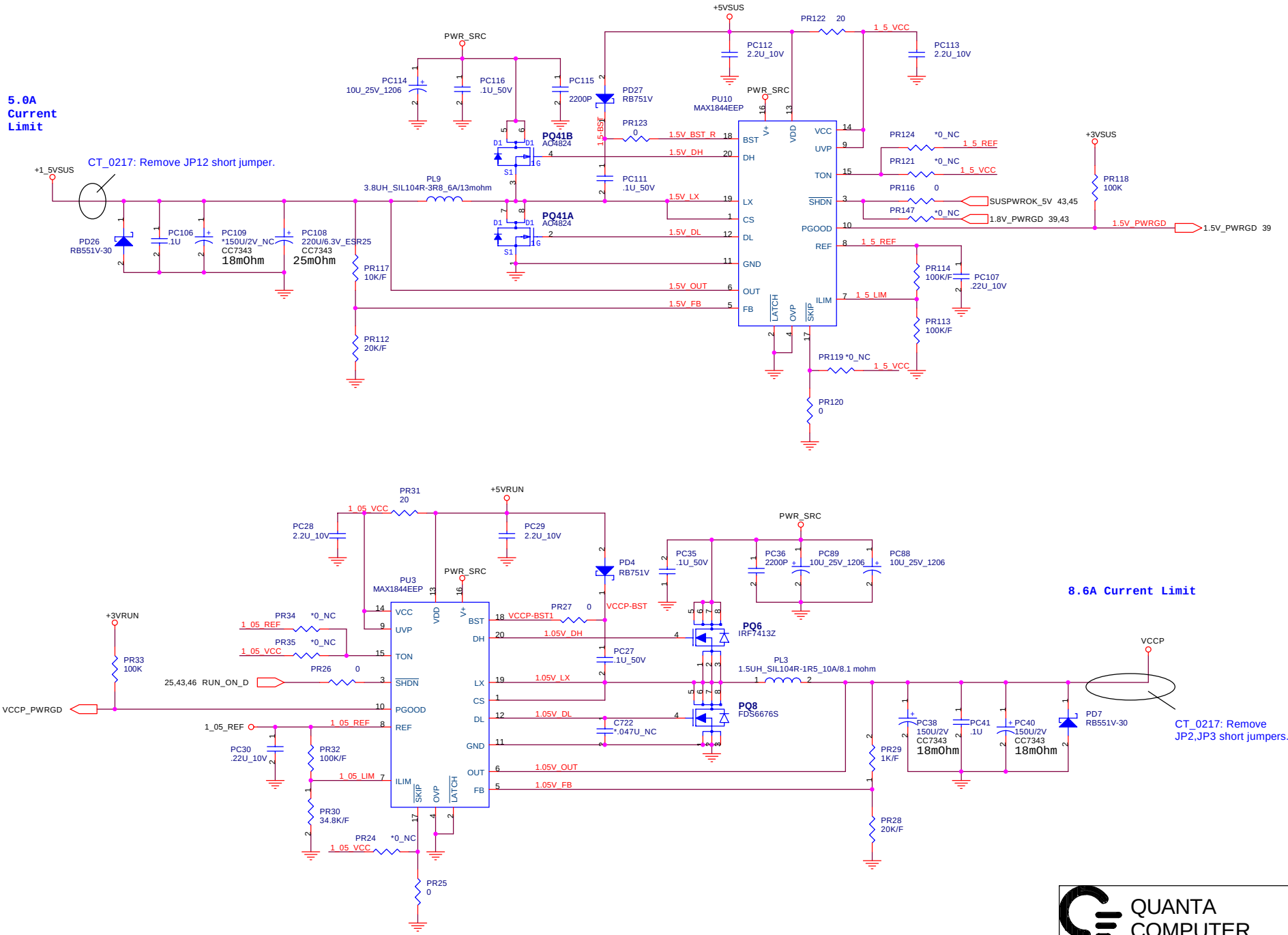


QUANTA COMPUTER

Title	1.8V,0.9V	
Size	Document Number Tahiti(DM3L)	Rev 1A
Date: 星期二, 三月 29, 2005	Sheet 43 of 49	


5.0A Current Limit
CT_0217: Remove JP12 short jumper.
+1_5VSUS
PD26 RB551V-30
PC106 .1U
PC109 *150U/2V_NC CC7343 18mOhm
PC108 220U/6.3V_ESR25 CC7343 25mOhm
PL9 3.8UH_SIL104R-3R8_6A/13mohm
PR117 10K/F
PR112 20K/F
PWR_SRC
PC114 10U_25V_1206
PC116 .1U_50V
PC115 2200P
PD27 RB751V
PQ41B AO4824
PQ41A AO4824
PR123 0
PC111 .1U_50V
1.5V_BST_R
1.5V_DH
1.5V_LX
1.5V_DL
1.5V_OUT
1.5V_FB
+5VSUS
PC112 2.2U_10V
PC113 2.2U_10V
PR122 20
1_5_VCC
PU10 MAX1844EEP
BST
DH
LX
CS
DL
GND
OUT
FB
VCC
UVP
TON
SHDN
PGOOD
REF
ILIM
V+
VDD
LATCH
OVP
SKIP
PR124 *0_NC
PR121 *0_NC
PR116 0
PR147 *0_NC
1_5_REF
SUSPWROK_5V 43,45
1.8V_PWRGD 39,43
+3VSUS
PR118 100K
1.5V_PWRGD
1.5V_PWRGD 39
PR114 100K/F
PC107 .22U_10V
PR113 100K/F
1_5_LIM
PR119 *0_NC
PR120 0
8.6A Current Limit
+5VRUN
PR31 20
1_05_VCC
PC28 2.2U_10V
PC29 2.2U_10V
PU3 MAX1844EEP
PD4 RB751V
VCCP-BST
PR27 0
VCCP-BST1
1.05V_DH
1.05V_LX
1.05V_DL
1.05V_OUT
1.05V_FB
PC35 .1U_50V
PC36 2200P
PC89 10U_25V_1206
PC88 10U_25V_1206
PQ6 IRF7413Z
PC27 .1U_50V
PL3 1.5UH_SIL104R-1R5_10A/8.1 mohm
PQ8 FDS6676S
C722 *.047U_NC
VCCP
PC38 150U/2V CC7343 18mOhm
PC41 .1U
PC40 150U/2V CC7343 18mOhm
PD7 RB551V-30
PR29 1K/F
PR28 20K/F
+3VRUN
PR33 100K
PR34 *0_NC
PR35 *0_NC
PR26 0
25,43,46 RUN_ON_D
39,42 VCCP_PWRGD
1_05_REF
PC30 .22U_10V
PR32 100K/F
PR30 34.8K/F
PR24 *0_NC
PR25 0
CT_0217: Remove JP2,JP3 short jumpers.
QUANTA COMPUTER
Title
1.5V,1.05V
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1A
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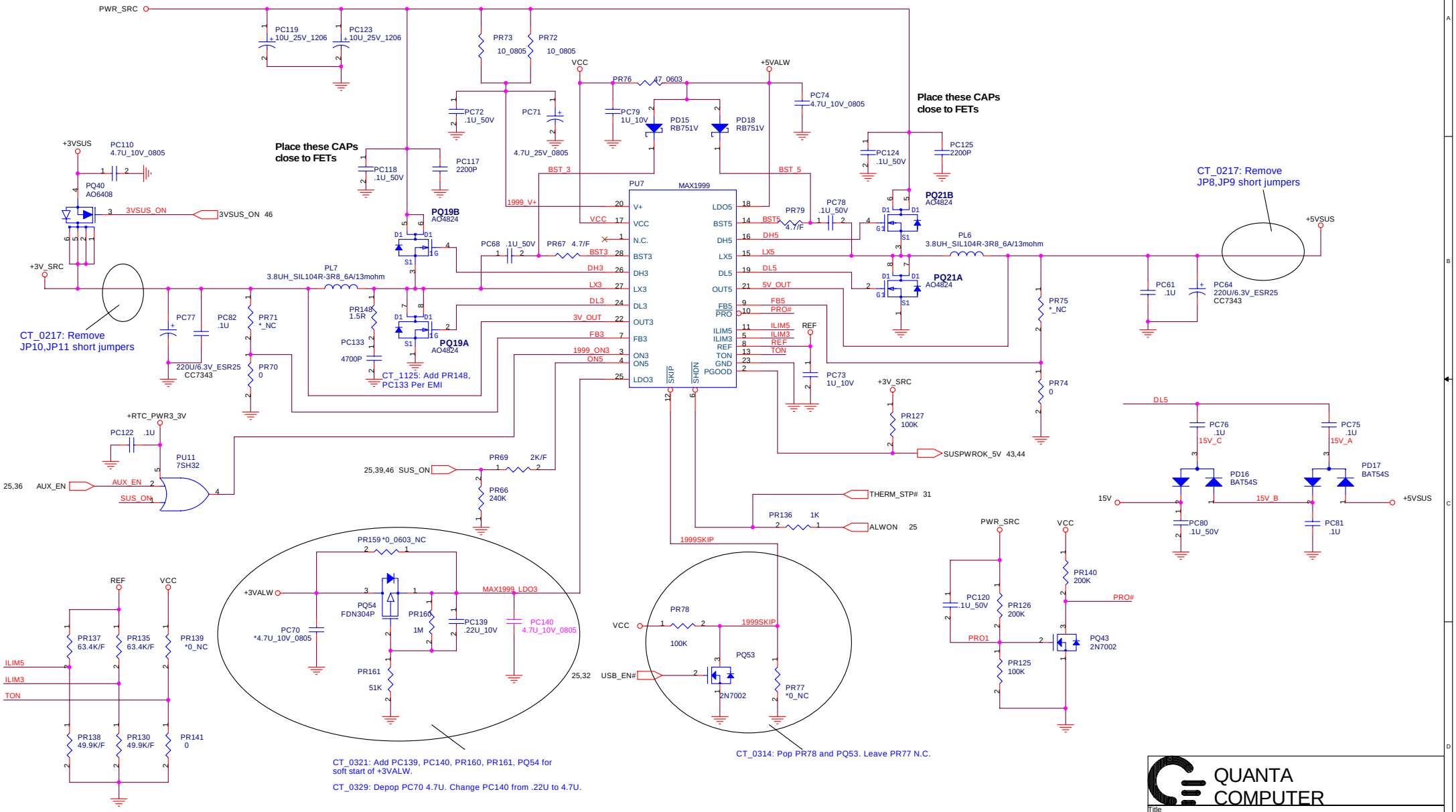

QUANTA COMPUTER

Title	3VALW,5V,3V, power on	
Size	Document Number Tahiti(DM3L)	Rev 1A
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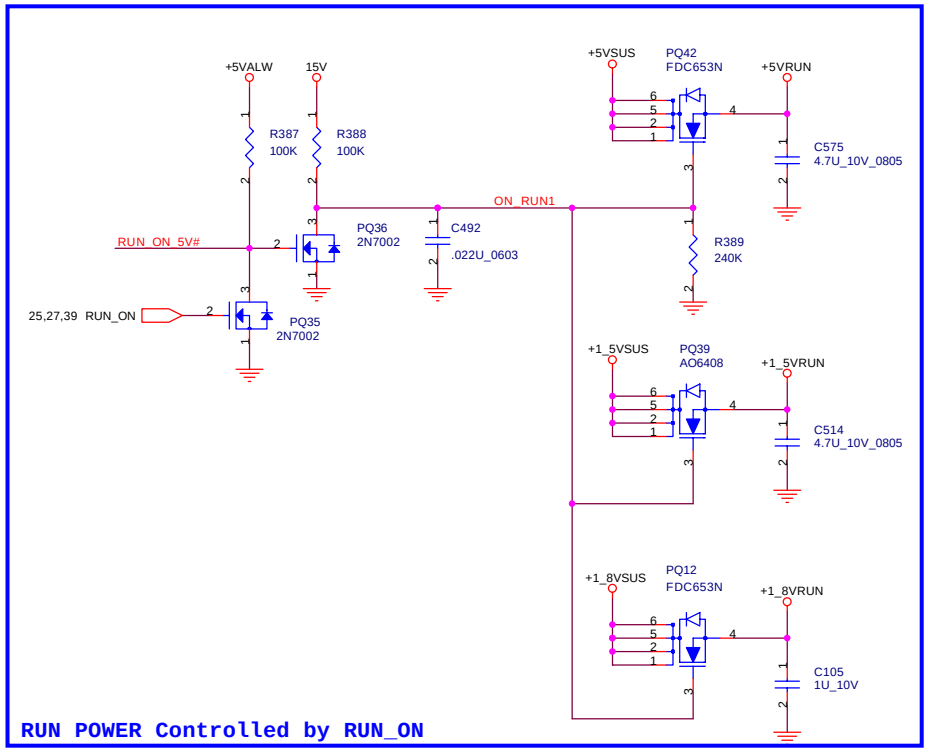

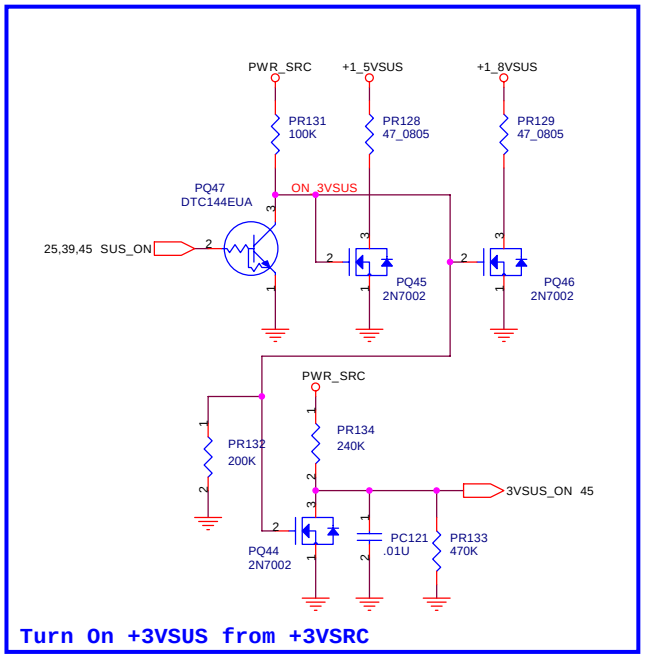

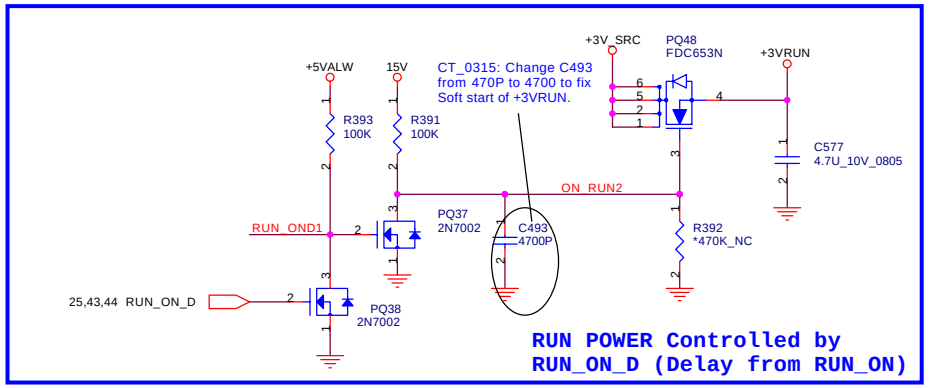

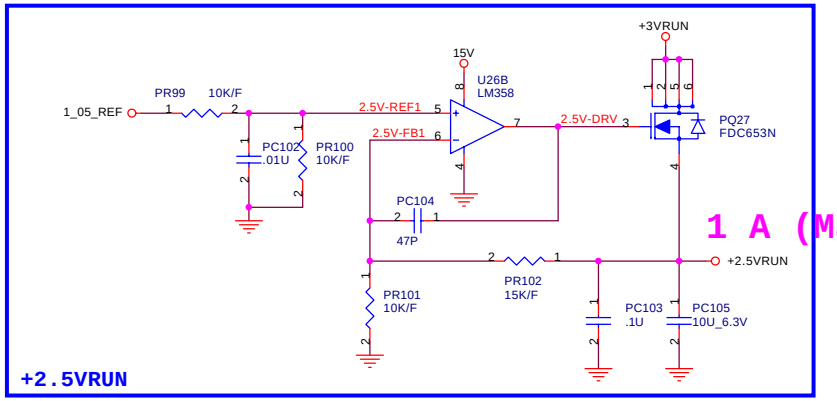

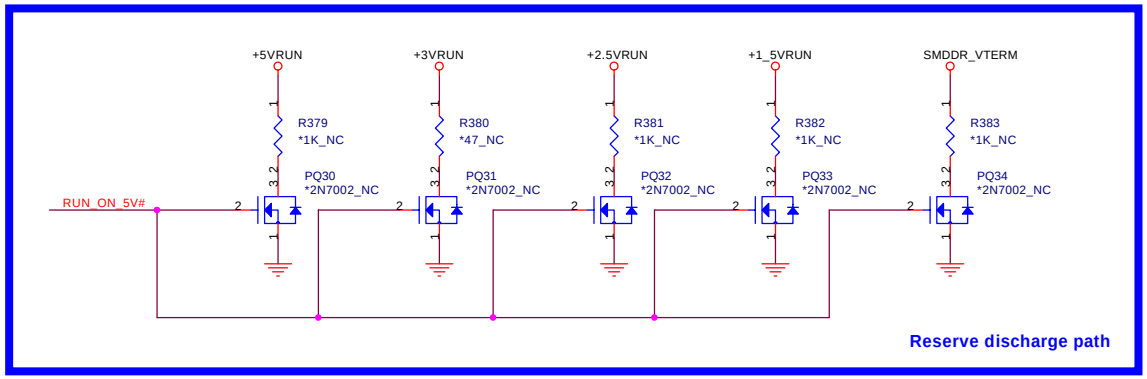

QUANTA COMPUTER

Title	RUN POWER SW	
Size	Document Number Tahiti(DM3L)	Rev 1A
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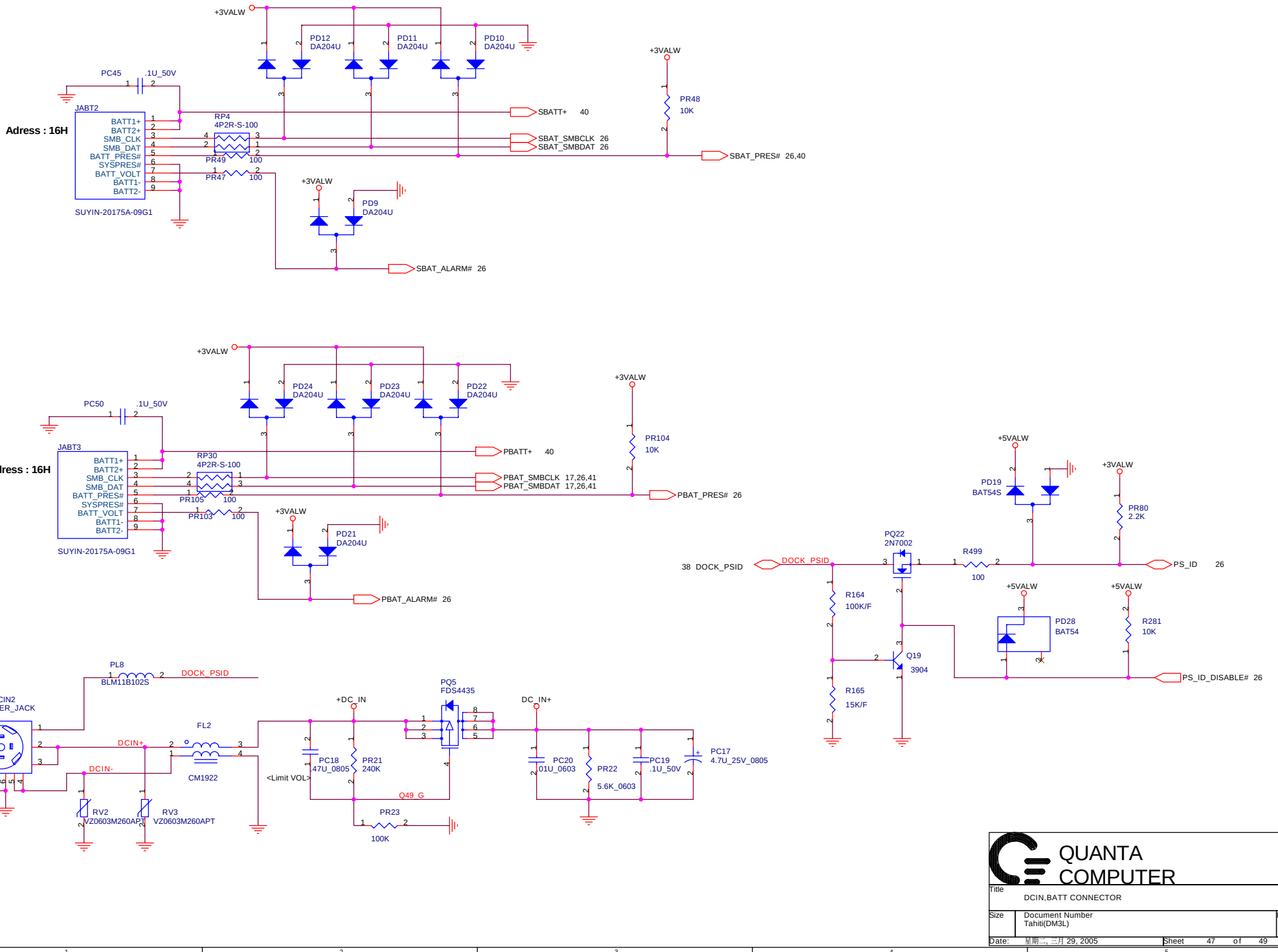

QUANTA COMPUTER

Title	DCIN,BATT CONNECTOR	
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