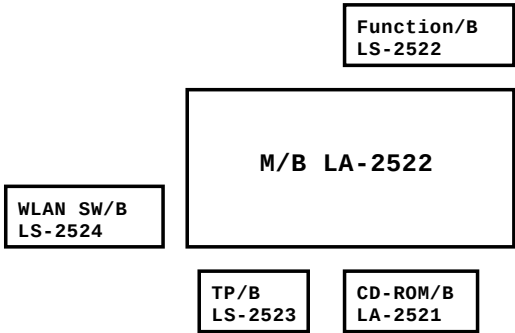


COMPAL CONFIDENTIAL

MODEL NAME : EGQ00
COMPAL P/N :
PCB NO :
Revision : 2.0

EGQ00 Schematics Document
uFCBGA/uFCPGA Mobile Dothan
ATI RS300MD + SB200
2 Channel DDR1

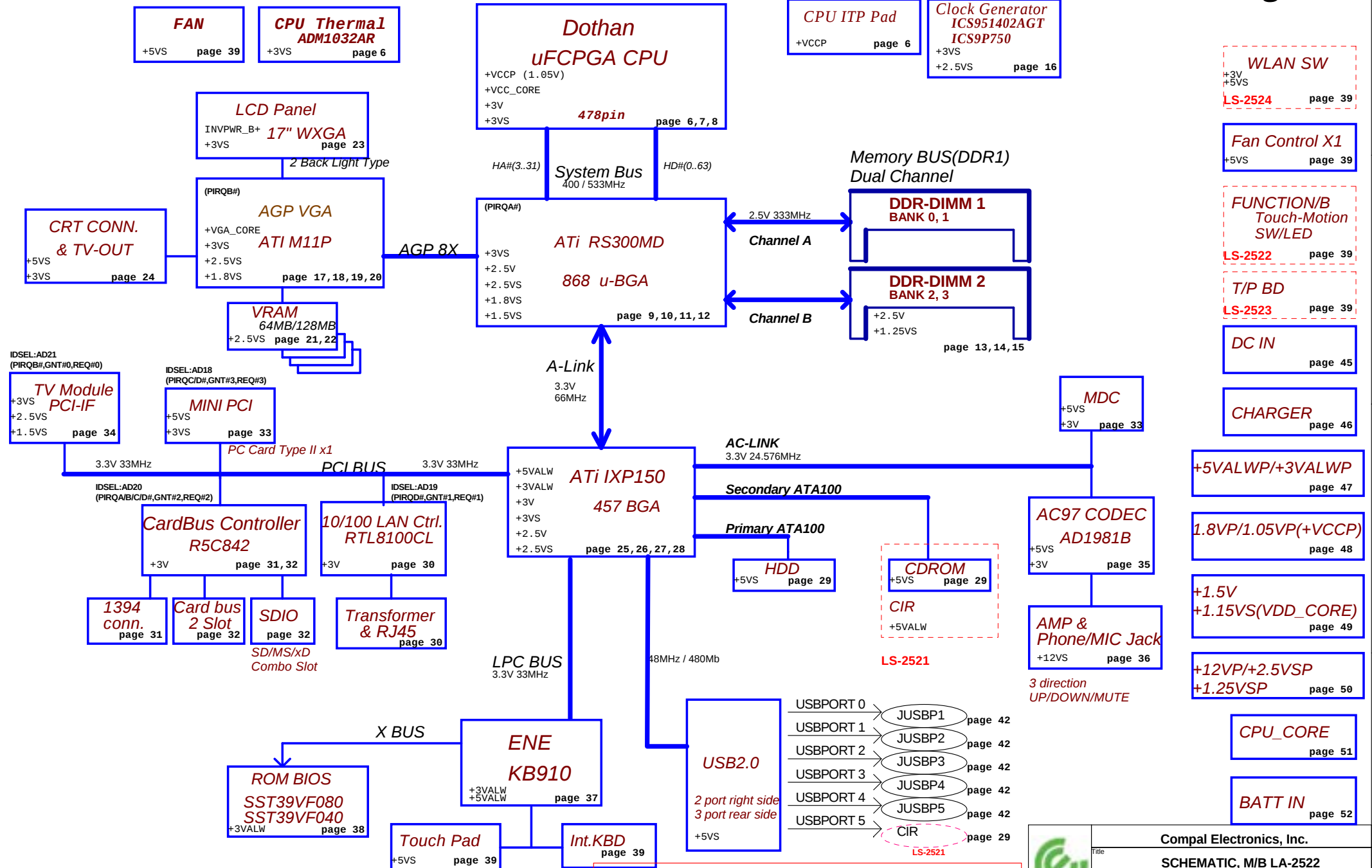
2005-01-11
REV : 2.0



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

			
Compal Electronics, Inc.			
Title			
SCHEMATIC, M/B LA-2522			
Size	Document Number		Rev
	401318		C
Date:	星期五, 二月 18, 2005		Sheet 1 of 52

Block Diagram



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

External PCI Devices

Symbol note:

 :means analog ground.

SMBUS Control Table

	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR (CPU)	SODIMM	CLK CHIP	MINI PCI	LCD
SMB_EC_CK1 SMB_EC_DA1	ENE KB910	✓	✓	✓	✗	✗	✗	✗	✗
SMB_EC_CK2 SMB_EC_DA2	ENE KB910	✗	✗	✗	✓	✗	✗	✗	✗
ICH_SMBCLK ICH_SMBDATA	ICH6-M	✗	✗	✗	✗	✓	✓	✗	✗
LCD_DDCCLK LCD_DDCDATA	ATi M22	✗	✗	✗	✗	✗	✗	✗	✓

Signal State			+CPU_CORE +VCCP +5VS +3VS +2.5VS +1.8VS +1.25VS +1.5VS
	+12VALW +3VALW +5VALW	+12V +5V +3V +2.5V	
S0	ON	ON	ON
S1	ON	ON	ON
S3	ON	ON	OFF
S5 S4/AC	ON	OFF	OFF
S5 S4/AC don't exist	OFF	OFF	OFF

Symbol	0	1	2	3	4	5	6	7
CODE	Z5U	Z5V	Z5P	Y5U	Y5V	Y5P	X5R	X7R

8	9	A	B	C	D	E	F	G
NP0	C0G		BJ	CH	CJ	CK	SH	SJ

H	I	J	
UJ	UK	SL	

Symbol	A	B	C	D	F	G	H	J
CODE	+0.05PF	+0.11PF	+0.25PF	+0.5PF	+1PF	+2%	+3%	+5%

K	M	N	P	Q	V	X	Z	
+ -10%	+ -20%	+ -30%	+100, -0%	+30, -10%	+20, -10%	+40, -20%	+80, -20%	

	PCB Rev	Data
Bringup-Build SST-Build	0.1	
EVT-Build		
DVT-Build		
PVT-Build		

[illegible]

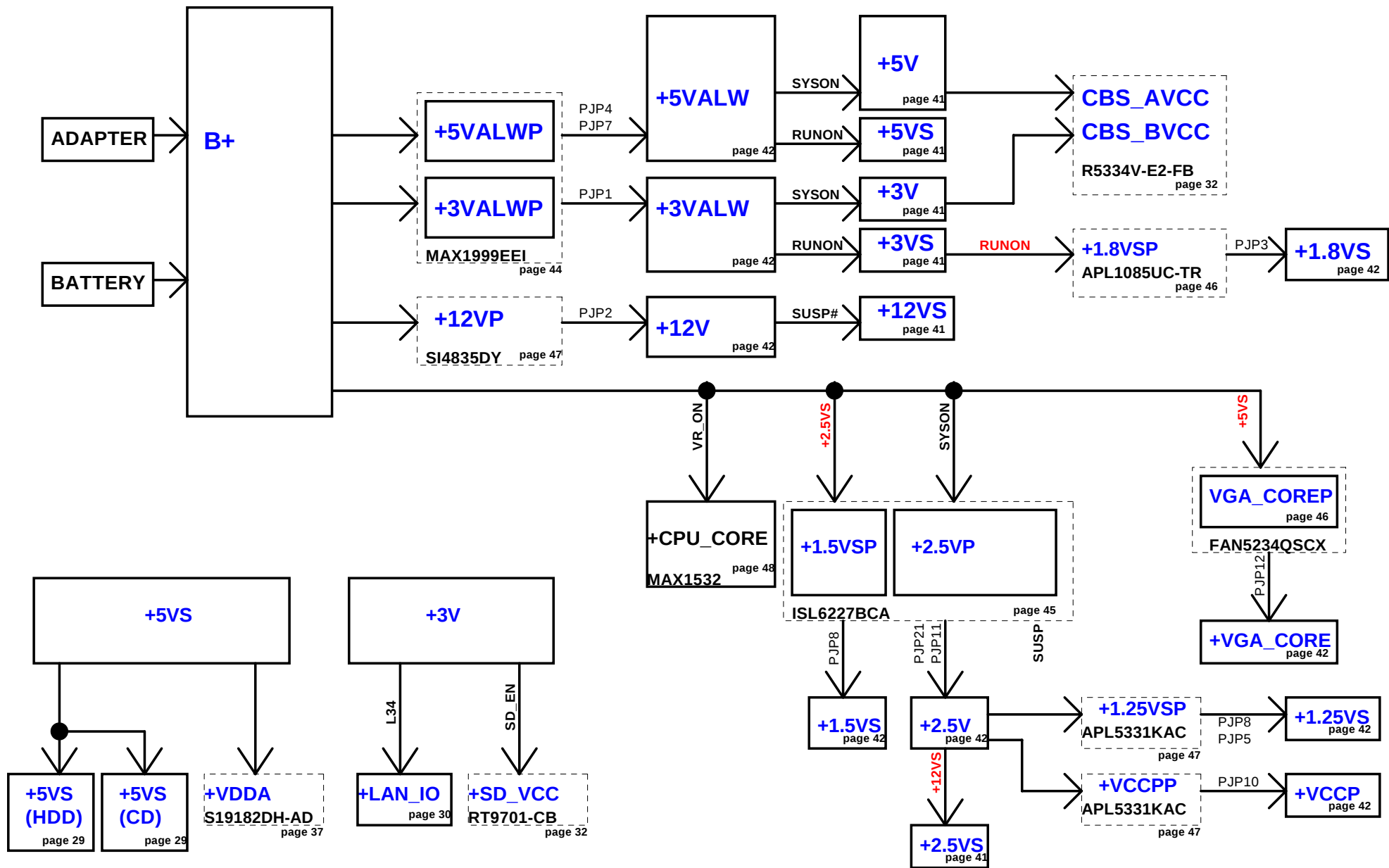
SCHEMATIC, M/B LA-2522

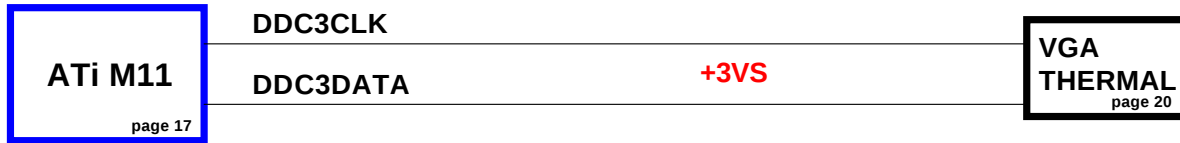
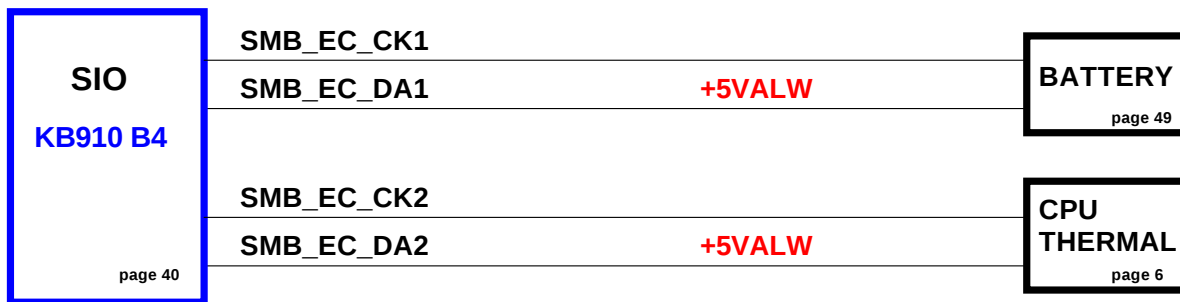
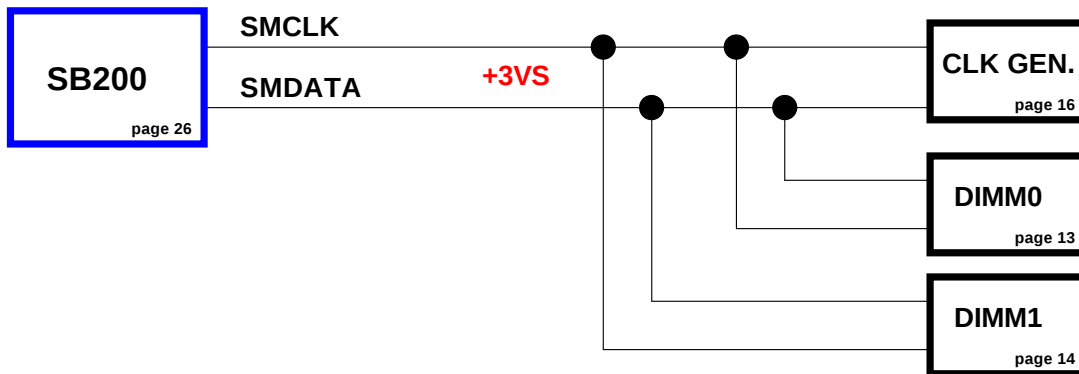
401318

Date: 星期五, 二月 18, 2005

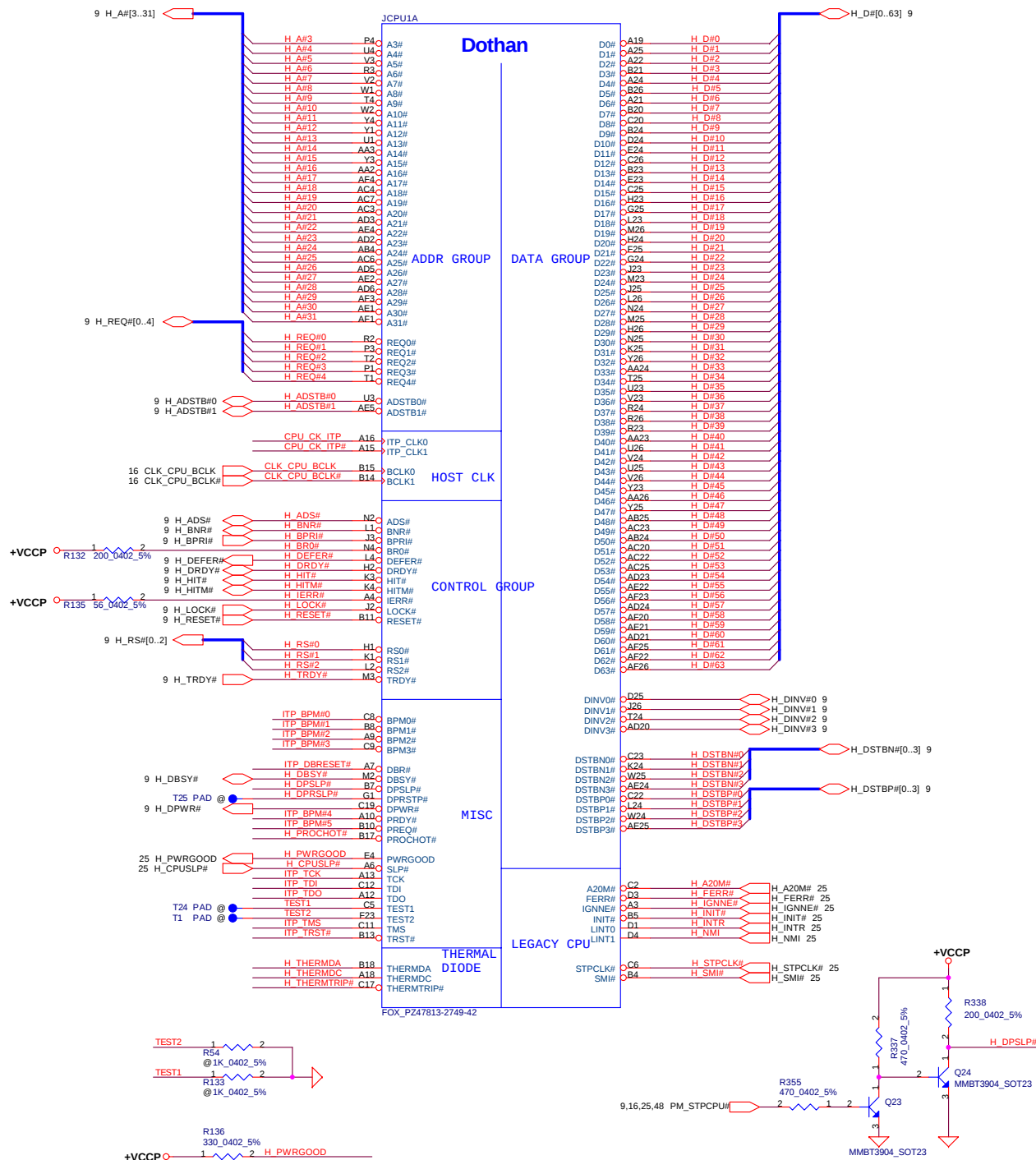
Sheet 3 of 52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

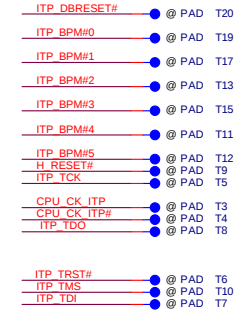




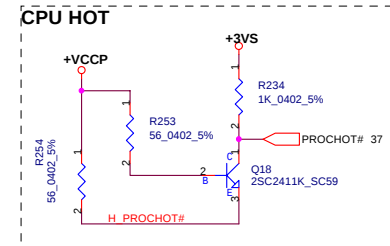
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



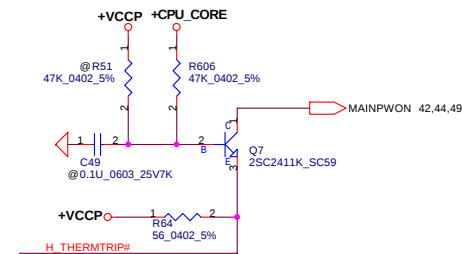
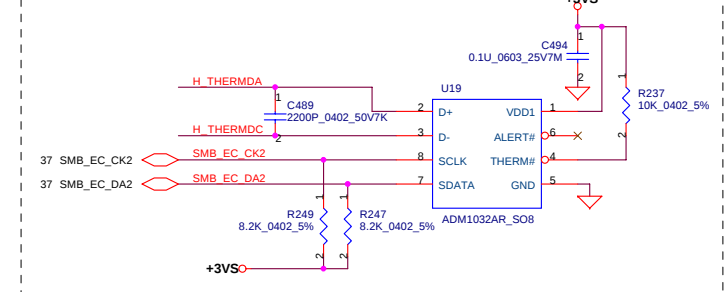
Check ITP connector.



Test pad as closed as possible



CPU Thermal Sensor

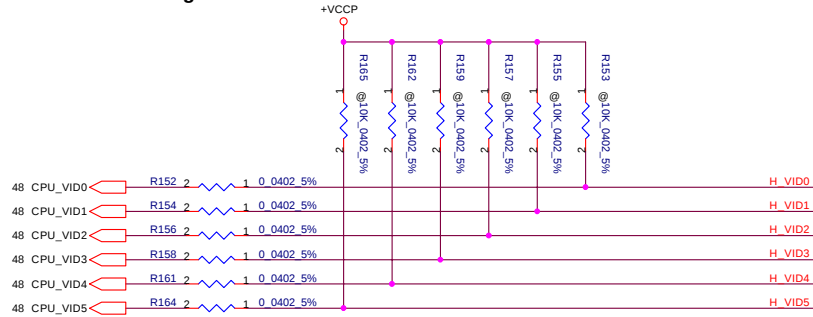


				Compal Electronics, Inc.	
				SCHEMATIC, M/B LA-2522	
Size	Document Number	401318		Rev	C
Date:	星期五, 二月 18, 2005	Sheet	6	of	52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

For test only ,CMOS output
CPU Voltage ID

Dothan A = J1 (+1.8VS)
Dothan B = J2 (+1.5VS)

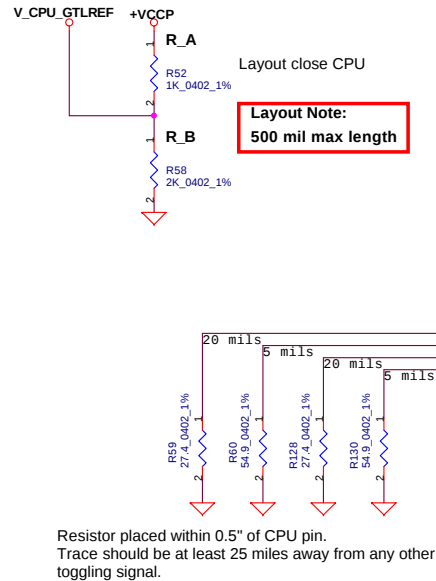


MOBILE BANIAS VID TABLE

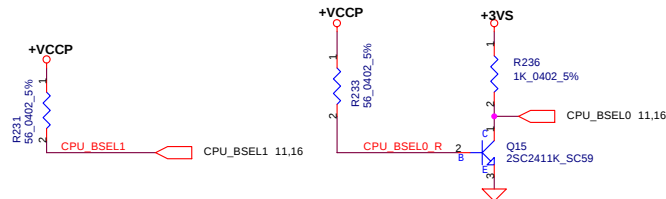
VID[5..0]	Voltage	VID[5..0]	Voltage
000000	1.708V	100000	1.196V
000001	1.692V	100001	1.180V
000010	1.676V	100010	1.164V
000011	1.660V	100011	1.148V
000100	1.644V	100100	1.132V
000101	1.628V	100101	1.116V
000110	1.612V	100110	1.100V
000111	1.596V	100111	1.084V
001000	1.580V	101000	1.068V
001001	1.564V	101001	1.052V
001010	1.548V	101010	1.036V
001011	1.532V	101011	1.020V
001100	1.516V	101100	1.004V
001101	1.500V	101101	0.988V
001110	1.484V	101110	0.972V
001111	1.468V	101111	0.956V
010000	1.452V	110000	0.940V
010001	1.436V	110001	0.924V
010010	1.420V	110010	0.908V
010011	1.404V	110011	0.892V
010100	1.388V	110100	0.876V
010101	1.372V	110101	0.860V
010110	1.356V	110110	0.844V
010111	1.340V	110111	0.828V
011000	1.324V	111000	0.812V
011001	1.308V	111001	0.796V
011010	1.292V	111010	0.780V
011011	1.276V	111011	0.764V
011100	1.260V	111100	0.748V
011101	1.244V	111101	0.732V
011110	1.228V	111110	0.716V
011111	1.212V	111111	0.700V

CPU CLK SPEED

CPU_BSEL0: CPU_BSEL1 00: 133 MHZ
10: 100 MHZ



Resistor placed within 0.5" of CPU pin.
Trace should be at least 25 mils away from any other toggling signal.



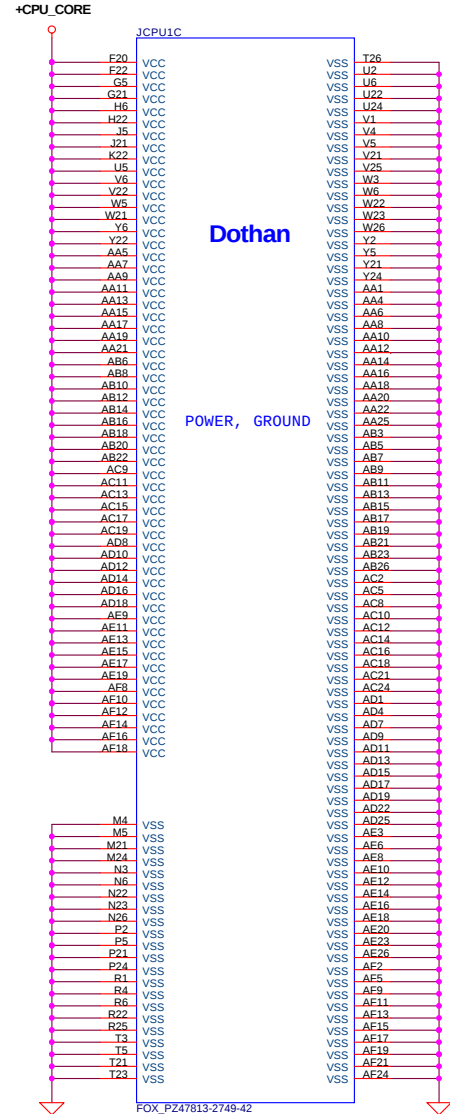
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Dothan

POWER, GROUND, RESERVED SIGNALS AND NC

Dothan

POWER, GROUND

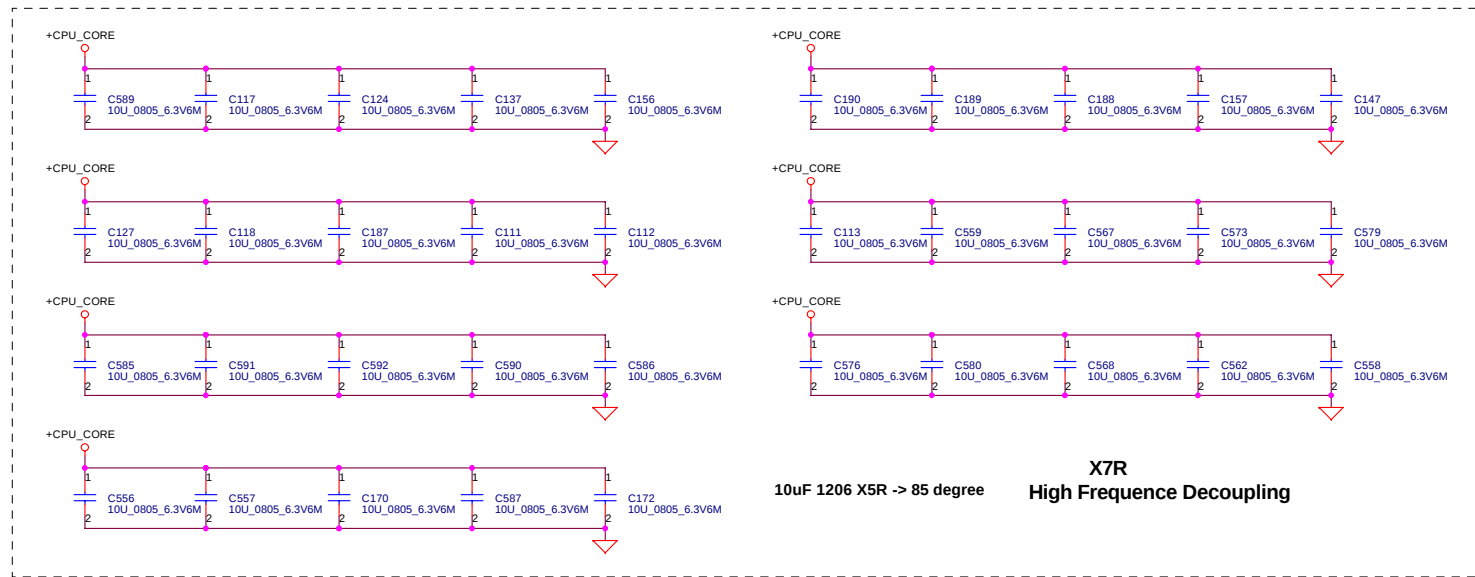


Compal Electronics, Inc.

SCHEMATIC, M/B LA-2522

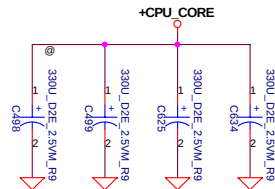
401318

Date: 星期五, 二月 18, 2005 Sheet 7 of 52

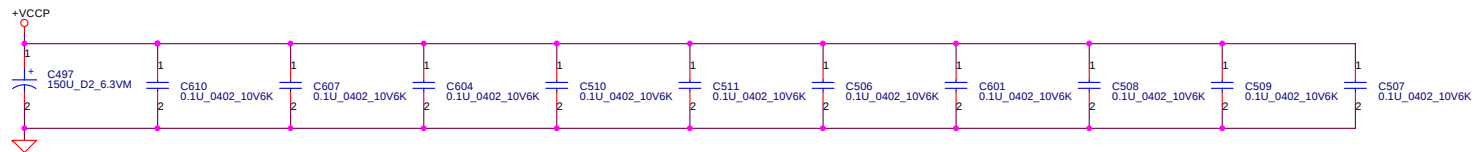


Vcc-core Decoupling	C, uF	ESR, mohm	ESL, nH
SPCAP, Polymer	3X330uF	9m ohm/3	3.5nH/4
MLCC 0805 X5R	35X10uF	5m ohm/35	0.6nH/35

Near VCORE regulator.

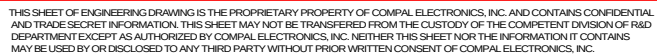
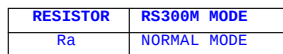


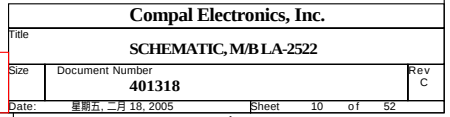
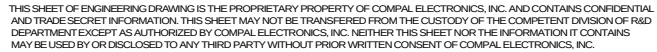
ESR <= 3m ohm
Capacitor > 880 uF

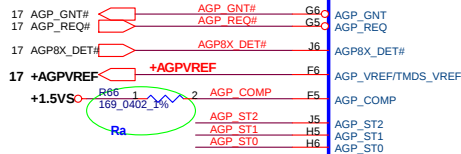
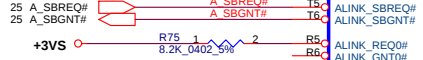
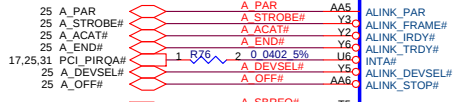
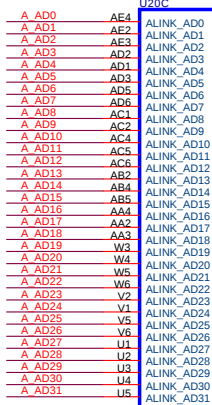
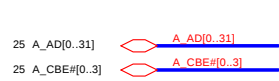


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

		Compal Electronics, Inc.	
		Title	
Size	Document Number	SCHEMATIC, M/B LA-2522	
		401318	
Date:	星期五, 二月 18, 2005	Sheet	8 of 52







CHS-216RS300MDA12_BGA888

AGP_COMP

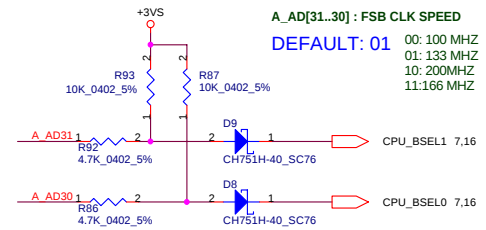
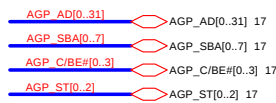
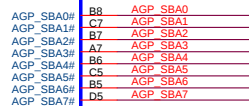
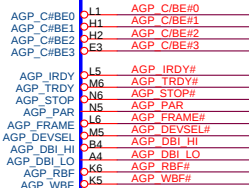
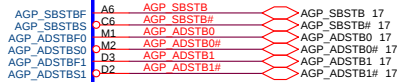
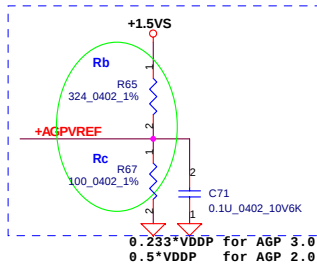
AGP_4X/8X#	8X=0	4X=1
Ra	169R	54.9R

+AGPVREF

	AGP 8X	AGP 4X
Rb	324_0402_1%	1K_0402_1%
Rc	100_0402_1%	1K_0402_1%

AGP8X_DET#

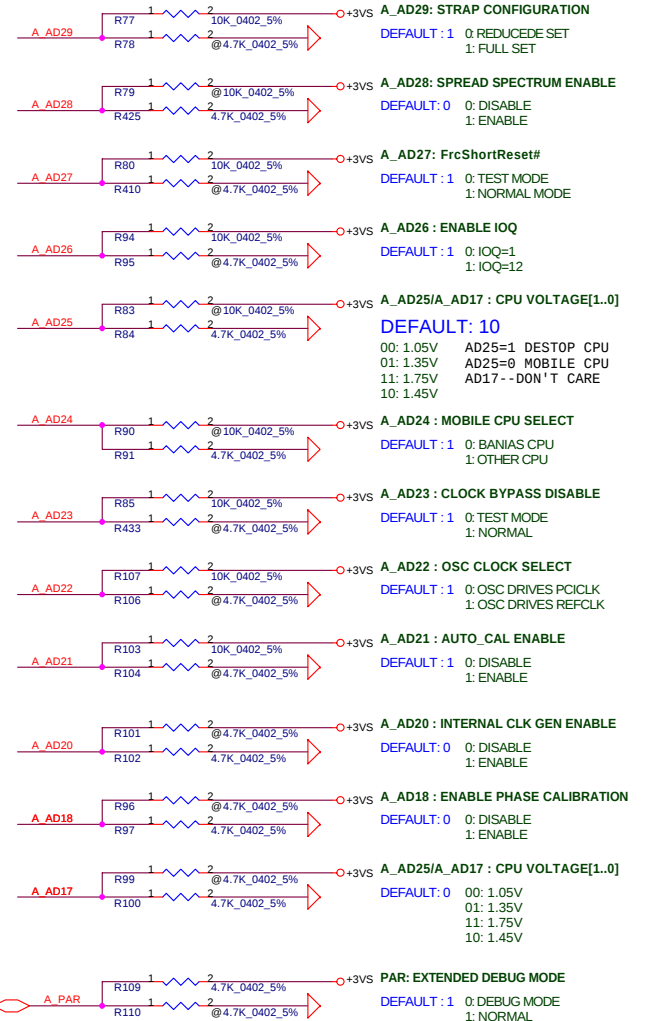
RS300MD internal ppull-up



A_AD[31..30] : FSB CLK SPEED

DEFAULT: 01

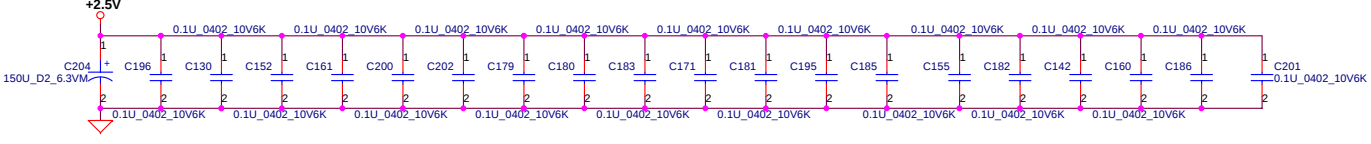
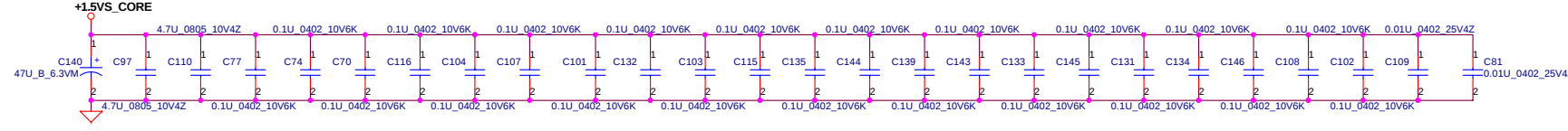
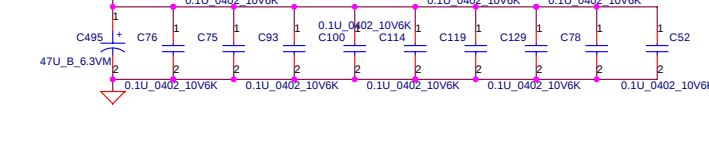
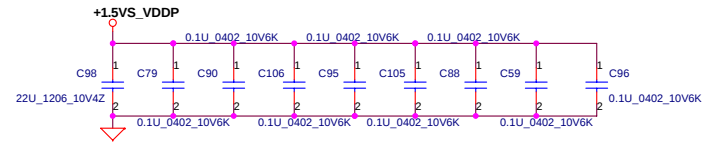
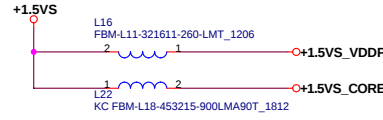
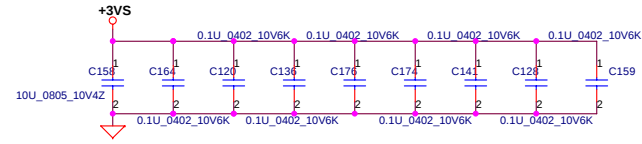
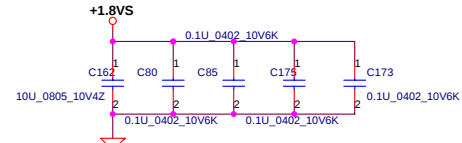
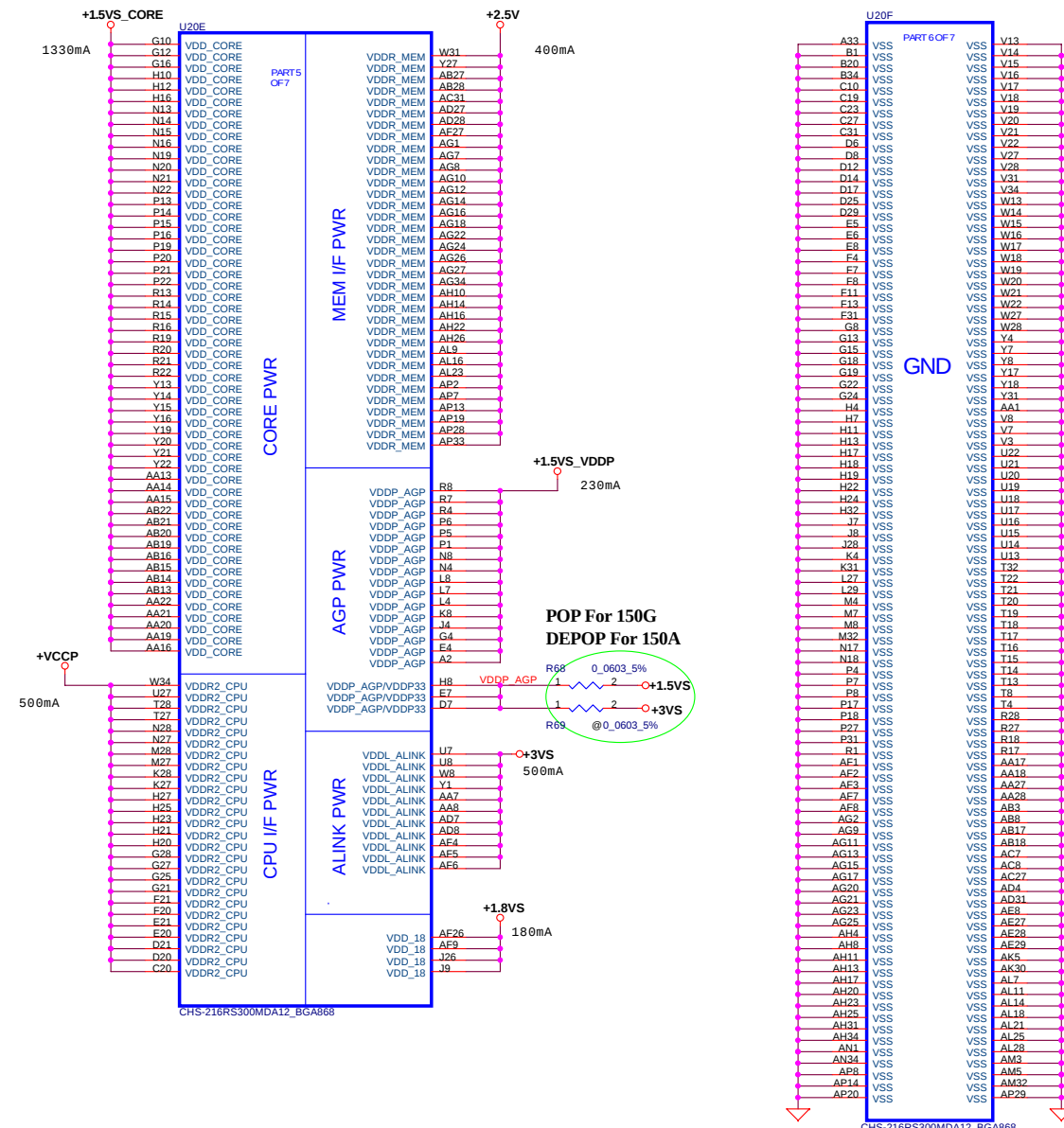
00: 100 MHz
01: 133 MHz
10: 200MHz
11: 166 MHz



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
SCHEMATIC, M/BLA-2522			
Title	Size	Document Number	Rev
		401318	C
Date: 星期五, 二月 18, 2005	Sheet	11	of 52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

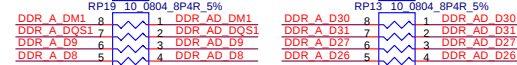
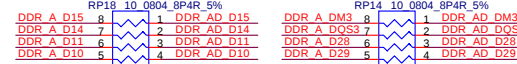
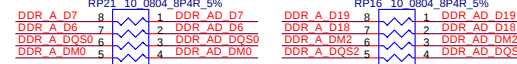
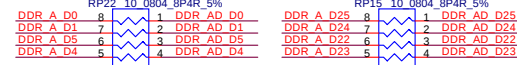
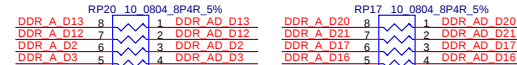
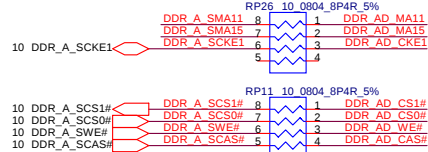
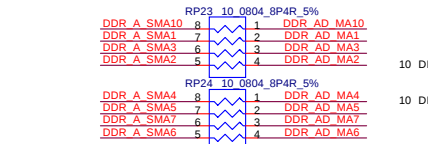


10 DDR_A_SMA[0..15] DDR_A_SMA[0..15]

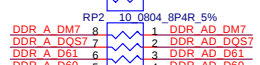
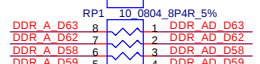
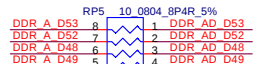
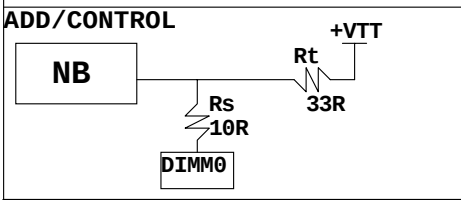
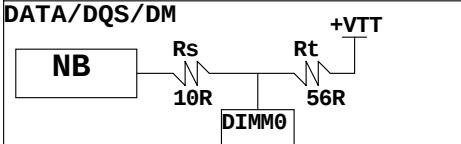
10 DDR_A_DM[0..7] DDR_A_DM[0..7]

10 DDR_A_D[0..63] DDR_A_D[0..63]

10 DDR_A_DQS[0..7] DDR_A_DQS[0..7]



System Memory Layout Topology



Layout note

Place these resistor closely DIMM0, all trace length Max=0.75"

14,16,26 SMDATA

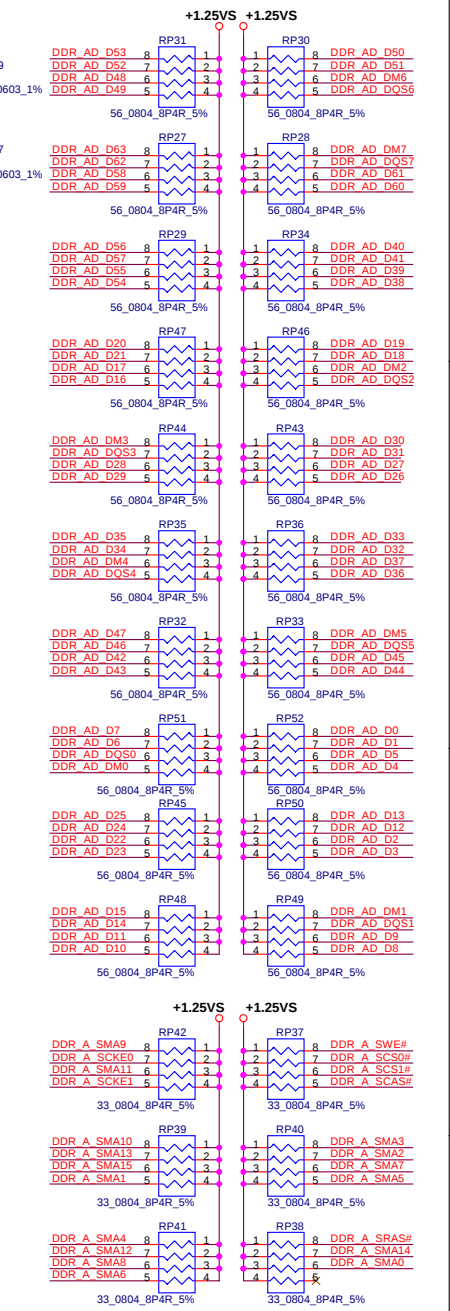
14,16,26 SMCLK

+3VS



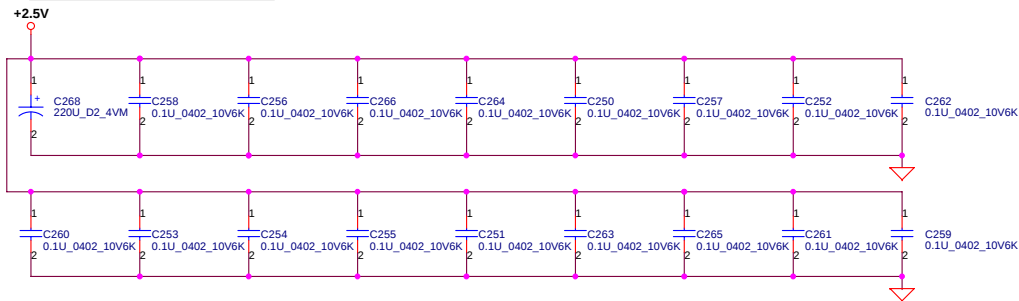
Place Add/Command resistors Close to Pin, max L = 300 mils

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, WITHOUT PRIOR WRITTEN PERMISSION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

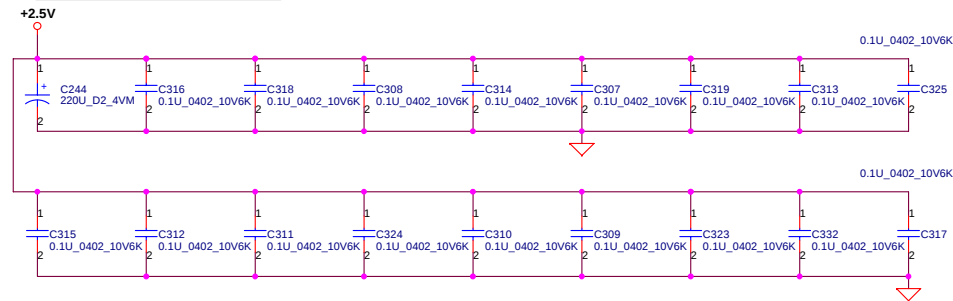


Compal Electronics, Inc.		
SCHEMATIC, M/BLA-2522		
Title	Document Number	Rev
	401318	C
Date	星期二, 二月 18, 2005	Sheet 13 of 52

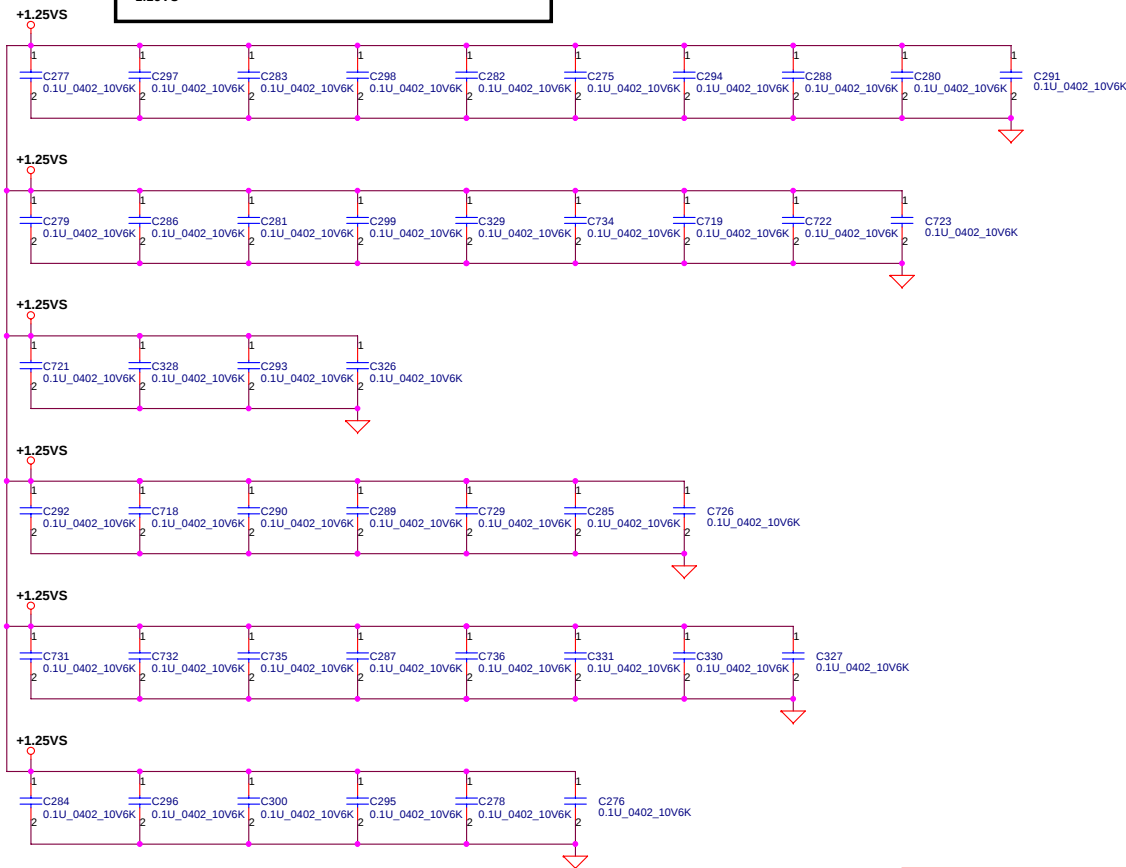
Layout note :
Distribute as close as possible
to DDR-SODIMM0.



Layout note :
Distribute as close as possible
to DDR-SODIMM1.

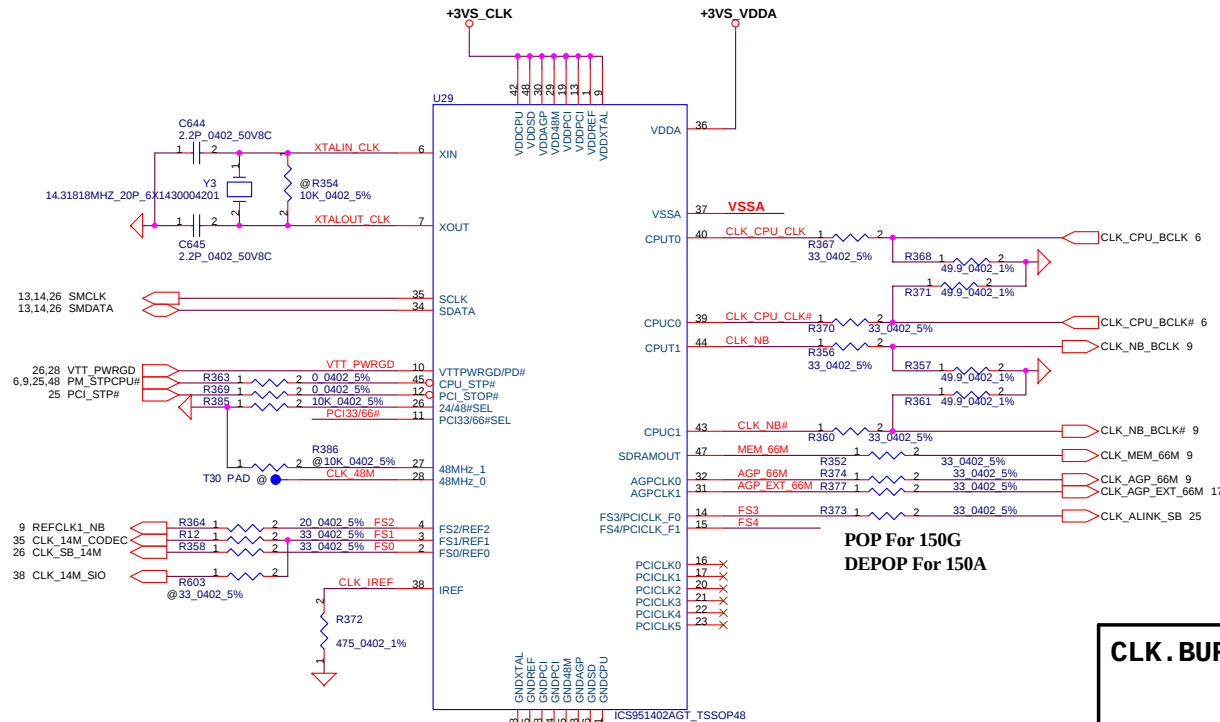


Layout note :
Place one cap close to every 2 pull up resistors termination to
+1.25VS

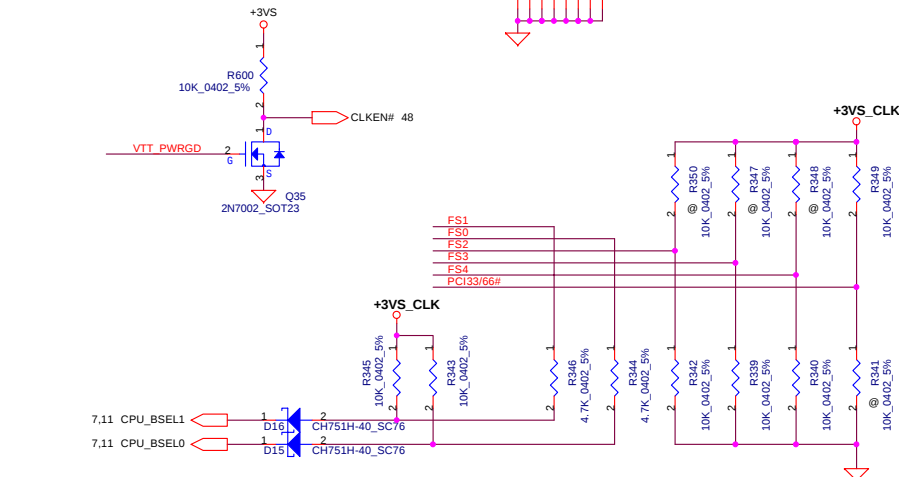


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
SCHEMATIC, M/BLA-2522			
Size	Document Number		Rev
	401318		C
Date:	星期五, 三月 18, 2005	Sheet	15 of 52



POP For 150G
DEPOP For 150A



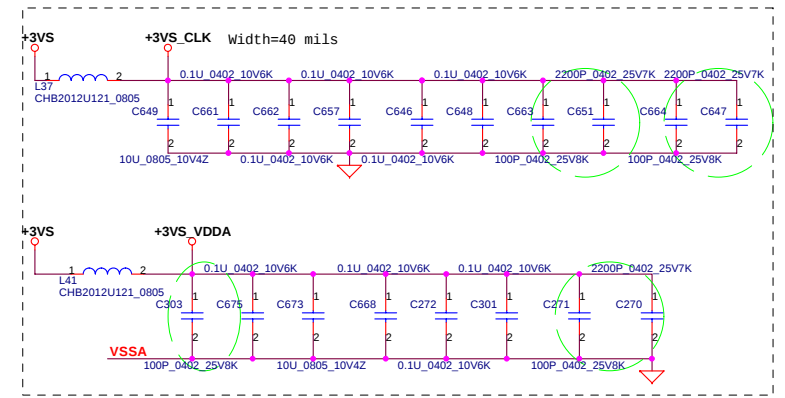
CLOCK FREQUENCY SELECT TABLE

FS4	FS3	FS2	FS1	FS0	CPU	MEM	With Spread Enabled...
0	0	0	1	0	200	200	
0	0	0	0	1	133	133	Spread OFF OR Center spread +/-0.3%
0	0	0	0	0	100	100	

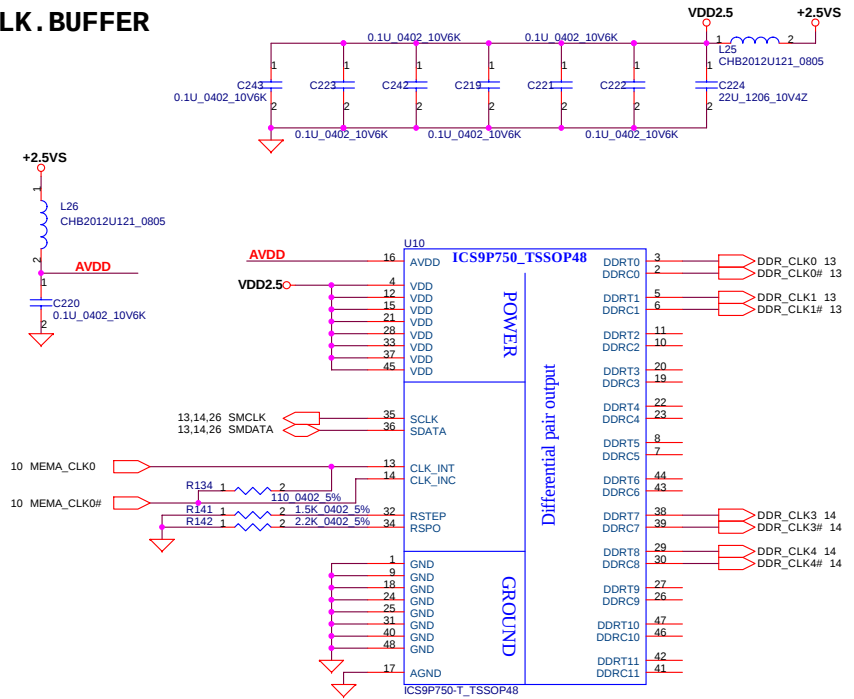
A-LINK FREQ

PCI33/66# = HIGH	66MHZ
PCI33/66# = LOW	33MHZ

Note: 0 = PULL LOW
1 = PULL HIGH



CLK . BUFFER

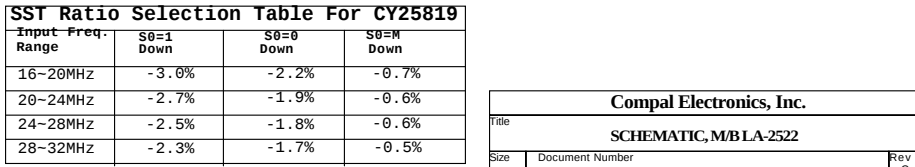
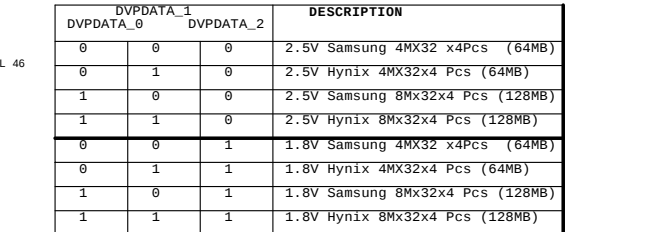


Compal Electronics, Inc.

SCHEMATIC, M/B LA-2522

Size	Document Number	Rev
401318		C

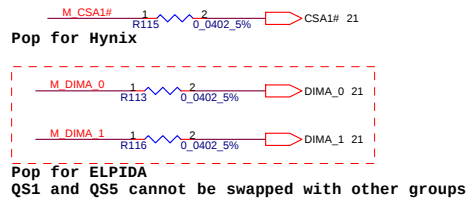
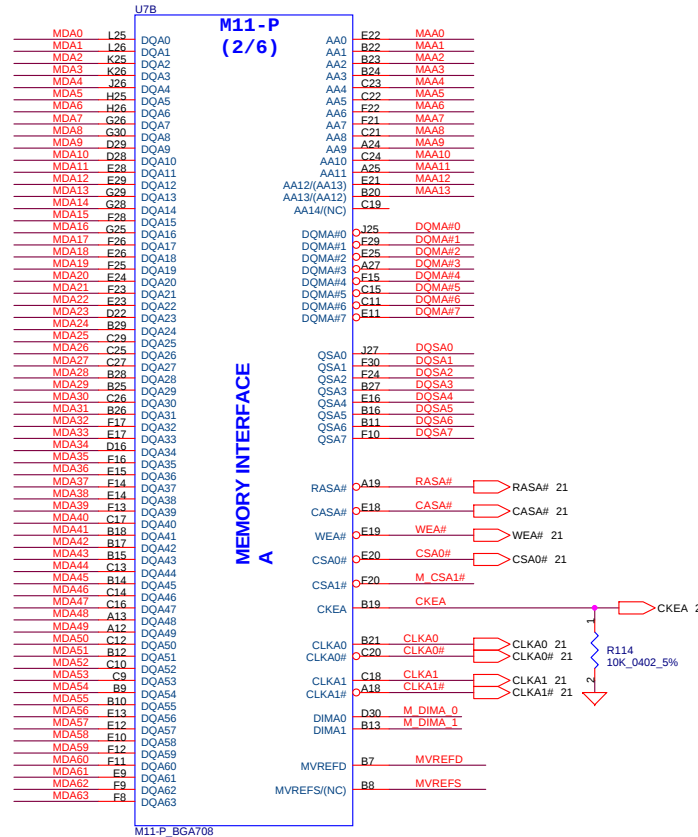
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



Compal Electronics, Inc.			
Title			
SCHEMATIC, M/B LA-2522			
Size	Document Number		Rev
	401318		C
Date:	星期五, 二月 18, 2005	Sheet	17 of 52

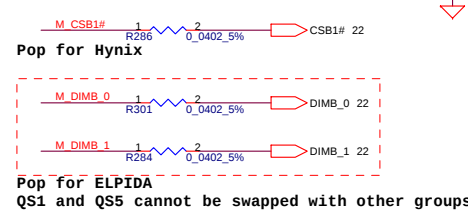
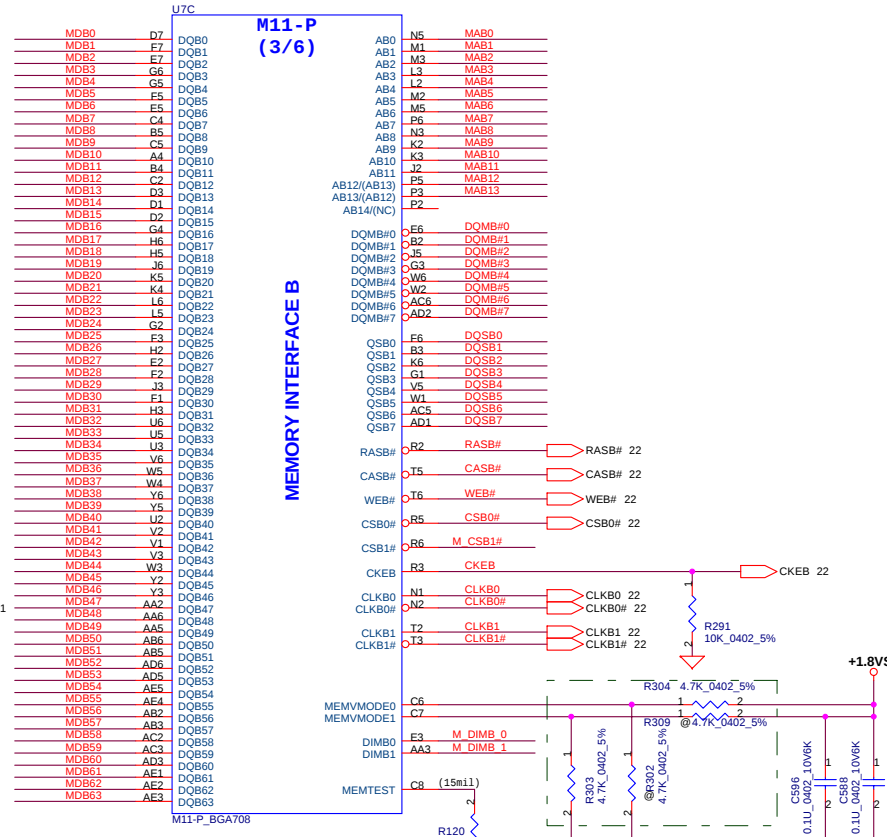
MDA[63:0] 21
DQSA[7:0] 21
MAA[13:0] 21
DQMA#[7:0] 21

MEMORY INTERFACE A



MDB[63:0] 22
DQSB[7:0] 22
MAB[13:0] 22
DQMB#[7:0] 22

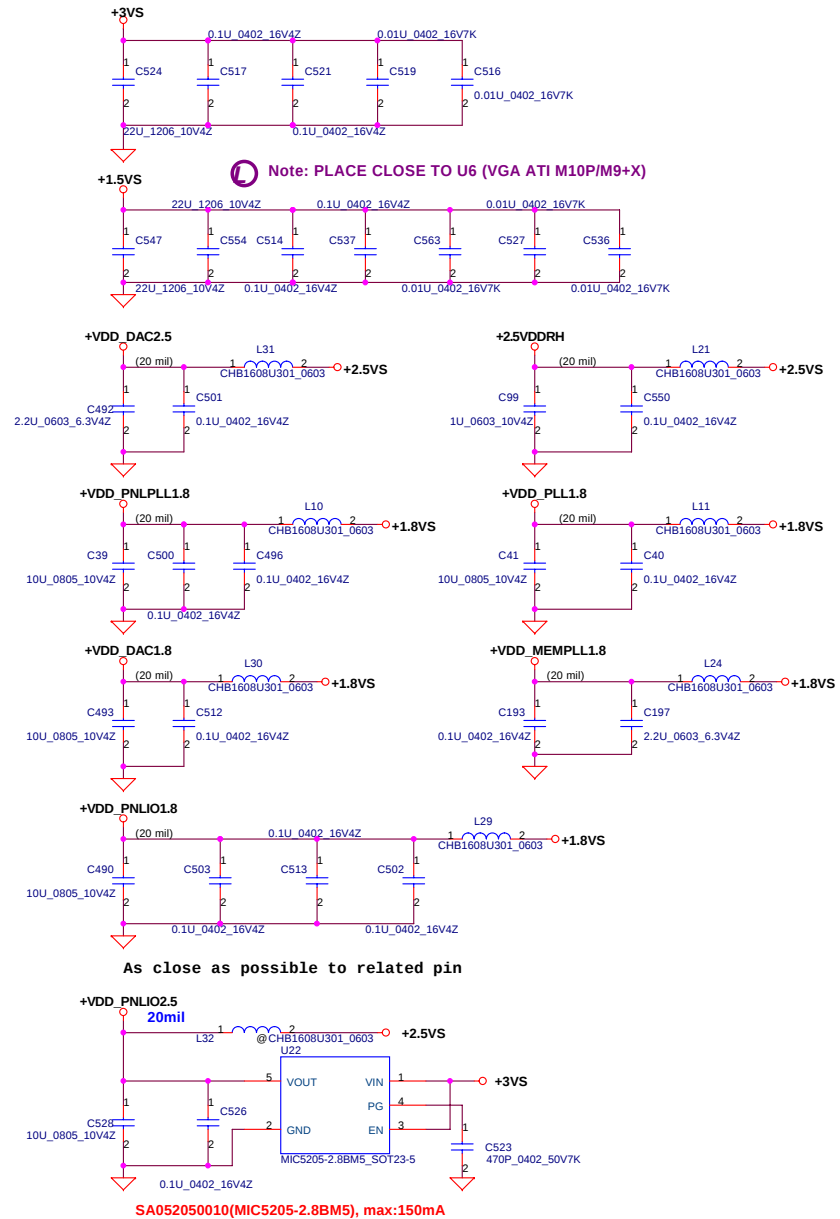
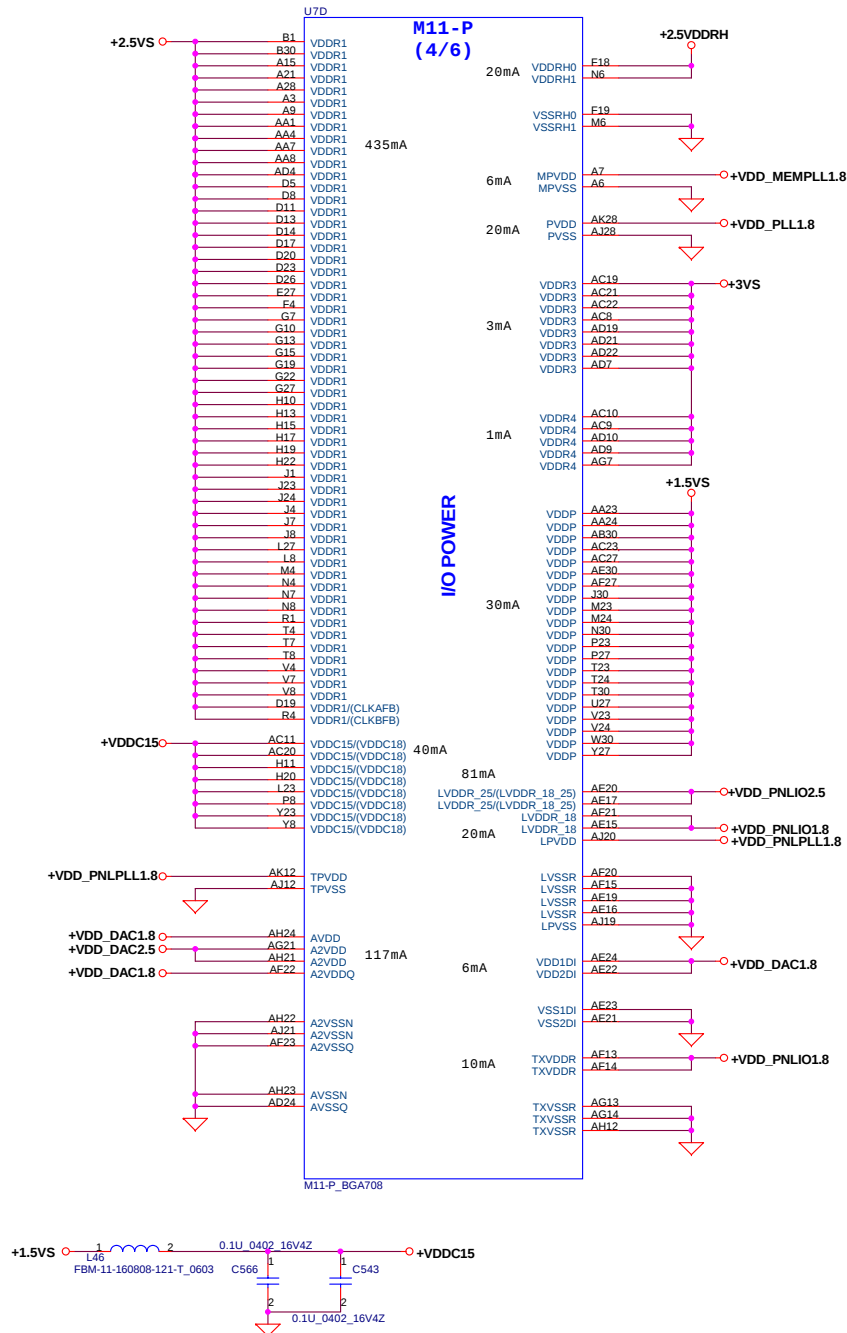
MEMORY INTERFACE B



MEM I/O Voltage Selection

	2.5V VDDR1	1.8V VDDR1 (ELPIDA)
MEMVMODE0	HI	LOW
MEMVMODE1	LOW	HI

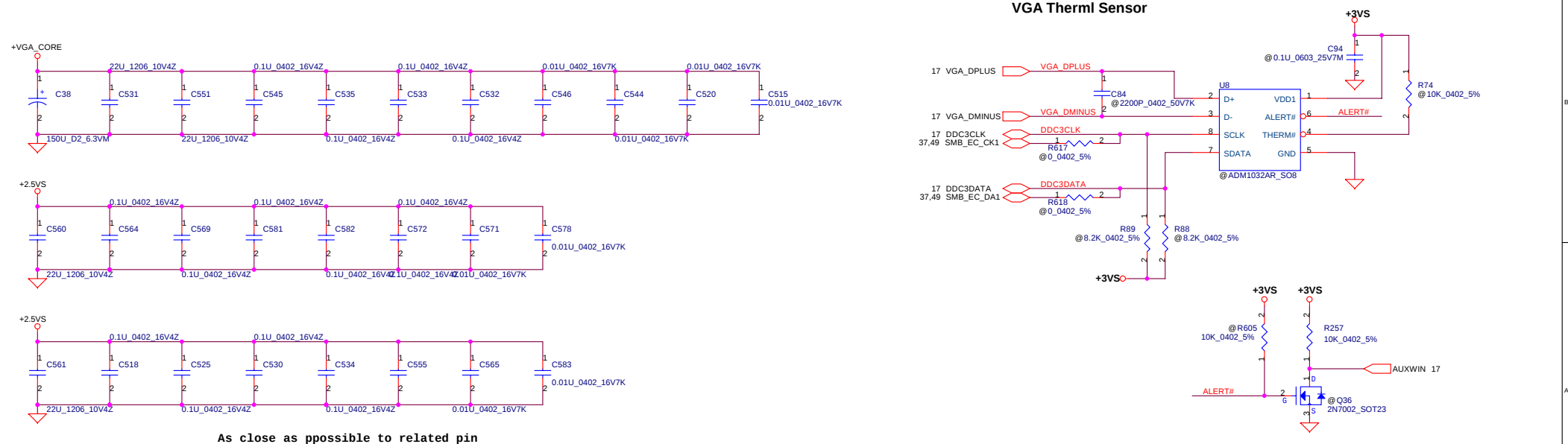
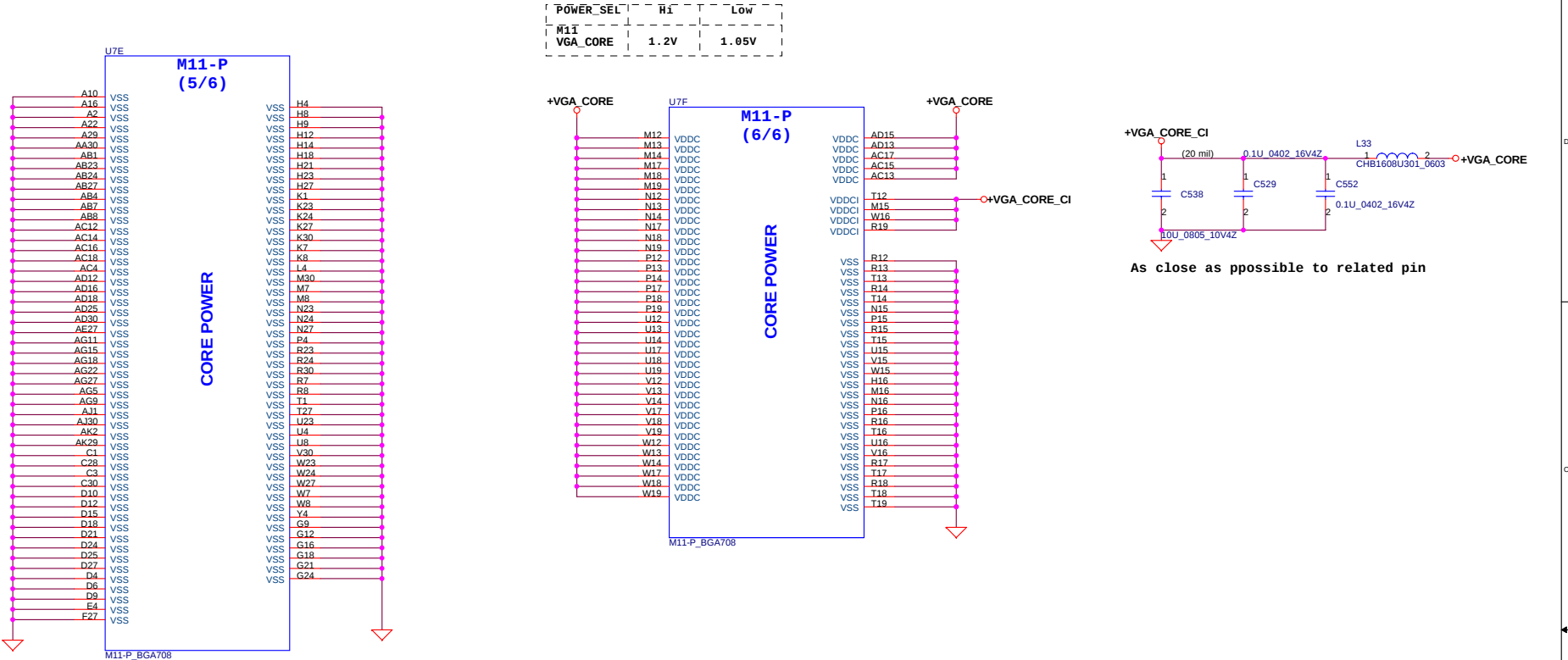
POWER INTERFACE



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

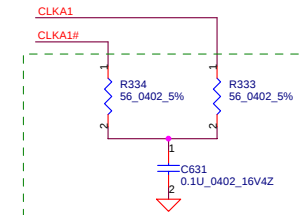
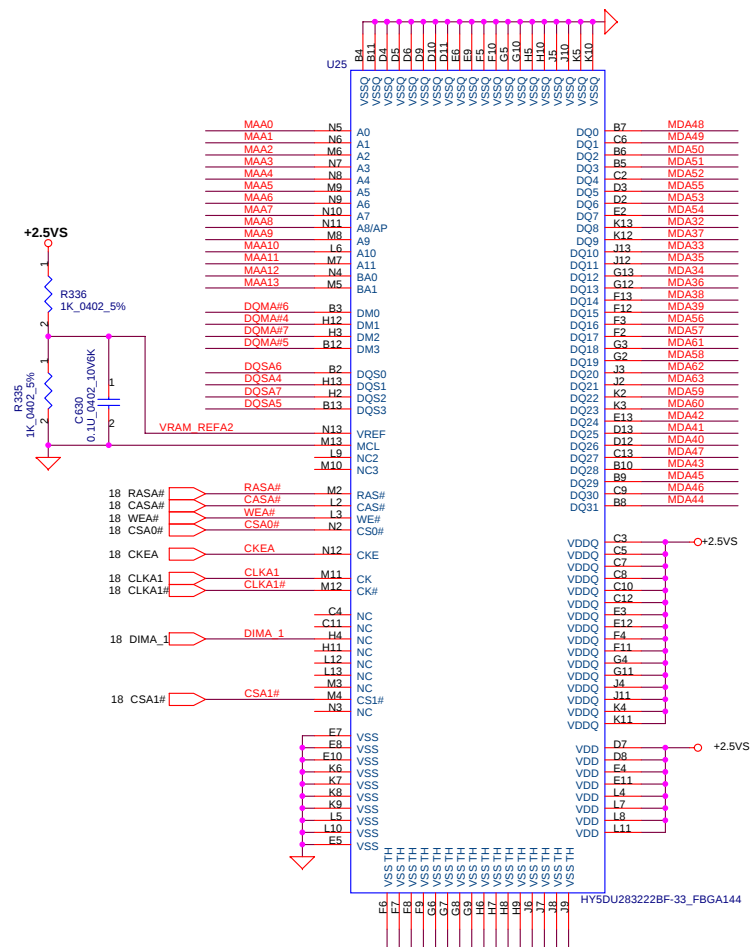
Compal Electronics, Inc.			
Title			
SCHEMATIC, M/BLA-2522			
Size	Document Number		Rev
	401318		C
Date:	星期五, 二月 18, 2005	Sheet	19 of 52

POWER
INTERFACE

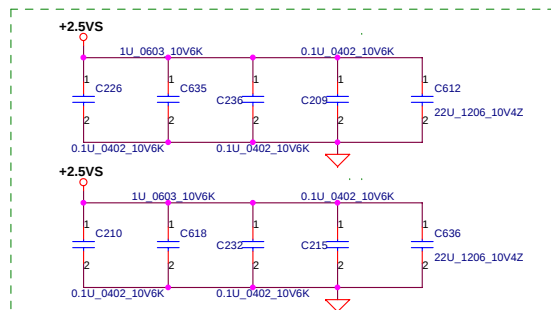


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

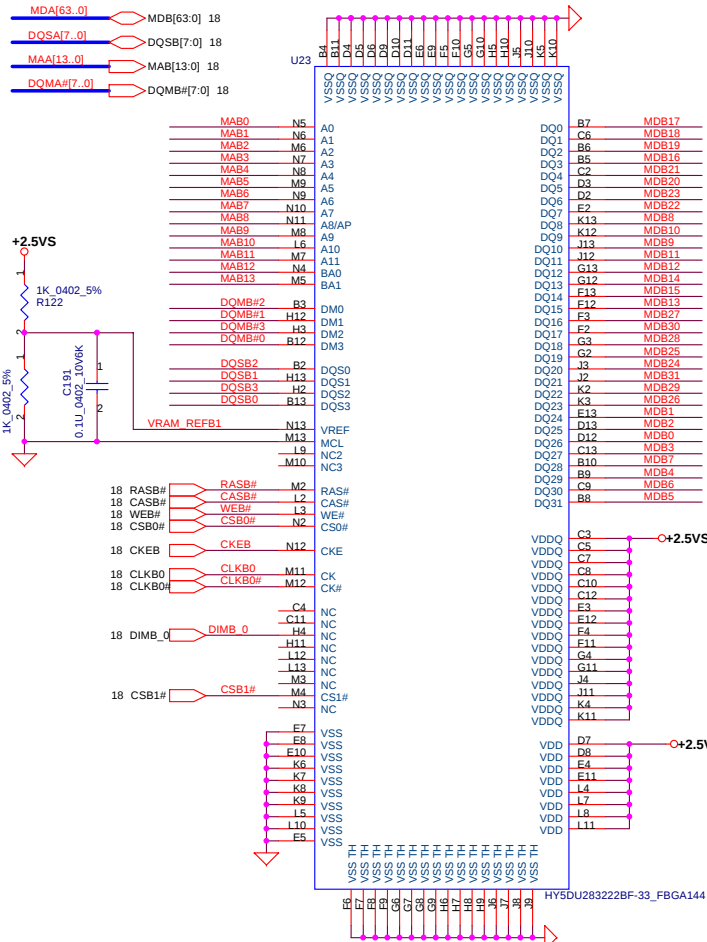
Compal Electronics, Inc.			
SCHEMATIC, M/BLA-2522			
Size	Document Number	Rev	
	401318	C	
Date:	星期五, 二月 18, 2005	Sheet	20 of 52



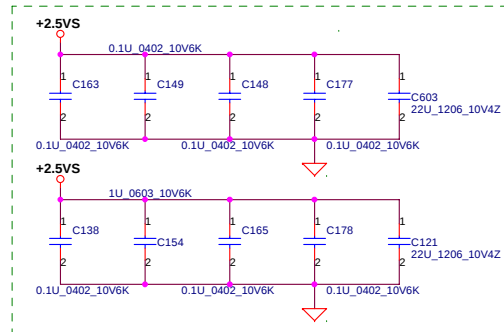
Place as close to U9 as possible



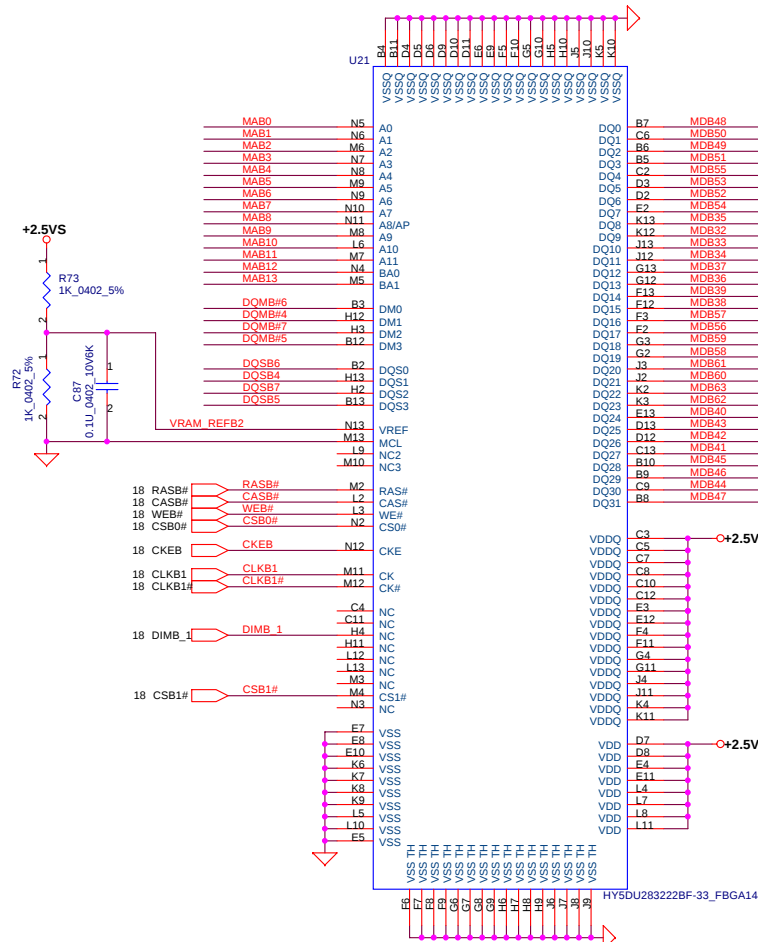
Place close to U25



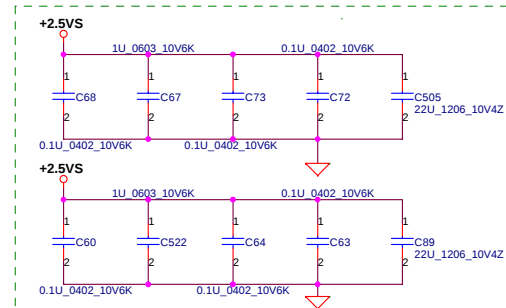
Hynix 8M32-33/600 FBGA 2.5V



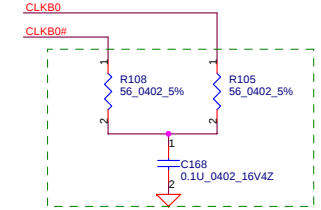
Place close to U23



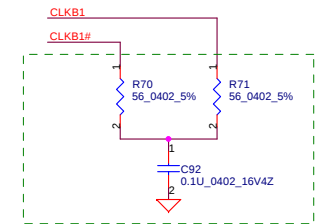
Hynix 8M32-33/600 FBGA 2.5V



Place close to U21

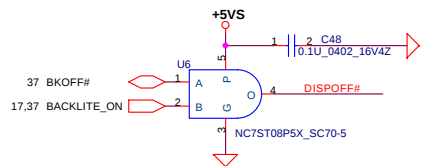


Place as close to U10 as possible



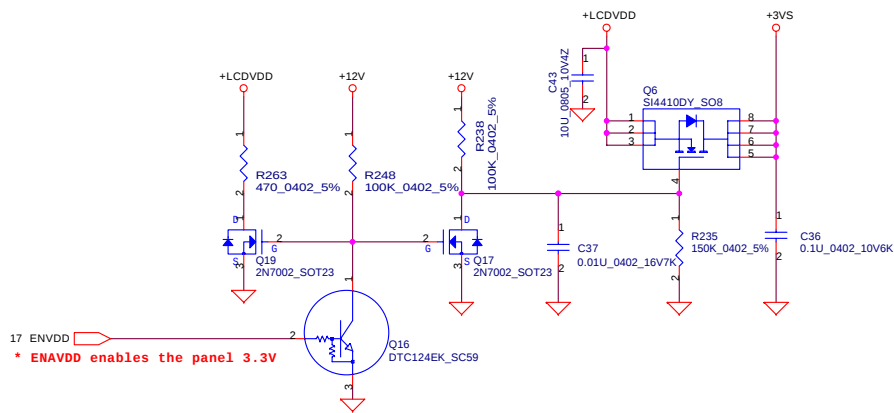
Place as close to U11 as possible



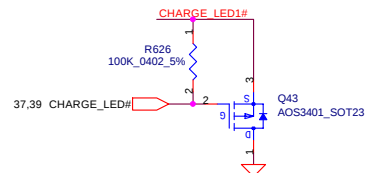
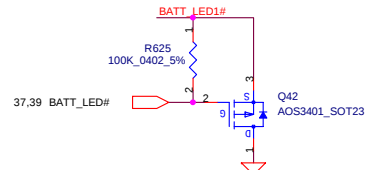
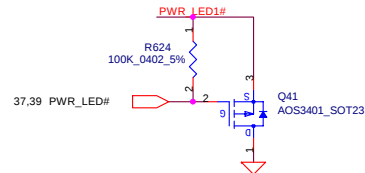
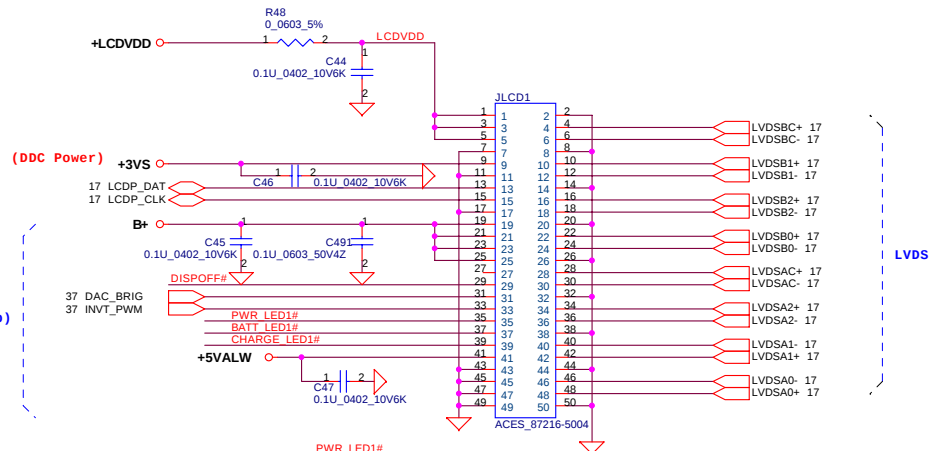


Inverter
(For Lamp)

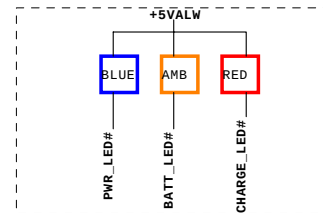
10 A, 30 V.
RDS(on) = 0.0135 W @ VGS = 10 V
RDS(on) = 0.020 W @ VGS = 4.5 V



17 ENVDD
* ENAVDD enables the panel 3.3V

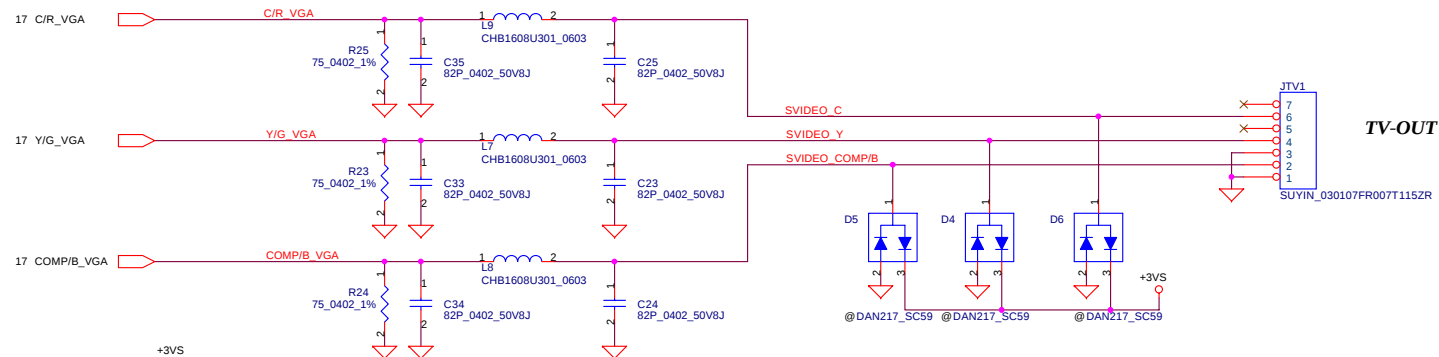


INVERTOR LED

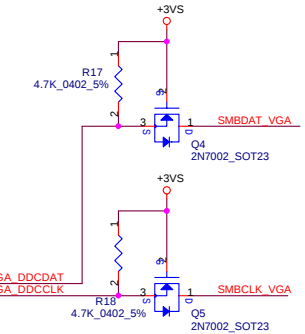


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

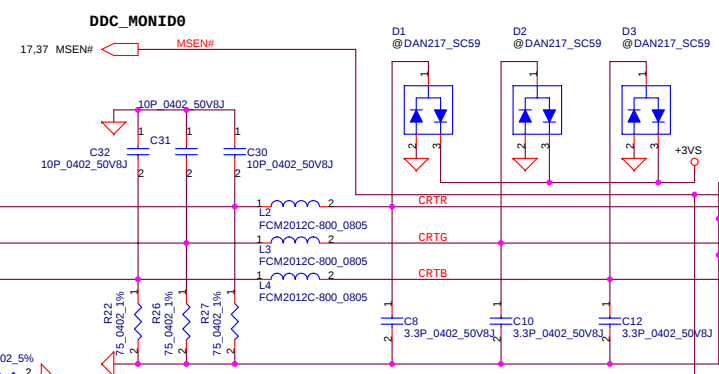
	Compal Electronics, Inc.			
	Title			
	SCHEMATIC, M/B LA-2522			
	Size	Document Number		
	401318			Rev C
Date:	星期五, 二月 18, 2005	Sheet	23	of 52



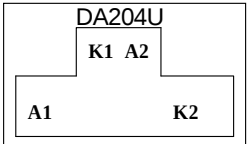
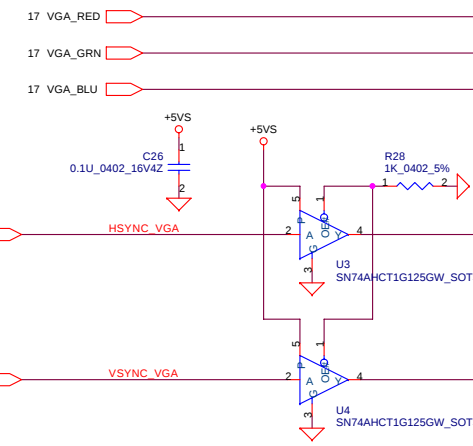
TV-OUT Connector



DVI Interface

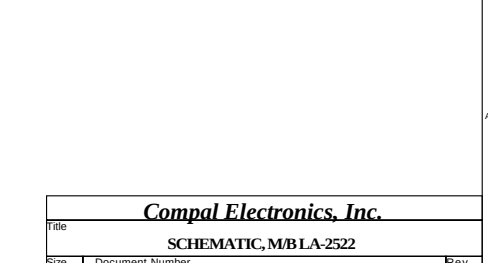
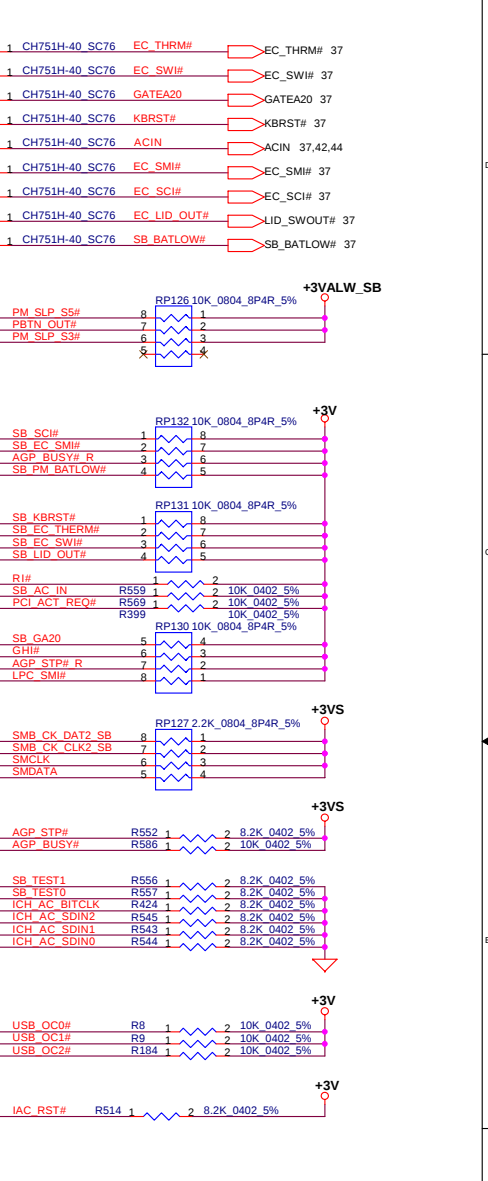
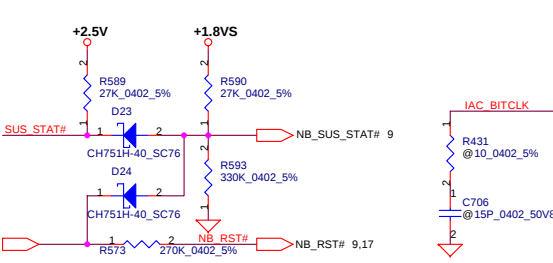
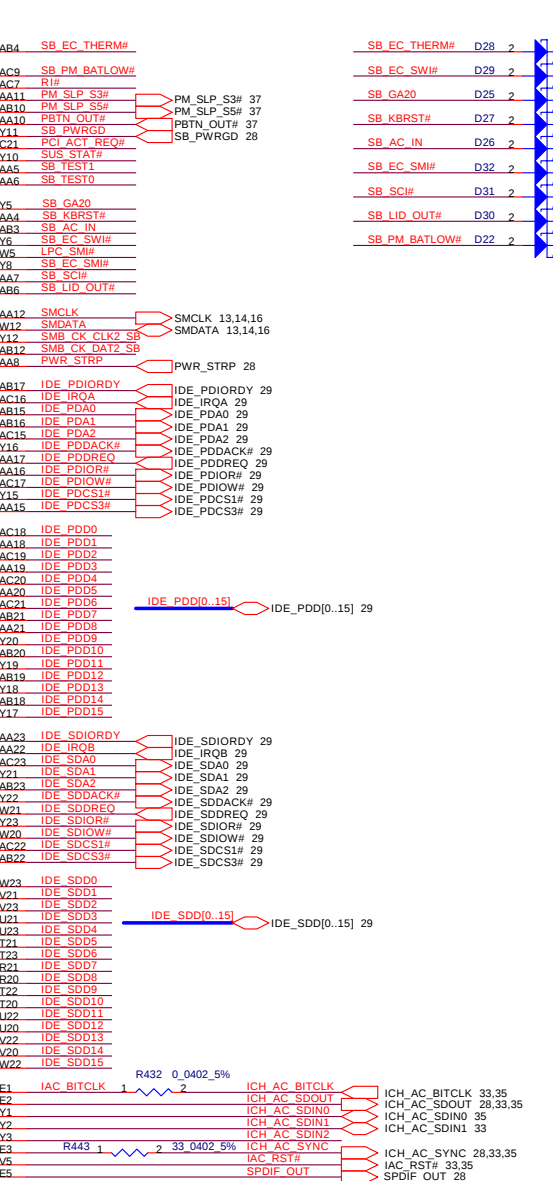
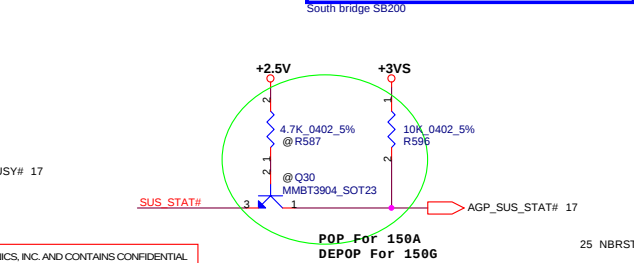
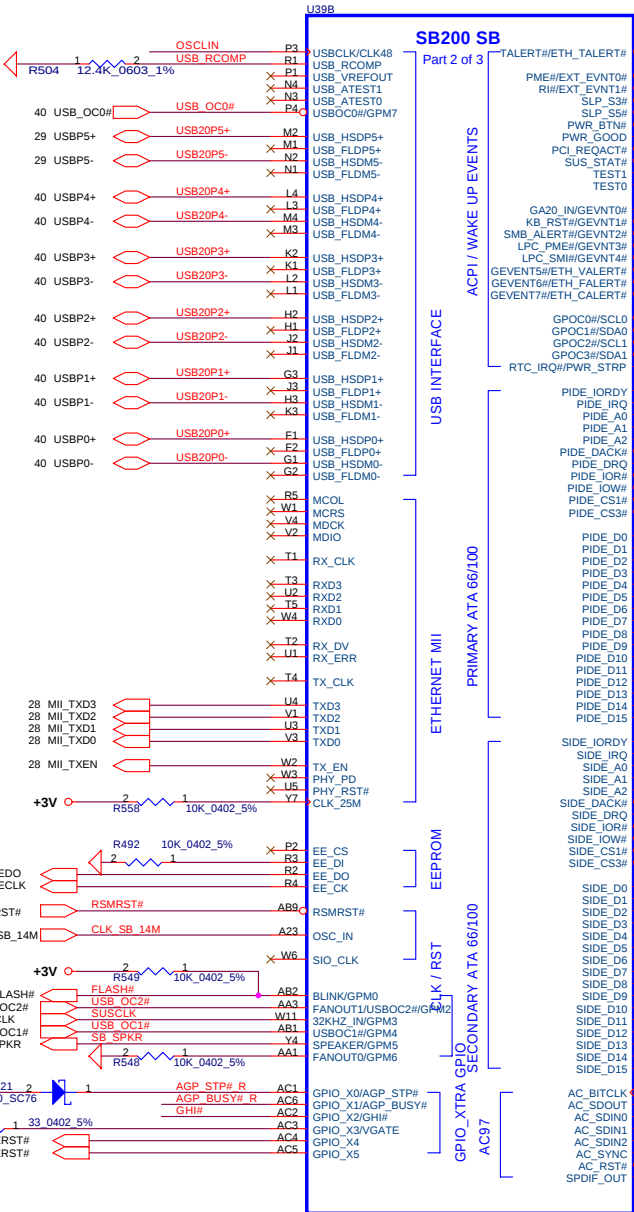
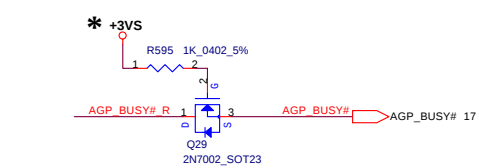
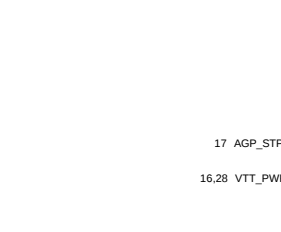
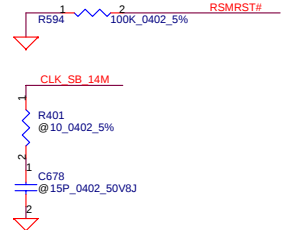
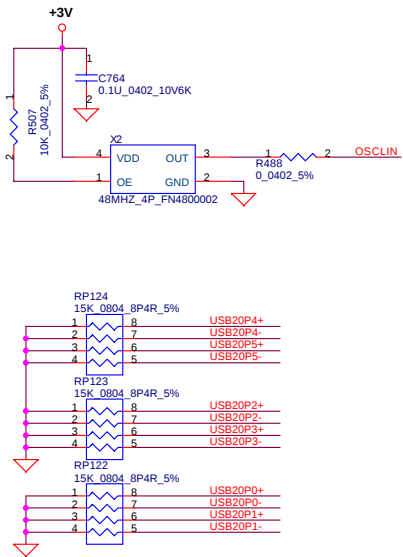


CRT Connector



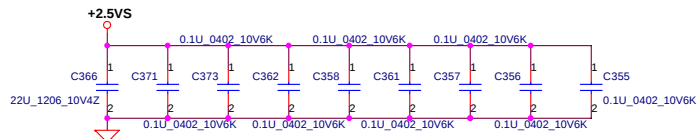
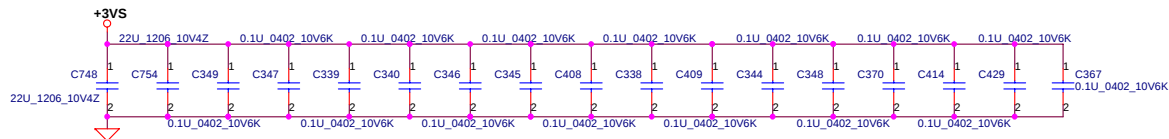
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

		Compal Electronics, Inc.	
		SCHEMATIC, M/B LA-2522	
Size	Document Number	401318	Rev C
Date:	星期五, 一月 18, 2005	Sheet 24	of 52

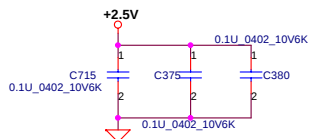
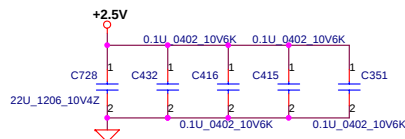


Compal Electronics, Inc.			
SCHEMATIC, M/BLA-2522			
401318			
Date:	星期五, 二月 18, 2005	Sheet	26 of 52

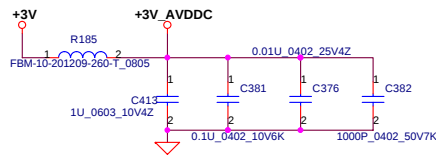
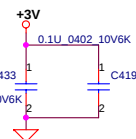
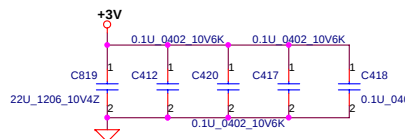
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



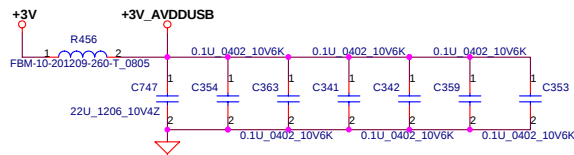
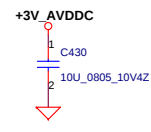
ATI request CLOSE TO L6,H6,J6



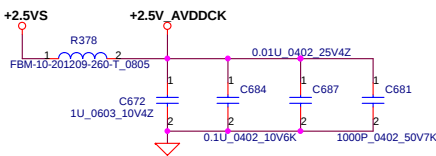
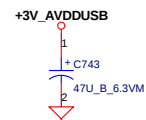
ATI request



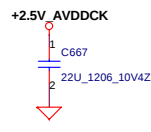
ATI request



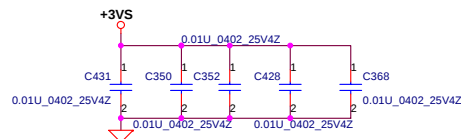
ATI request



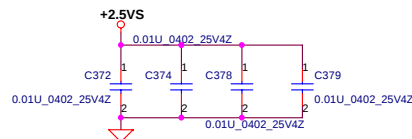
ATI request



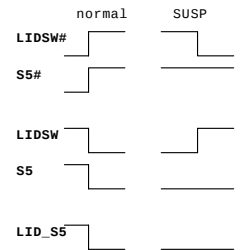
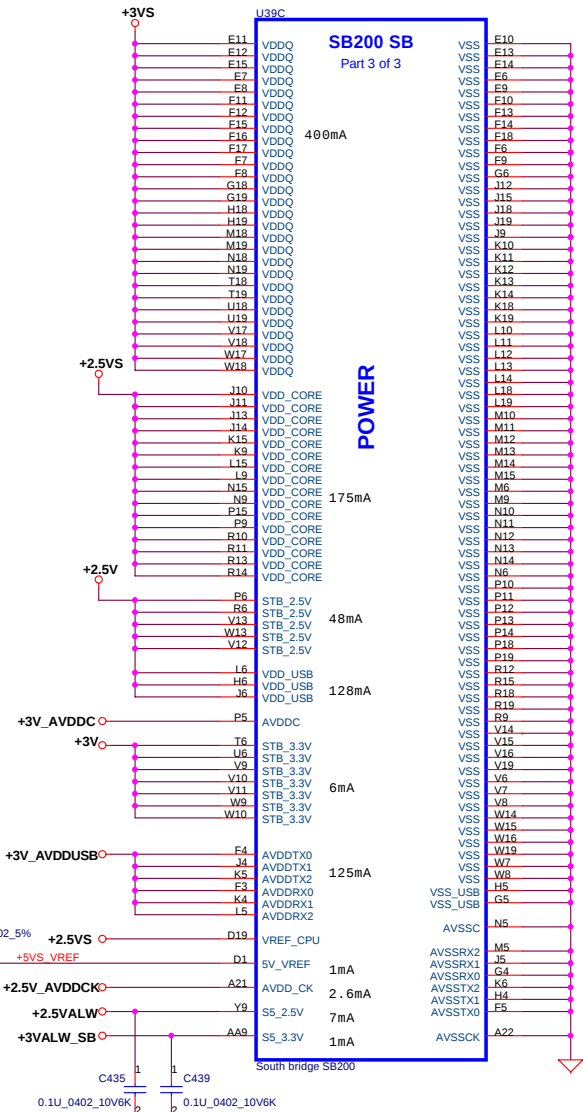
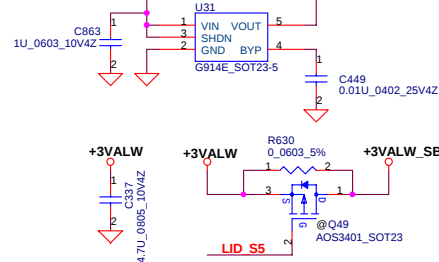
ATI request



ATI request

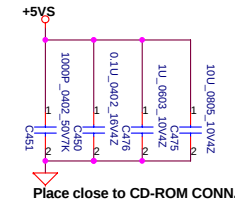
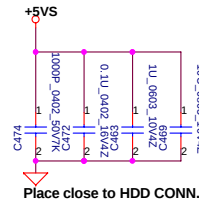
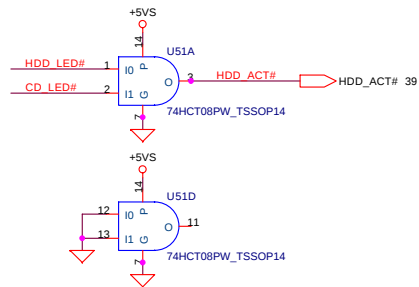
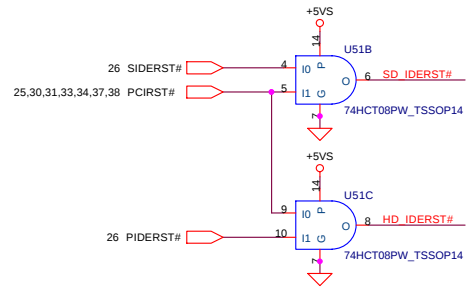


ATI request

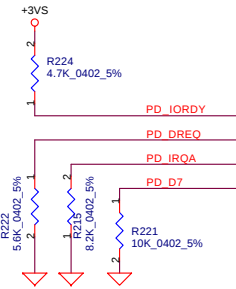
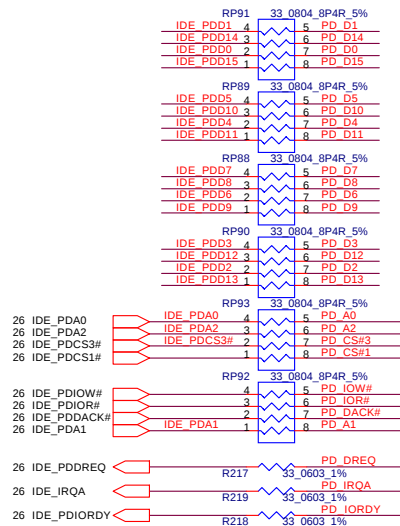


Compal Electronics, Inc.			
SCHEMATIC, MBLA-2522			
Title	Document Number	Rev	C
	401318		
Date	星期五, 二月 18, 2005	Sheet	27 of 52

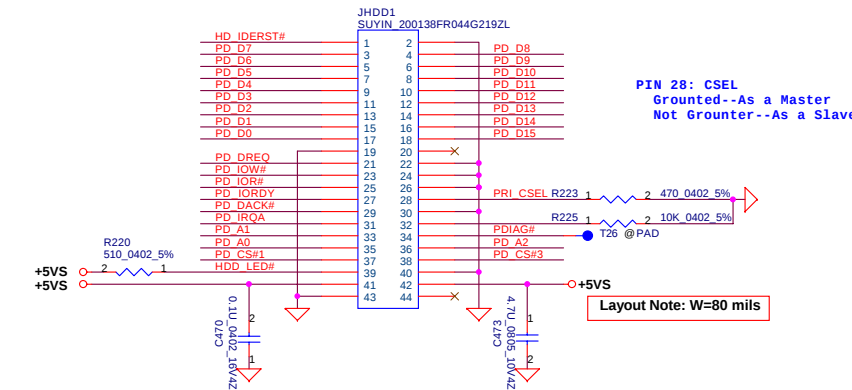
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



26 IDE_PDD[0..15] IDE_PDD[0..15]

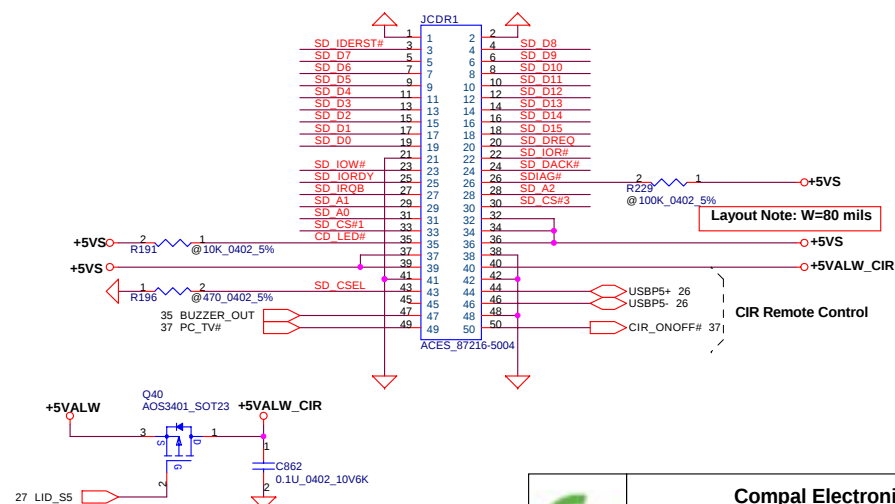
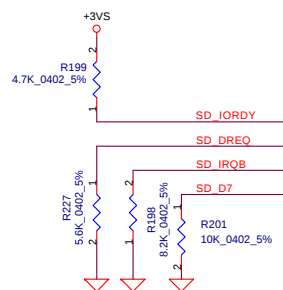
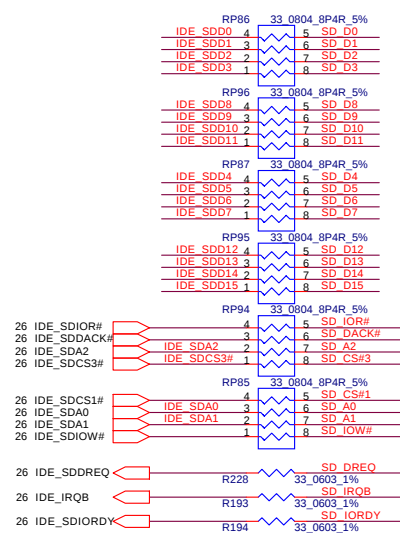


HDD Connector



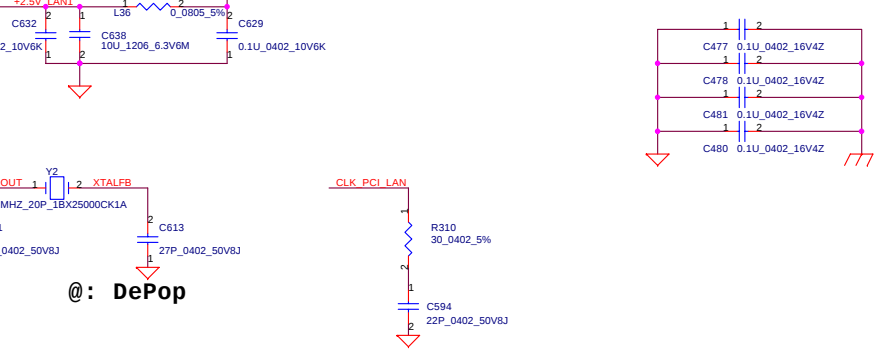
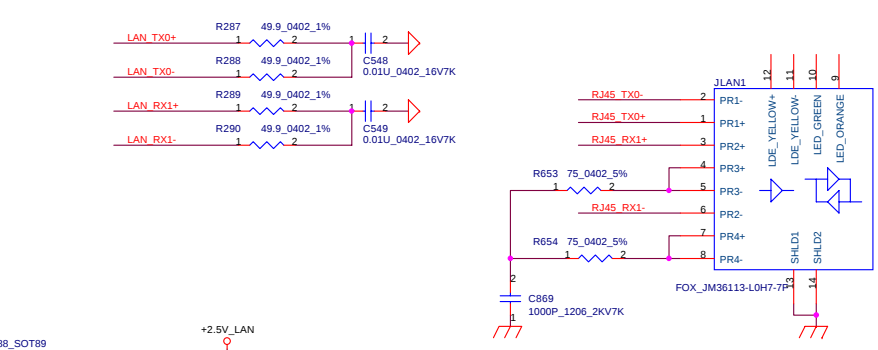
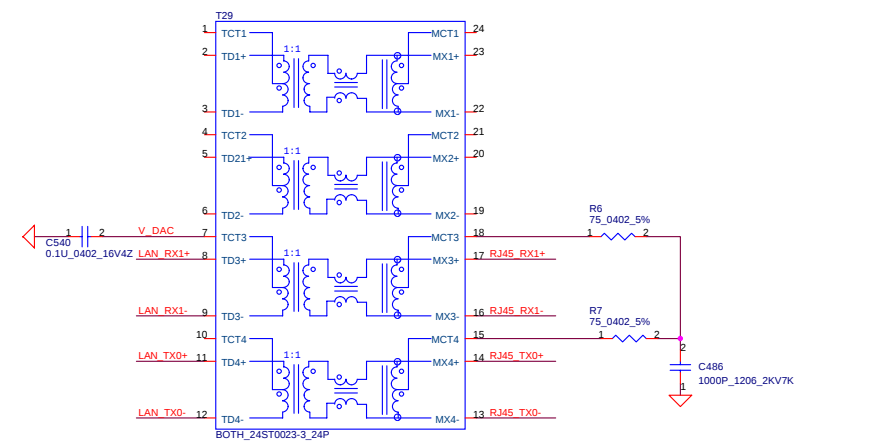
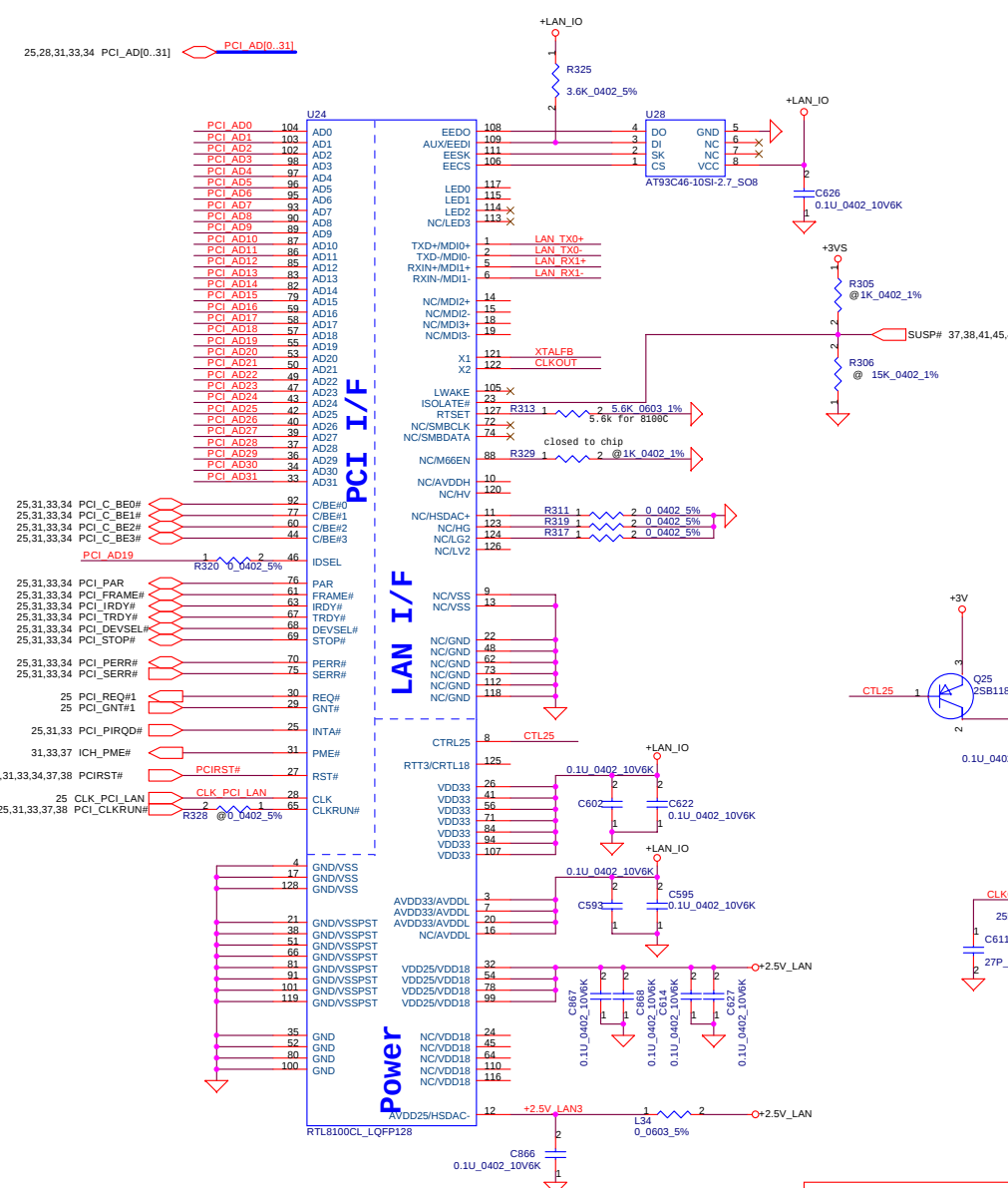
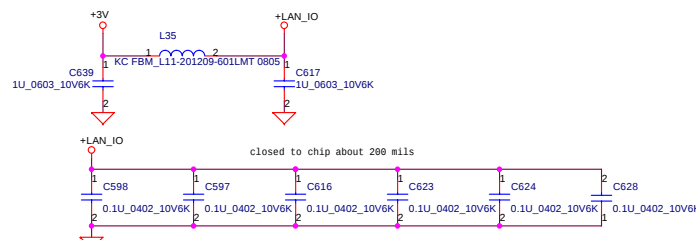
CD-ROM Connector

26 IDE_SDD[0..15] IDE_SDD[0..15]



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

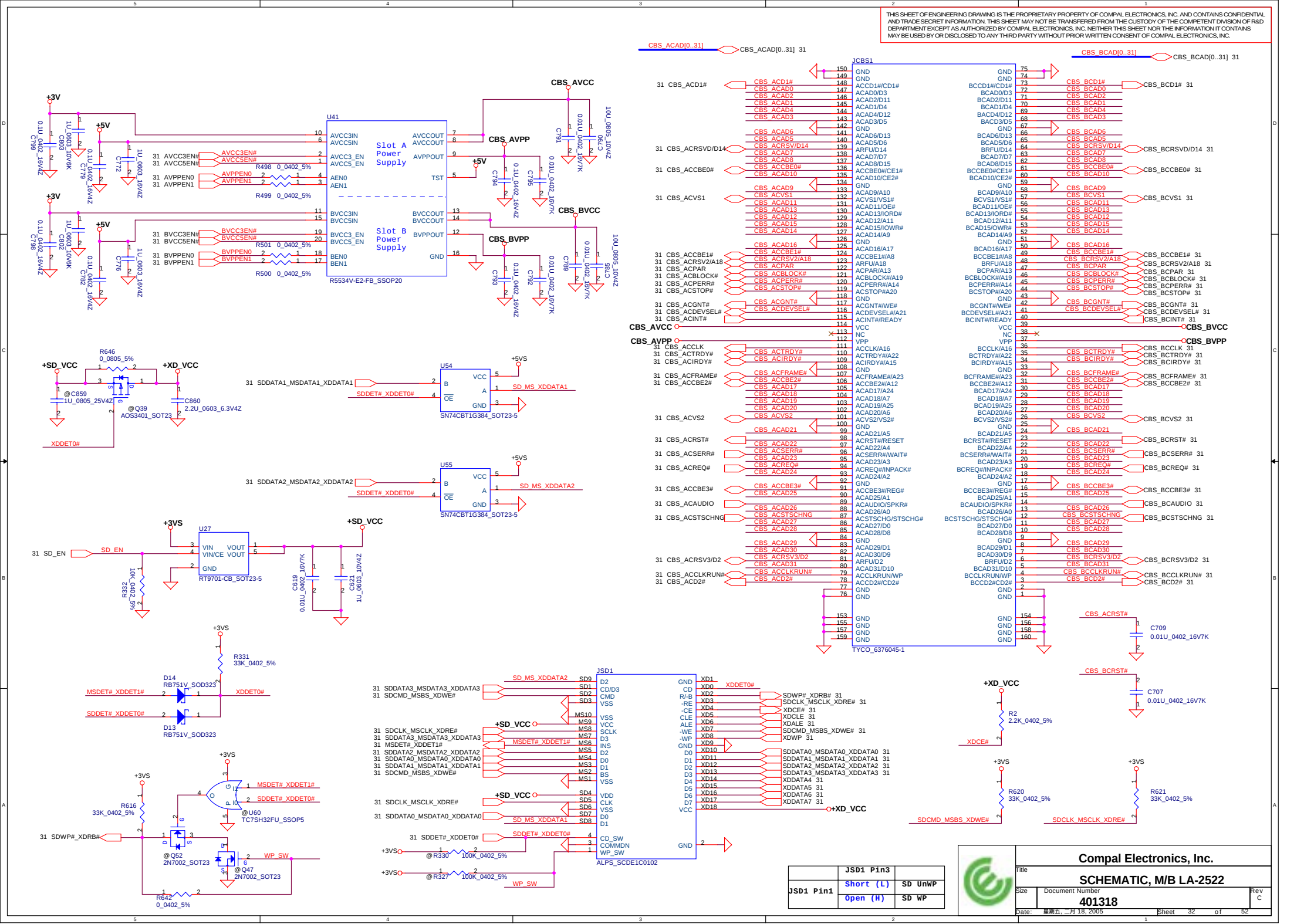
Compal Electronics, Inc.			
SCHEMATIC, M/B LA-2522			
Size	Document Number	Rev	
	401318	C	
Date:	星期五, 二月 18, 2005	Sheet	29 of 52

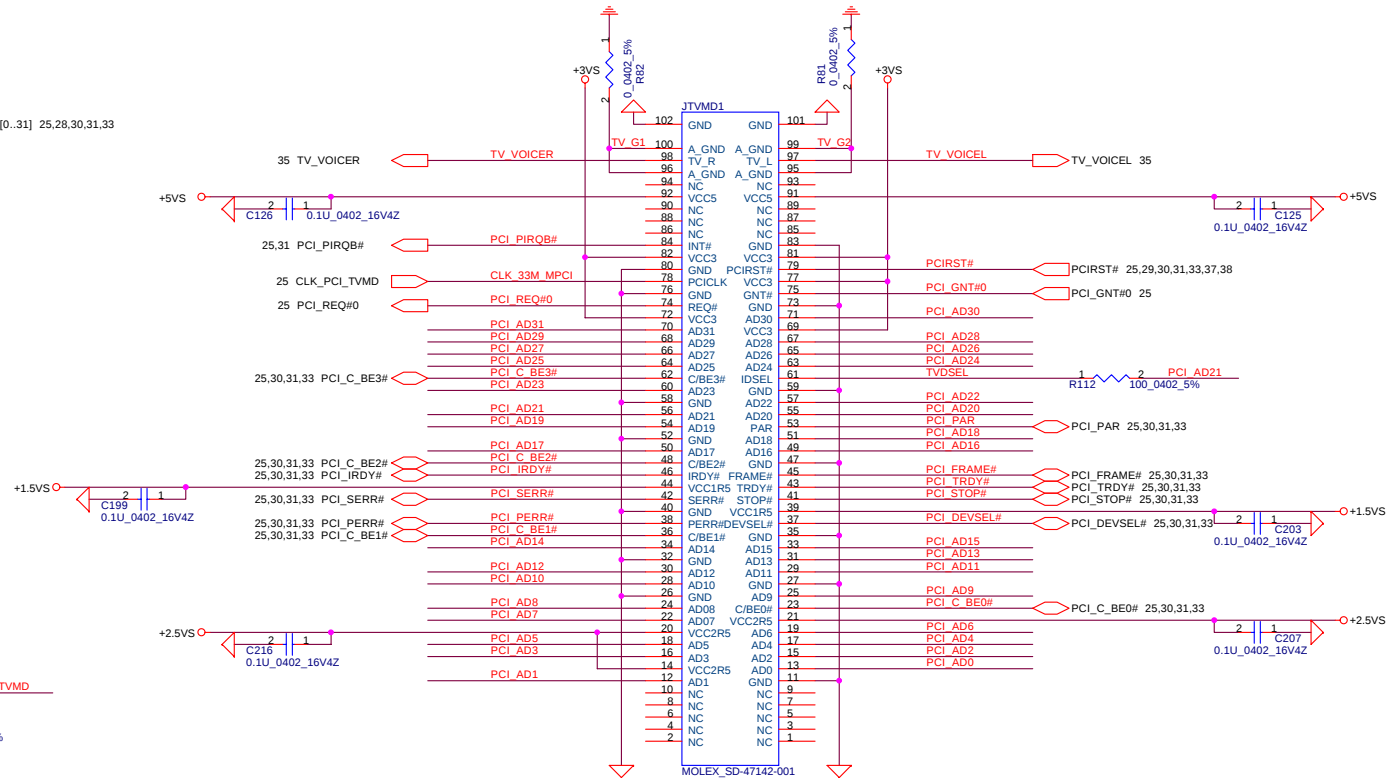
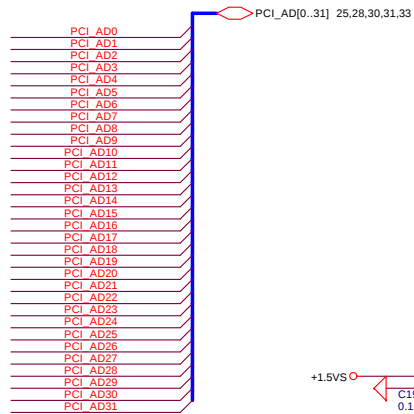


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.	
SCHEMATIC, M/B LA-2522	
File	Document Number
Size	401318
Date	Rev C
2005.02.18	30 of 52

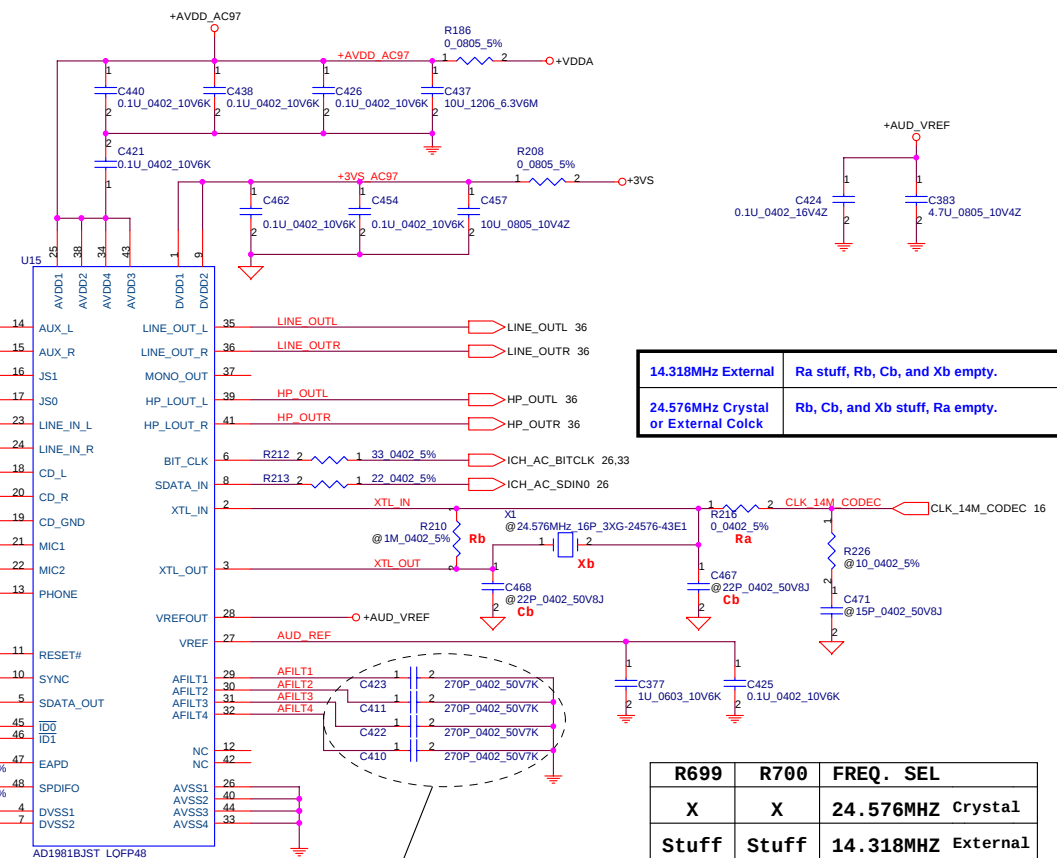
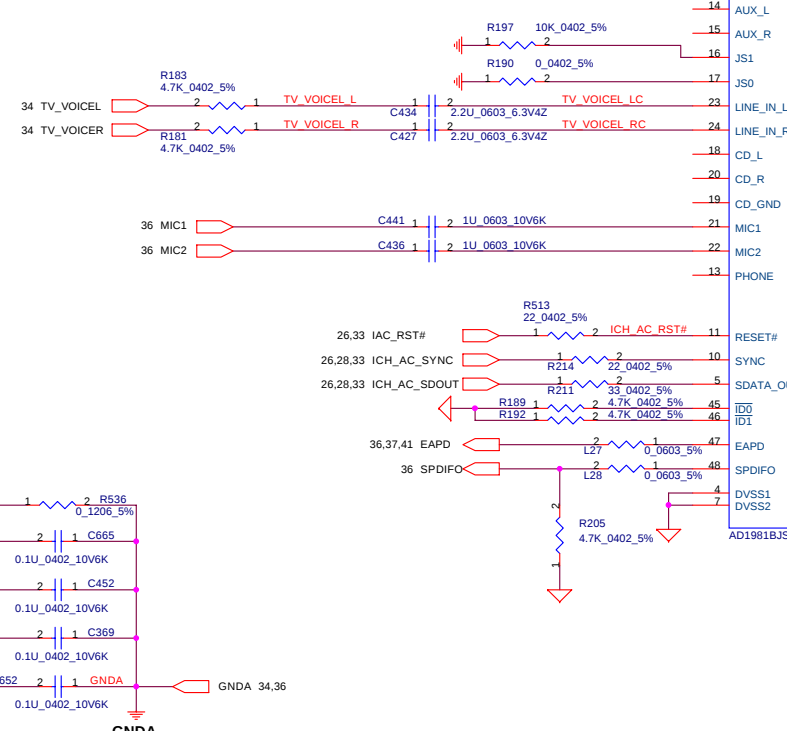
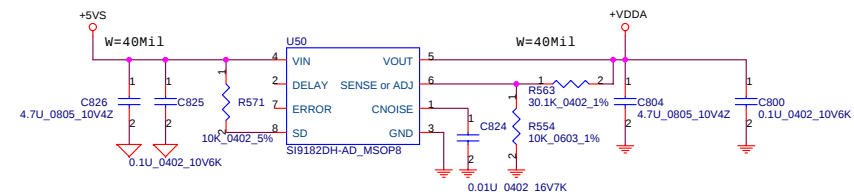
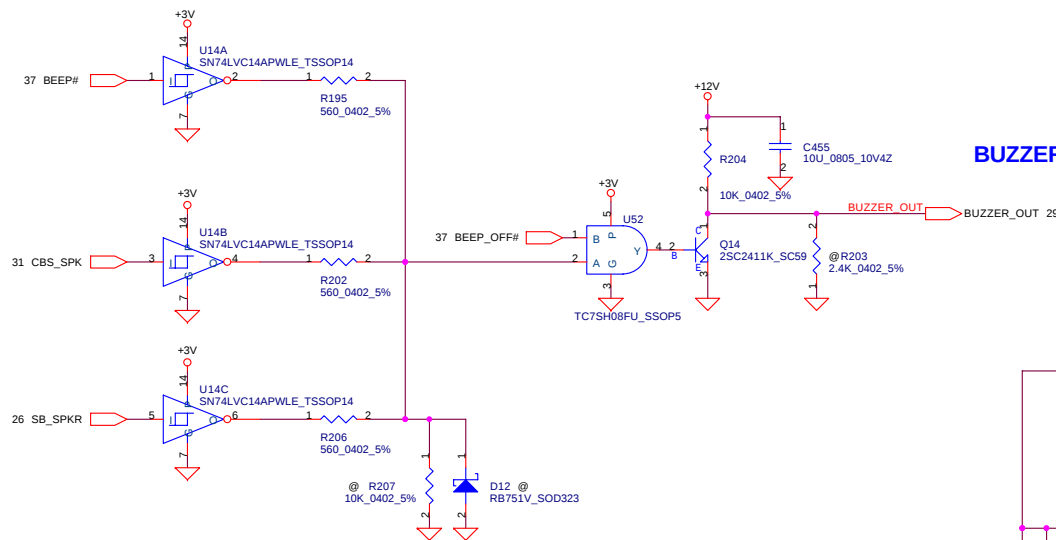






THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

		Compal Electronics, Inc.	
Title		SCHEMATIC, M/B LA-2522	
Size	Document Number	Rev C	
401318			
Date:	星期五, 二月 18, 2005	Sheet	34 of 52

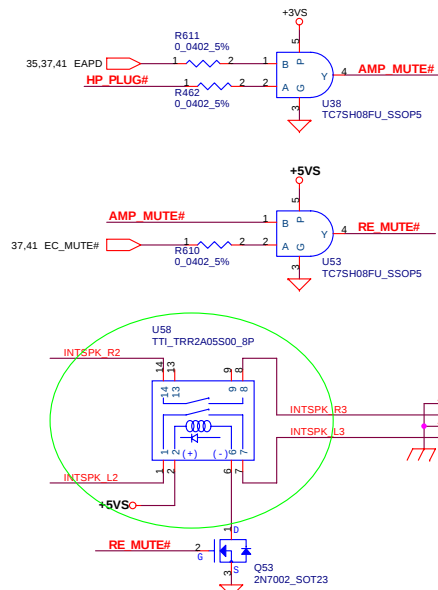
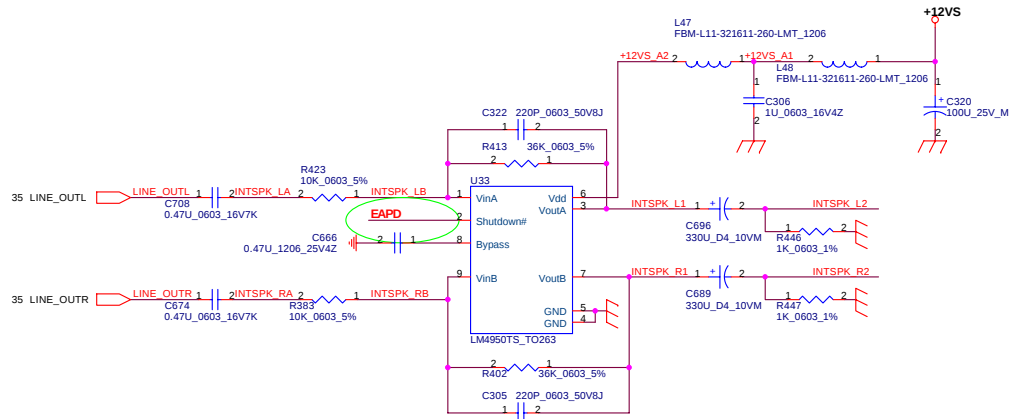


14.318MHz External	Ra stuff, Rb, Cb, and Xb empty.
24.576MHz Crystal or External Colck	Rb, Cb, and Xb stuff, Ra empty.

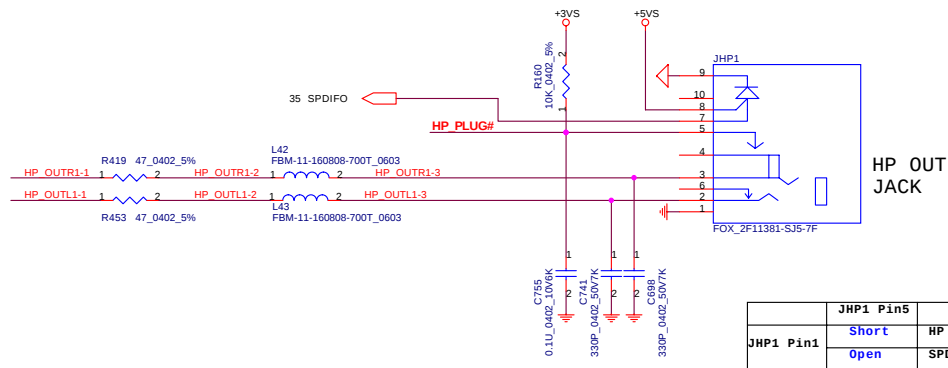
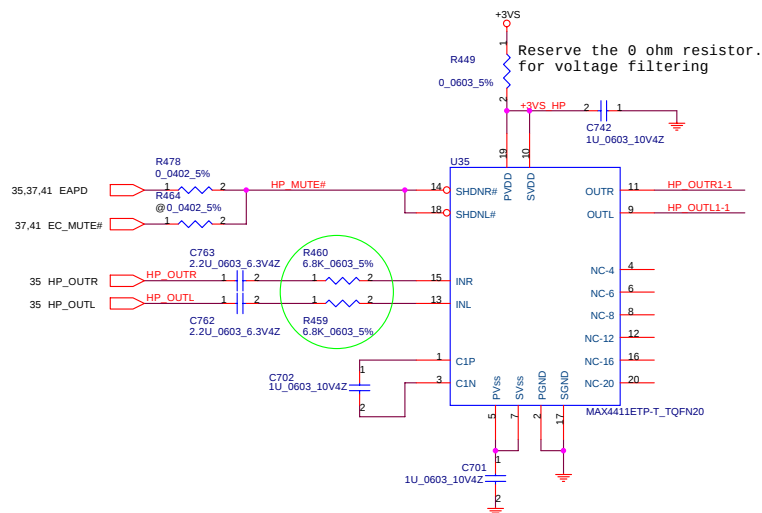
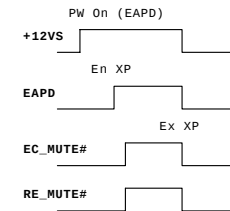
R699	R700	FREQ. SEL
X	X	24.576MHZ Crystal
Stuff	Stuff	14.318MHZ External

Close to AD1981B

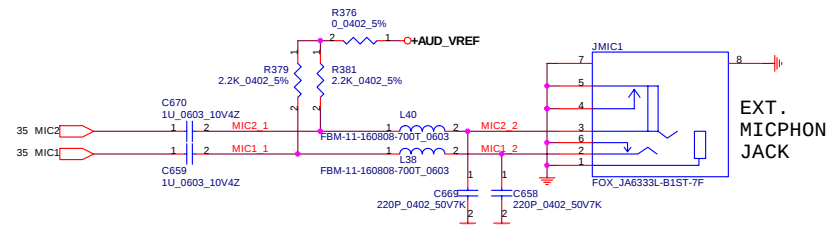
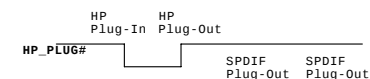
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



Connect AMPGND to GND via one single point to HOLE close to U33

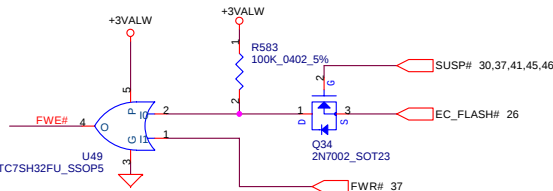
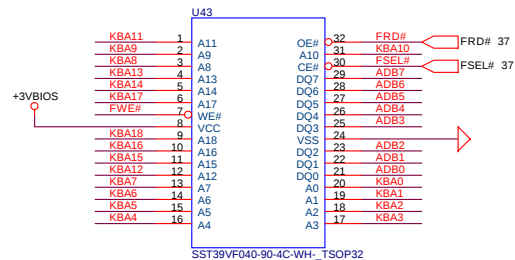
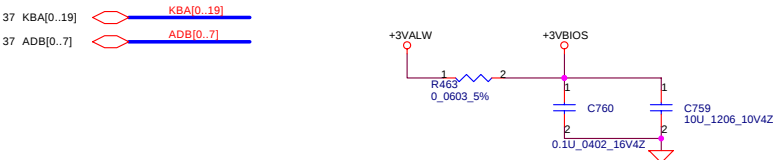


JHP1 Pin5	JHP1 Pin1
Short	Open
HP Plug-In	SPDIF Plug-In

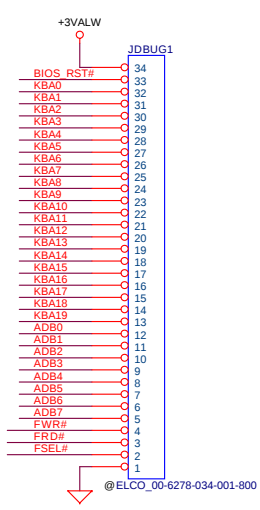


Compal Electronics, Inc.			
Title			
SCHEMATIC, M/B LA-2522			
Size	Document Number	Rev	
	401318	C	
Date:	Rev	Sheet	of
2005.02.18	401318	36	52

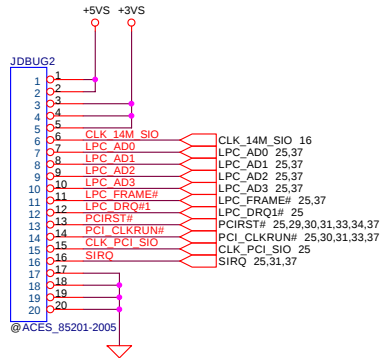
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



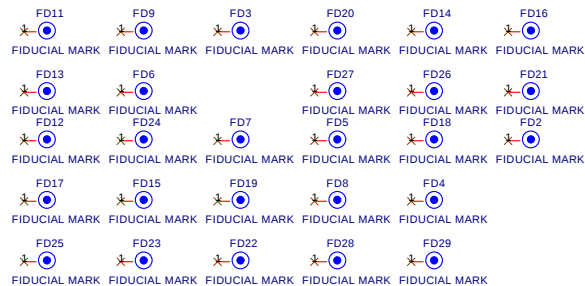
BIOS DEBUG CONN



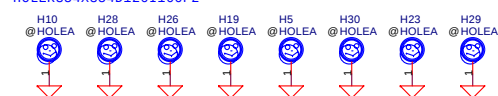
LPC DEBUG CONN



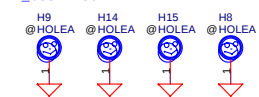
Fiducial Mark



A



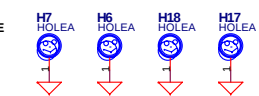
B



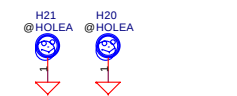
C



D

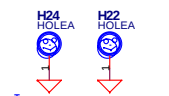


E



TV MODULE

F



G



H



MDC

I



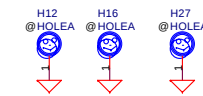
J



K



L



N



M

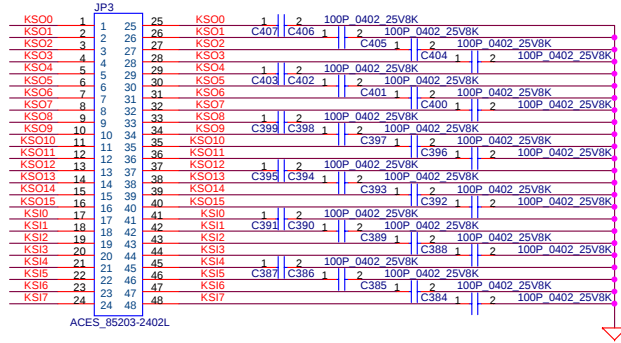


Compal Electronics, Inc.			
Title			
SCHEMATIC, M/B LA-2522			
Size	Document Number	Rev	
Custor	401318	C	
Date:	星期五, 二月 18, 2005	Sheet	38 of 52

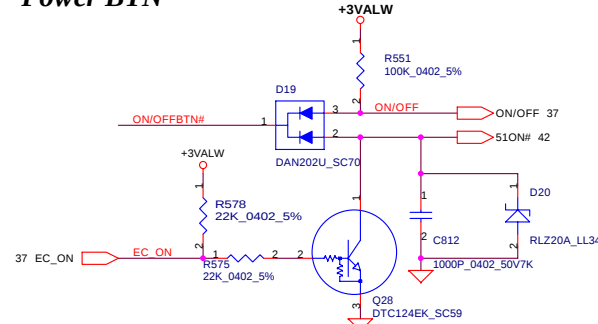
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

KeyBoard

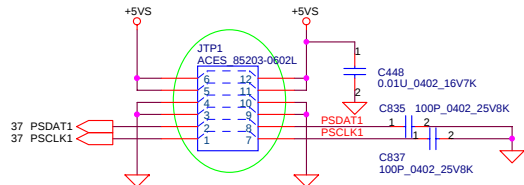
37 KSI[0..7]
37 KSO[0..15]



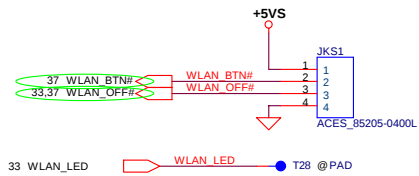
Power BTN



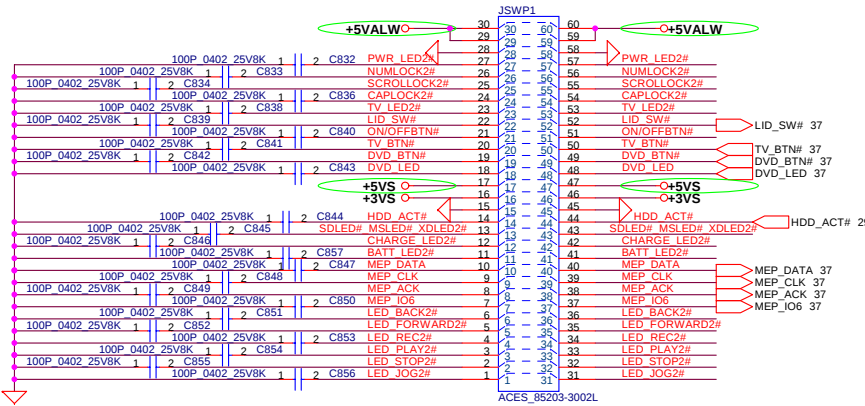
Touch Pad Board



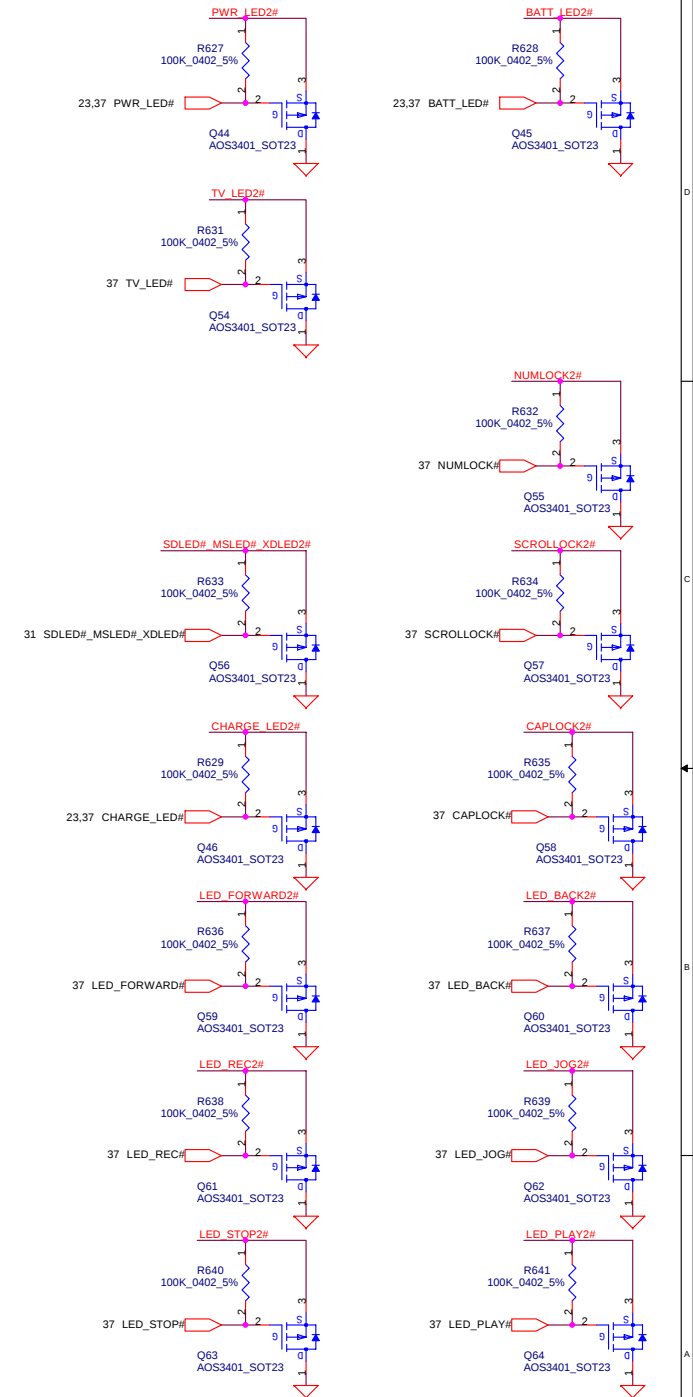
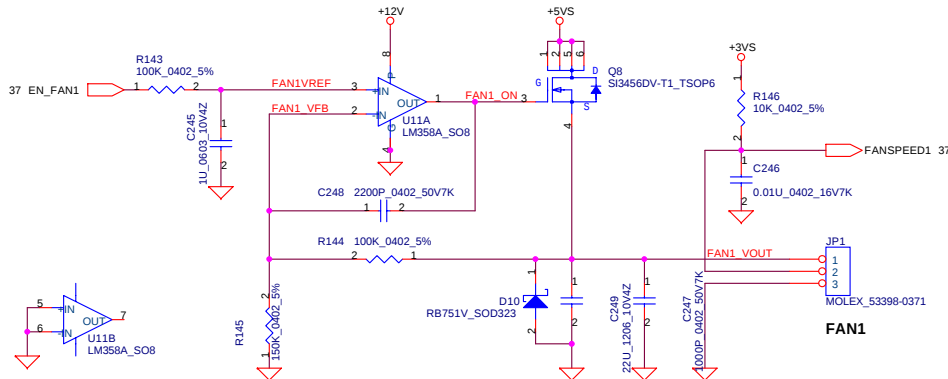
WLAN Board



Function Board SW/LED

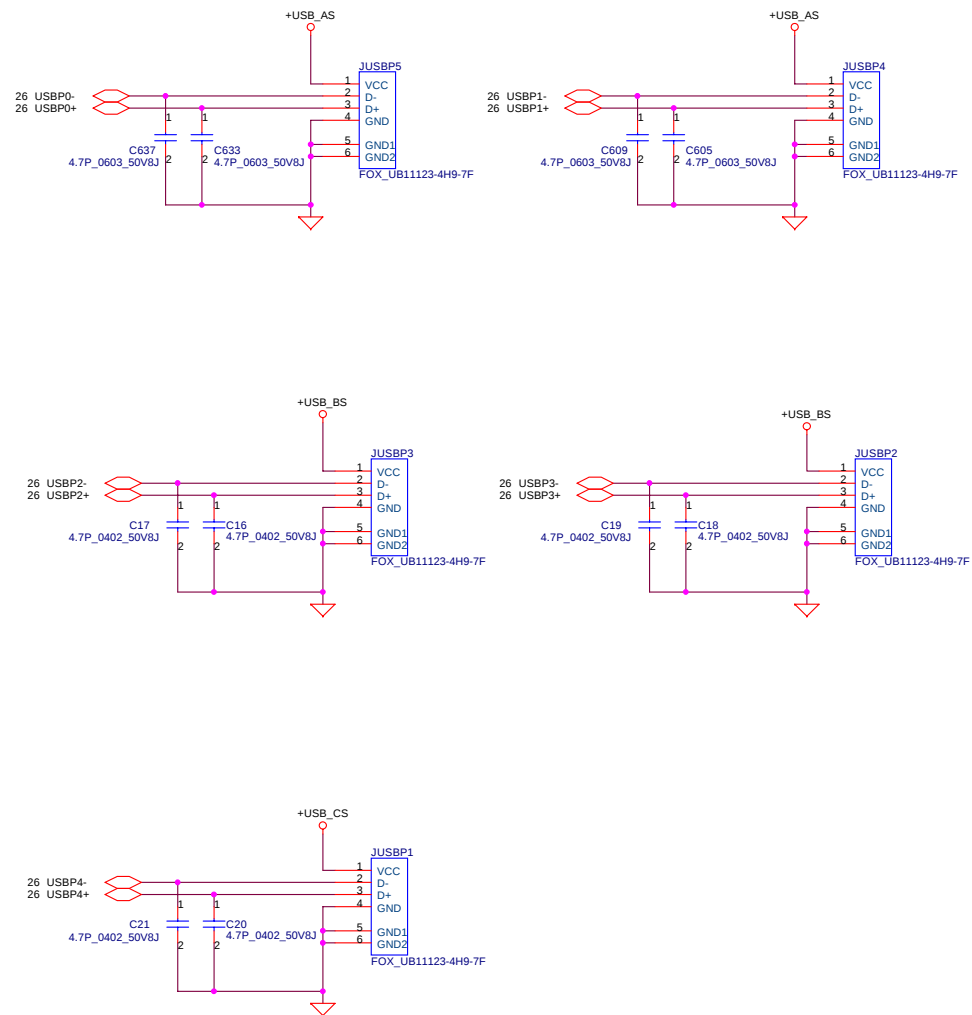
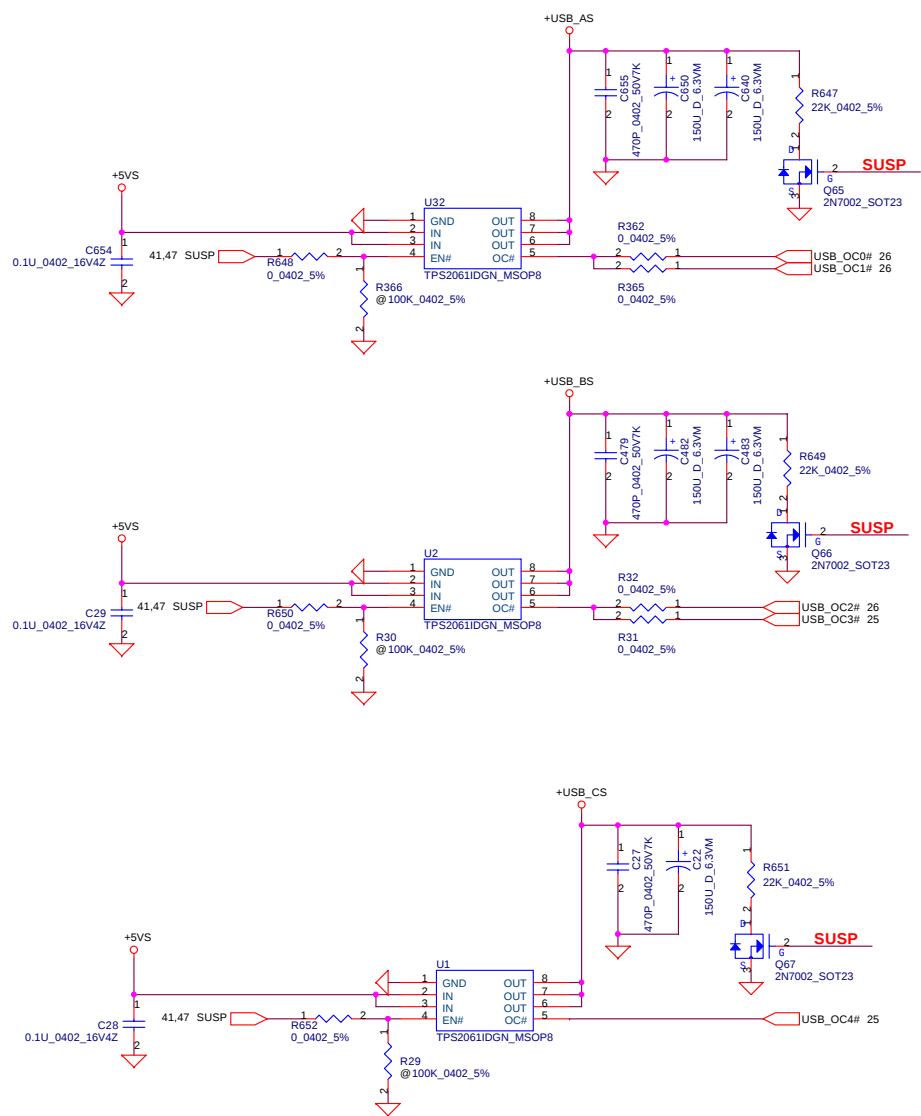


FAN1 Control and Tachometer



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

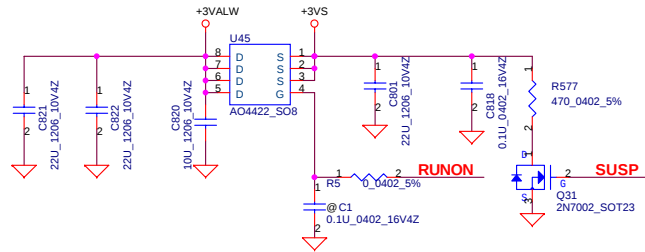
				Compal Electronics, Inc.			
Title				SCHEMATIC, M/B LA-2522			
Size				Document Number			
401318				Rev C			
Date: 星期五, 二月 18, 2005				Sheet 39 of 52			



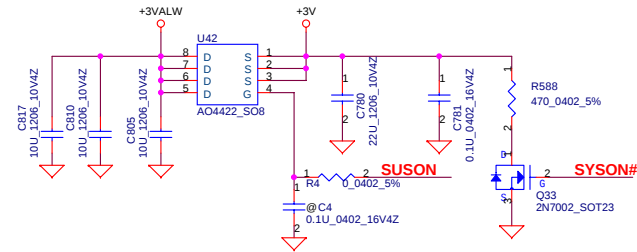
Compal Electronics, Inc.			
Title			
SCHEMATIC, M/B LA-2522			
Size	Document Number	Rev	
Custom	401318	C	
Date:	星期五, 二月 18, 2005	Sheet	40 of 52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

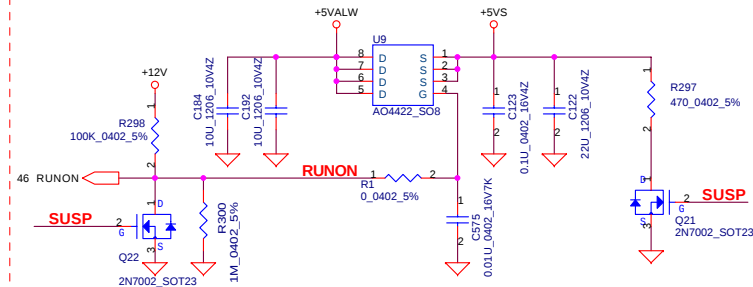
+3VALW to +3VS Transfer



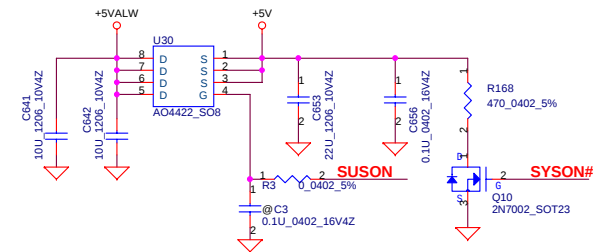
+3VALW to +3V Transfer



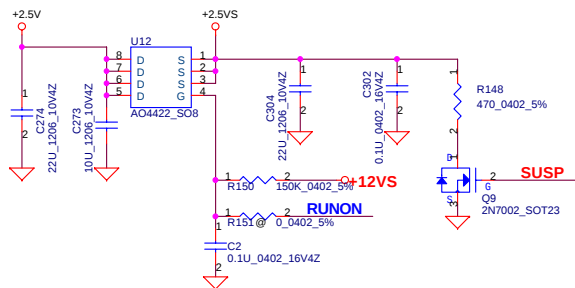
+5ALW to +5VS Transfer



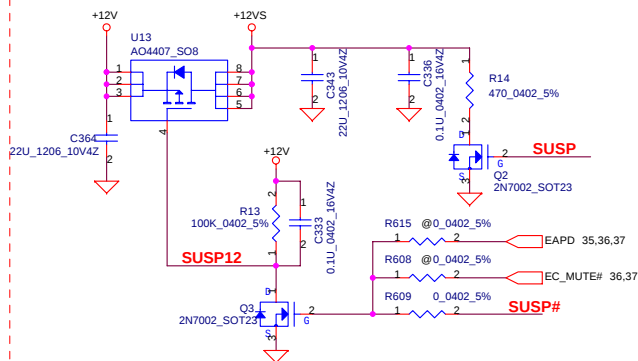
+5VALW to +5V Transfer



+2.5V to +2.5VS Transfer

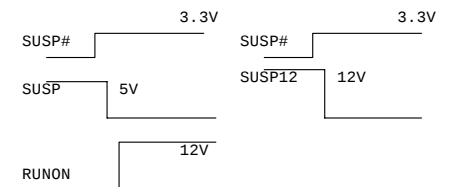
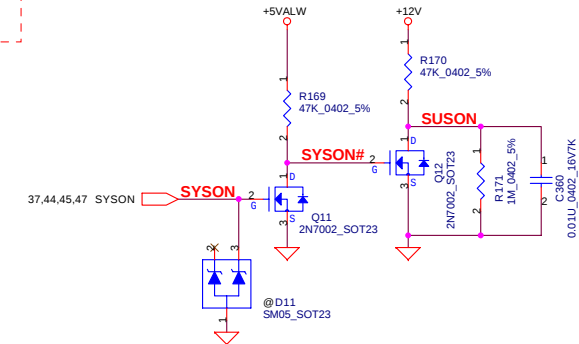
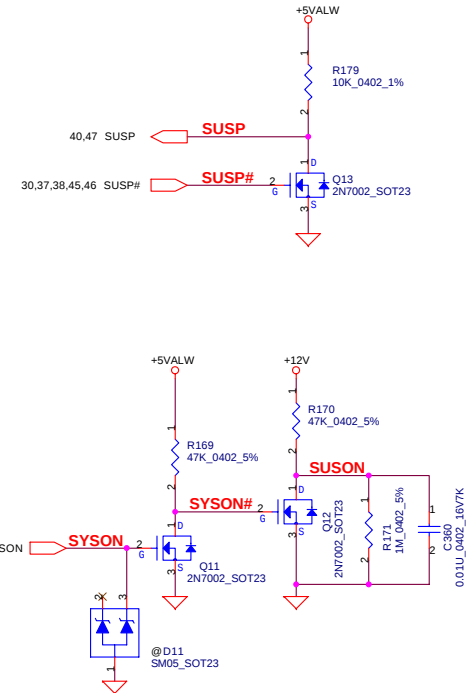


+12V to +12VS Transfer



SI2301DS: P CHANNEL
VGS: -4.5V, RDS: 130 mOHM
VGS: -2.5V, RDS: 190mOHM
Id(MAX): 2.3A
VGS(MAX): +8V

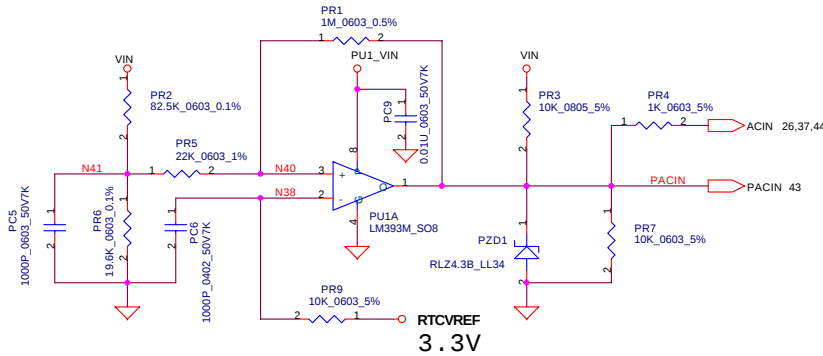
SI2302DS: N CHANNEL
VGS: 4.5V, RDS: 85 mOHM
VGS: 2.5V, RDS: 115mOHM
Id(MAX): 2.8A
VGS(MAX): +8V



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

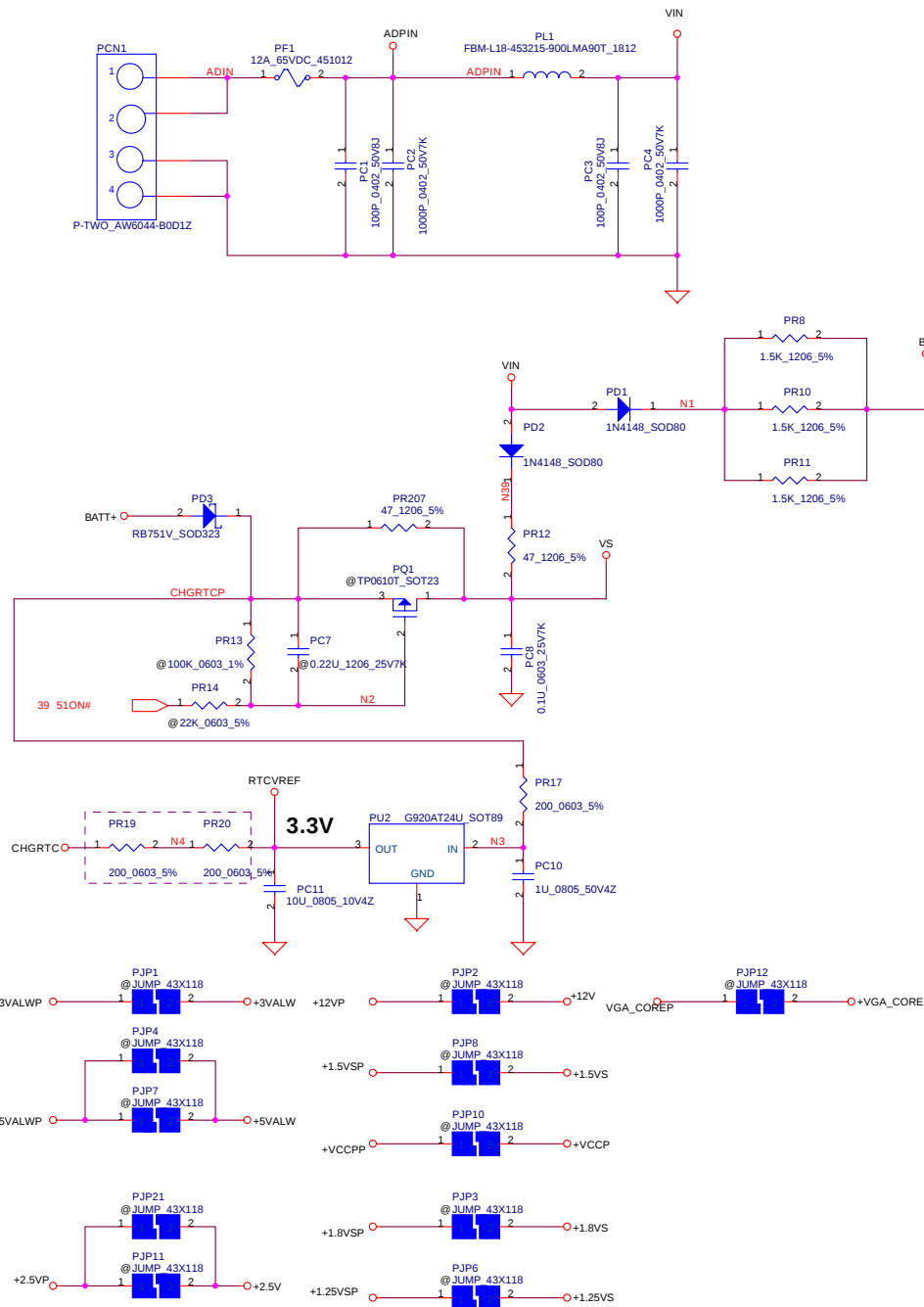
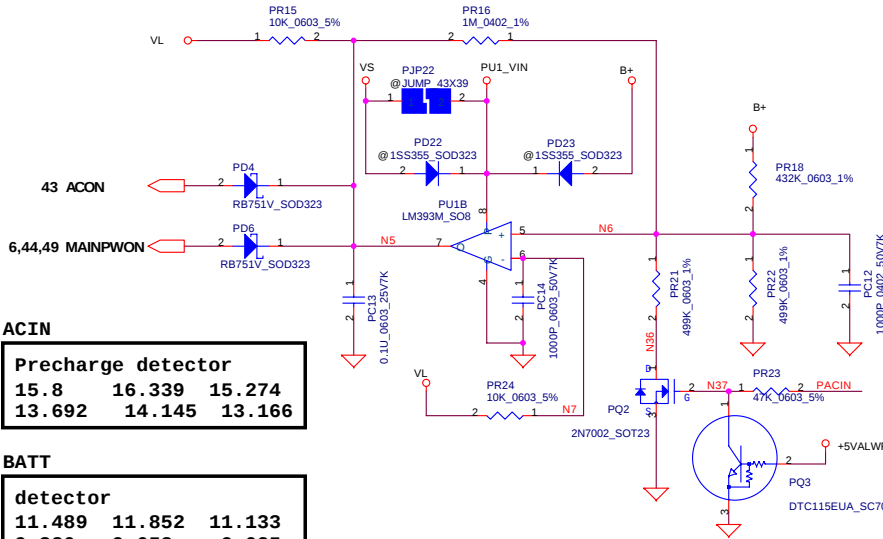
		Compal Electronics, Inc.	
		SCHEMATIC, M/B LA-2522	
Size	Document Number	401318	Rev C
Date: 星期五, 二月 18, 2005	Sheet	41	of 52

Vin Detector			
18.234	17.841	17.449	
17.597	17.210	16.813	



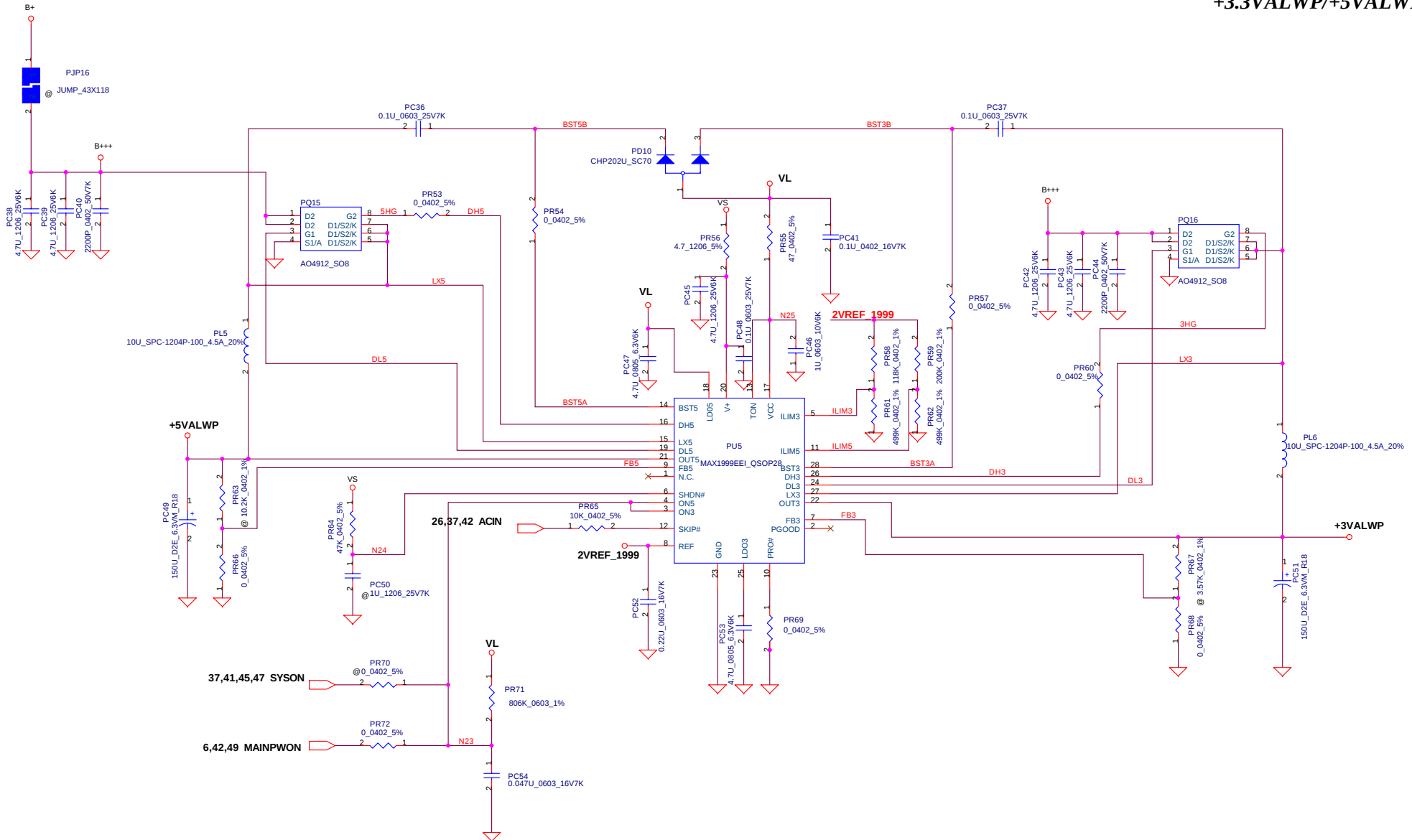
Precharge detector			
15.8	16.339	15.274	
13.692	14.145	13.166	

BATT detector			
11.489	11.852	11.133	
9.380	9.658	9.025	



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

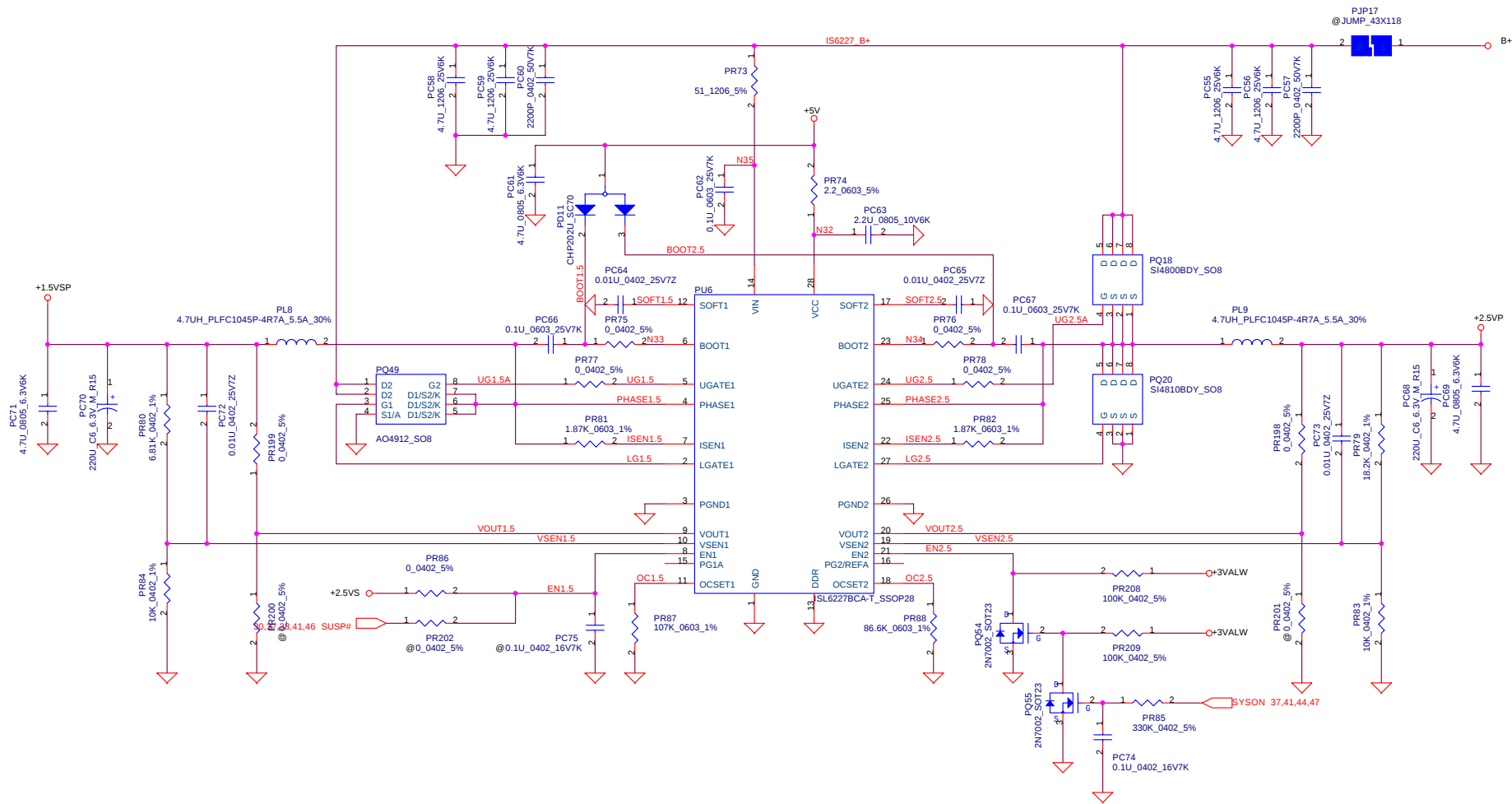
+3.3VALWP/+5VALWP



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
SCHEMATIC, M/BLA-2522			
Size	Document Number	Rev	
B	401318		
Date:	星期五, 二月 18, 2005	Sheet	44 of 52

+2.5VP/+1.5VSP



Compal Electronics, Inc.

SCHEMATIC, M/B LA-2522

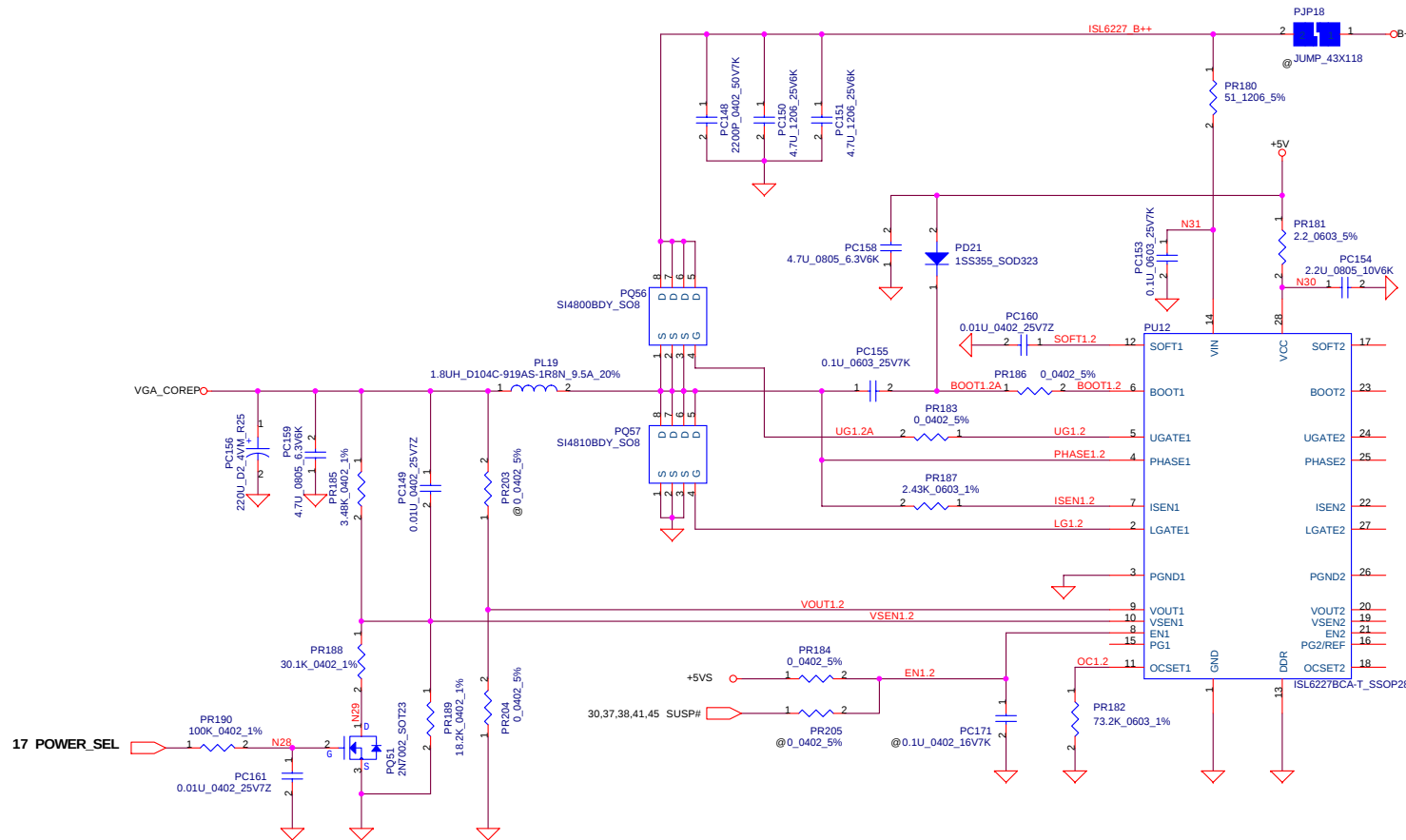
Size B Document Number 401318

Date: 星期三, 二月 18, 2005

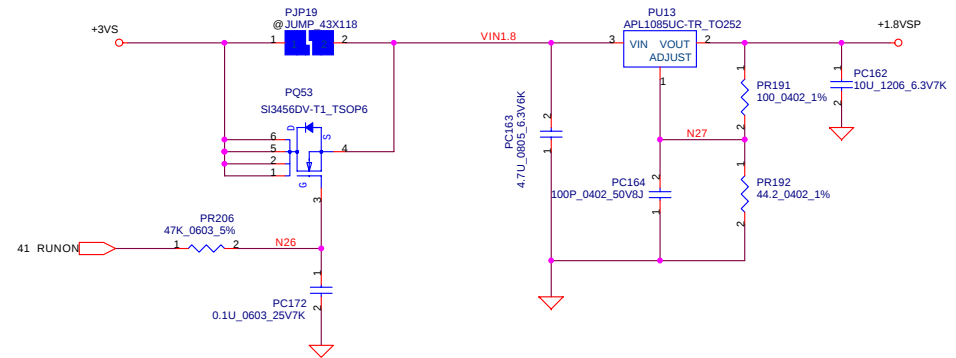
Sheet 45 of 52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

VDD_CORE/+1.8VSP



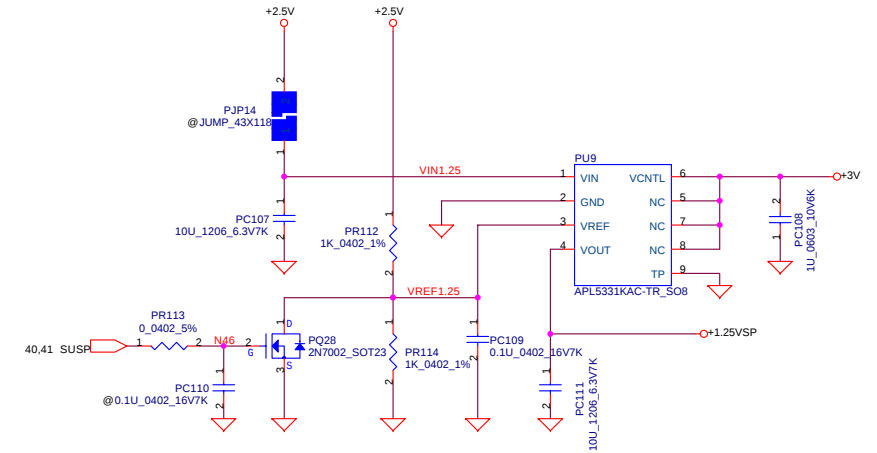
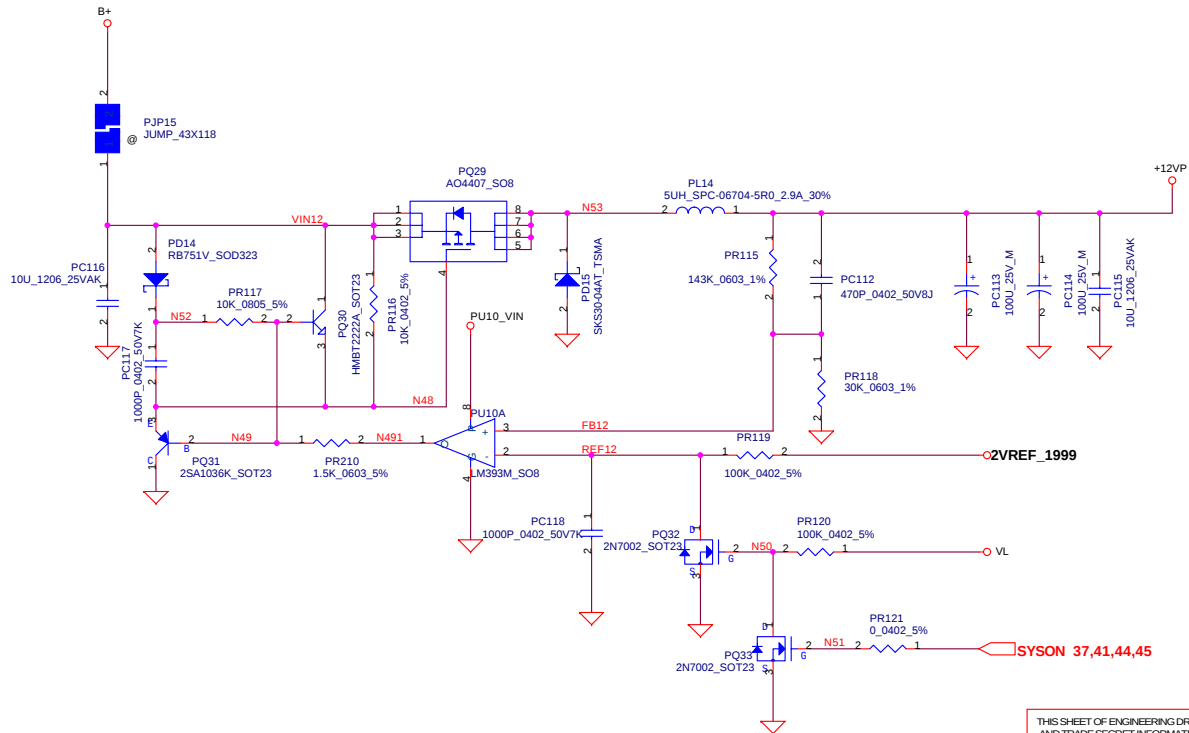
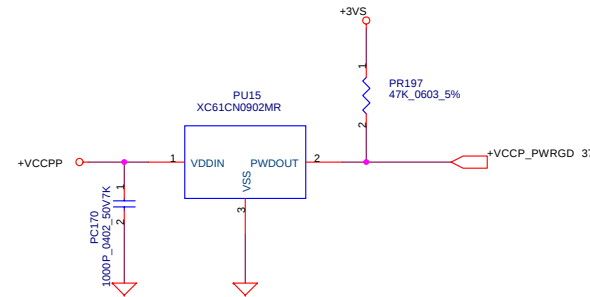
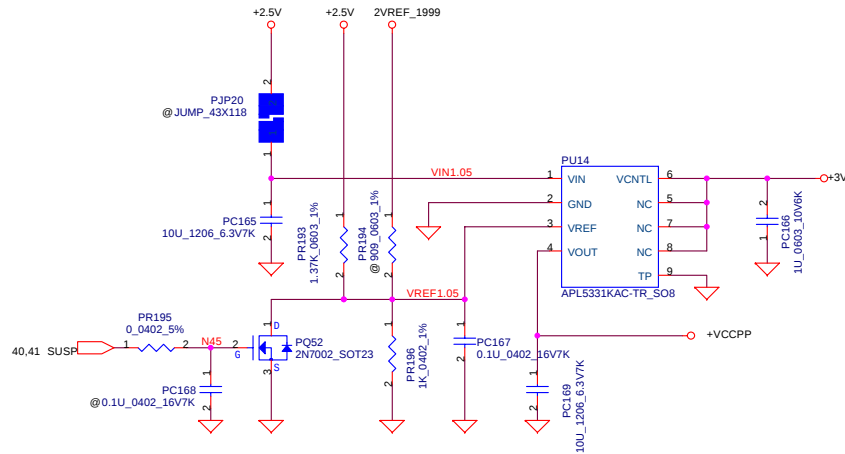
	POWER_SEL	VGA_CORE
VGA_CORE for ATI-M11	H	1.2V
	L	1.05V



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
SCHEMATIC, M/B LA-2522			
Size	Document Number	Rev	
	401318	C	
Date:	星期五, 二月 18, 2005	Sheet	46 of 52

+12VSP/+2.5VSP/+VCCP



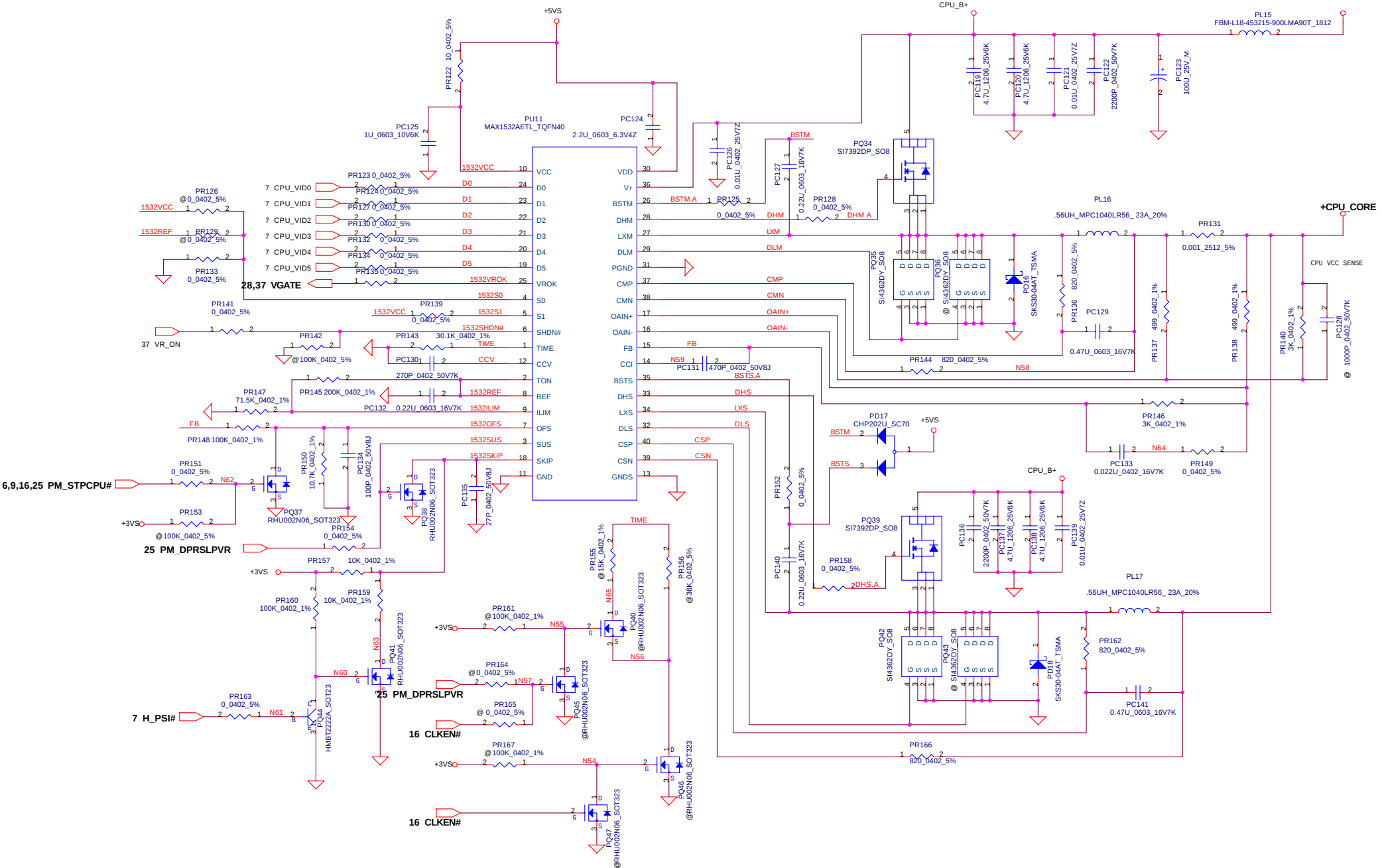
Compal Electronics, Inc.

SCHEMATIC, M/B LA-2522

Size	Document Number	Rev
B	401318	C
Date:	星期五, 三月 18, 2005	Sheet 47 of 52

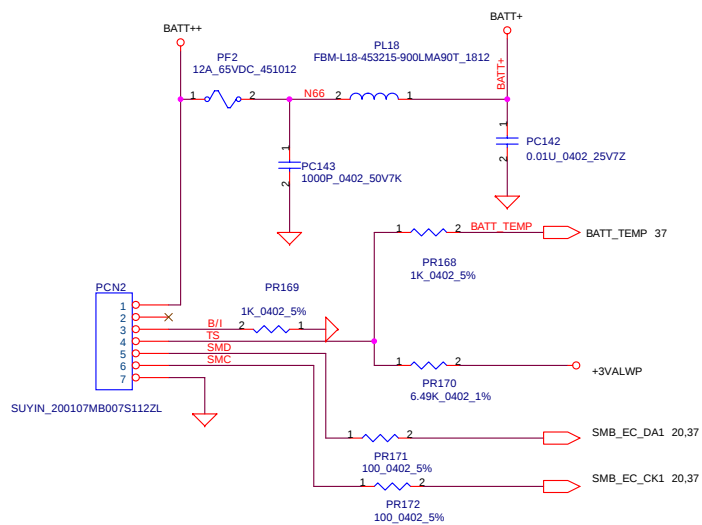
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

CPU CORE



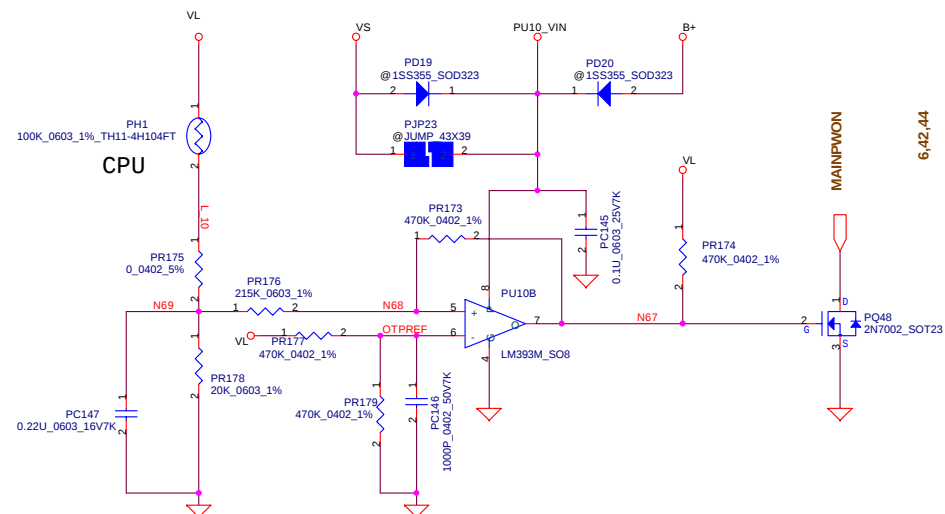
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
SCHEMATIC, M/BLA-2522			
Title	Size	Document Number	Rev
		401318	C
Date	星期五, 二月 18, 2005	Sheet	48 of 52



PJPB1 battery connector
SMART
Battery:
1.BAT+
2.ID
3.B/I
4.TS
5.SMD
6.SMC
7.GND

PH1 under CPU botten side :
CPU thermal protection at 90 +-3 degree C
Recovery at 50 +-3 degree C



Battery Connect/OTP

Compal Electronics, Inc.			
SCHEMATIC, M/B LA-2522			
Size	Document Number	Rev	
	401318	C	
Date:	星期五, 二月 18, 2005	Sheet	49 of 52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Version change list (P.I.R. List)

Item	Reason for change	Modify List	PG#	Rev.	Phase
1	Increase Q7 Vb voltage	Change R51 from S RES 1/16W 330 +-5% 0402 to S RES 1/16W 47K +-5% 0402	06	0.2	PP
2	ATi RS300MD pin REF27 required 3.3V level	Change R63 tie voltage from GND to +3VS	09	0.2	PP
3	DDR1 MA15 connection change for 1G Memory	Change DDR_AD_MA15 connection from JDIMM1 pin 97 to pin 123 Change DDR_BD_MA15 connection from JDIMM2 pin 97 to pin 123	13 14	0.2	PP
4	Clock generator VDDA should not be tied together with VDD48M/VDDXTAL	Change VDD48M/VDDXTAL tie voltage from +3VS_VDDA to +3VS_CLK	16	0.2	PP
5	Correct CPU frequency Auto-Select function	Change R345 connection sequence from R345->R346 to R346->R345 Change R343 connection sequence from R343->R344 to R344->R343	16	0.2	PP
6	Separate AGP_STOP# and AGP_STP# trace	Change R270 connection from AGP_STP# to AGP_STOP# Modify RS300MD pin AH30 net name AGP_STP# and connect to SB200 pin AC1	17	0.2	PP
7	Connect H_CPUSLP# to Pull-up +VCCP	Change R326 connection from H_SLP#to H_CPUSLP#	25	0.2	PP
8	Add USB Over Current Pull-up voltage	Add R547,R11,R10 S RES 1/16W 10K +-5% 0402 and connect to +3V Add R8,R9,R184 S RES 1/16W 10K +-5% 0402 and connect to +3V	25 26	0.2	PP
9	Unuse for External VGA Chip	Unpop R587 S RES 1/16W 4.7K +-5% 0402 and Q30 S TR MMBT3904 (S0T-23)	26	0.2	PP
10	Change EC strap Select to ENE KB910	Unpop R452 S RES 1/16W 10K +-5% 0402 Pop R451 S RES 1/16W 10K +-5% 0402	28	0.2	PP
11	JCDR1 change for ME requirement	Change JCDR1 from S H-CONN QTC 03A-0500F050-A510 H5.0 to S W-CONN ACES 87216-5002 50P P1	29	0.2	PP
12	Correct CIR on/off Function	Change JCDR1 pin 50 net name "CIR_ONOFF" and connect to U44 pin 109	29	0.2	PP
13	Remove RTL8100CL NA pin power	Add L1 S SUPPRE_ CHENG-HANN CHB1608U301 (0603) and Unpop	30	0.2	PP
14	J1394A1 change for ME requirement	Change J1394A1 from CONN SUYIN 020204FR004S506ZL 4P 1394 toCONN SUYIN 020115FR004S509ZL 4P P.8 1394	31	0.2	PP
15	Correct XDET0# pull-up Resistor	Change R331 from S RES 1/16W 0 +-5% 0402 to S RES 1/16W 33K +-5% 0402	32	0.2	PP
16	Modify WLAN Function	Change JMPCI1 pin 13 net name to "WLAN_OFF#" and connect to U44 pin 36 and JKS1 pin 3 Change JKS1 pin 2 net name to "WLAN_BTN#" and connect to U44 pin 90 Change JKS1 to S W-CONN ACES 85205-0400 4P P1.25	33 39 39	0.2	PP
17	Correct Buzzer Sound Small Function	UnPOP R203 S RES 1/16W 2.4K +-5% 0402 Change C661 S CER CAP 1U 10V Z Y5V 0603 to R2 S RES 1/16W 0 +5% 0603 Remove R200 S RES 1/16W 10K +-5% 0402	35	0.2	PP
18	Correct Codec ADI1981B function	Connect U15 pin5 to "ICH_AC_SDOUT" and link to SB200 Connect U15 pin8 to "ICH_AC_SDIN0" and link to SB200 Connect U15 pin2 to "CLK_14M_CODEC" and link to CLOCK Generator U29	35	0.2	PP
19	Correct Play music then plug in head phone AMP still occur sound	Change R160 from pull-down to pull-up and connect to +3VS Delete R409 and Connect JHP1 pin4 to GND	36 36	0.2	PP
20	Correct LED function	Change U44 pin24 net name to "TV_LED#" and connect to JSWP1 pin38 Change U44 pin162 net name to "DVD_LED#" and connect to JSWP1 pin43 Change JSWP1 pin34 net name to "PWR_LED#" and connect to U44 pin85	37	0.2	PP
21	Add BIOS Debug Solution	Add R603 S RES 1/16W 33 +-5% 0402 and connect to U29 pin3 Add net name to "CLK_14M_SIO" and connect from R603 to JDBUG2 pin6 Add R604 S RES 1/16W 33 +-5% 0402 and connect to U39 pin A17 Add net name to "CLK_PCI_SIO" and connect from R604 to JDBUG2 pin6 Change JDBUG2 to S H-CONN ACES 85201-2005 20P P1.0	16 16 25 25 38	0.2	PP

Compal Electronics, Inc.

Title

SCHEMATIC, M/B LA-2522

Size

Document Number

401318

Rev
C

Date:

星期五, 二月 18, 2005

Sheet

50

of 52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Version change list (P.I.R. List)

Item	Reason for change	Modify List	PG#	Rev.	Phase
22	Add Buzzer Disable Function	Add U52 S IC TC7SH08FU SSOP-5 AND Add Net "BEEP_OFF#" link to U44 pin 42	35	0.2	PP
23	Correct CUP Clock Auto Select Function	Delete R232 S RES 1/16W 1K +-5% 0402 and U18 S IC NC7SZ04P5X SC70-5 INVERTER Change Net "CPU_BSEL0_R" connection	7	0.2	PP
24	Correct LAN function Error	Delete C539,C541,C542 S CER CAP .1U 16V Z Y5V 0402	30	0.3	MP1
25	Correct CIR auto power on problem	Add R613 S RES 1/16W 10K +-5% 0402 for CIR_ONOFF# pull-up to +3VLAW	37	0.3	MP1
26	Correct Buzzer sound too small issue	Delete C449, C453, C456 S CER CAP 1U 10V Z Y5V 0603 R2 S RES 1/16W 0 +5% 0603	35	0.3	MP1
27	Modify Speaker POP noise	Delete R612 S RES 1/16W 0 +-5% 0402 C858 S CER CAP .1U 16V Z Y5V 0402 Add U58 S RELAY 5V TTI TRR2A05S00	36	0.3	MP1
28	Delete Audio MX3000 function	Delete U40 S IC MX3000AS SO-16 BASS EXTENSION R450, R487, R494, C714, R461, C739, Y5, C740, C750, C738, C777, C778, C744, C758, 761 C787, R534, R541, C783, C744, R175, C745, R166, R446 C788, R535, R542, C784, C775, R174, C746, R167, R447	36	0.3	MP1
29	Increase LED brightness	Change JLCD1 pin41 net from +3VALW to +5VALW Add Q41,Q42,Q43 S TR A03401 1P SOT-23 Change JSWP1 pin17,47 net from +3VS to +5VS Change JSWP1 pin29,30,59,60 net from +3VALW to +5VALW Add Q44,Q45,Q46 S TR A03401 1P SOT-23	23 39	0.3	MP1
30	CIR Power Saving when LID SW On	Add Q40 S TR 2N7002 1N SOT-23 C862 S CER CAP .1U 10V +-10% X5R 0402	29	0.3	MP1
31	Modify SD WP function Issue	Add Q47 S TR 2N7002 1N SOT-23 R616 S RES 1/16W 100K +-5% 0402	32	0.3	MP1
32	Modify XD Power Off Sequence Issue	UnPop C860 S CER CAP .1U 25V K X7R 0603	32	0.3	MP1
33	S5 South Bridge Power Saving	Delete Q48 S TR A03401 1P SOT-23 Add U31 S IC G914E SOT23-5 2.5V LDO REG.	27	1.0	IRT1
34	Modify SD Write Protect Issue	UnPop Q47,Q52 S TR 2N7002 1N SOT-23 and U60 S IC TC7SH32FU SSOP-5 0R	32	1.0	IRT1
35	Reduce Reed Relay Noise	Add Q53 S TR 2N7002 1N SOT-23	36	1.0	IRT1
36	Reduce LAN EMI	Add C866 SE095104K00 S CER CAP .1U 10V +-10% X5R 0402	30	1A	MP_1A
37	Change XD Power to +SD_VCC	Add R646 SD0020000T8 S RES 1/10W 0 +5% 0805	32	1A	MP_1A
38	Remove HP POP noise	Del R464 SD028000010 S RES A34 1/16W 0 +-5% 0402 Add R478 SD028000010 S RES A34 1/16W 0 +-5% 0402	36	1A	MP_1A
39	Add LAN +2.5V Stable	Add C867,C868 SE095104K00 S CER CAP .1U 10V +-10% X5R 0402	30	1A	MP_1A
40	Modify USB Current Limit Voltage Back issue Modify USB Current Limit Voltage Remain issue	Add R648 SD028000010 S RES A34 1/16W 0 +-5% 0402, link to SUSP for U32 R647 SD028000010 S RES A34 1/16W 0 +-5% 0402, link to SUSP for +USB_AS Q66 SB570020000 S TR 2N7002 1N SOT-23, link to SUSP for +USB_AS Add R650 SD028000010 S RES A34 1/16W 0 +-5% 0402, link to SUSP for U2 R649 SD028000010 S RES A34 1/16W 0 +-5% 0402, link to SUSP for +USB_BS Q67 SB570020000 S TR 2N7002 1N SOT-23, link to SUSP for +USB_BS Add R652 SD028000010 S RES A34 1/16W 0 +-5% 0402, link to SUSP for U2 R651 SD028000010 S RES A34 1/16W 0 +-5% 0402, link to SUSP for +USB_CS Q68 SB570020000 S TR 2N7002 1N SOT-23, link to SUSP for +USB_CS	40	1A	MP_1A
41	Modify +3V Voltage Remain issue	Change SYS0N# link from +12V to +5VALW	41	1A	MP_1A
42	LAN Immunity Issue	Del NET (LAN_TX2+, LAN_TX2-) and (LAN_TX3+, LAN_TX3-) Del NET (RJ45_TX2+, RJ45_TX2-) and (RJ45_TX3+, RJ45_TX3-) Add R653 link to JLAN1 pin4,pin5 & Add R654 link to JLAN1 pin7,pin8	30	2.0	MP

Compal Electronics, Inc.

Title

SCHEMATIC, M/B LA-2522

Size

Document Number

401318

Rev

C

Date:

星期五, 二月 18, 2005

Sheet

51

of 52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.