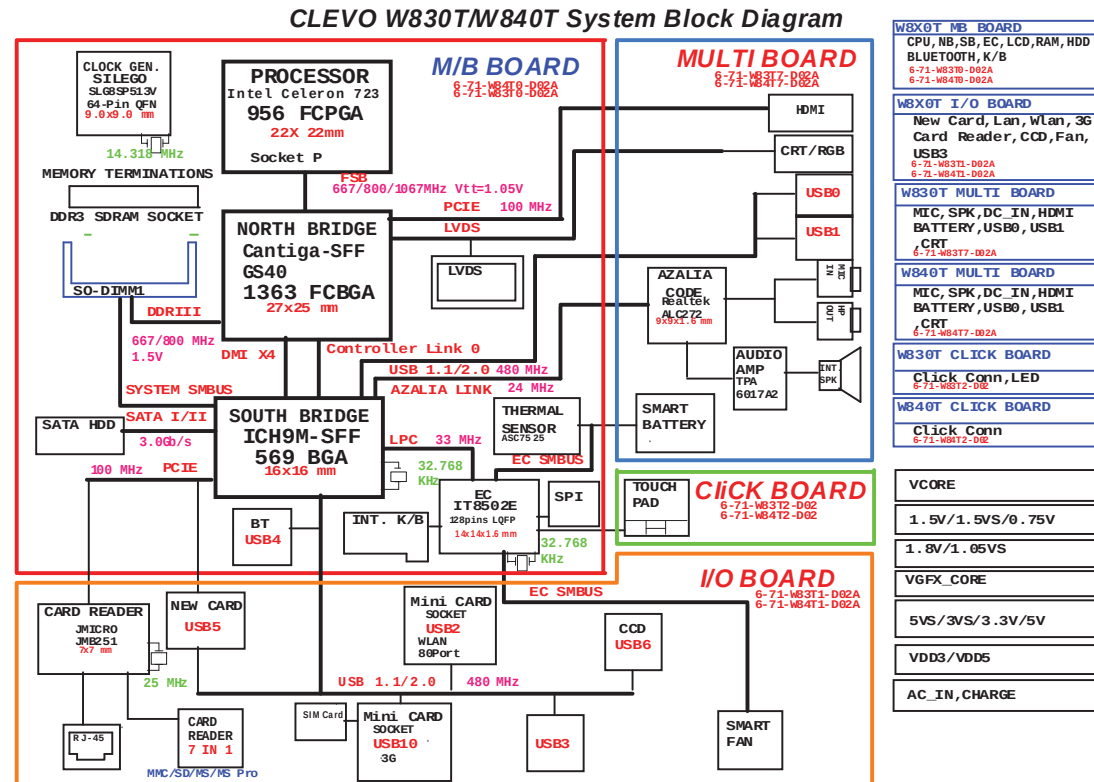


System Block Diagram

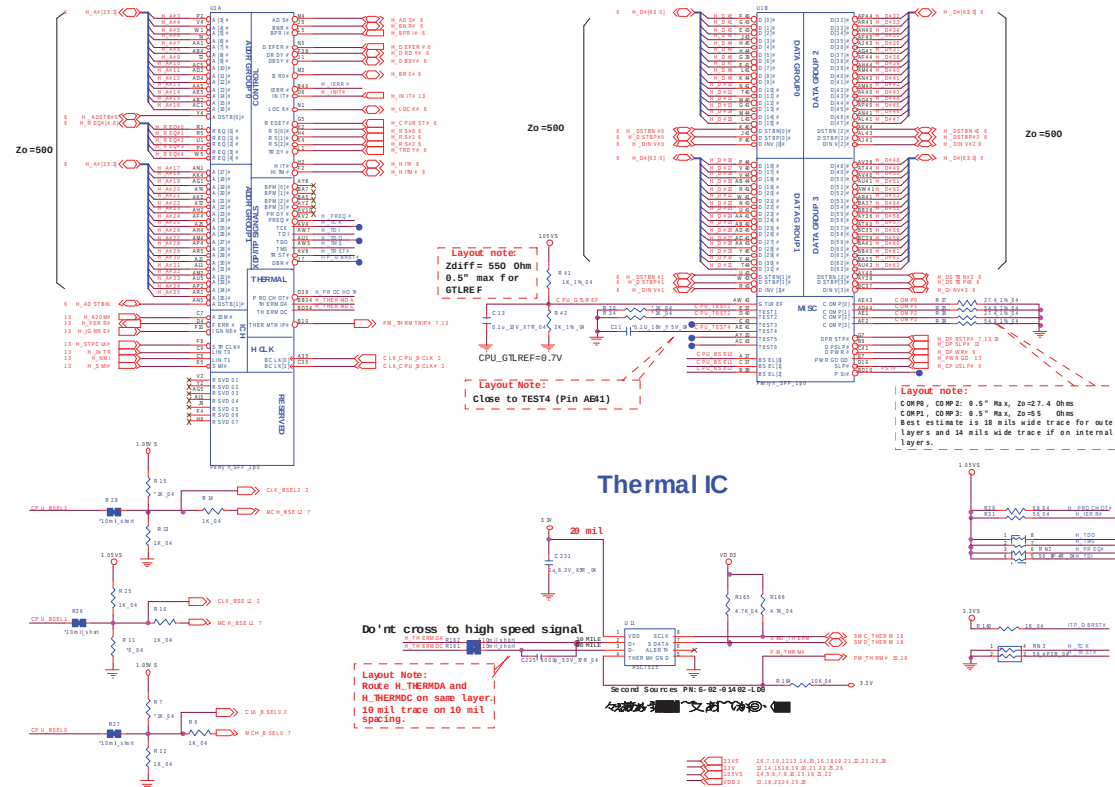
Sheet 1 of 46
System Block
Diagram



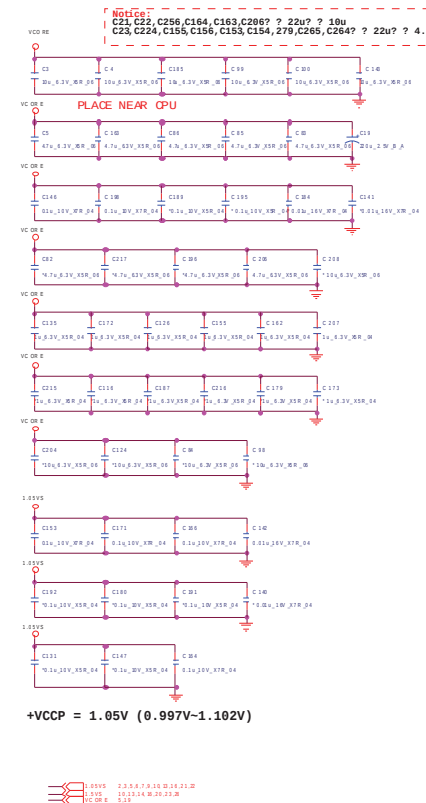
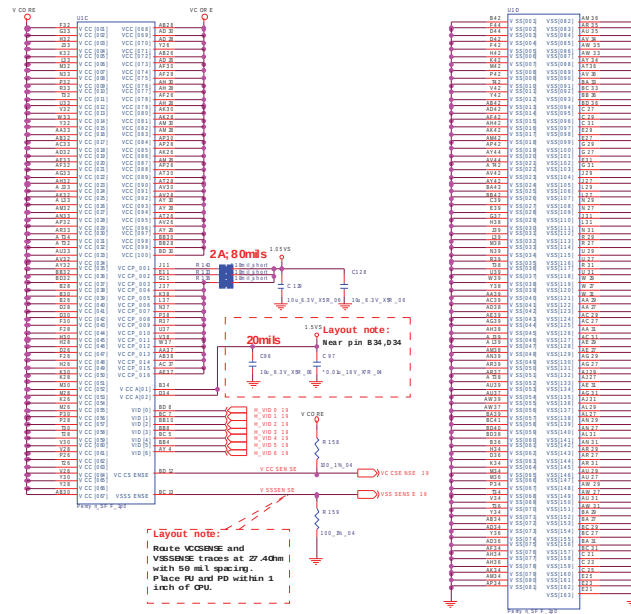
Schematic Diagrams

Penryn SFF 1/3

Sheet 3 of 46
Penryn SFF 1/3



Penryn SFF 2/3

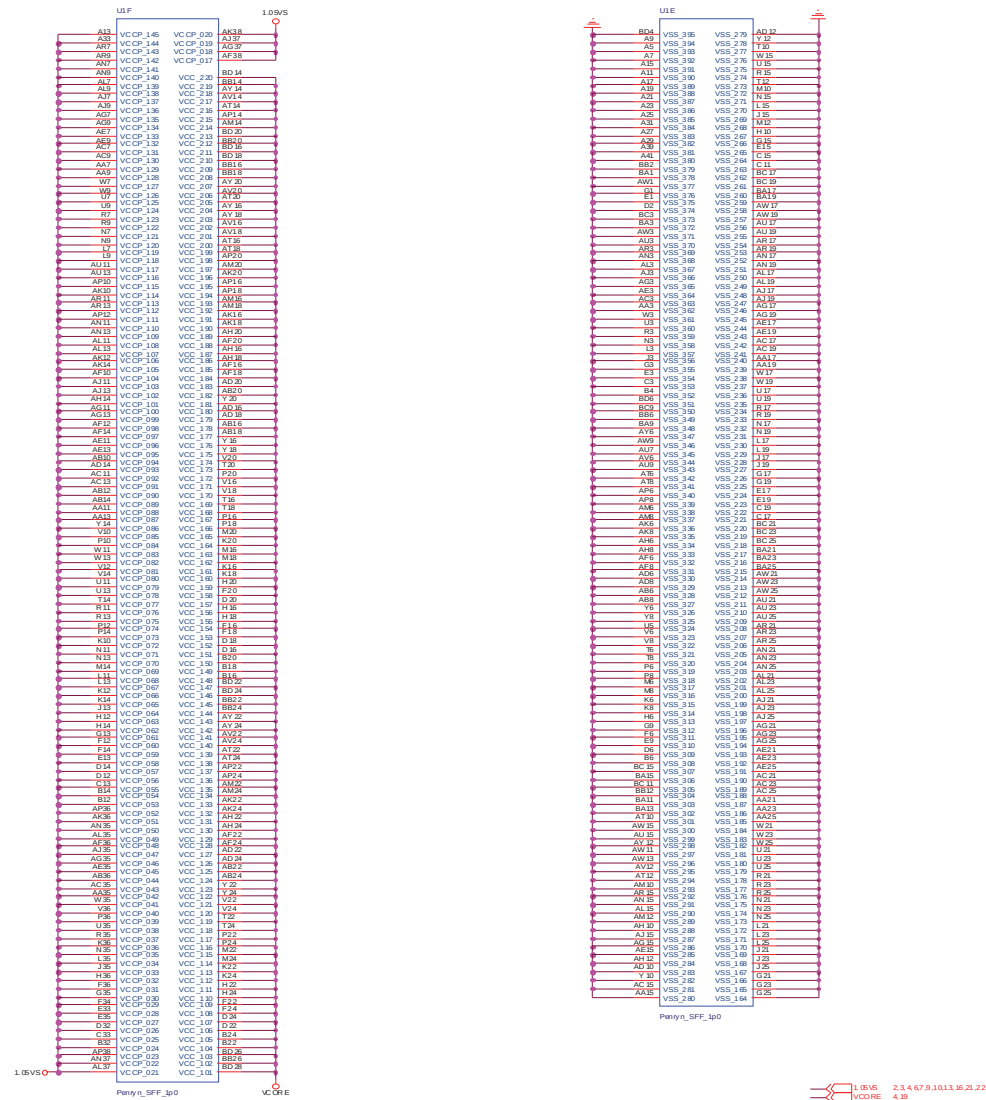


Sheet 4 of 46
Penryn SFF 2/3

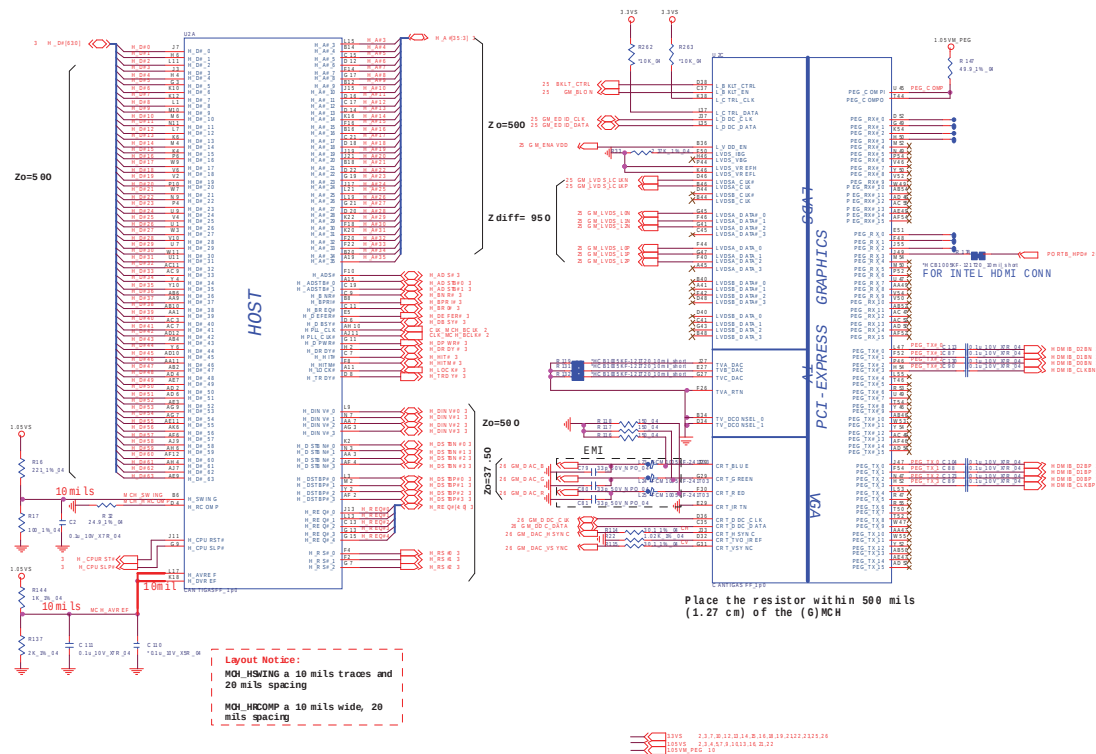
B.Schematic Diagrams

Penryn SFF 3/3

Sheet 5 of 46
Penryn SFF 3/3



Cantiga SFF 1/6, PEG

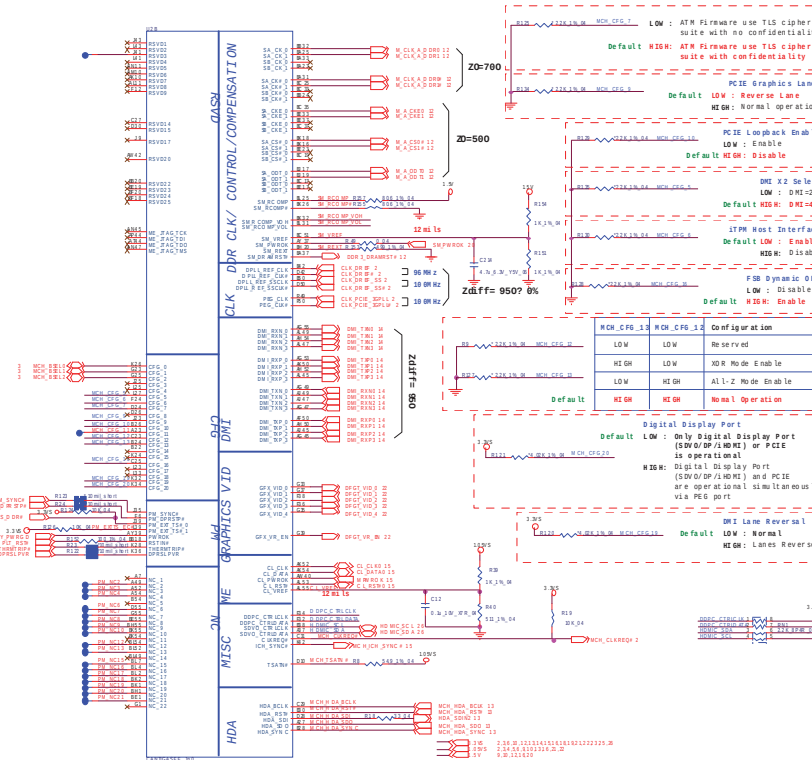
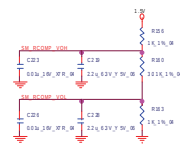


Sheet 6 of 46
 Cantiga SFF 1/6,
 PEG

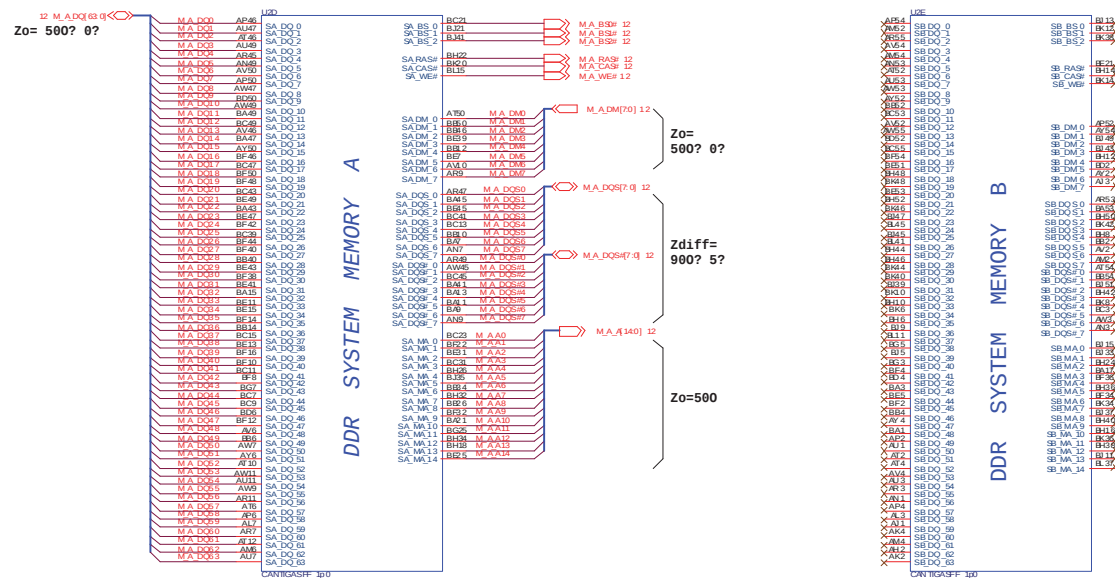
Cantiga SFF 2/6, CLK

Sheet 7 of 46
Cantiga SFF 2/6,
CLK

Pin Name	Signal Description	Configuration
CF02.0	RSBI agency	000 = 3000 001 = 6000 011 = 9000 Others = Reserved
CF02.3	Reserved	
CF05	Chassis Detect	Low = 750mV High = 900mV (default)
CF06	Interrupt/Trusted Platform Module/TPM Host Interface	Low = TPM Host Interface is enabled High = Disabled (default)
CF07	Trust Management Engine/Chipset Management Layer Security (CLS) engine	Low = Trust Management Engine/Chipset Management Layer Security (CLS) engine is enabled High = Trust Management Engine/Chipset Management Layer Security (CLS) engine is disabled (default)
CF08	Reserved	
CF09	PCIe Graphics Lane	Low = Reverse Lane, 20=0, 14=100 High = Normal operation (default) Lane Name bit is in CF09
CF10	PCIe Linkback enable	Low = Enabled High = Disabled (default)
CF11	Reserved	
CF12	ALLZ	Low = ALLZ mode enabled High = Disabled (default)
CF13	XOR	Low = XOR mode enabled High = Disabled (default)
CF13.14	Reserved	
CF14	PCIe Dynamic CLK	Low = Dynamic CLK enabled High = Dynamic CLK disabled (default)
CF14.17	Reserved	
CF15	DMI Lane Reverse	Low = Normal operation (Default) High = Lane Reversed (Default) Lane Reversed bit is in CF15
CF16	Digital Display Port (DDP) and PCIe Concurrent with PCIe	Low = Only digital display port (DDP) operates as PCIe is operational (Default) High = Digital display port (DDP) and PCIe operate simultaneously via the PCIe port



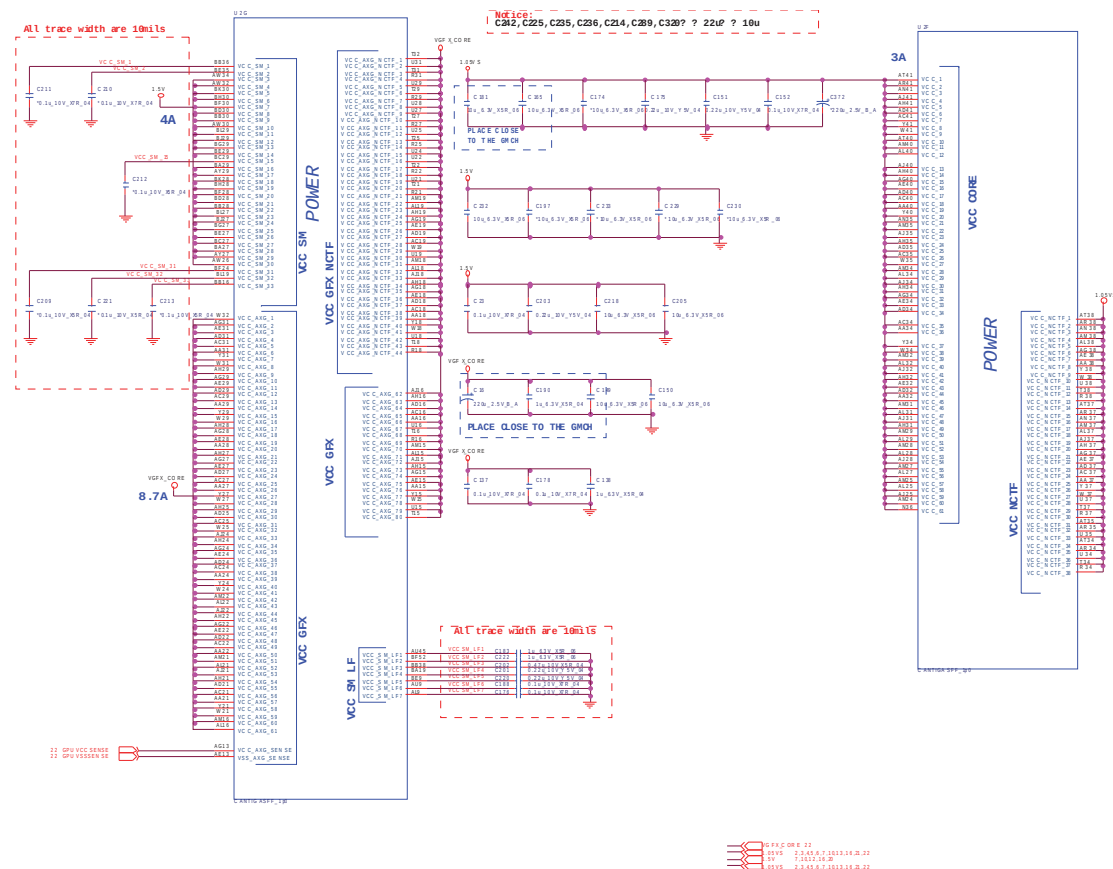
Cantiga SFF 3/6, DDR



Sheet 8 of 46
Cantiga SFF 3/6,
DDR

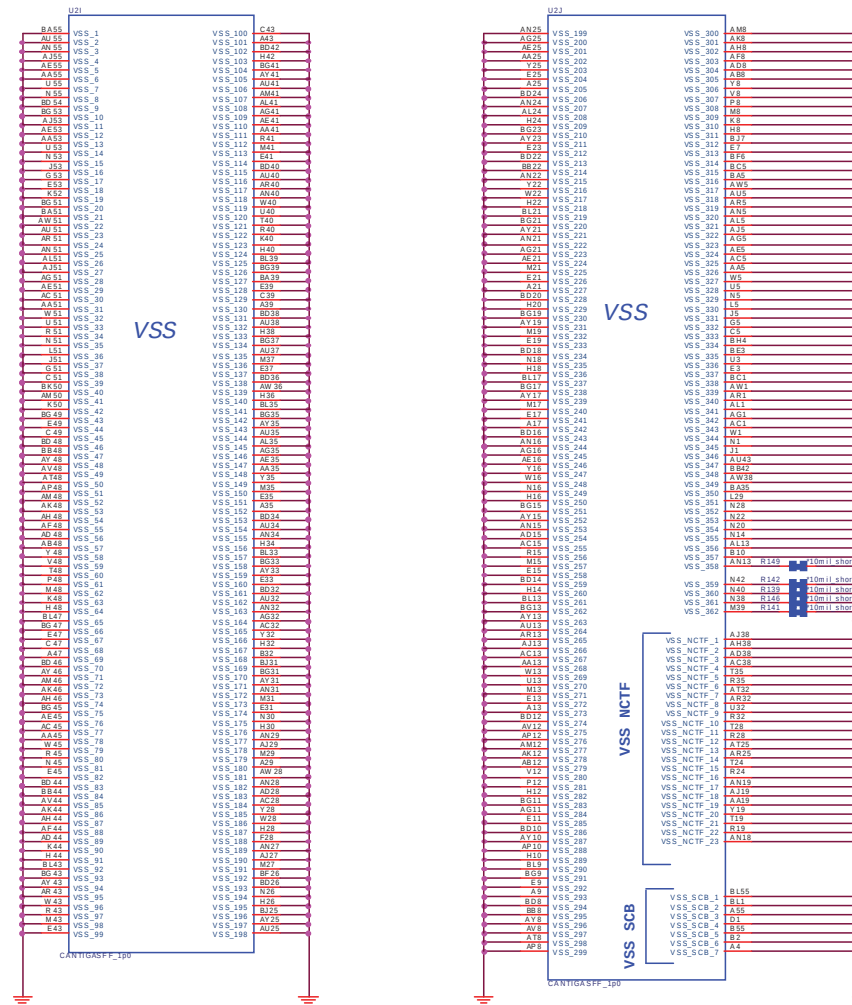
Cantiga SFF 4/6, Power 1

Sheet 9 of 46
Cantiga SFF 4/6,
Power 1



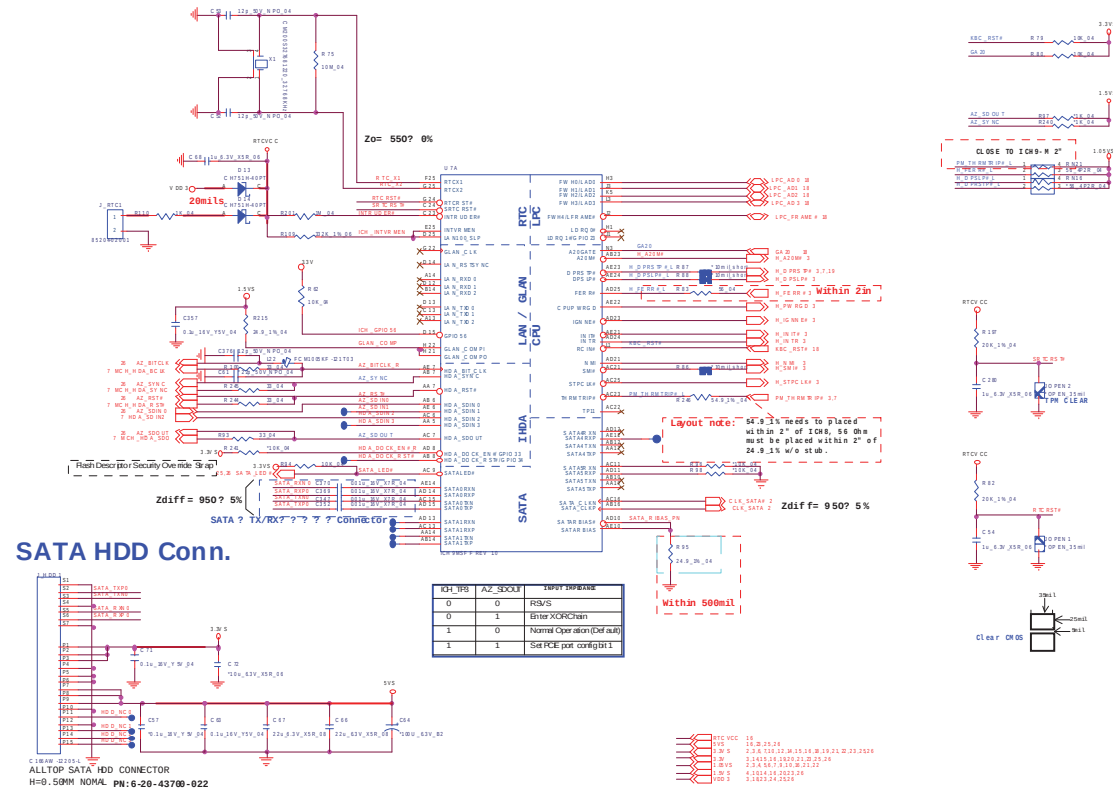
Cantiga SFF 6/6, GND

Sheet 11 of 46
Cantiga SFF 6/6,
GND

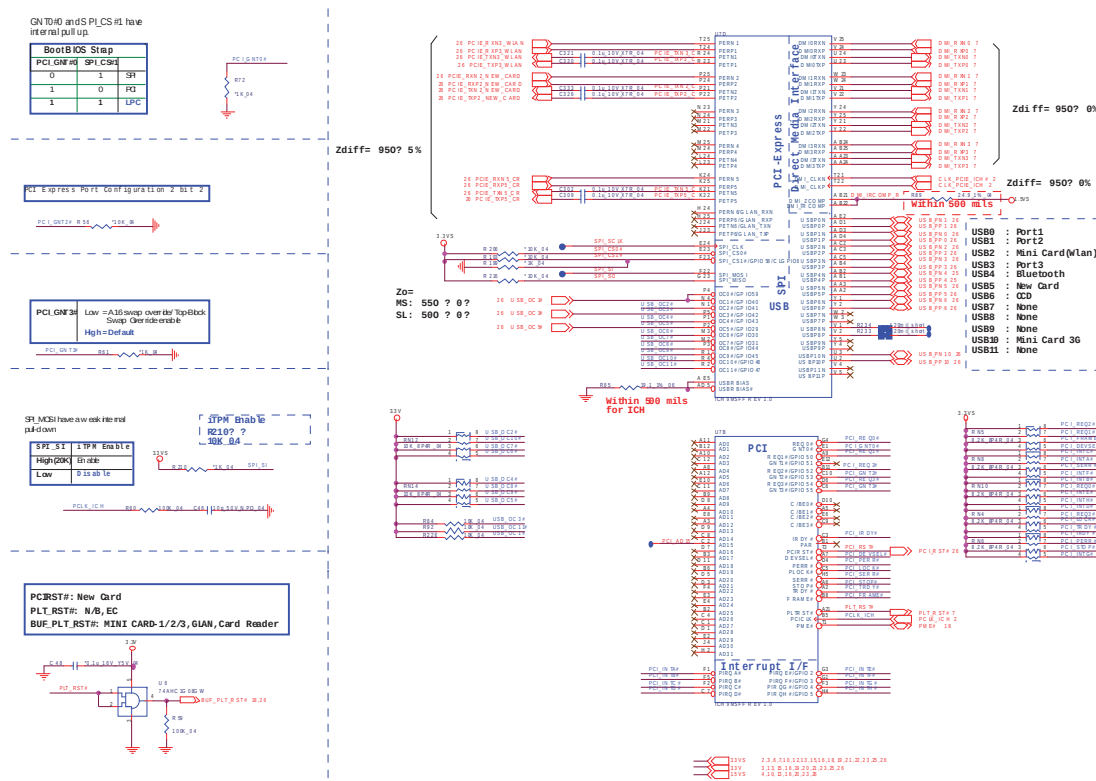


ICH9-M SFF 1/5, SATA, HDD CON

Sheet 13 of 46
ICH9-M SFF 1/5,
SATA, HDD CON



ICH9-M SFF 2/5, PCI, USB

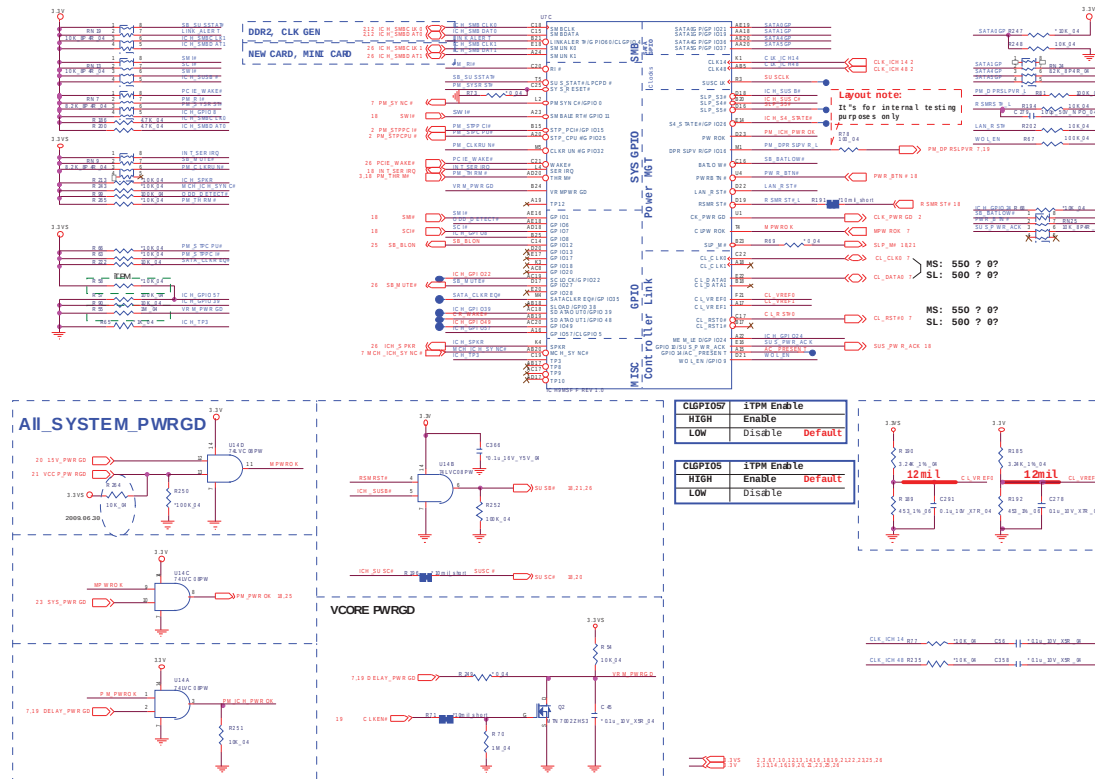


Sheet 14 of 46
ICH9-M SFF 2/5,
PCI, USB

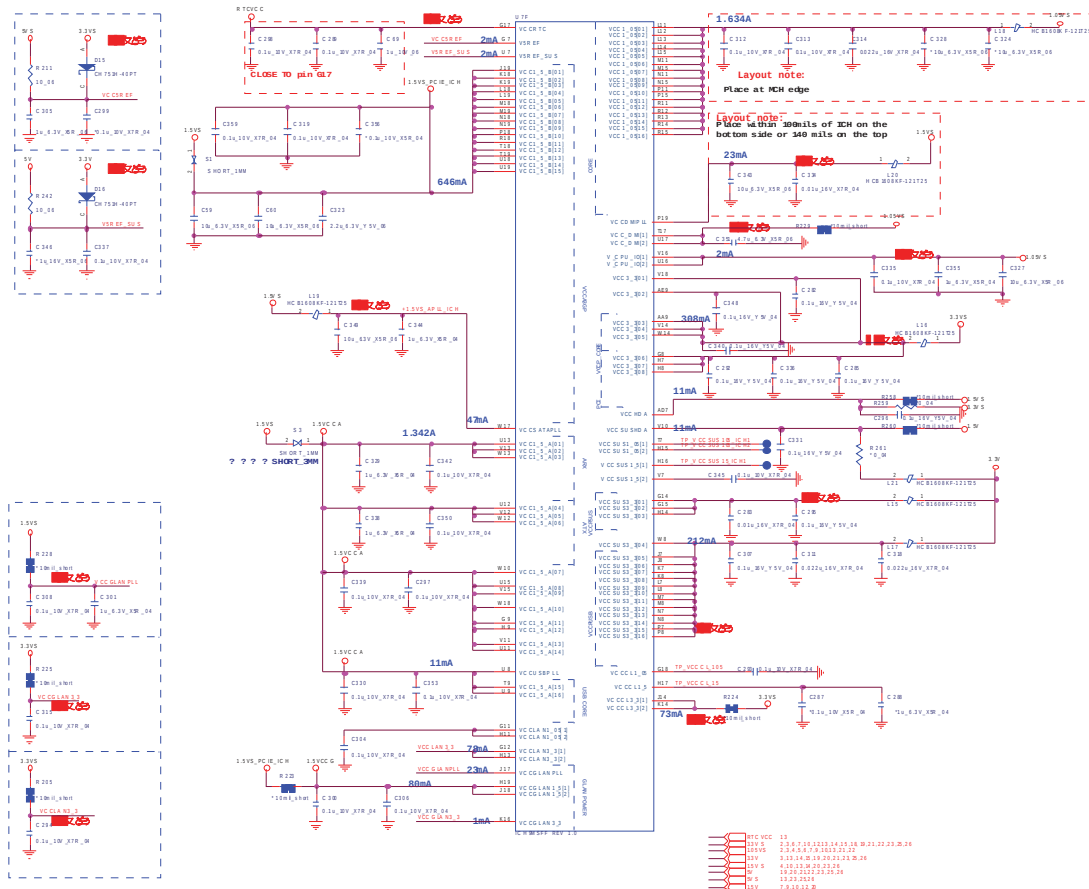
Schematic Diagrams

ICH9-M SFF 3/5, GPIO

Sheet 15 of 46
ICH9-M SFF 3/5,
GPIO



ICH9-M SFF 4/5, Power

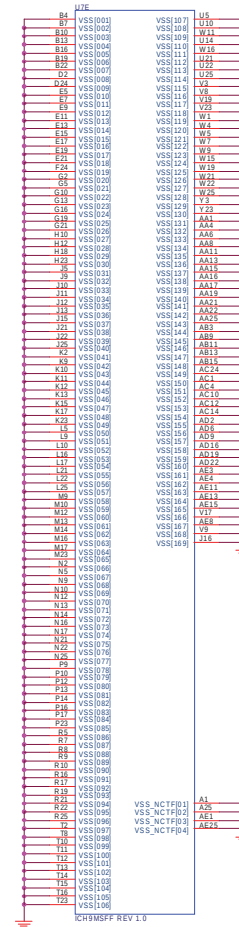


Sheet 16 of 46
ICH9-M SFF 4/5,
Power

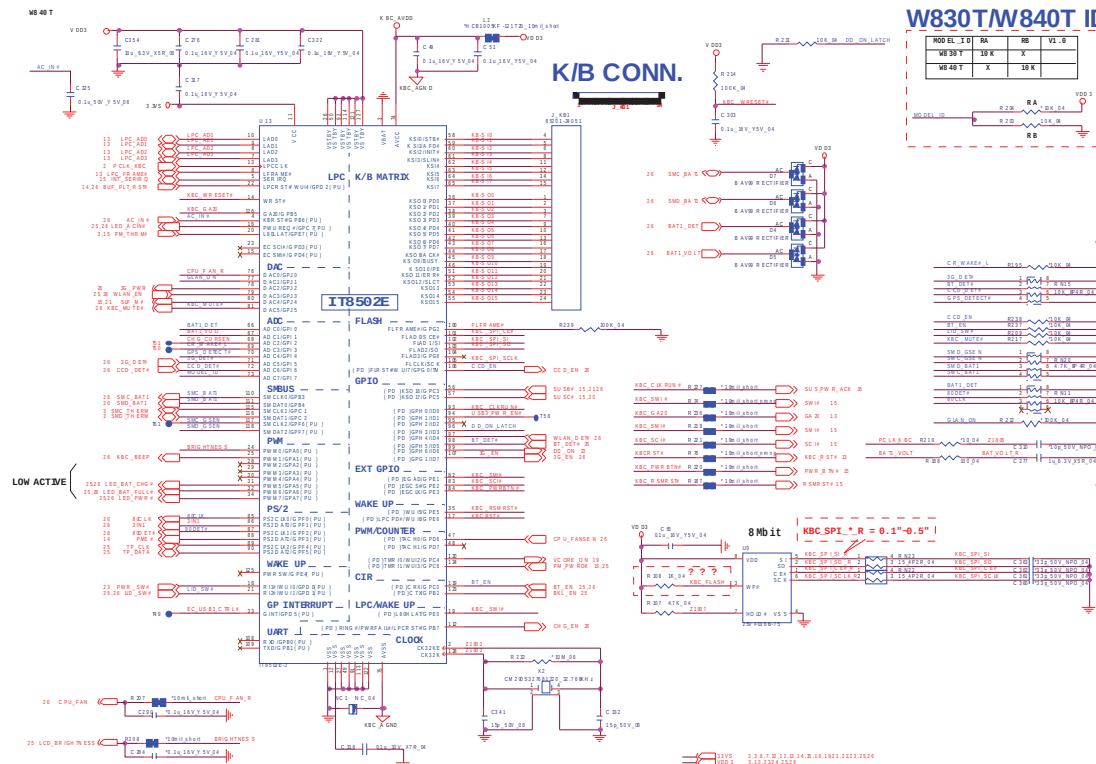
Schematic Diagrams

ICH9-M SFF 5/5, GND

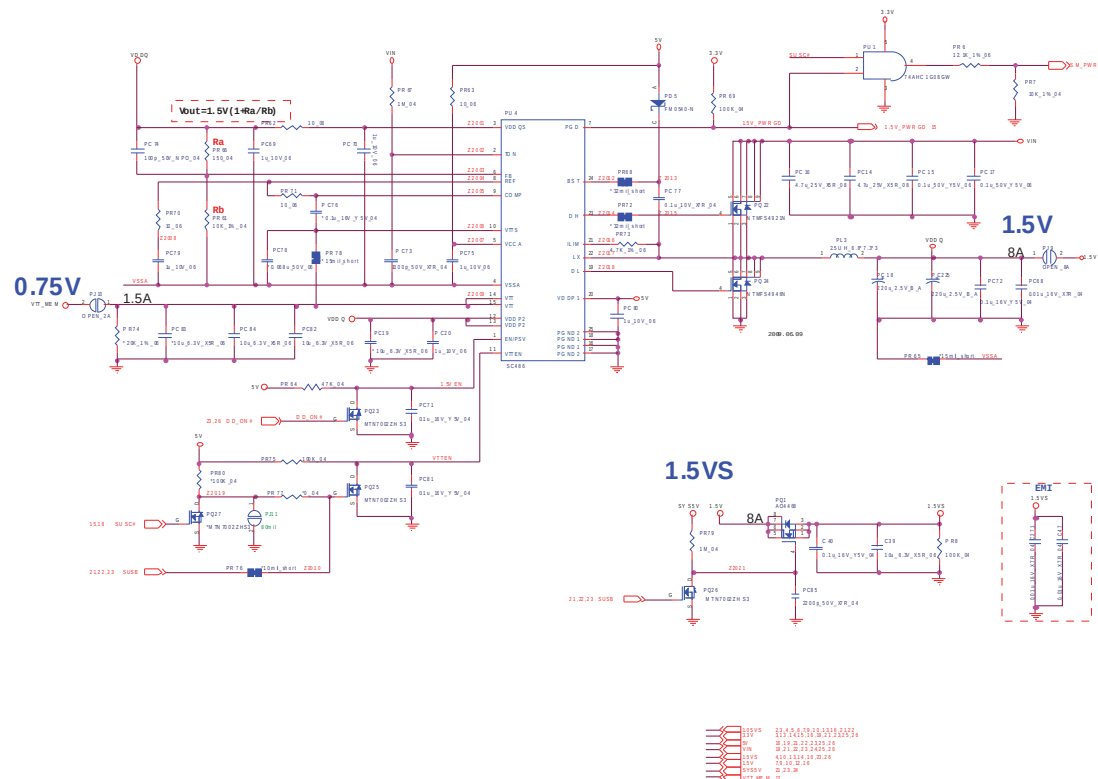
Sheet 17 of 46
ICH9-M SFF 5/5,
GND



EC KBC-ITE IT8502E

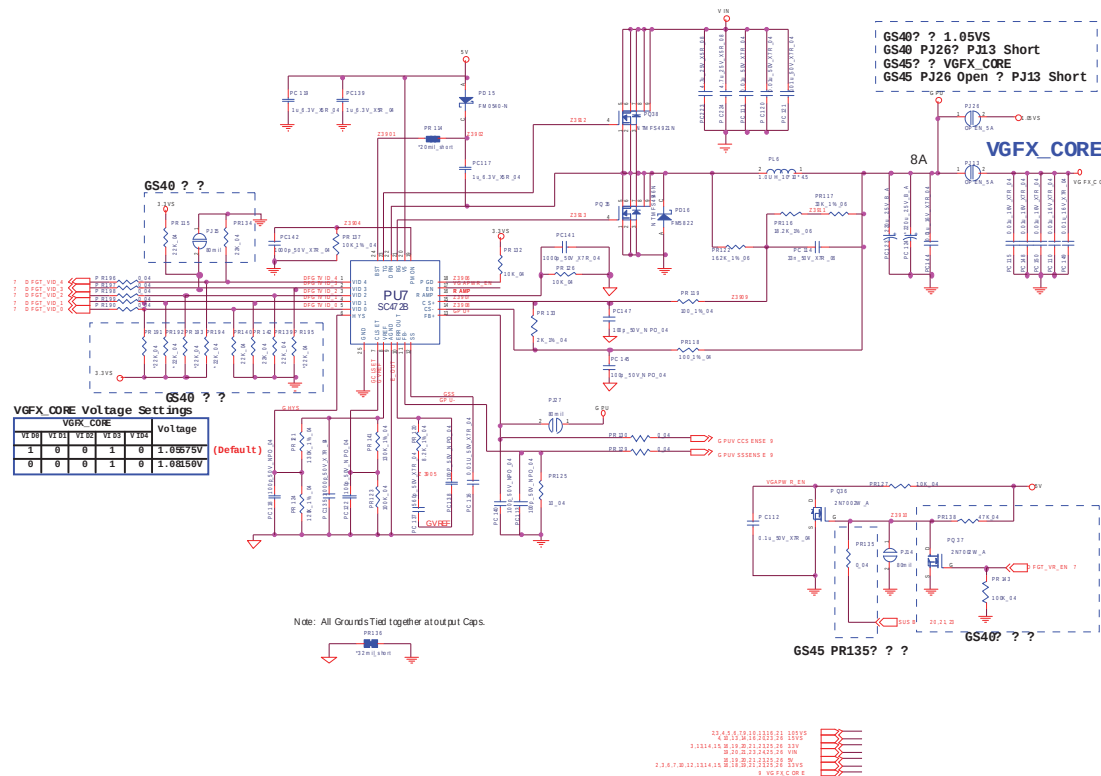


Sheet 18 of 46
EC KBC-ITE
IT8502E



Sheet 20 of 46
Power 1.5V/0.75V/
1.5VS

Power VGA GPU (VGFX_Core)

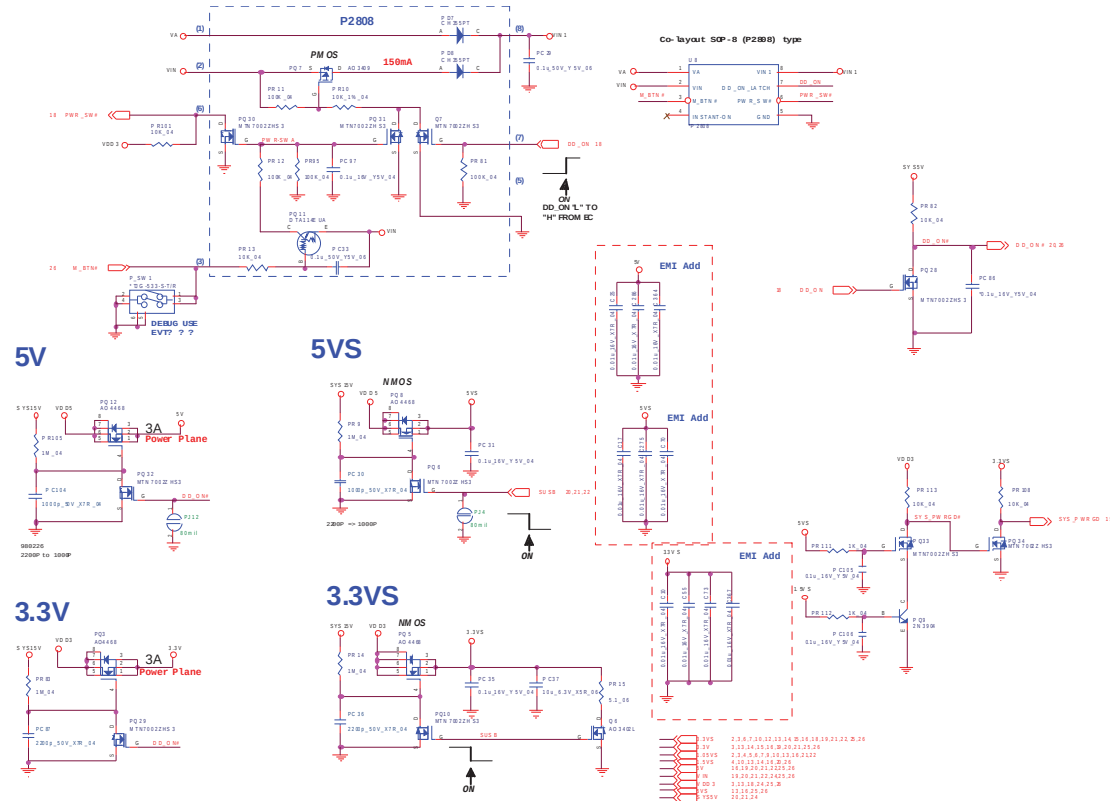


Sheet 22 of 46
Power VGA GPU
(VGFX_Core)

Schematic Diagrams

Power 5VS, 3VS, 3.3V, 5V

Sheet 23 of 46
Power 5VS, 3VS,
3.3V, 5V

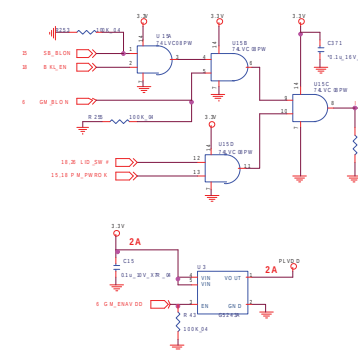
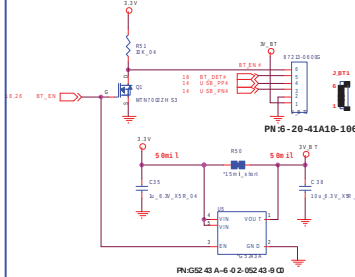


Schematic Diagrams

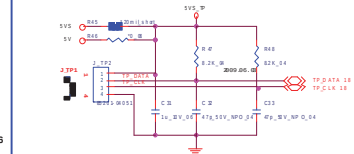
LCD/CCD/Click/BT Conn

Sheet 25 of 46
LCD/CCD/Click/
BT Conn

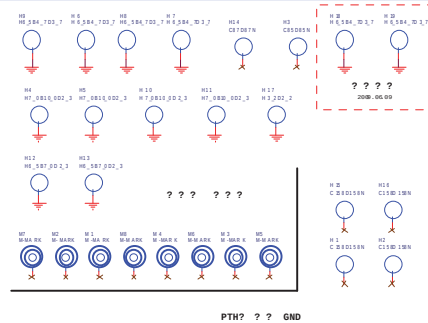
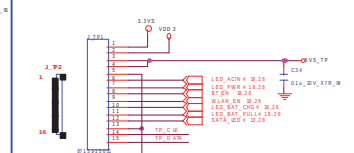
LCD PANEL BACKLIGHT

Bluetooth CONN
(USB Port4)

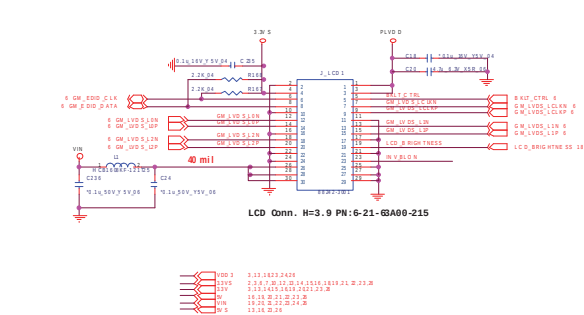
W840T CLICK CONN



W830T CLICK/LED CONN

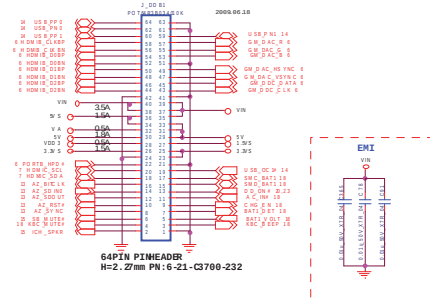


LCD PANEL CONN

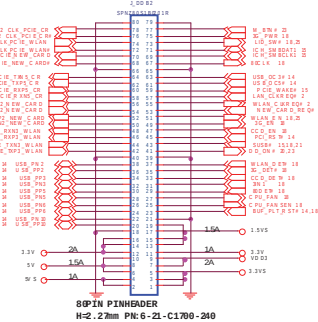


To Multi, I/O Conn, W840T LED

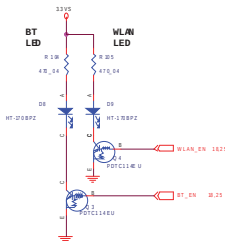
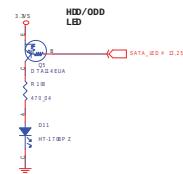
MULTI Board CONN.



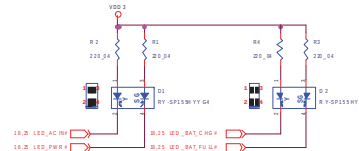
I/O Board CONN.



W840T LED
(W830T LED? ? ?)



POWER ON LED BAT CHARGER LED



3.3VS 2.3A7.10.2.13.15.15.1618.1821.2223.25
3.3VS 2.3A7.10.2.13.15.15.1618.1821.2223.25
3.3VS 2.3A7.10.2.13.15.15.1618.1821.2223.25
3.3VS 2.3A7.10.2.13.15.15.1618.1821.2223.25
3.3VS 2.3A7.10.2.13.15.15.1618.1821.2223.25
3.3VS 2.3A7.10.2.13.15.15.1618.1821.2223.25

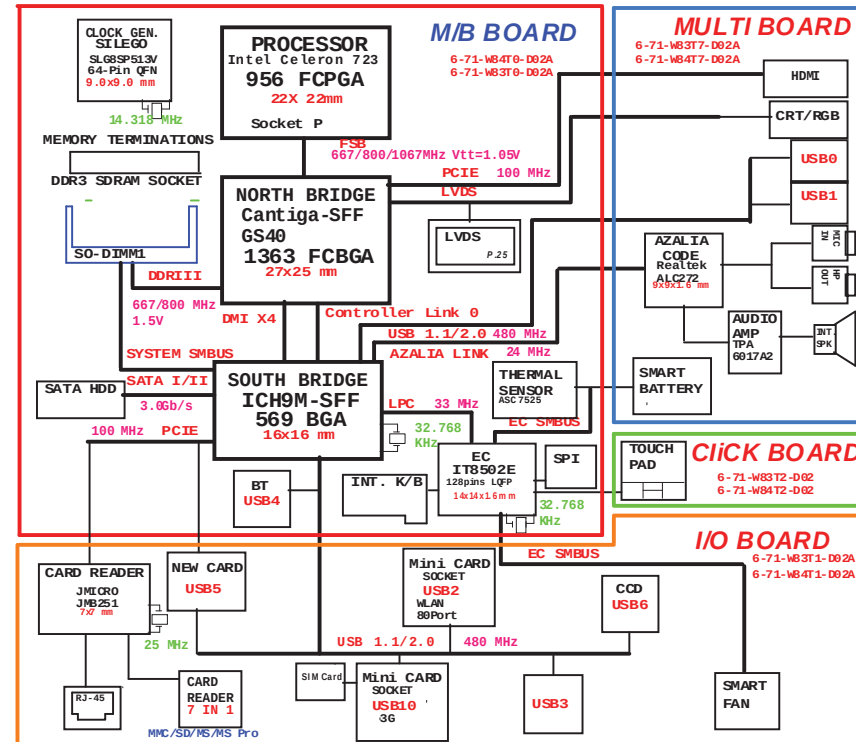
Sheet 26 of 46
To Multi, I/O Conn,
W840T LED

System Block Diagram (W830T)

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System Block
Diagram (W830T)

6-71-W83T1-D02A
6-71-W83T2-D02A

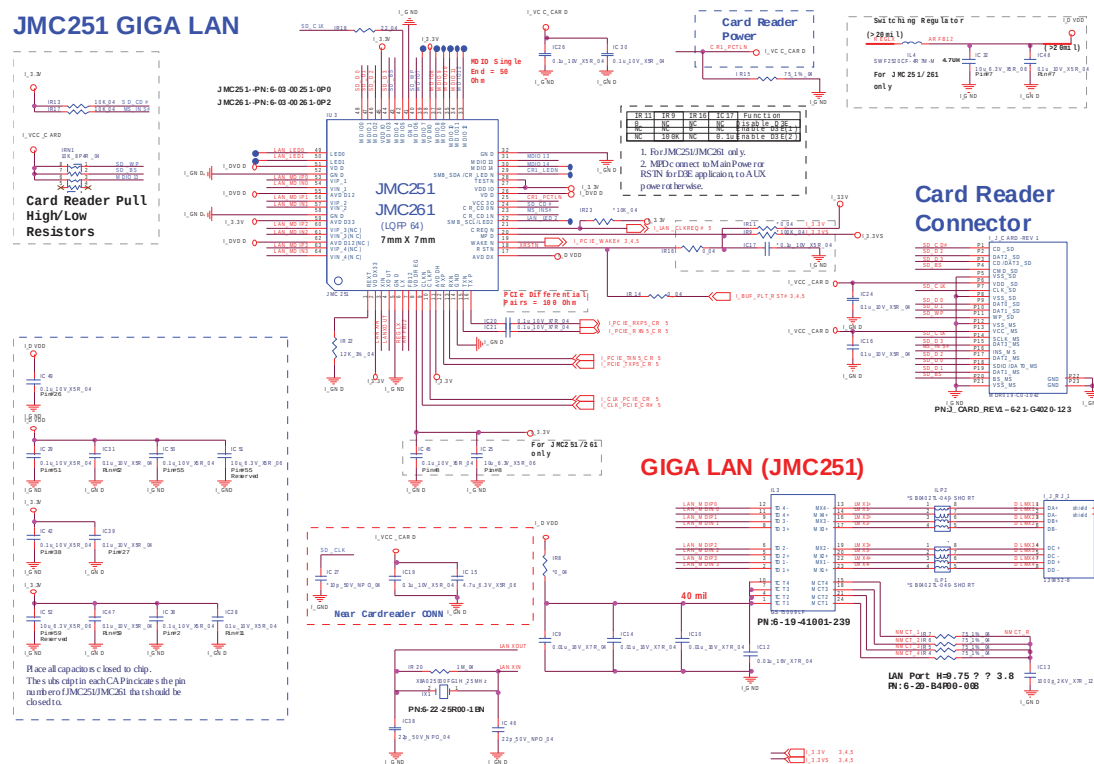
CLEVO W830T System Block Diagram



W830T MB BOARD
CPU, NB, SB, EC, LCD, RAM, HDD
BLUETOOTH, K/B
6-71-W83T1-D02A
6-71-W83T2-D02A
W830T I/O BOARD
New Card, Lan, Wlan, 3G,
Card Reader, CCD, Fan,
USB3
6-71-W83T1-D02A
6-71-W83T2-D02A
W830T MULTI BOARD
MIC, SPK, DC IN, HDMI
BATTERY, USB0, USB1
CRT
6-71-W83T1-D02A
W840T MULTI BOARD
MIC, SPK, DC IN, HDMI
BATTERY, USB0, USB1
CRT
6-71-W83T1-D02A
W830T CLICK BOARD
Click Conn, LED
6-71-W83T2-D02
W840T CLICK BOARD
Click Conn
6-71-W83T2-D02
VCORE
1.5V/1.5VS/0.75V
1.8V/1.05VS
VGF_X_CORE
5VS/3VS/3.3V/5V
VDD3/VDD5
AC_IN, CHARGE

W830T I/O (JMC251)

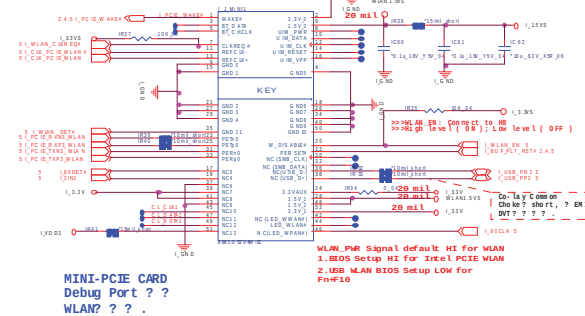
Sheet 28 of 46
W830T I/O
(JMC251)



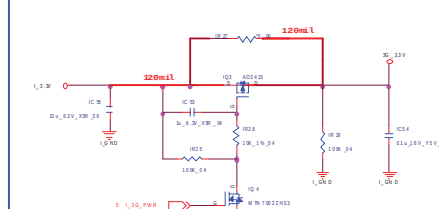
W830T I/O (CCD, MINI, WLAN/3G)

Sheet 29 of 46
W830T I/O (CCD,
MINI, WLAN/3G)

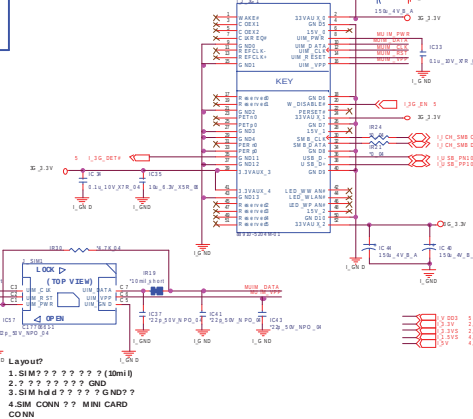
MINI-PCIE CARD WLAN (USB Port2)



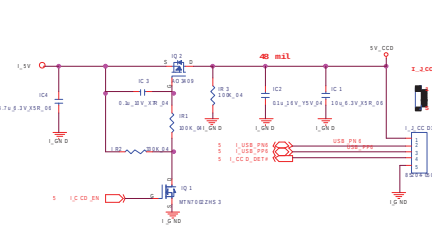
3G POWER



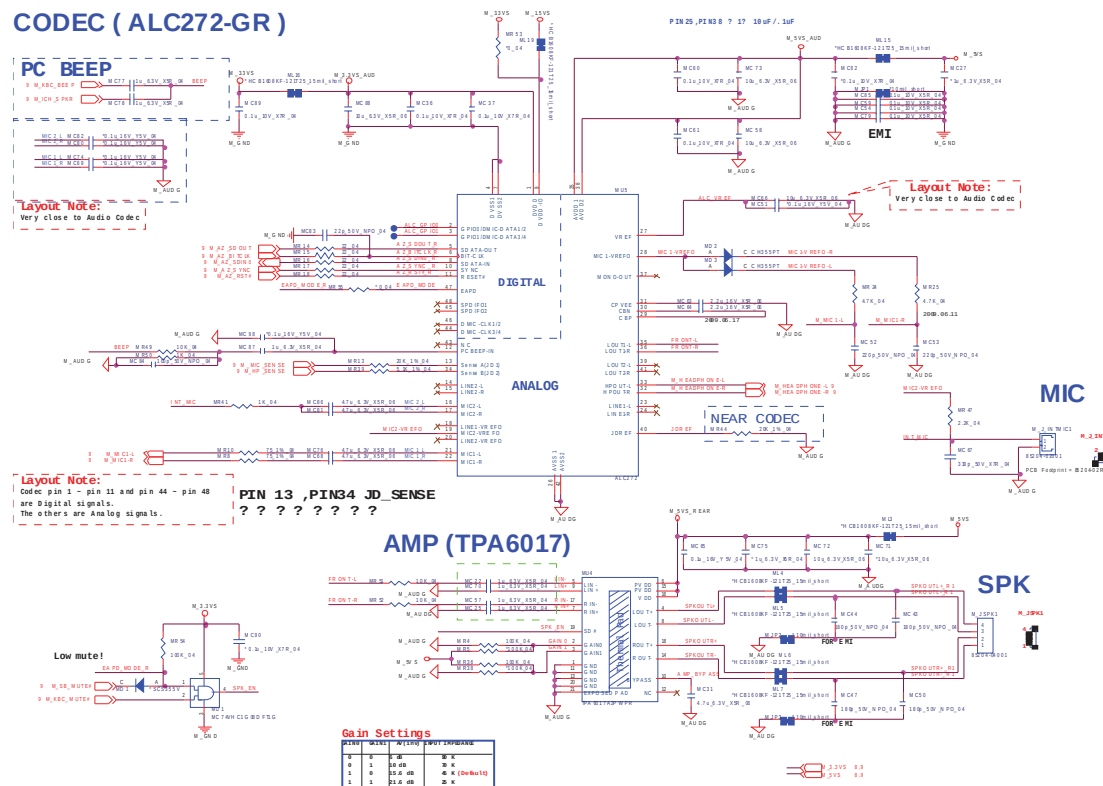
MINI-PCIE CARD 3G (USB Port10)



CCD (USB Port6)



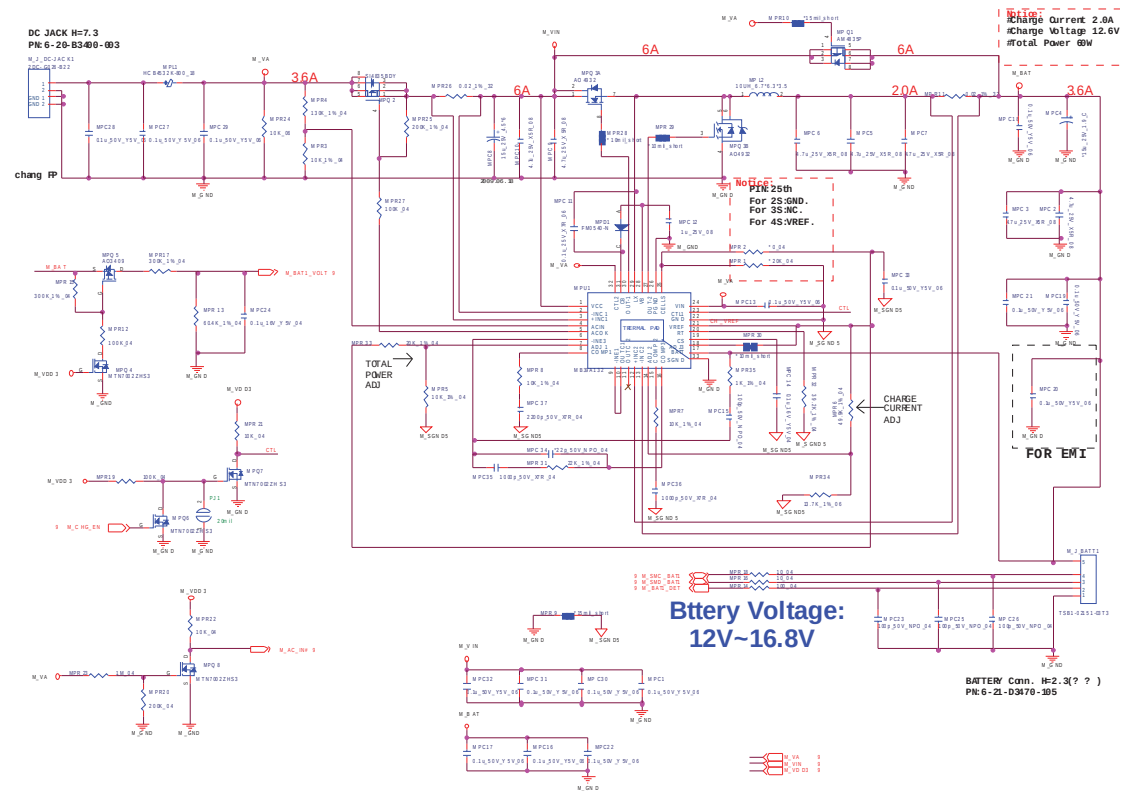
W830T Multi (Audio ALC272)



Sheet 32 of 46
W830T Multi
(Audio ALC272)

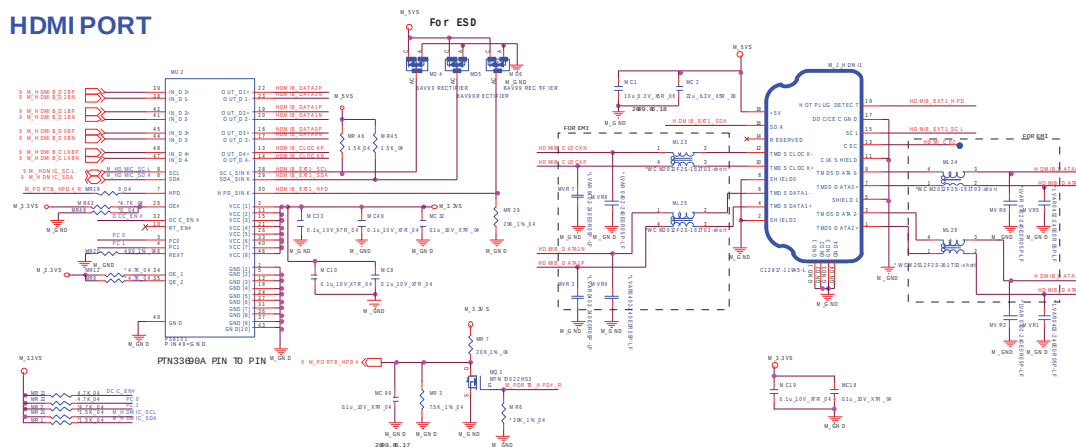
W830T Multi (AC_In, Charger)

Sheet 33 of 46
W830T Multi
(AC_In, Charger)

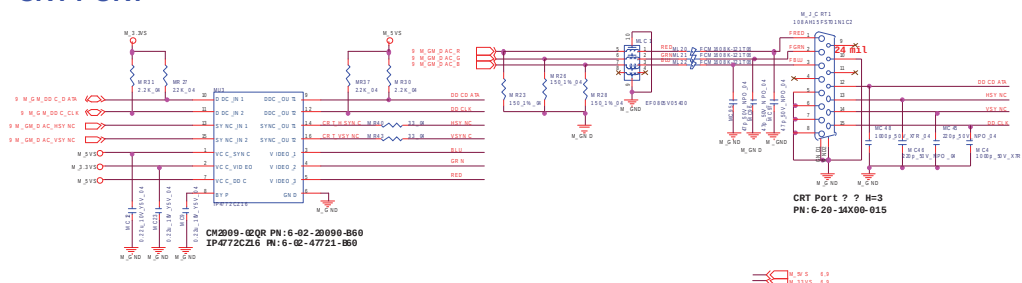


W830T Multi (HDMI/CRT Port)

HDMI PORT



CRT PORT



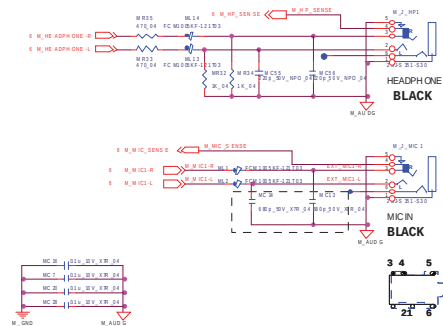
Sheet 34 of 46
W830T Multi (HDMI/
CRT Port)

Schematic Diagrams

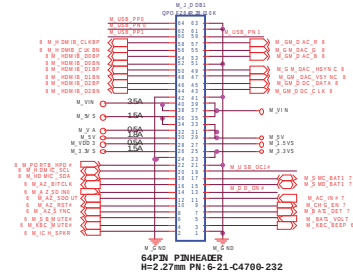
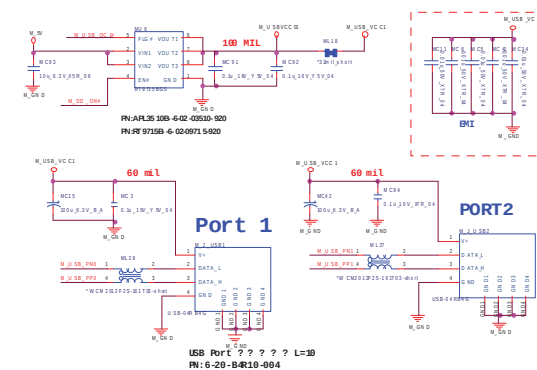
W830T to CPU Conn (Jack, USB)

Sheet 35 of 46
W830T to CPU
Conn (Jack, USB)

PHONE JACK

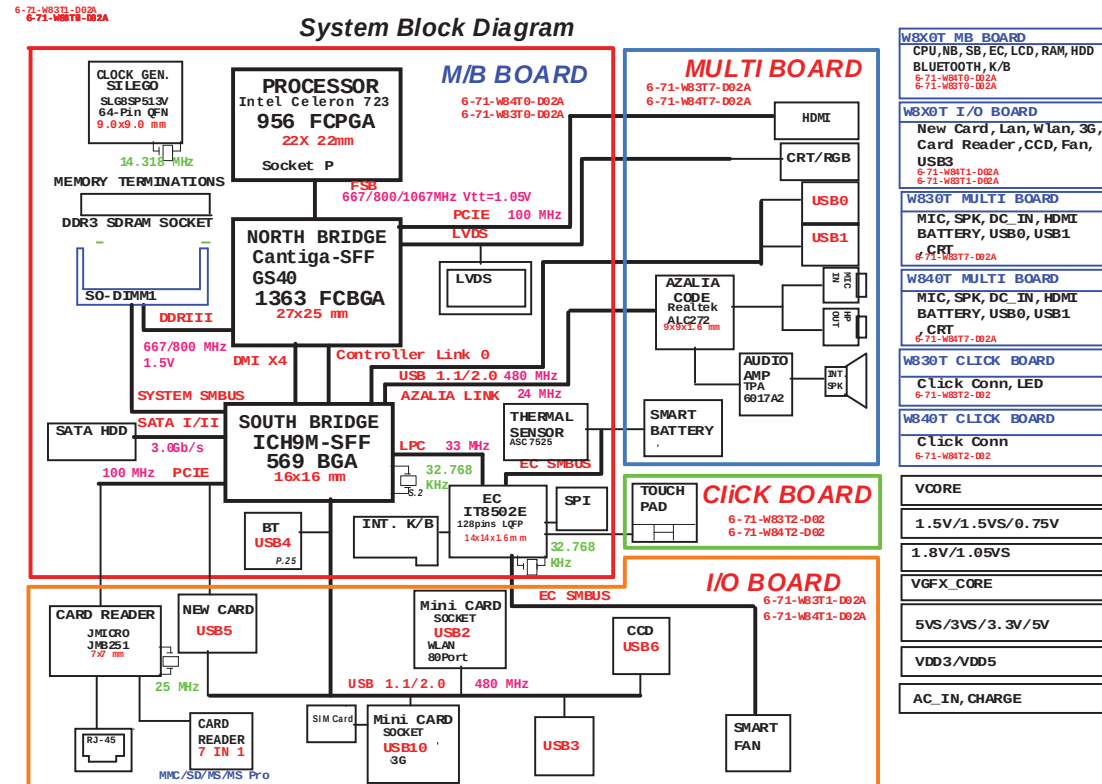


MULTI Board TO CPU Board CONN.

W830T USB
(USB Port1, Port3)

System Block Diagram (W840T)

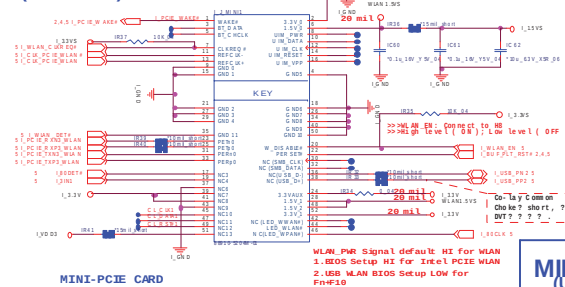
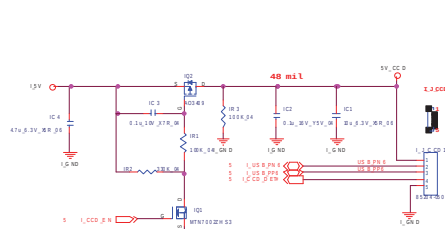
Sheet 37 of 46
System Block
Diagram (W840T)



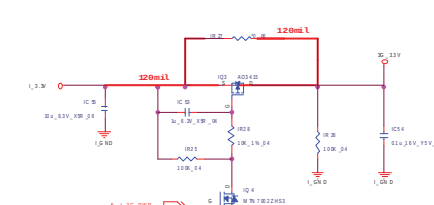
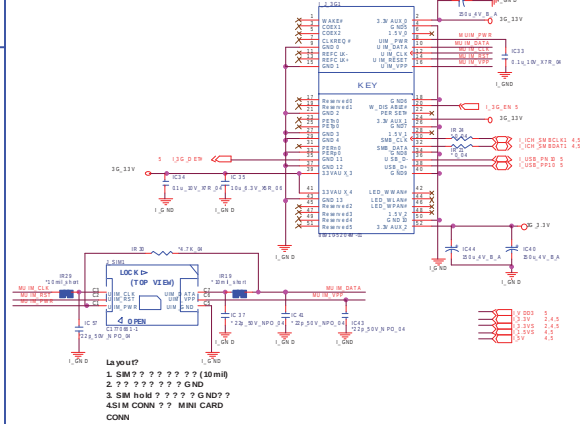
Schematic Diagrams

W840T I/O (CCD, MINI, WLAN/3G)

Sheet 39 of 46
W840T I/O (CCD,
MINI, WLAN/3G)

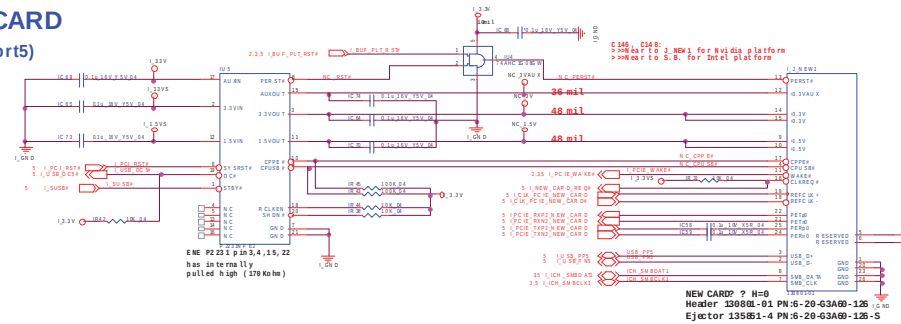
MINI-PCIE CARD WLAN
(USB Port2)CCD
(USB Port6)

3G POWER

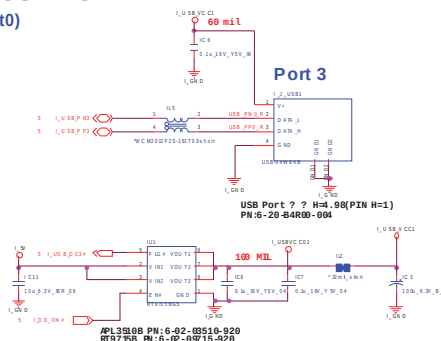
MINI-PCIE CARD 3G
(USB Port10)

W840T I/O (New Card, Fan, USB)

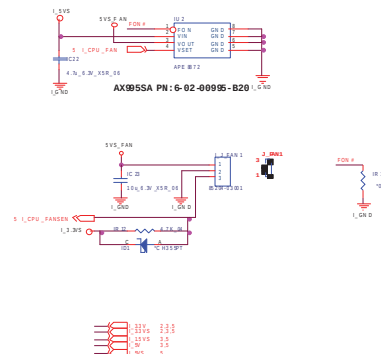
NEW CARD (USB Port5)



W830T USB PORT (USB Port0)

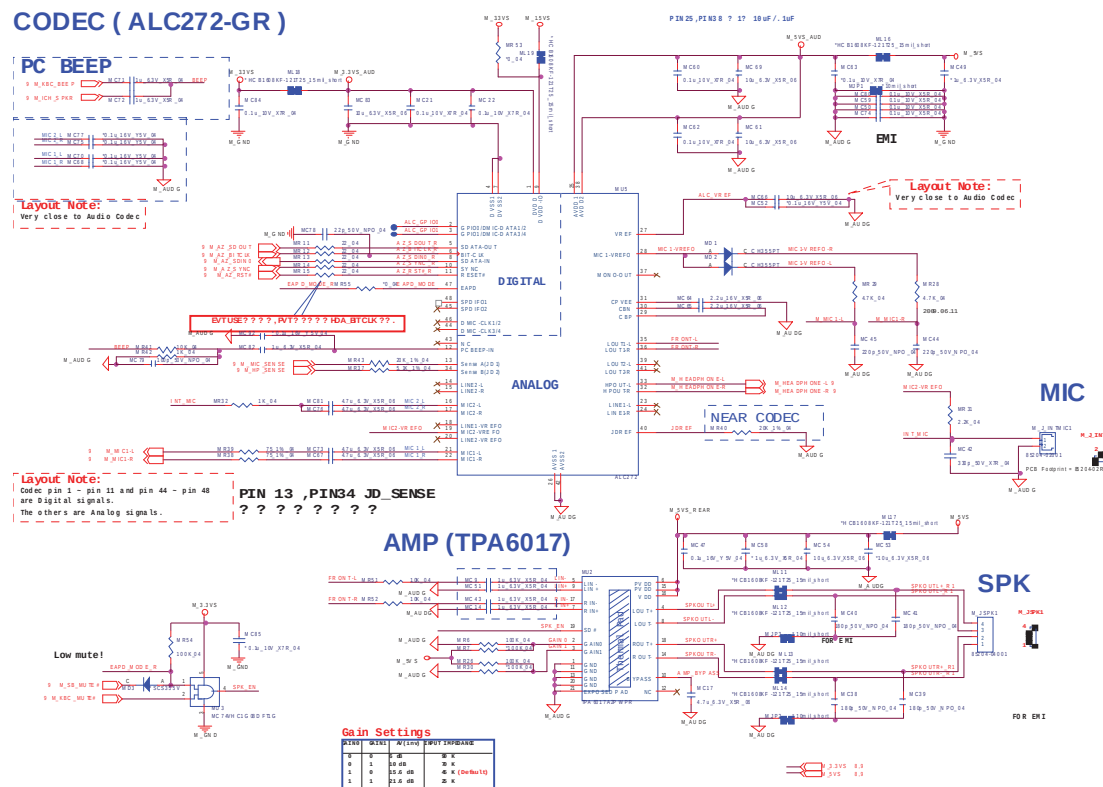


FAN CONTROL



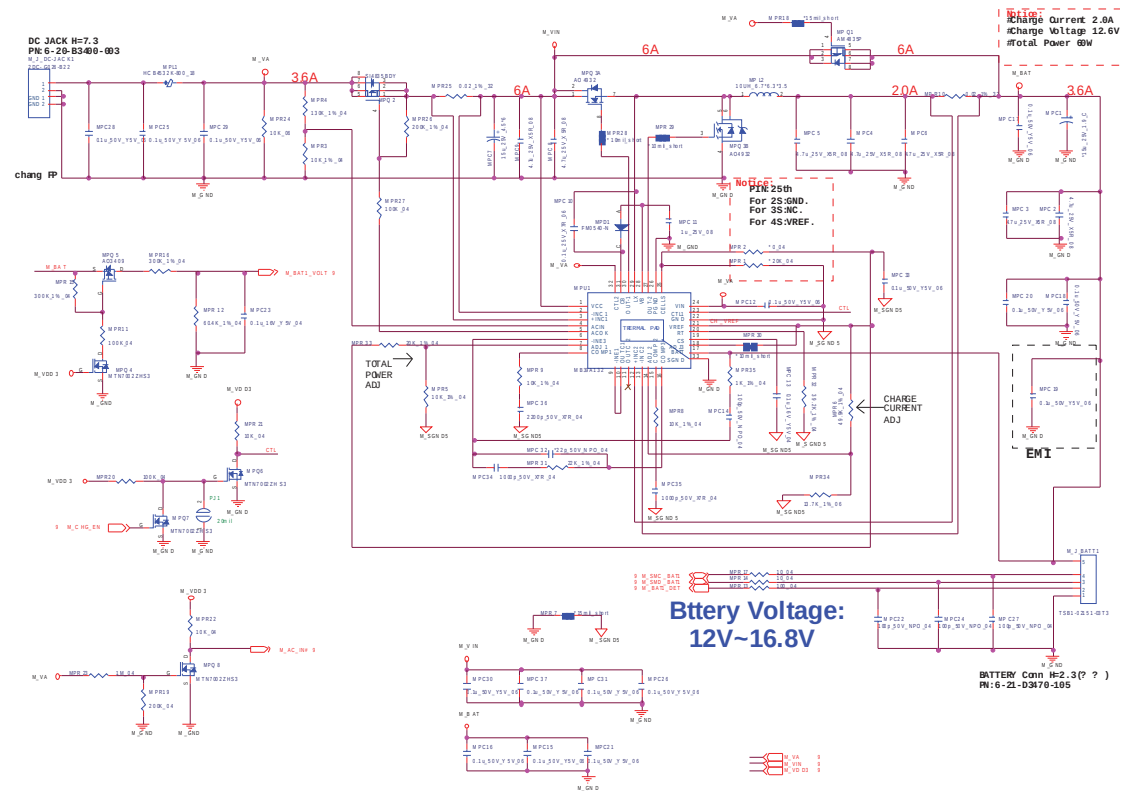
Sheet 40 of 46
W840T I/O
(New Card, Fan,
USB)

W840T Multi (Audio ALC272)



Sheet 42 of 46
W840T Multi
(Audio ALC272)

Sheet 43 of 46
W840T Multi
(AC_In, Charger)



Sheet 44 of 46
W840T Multi (HDMI/
CRT Port)

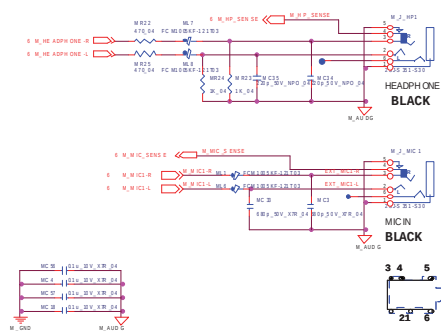
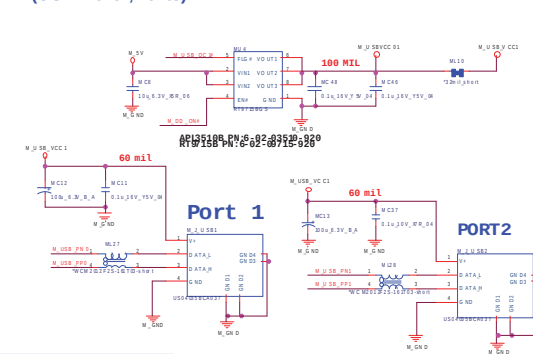


Schematic Diagrams

W840T to CPU Conn (Jack, USB)

Sheet 45 of 46
W840T to CPU
Conn (Jack, USB)

PHONE JACK

W840T USB
(USB Port1, Port3)

MULTI Board TO CPU Board CONN.

