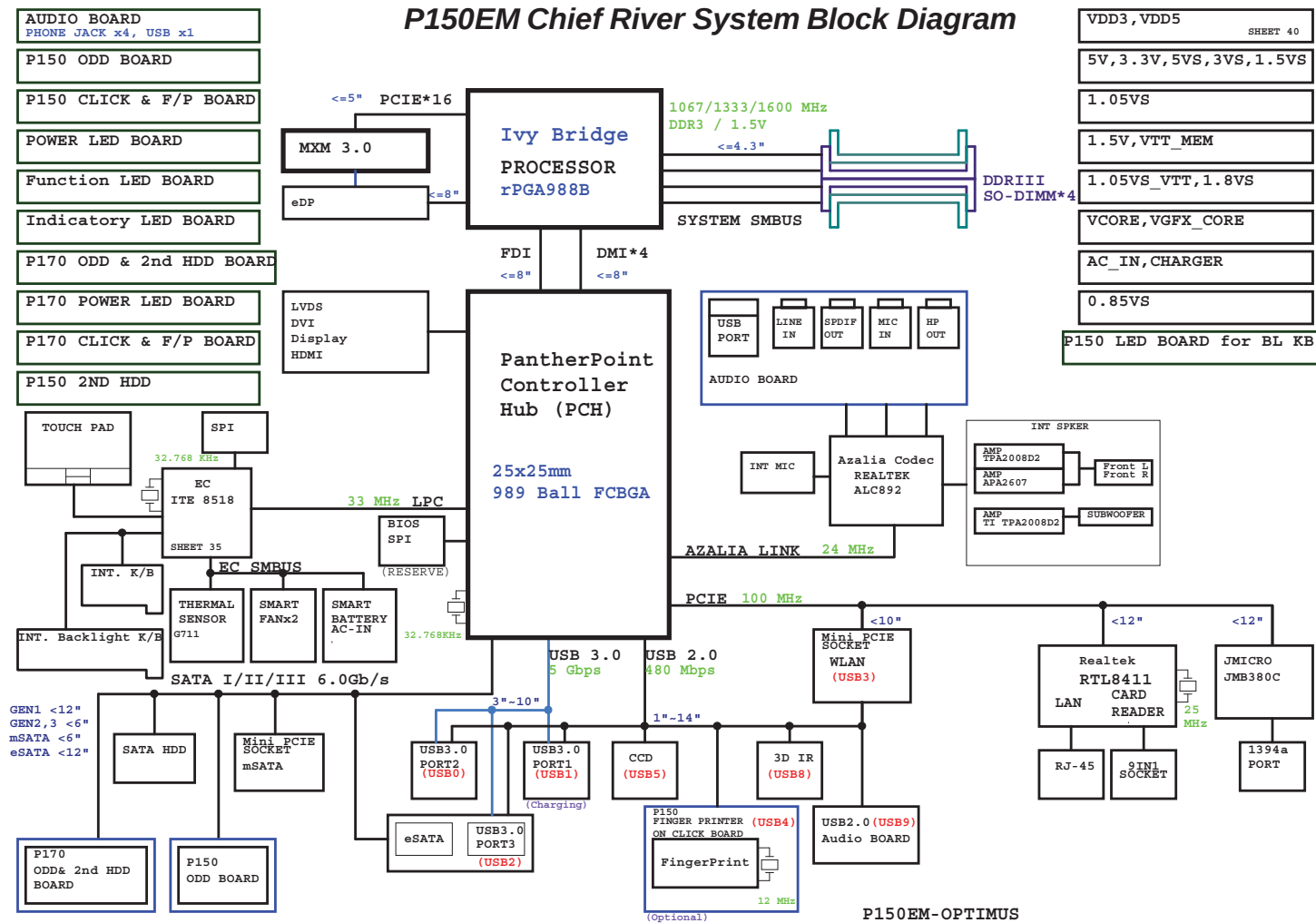


System Block Diagram

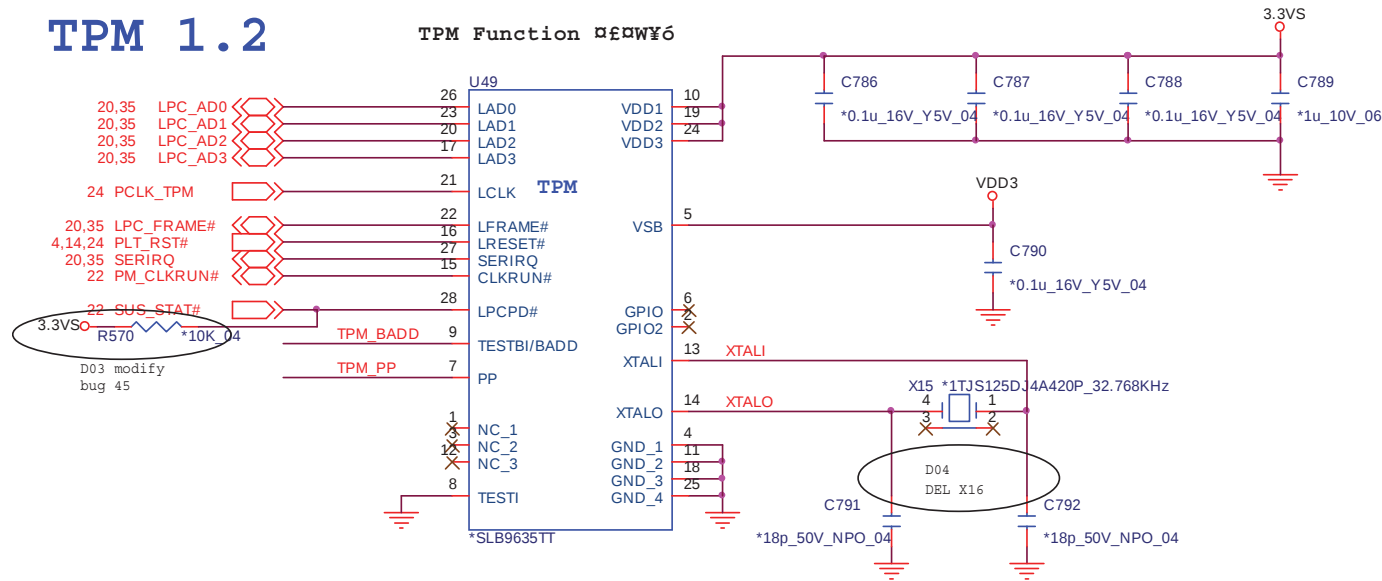
Sheet 1 of 61
System Block
Diagram



TPM

TPM 1.2

TPM Function 引脚

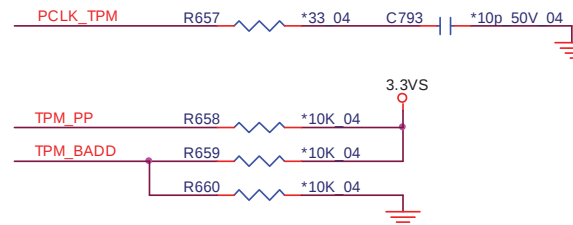


Asserted before entering S3

LPC reset timing:

LPCPD# inactive to LRST# inactive 32~96us

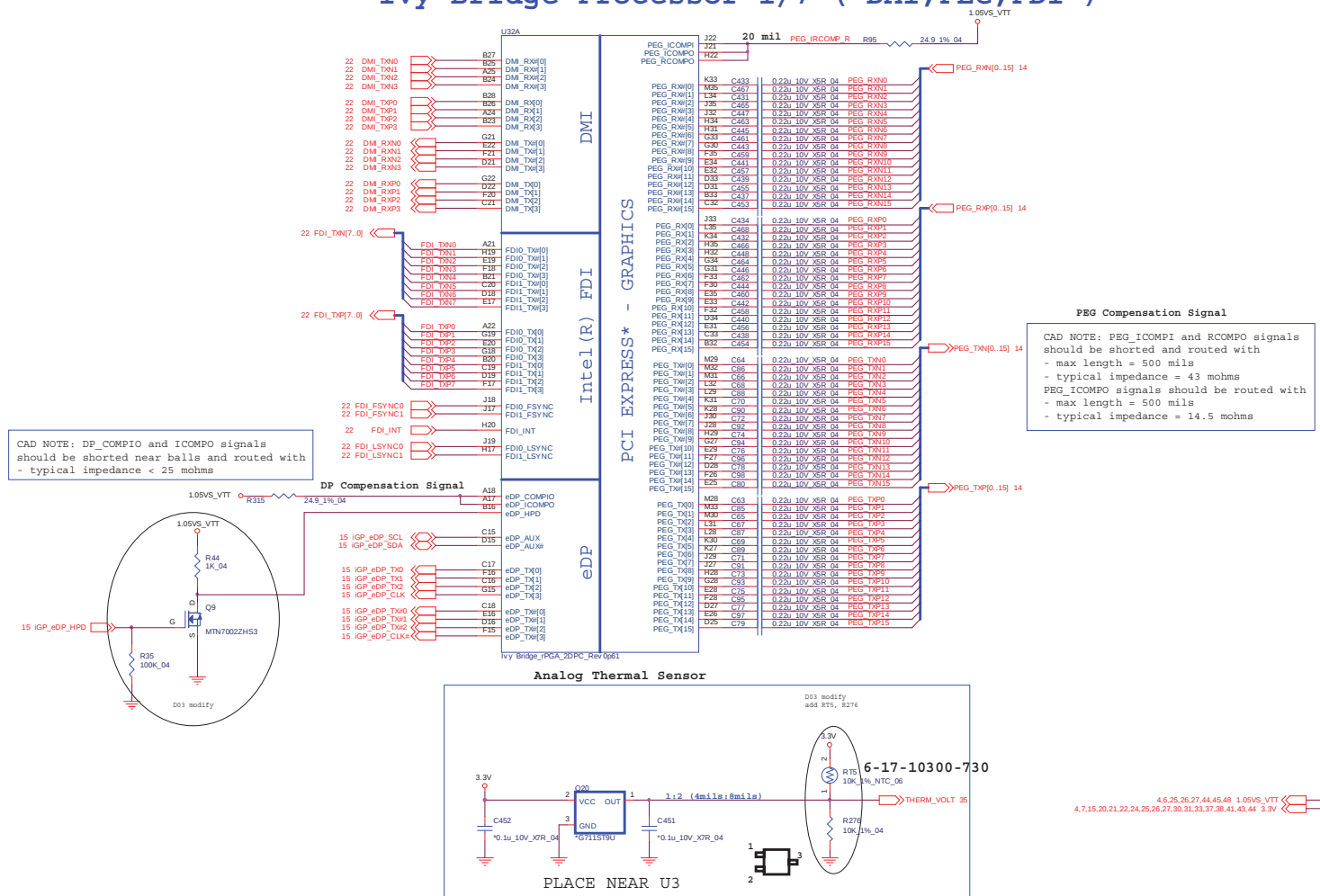
TPM_PP	Hi: ACCESS LOW: NORMAL (Internal PD)
TPM_BADD	Hi: 4E/ 4F H LOW: 2E/ 2F H



20,30,35,37,38,40,41,47 VDD3 3.3VS
4,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,27,29,30,31,33,34,35,36,37,38,41,45,48

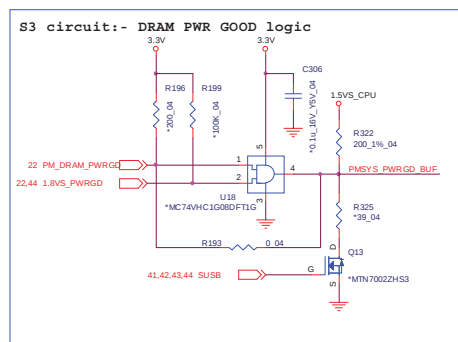
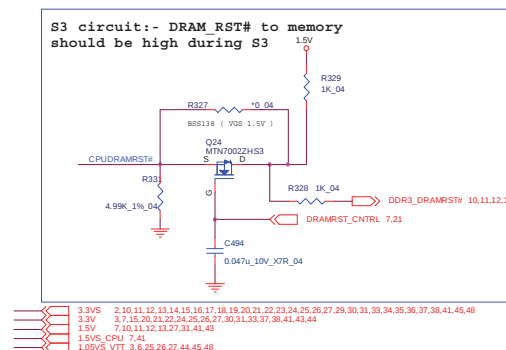
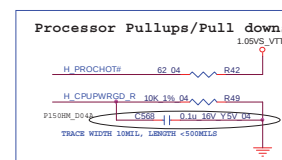
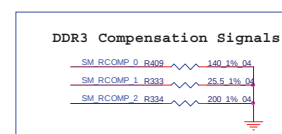
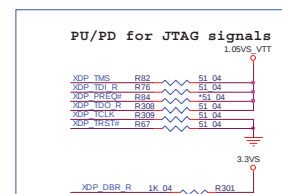
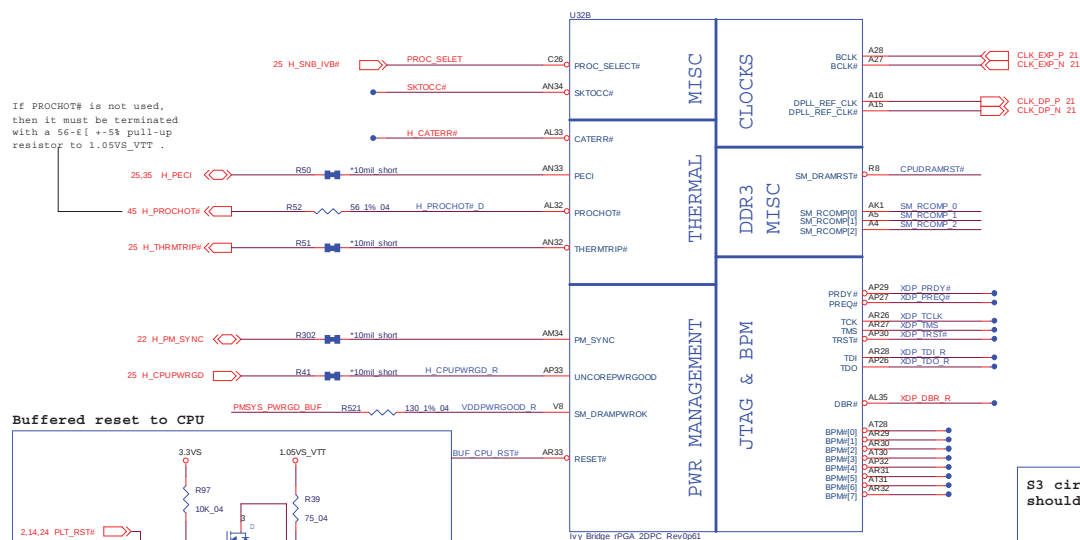
Sheet 2 of 61
TPM

Sheet 3 of 61
Processor 1/7



Processor 2/7

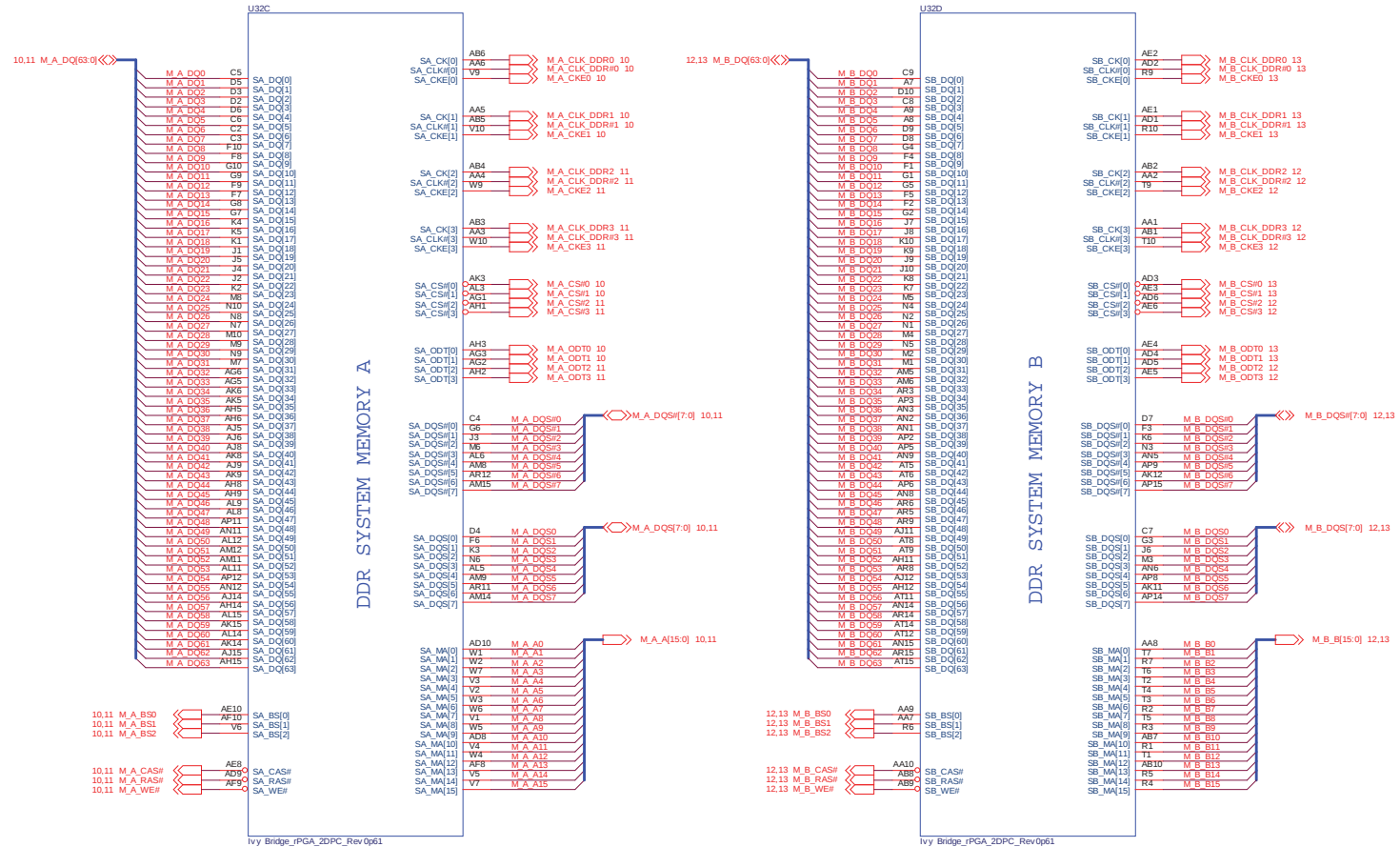
Ivy Bridge Processor 2/7 (CLK,MISC,JTAG)



Sheet 4 of 61
Processor 2/7

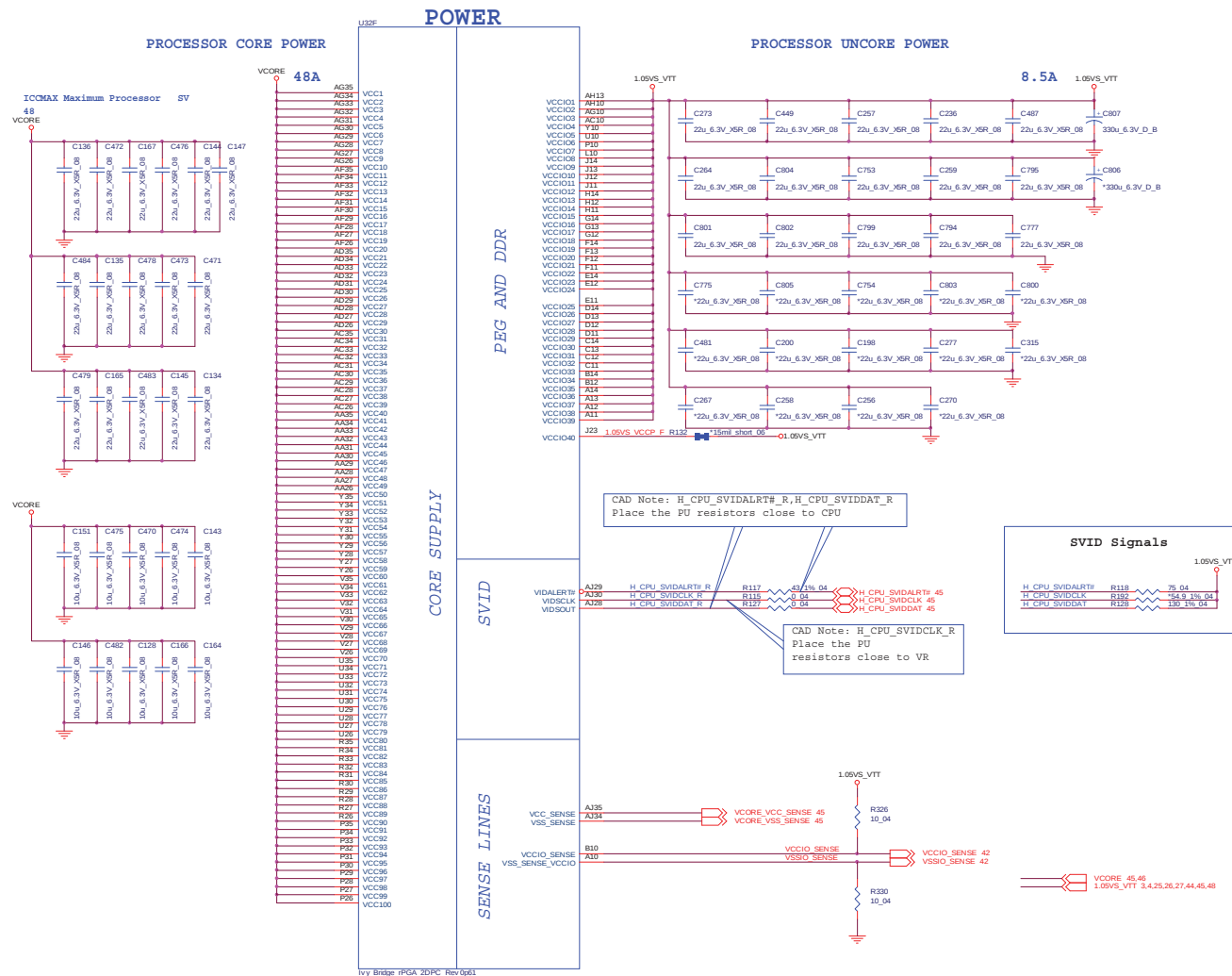
Processor 3/7

Ivy Bridge Processor 3/7 (DDR3)

Sheet 5 of 61
Processor 3/7

Processor 4/7

Ivy Bridge Processor 4/7 (POWER)

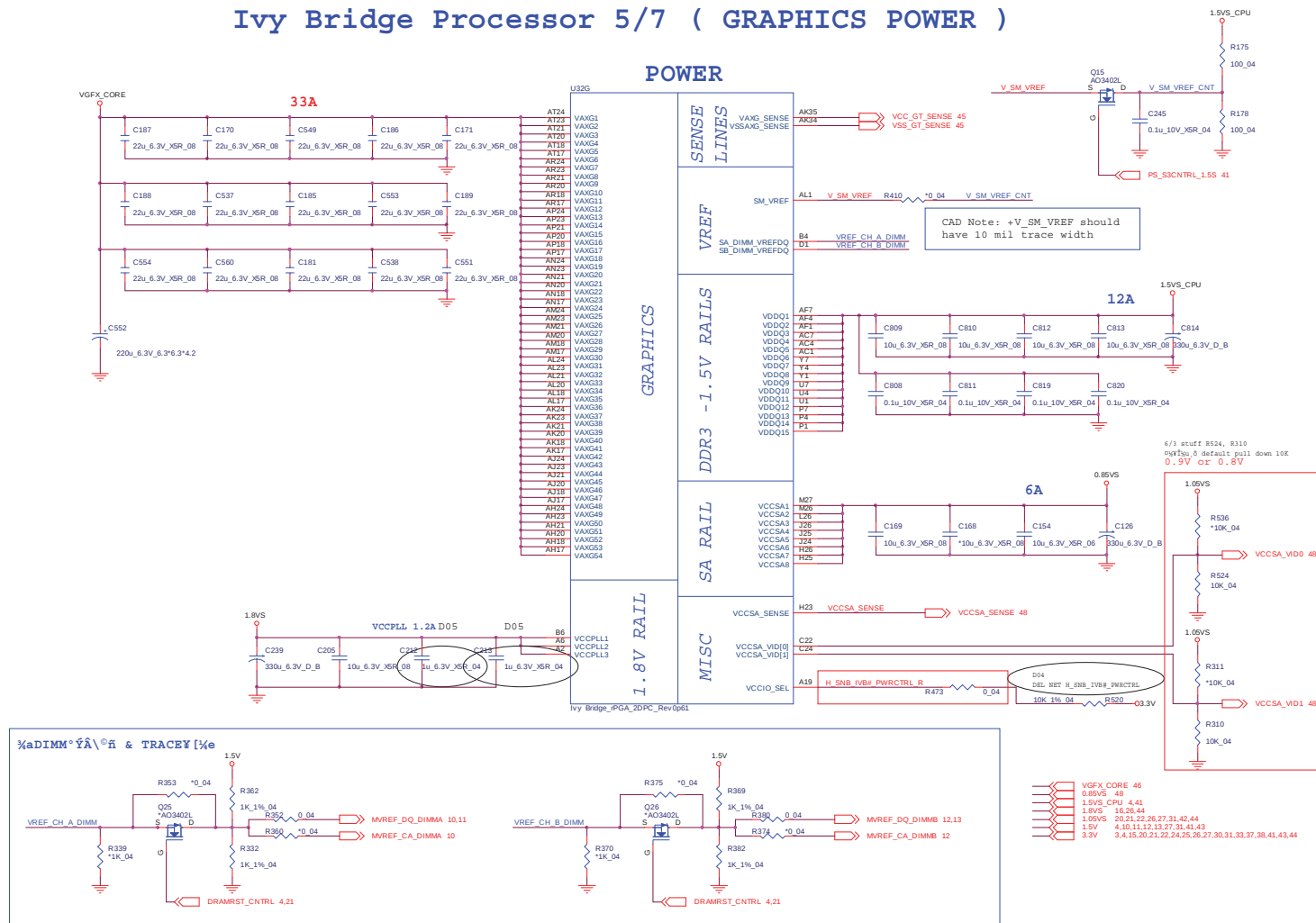


Sheet 6 of 61
Processor 4/7

B.Schematic Diagrams

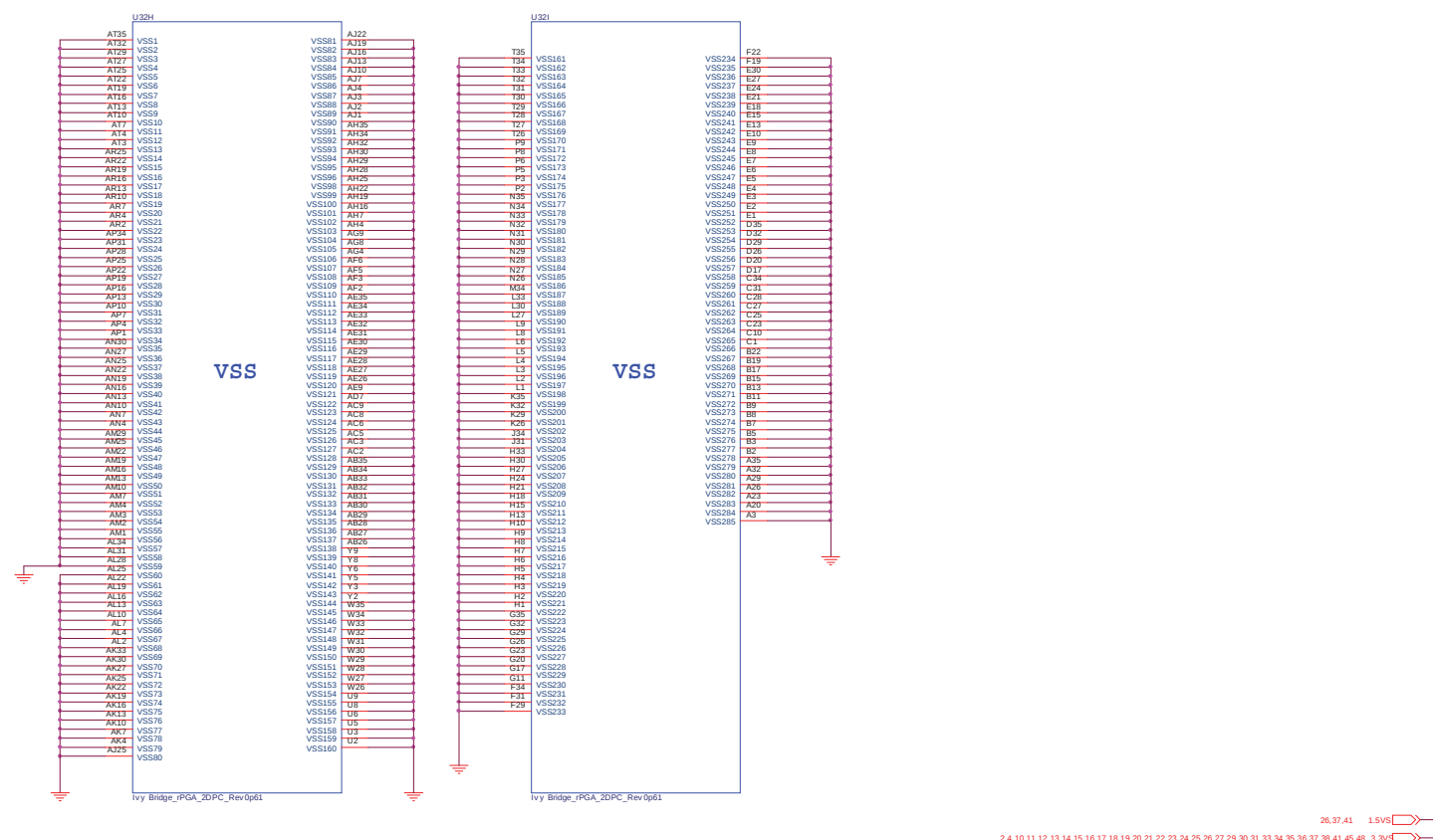
Processor 5/7

Ivy Bridge Processor 5/7 (GRAPHICS POWER)



Processor 6/7

Ivy Bridge Processor 6/7 (GND)



Sheet 8 of 61
Processor 6/7

B.Schematic Diagrams

Processor 7/7

Ivy Bridge Processor 7/7 (RESERVED)

Sheet 9 of 61
Processor 7/7

CFG Straps for Processor

PEG Static Lane Reversal - CFG2 is for the 16x	
CFG2	1:(Default) Normal Operation; Lane # definition matches socket pin map definition 0:Lane Reversed

CFG2 R83 *1K 04

Display Port Presence Strap	
CFG4	1: (Default) Disabled; No Physical Display Port attached to Embedded Display Port 0: Enabled; An external Display Port device is connected to the Embedded Display Port

CFG4 R81 1K 04 PANEL_SEL 23 Add net PANEL_SEL

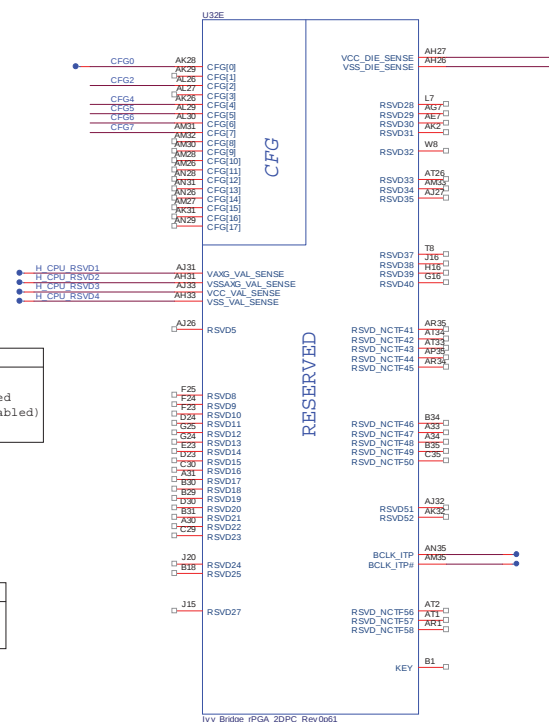
PCIE Port Bifurcation Straps	
CFG[6:5]	11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled; function 2 disabled 01: Reserved - (Device 1 function 1 disabled; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled

CFG5 R77 *1K 04

CFG6 R74 *1K 04

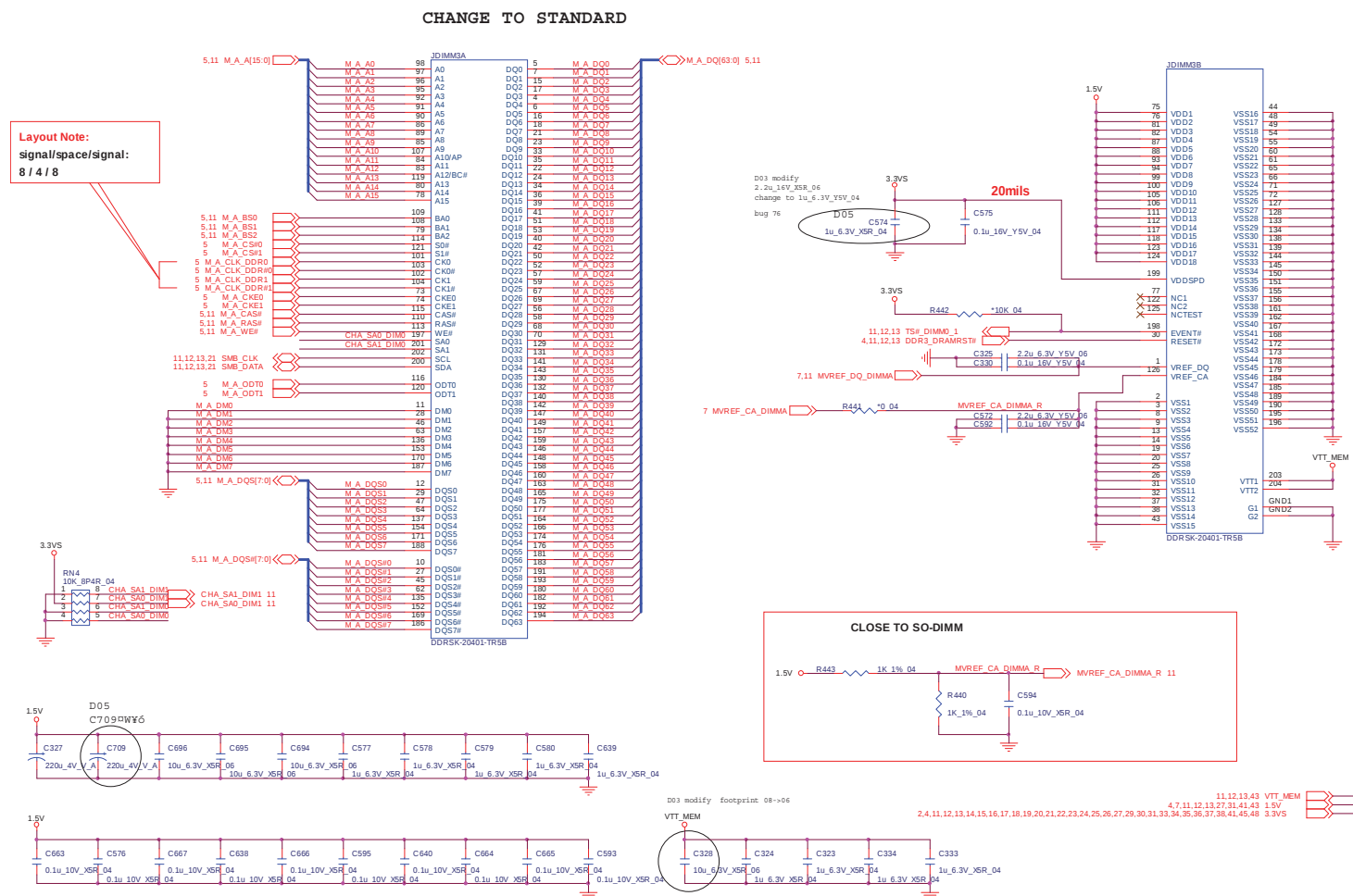
PEG DEFER TRAINING	
CFG7	1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training

CFG7 R73 *1K 04



DDRIII CHA SO-DIMM_0

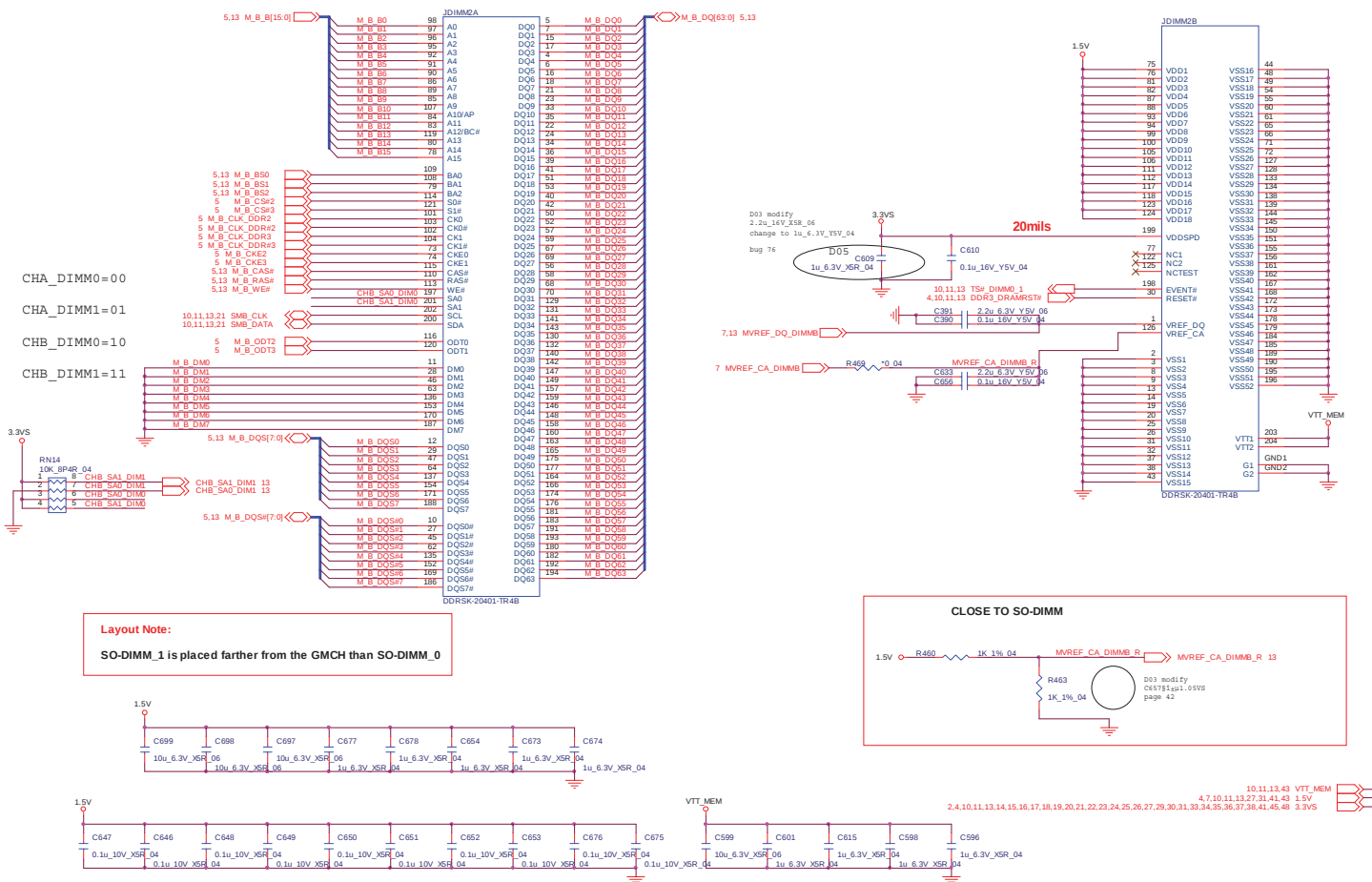
Channel A SO-DIMM 0



DDRIII CHB SO-DIMM_0

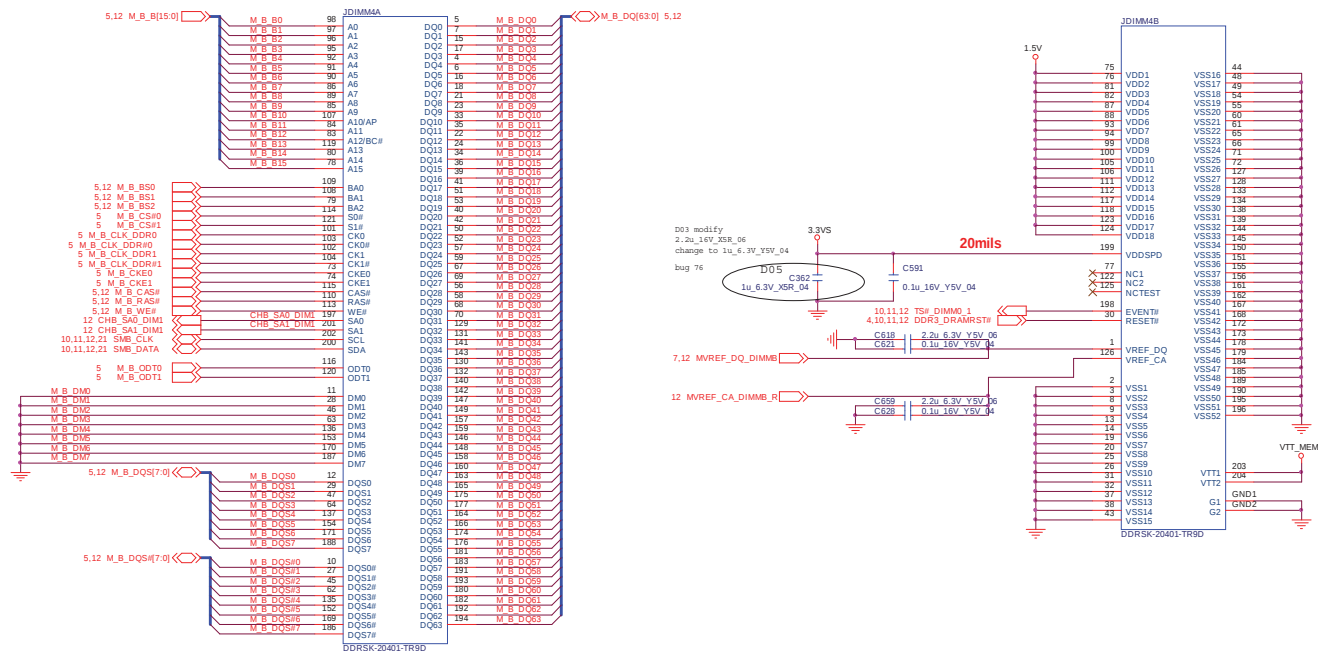
Channel B SO-DIMM 0

CHANGE TO STANDARD

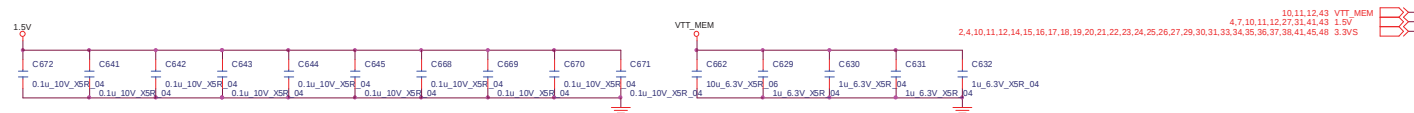


Channel B SO-DIMM 1

Sheet 13 of 61
DDRIII CHB SO-
DIMM 1



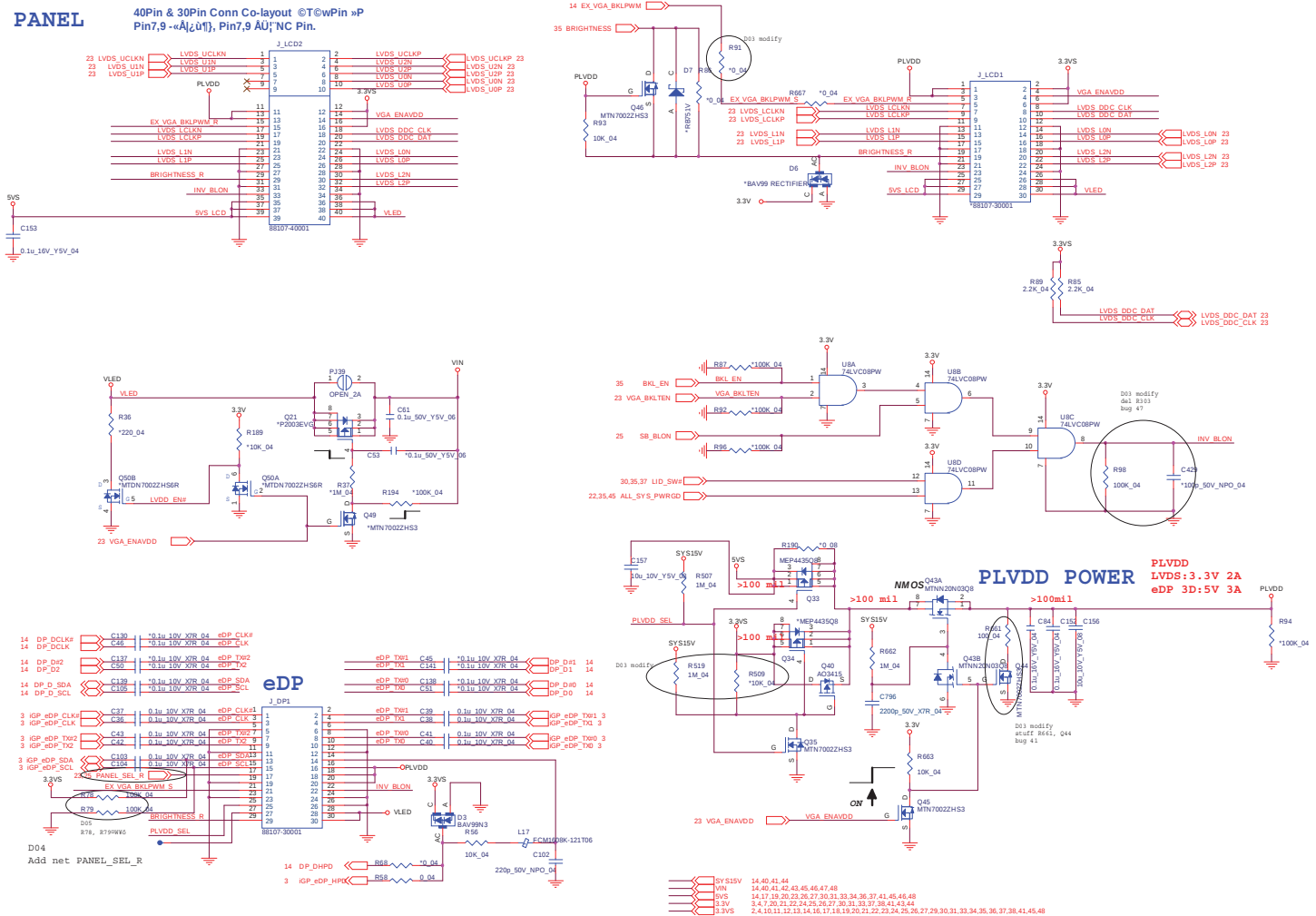
SO-DIMM_1 is placed farther from the GMCH than SO-DIMM_0



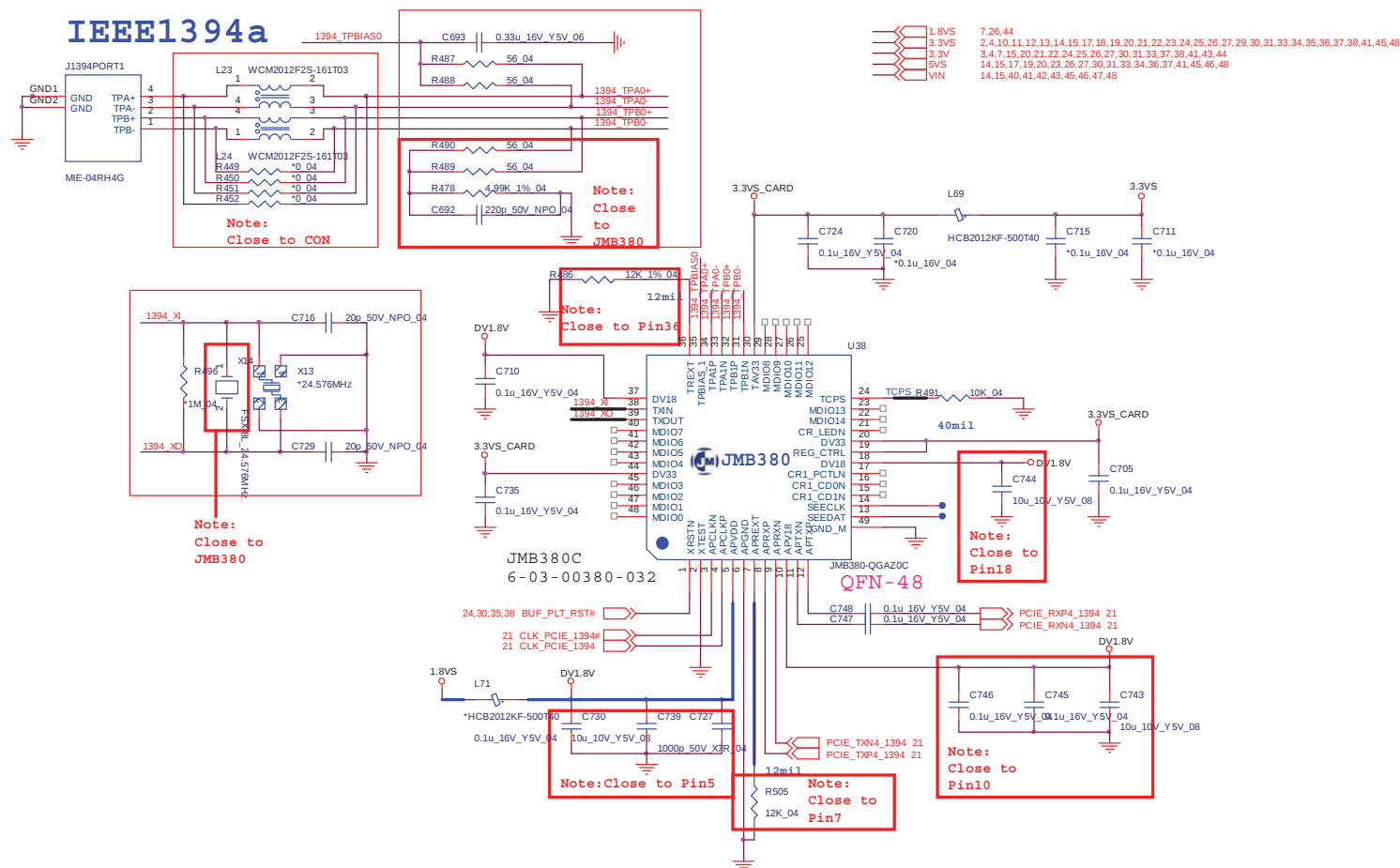
B.Schematic Diagrams



Panel, Inverter, CRT



Sheet 16 of 61
1394_JMB380C

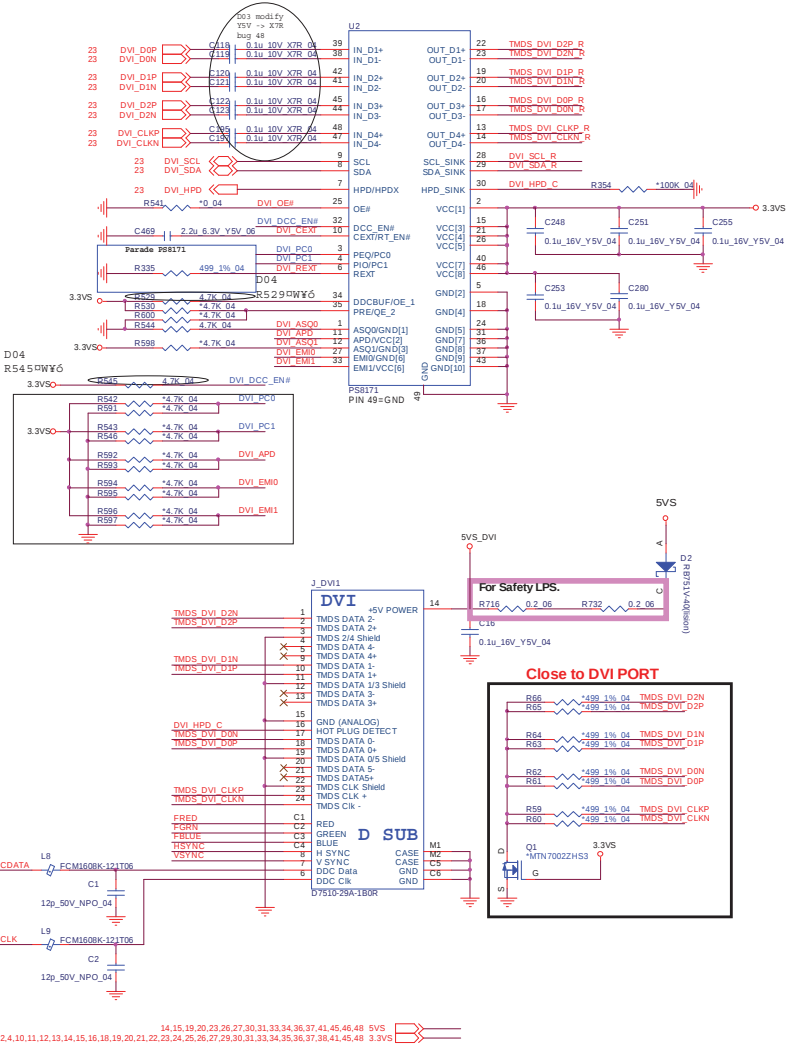
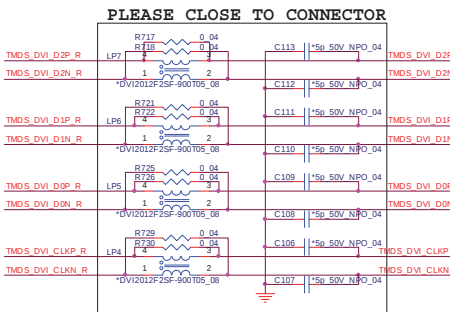
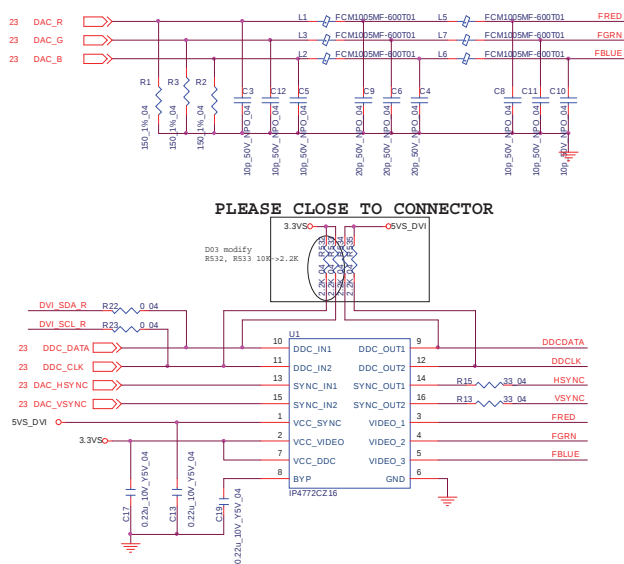


Schematic Diagrams

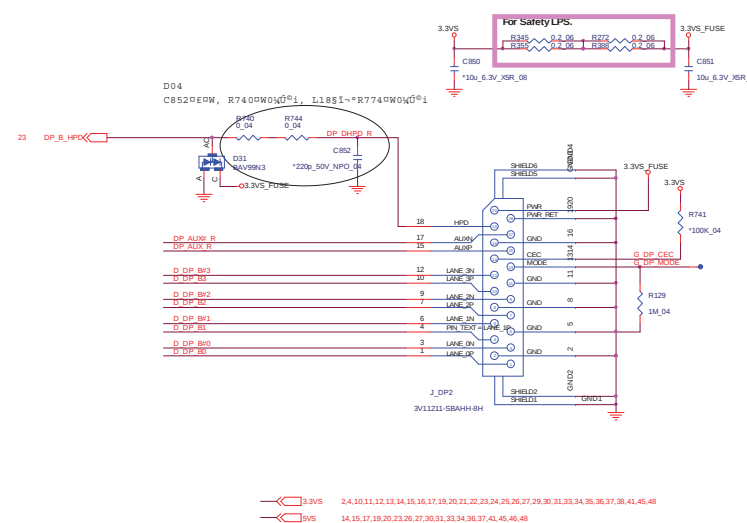
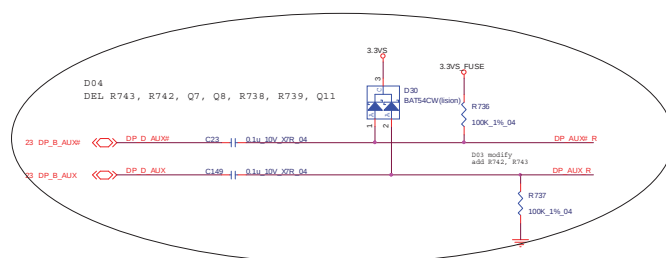
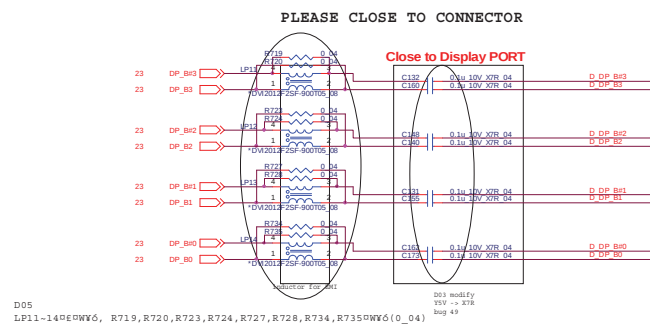
<http://hobi-elektronika.net>

DVI

Sheet 17 of 61
DVI



dGPU DISPLAY PORT



Sheet 18 of 61
Display Port

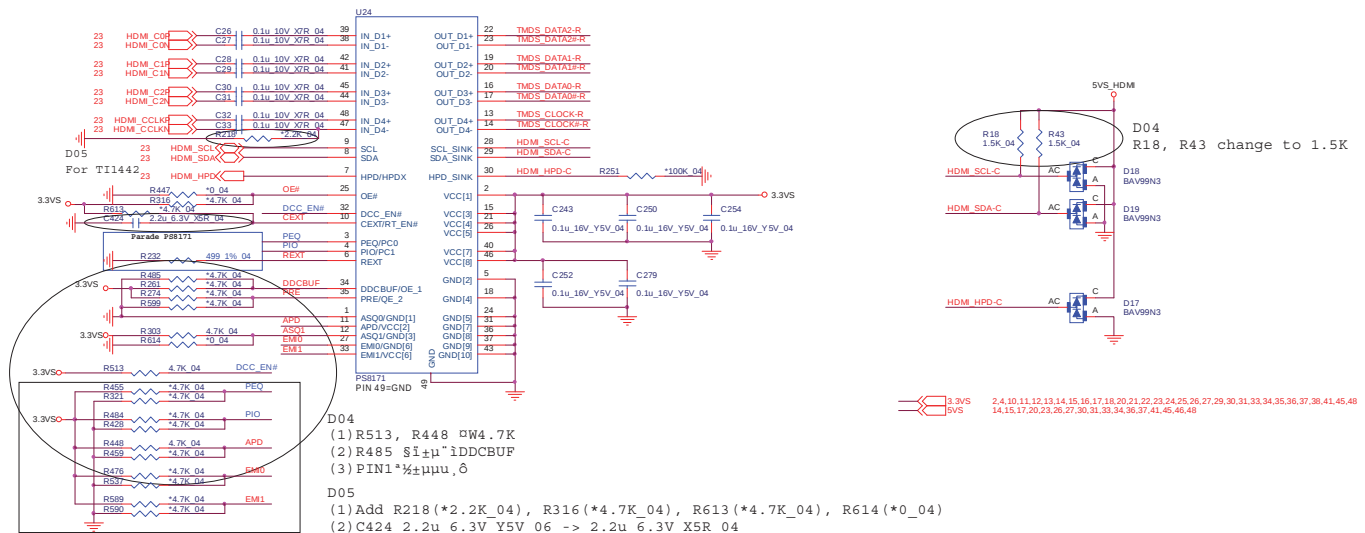
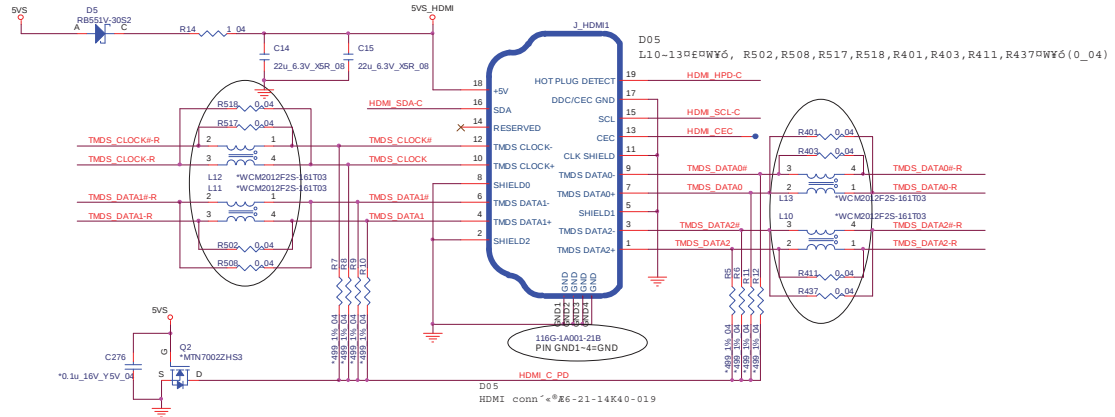
Schematic Diagrams

<http://hobi-elektronika.net>

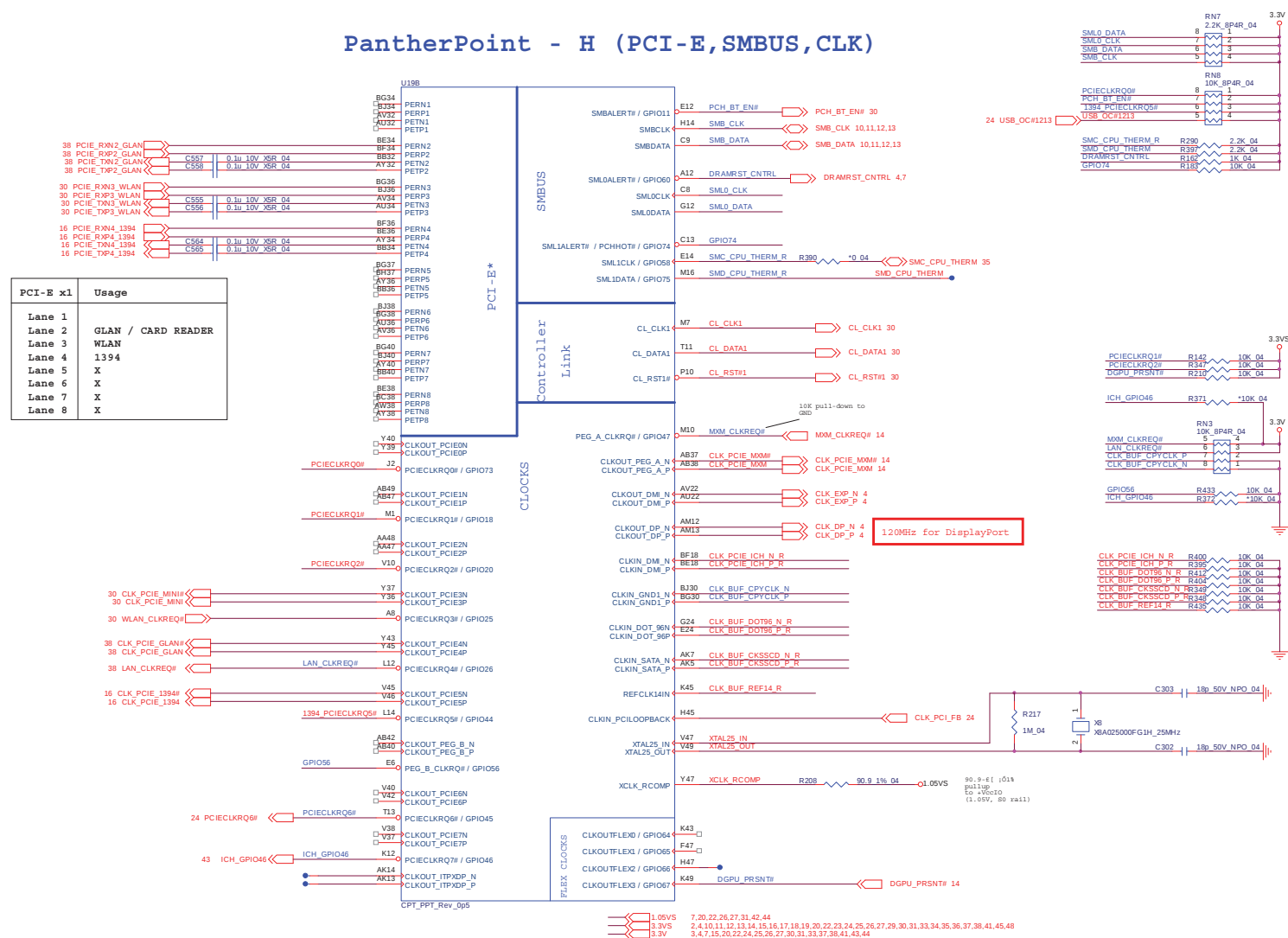
HDMI

HDMI CONNECTOR

Sheet 19 of 61
HDMI

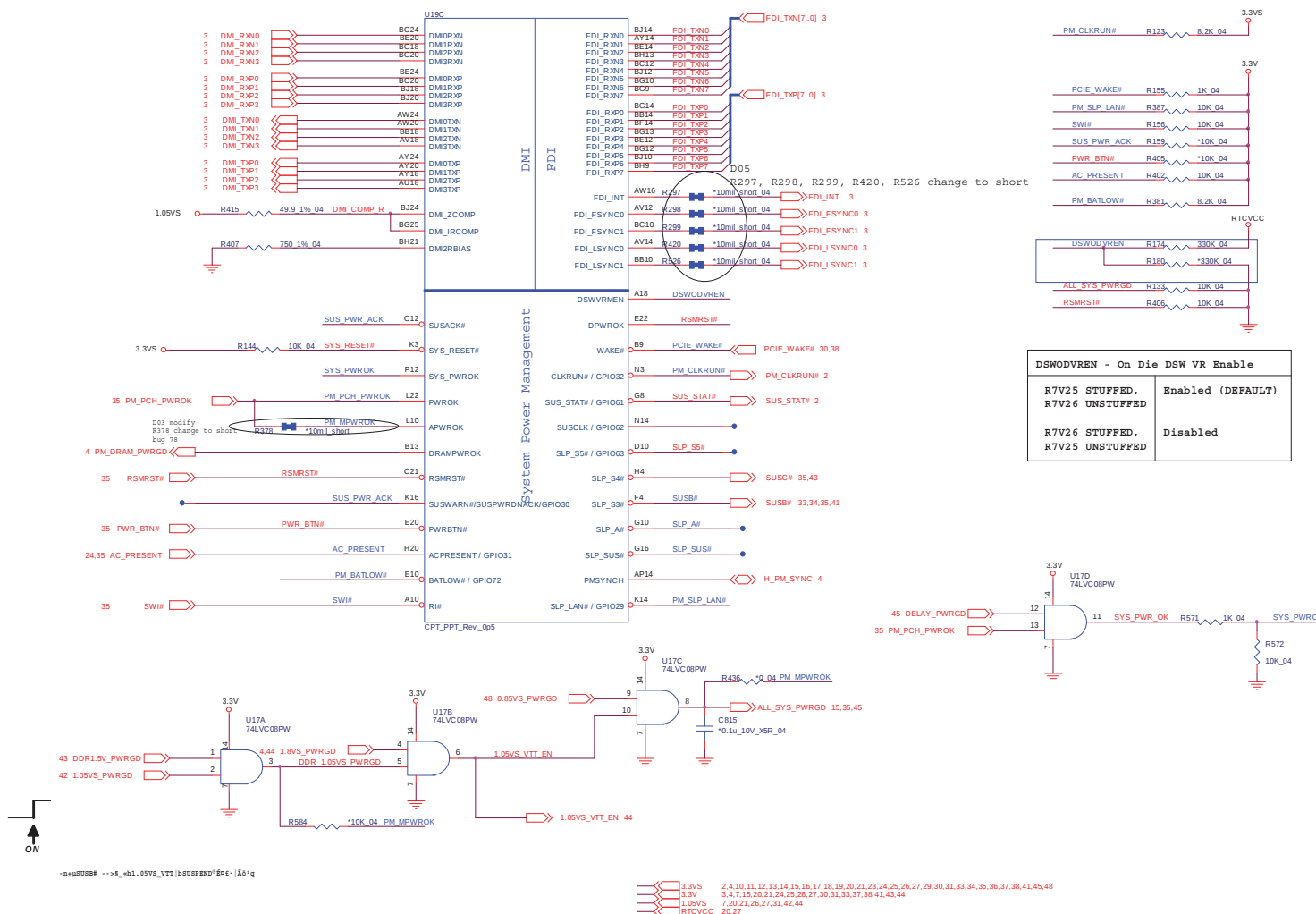


PCH 1/9 - RTC, HDA, SATA



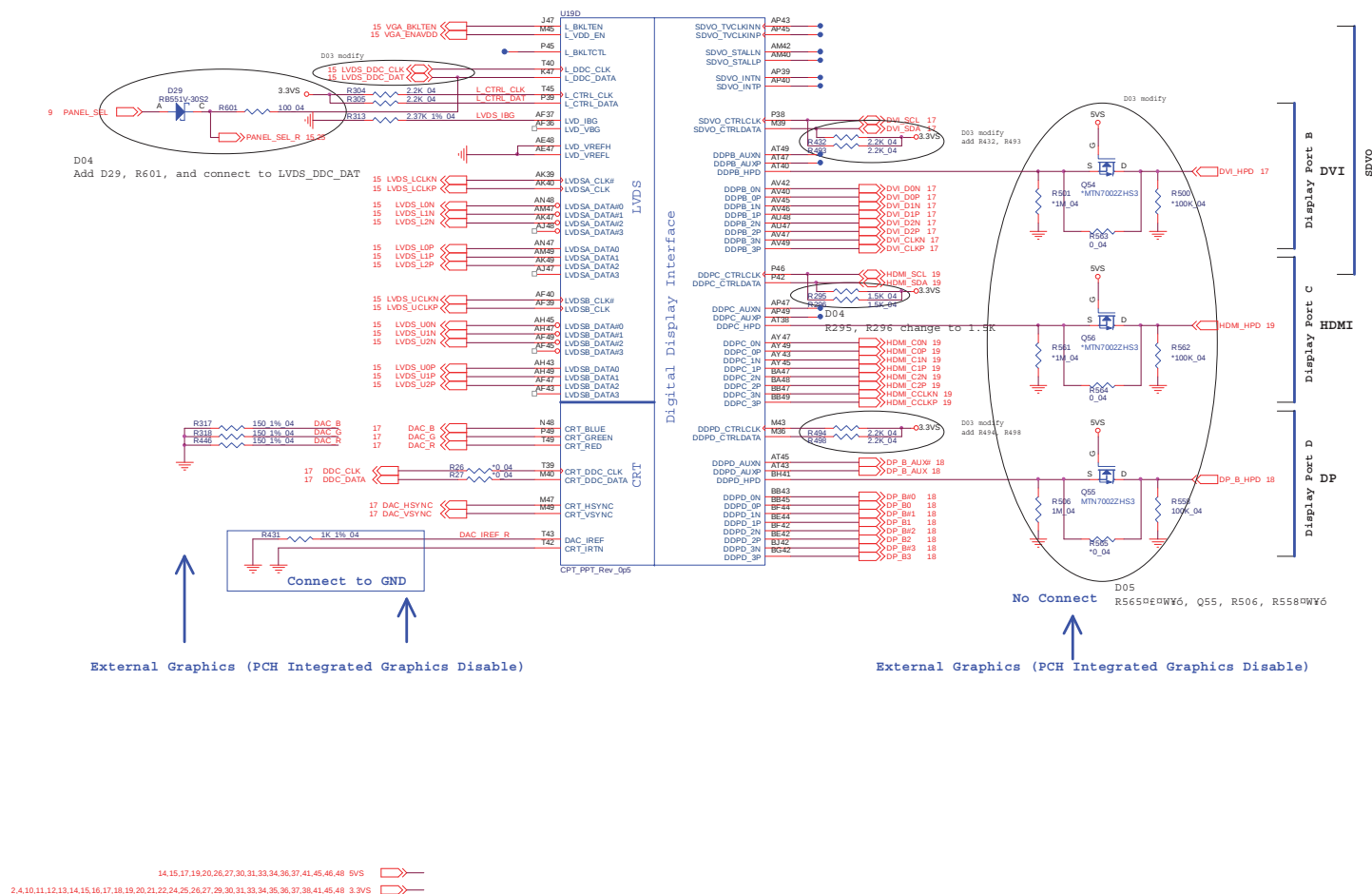
PCH3/9 - DMI, FDI, PWRGD

PantherPoint - H (DMI, FDI, GPIO)



Sheet 22 of 61
PCH 3/9 - DMI, FDI,
PWRGD

PantherPoint - H (LVDS,DDI)

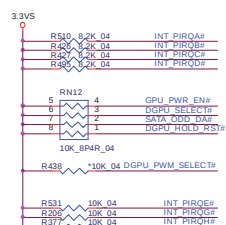
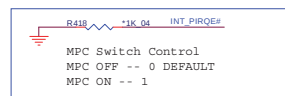
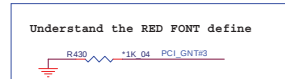


PCH 5/9 - PCI, USB, RSVD

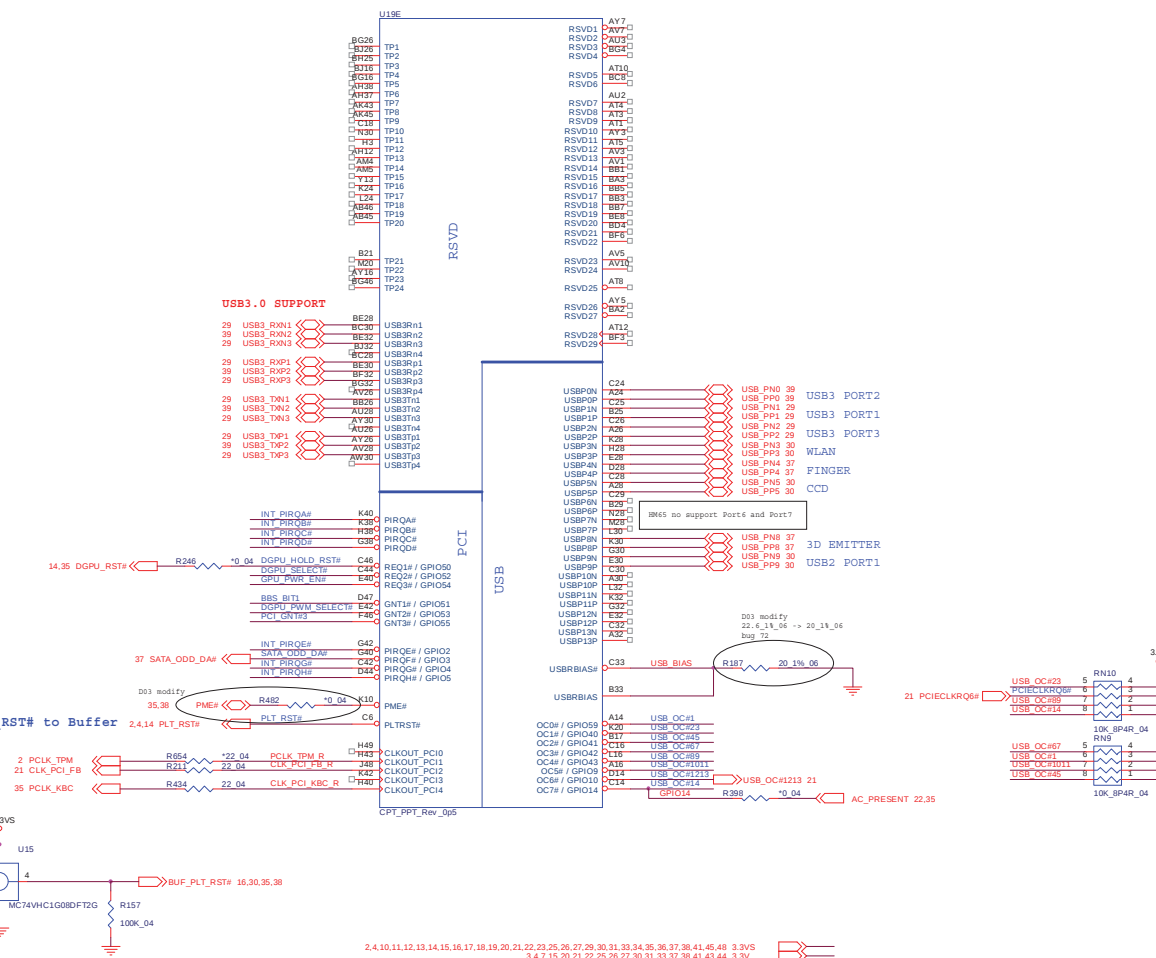
Boot BIOS Strap		
BBS_BIT1	BBS_BIT0	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI



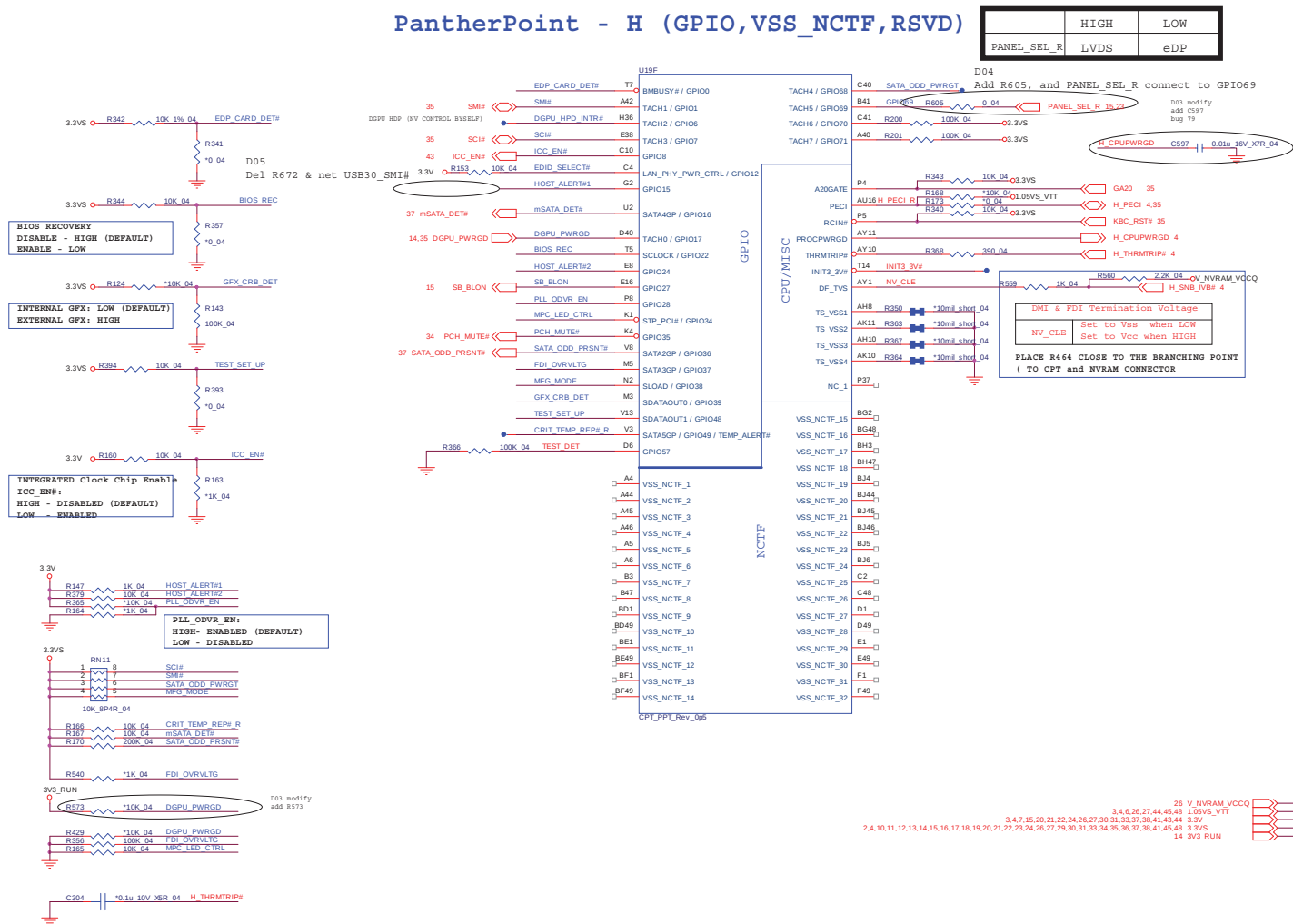
Flash Descriptor security override strap	
PCI_GNT#3	LOW = PCI_GNT#3 swap override HIGH = Default



PIN PLT RST# to Buffer

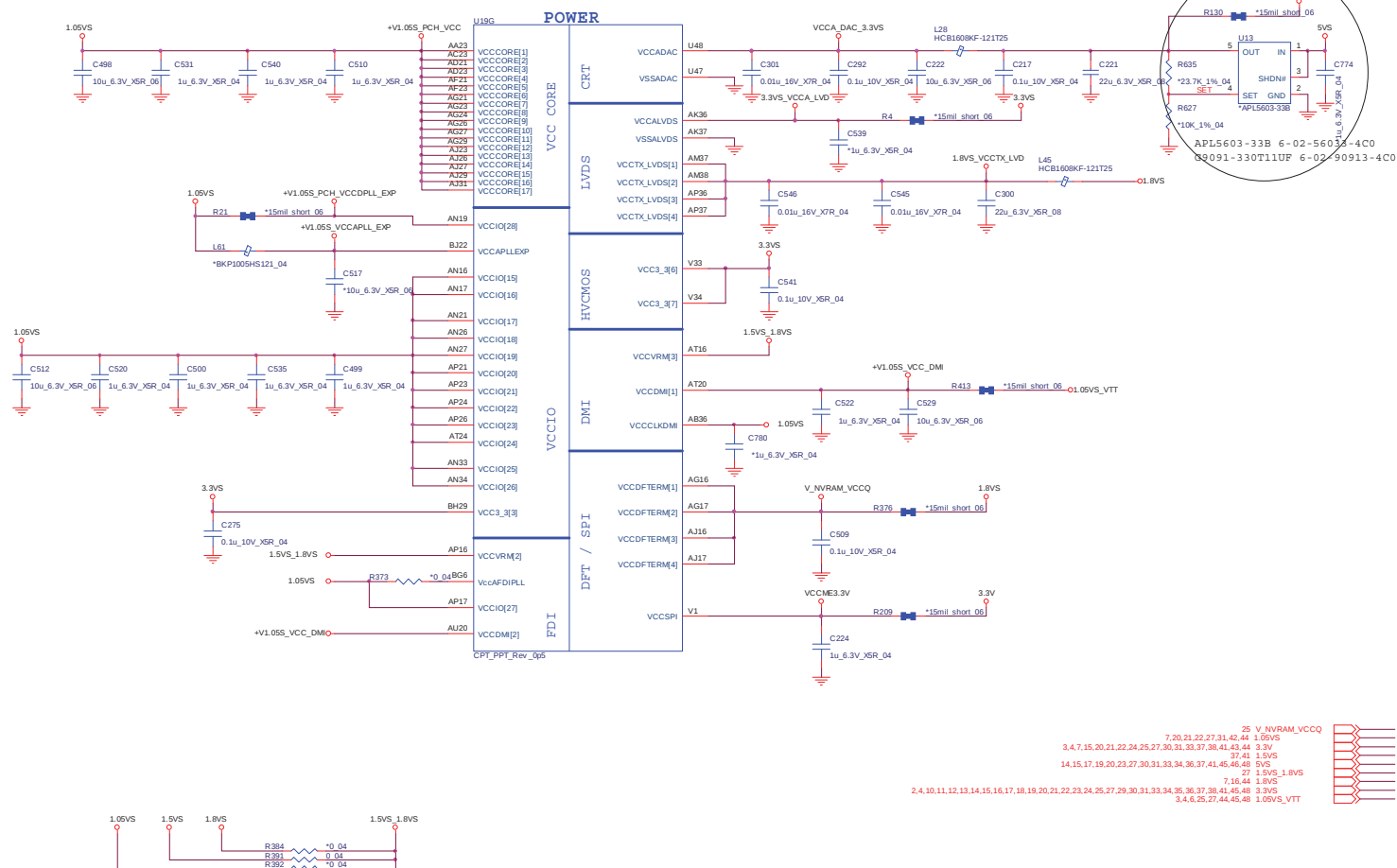


Sheet 24 of 61
PCH 5/9 - PCI, USB,
RSVD



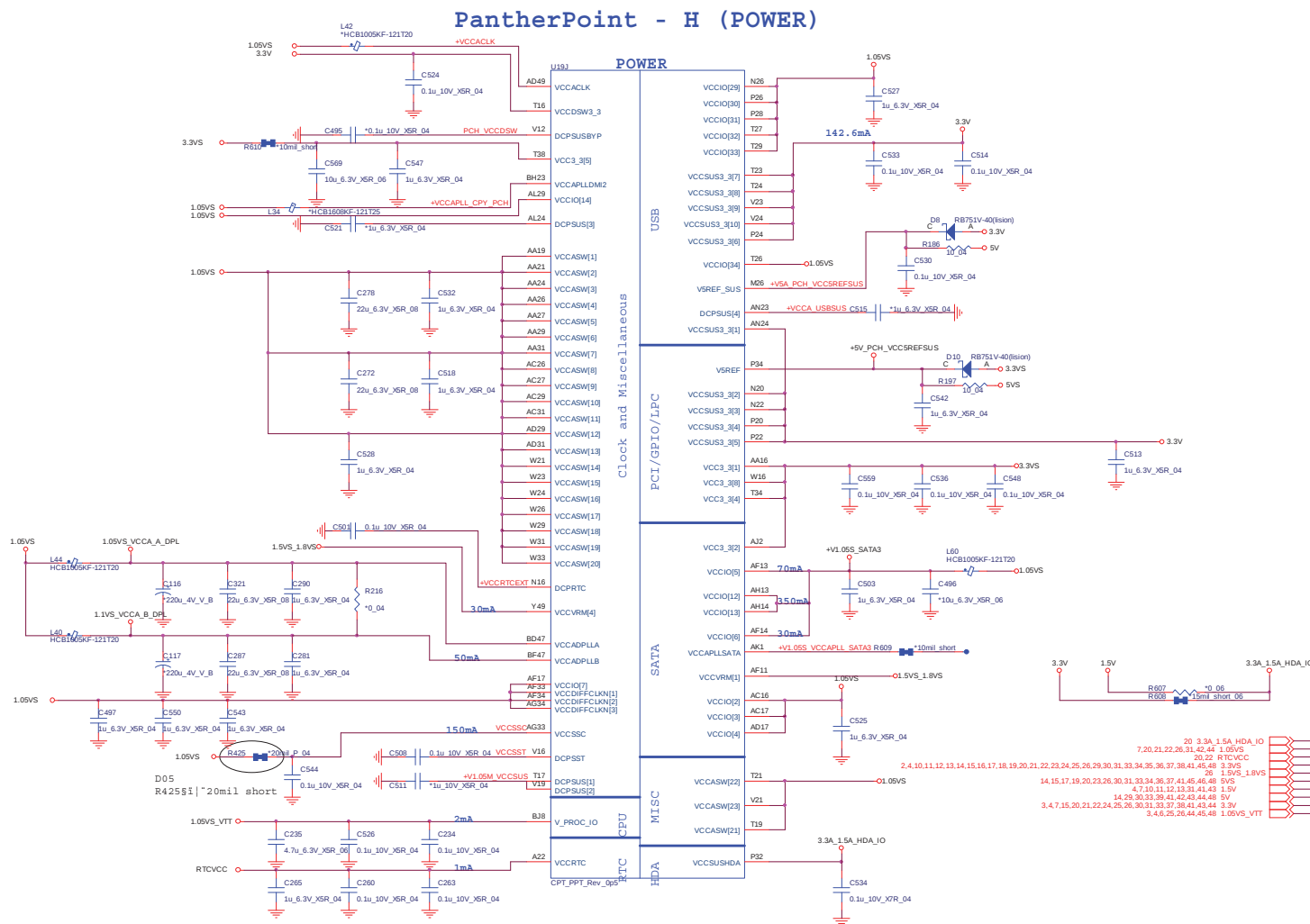
PCH 7/9 - Power

PantherPoint - H (POWER)



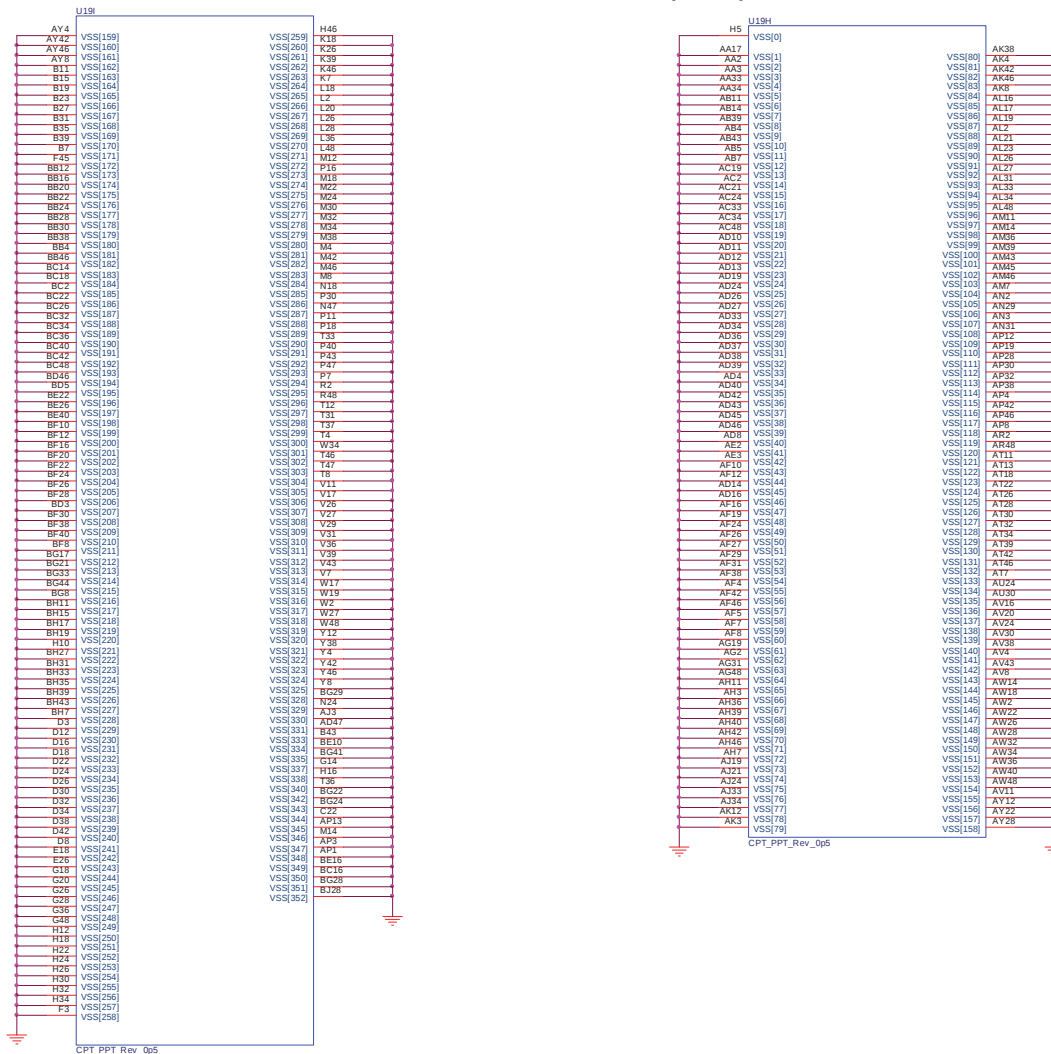
Sheet 26 of 61
PCH 7/9 - Power

B - 28 PCH 8/9 - Power



PCH 9/9 - GND

PantherPoint - H (GND)

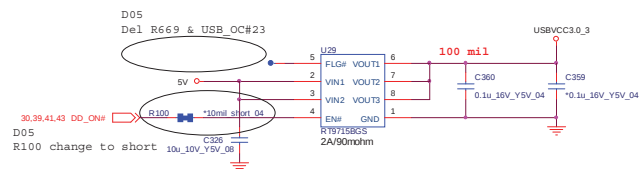


Sheet 28 of 61
PCH 9/9 - GND

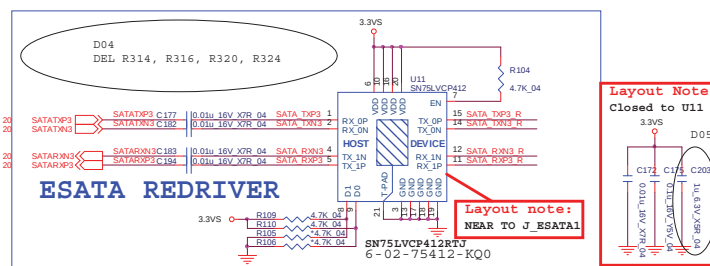
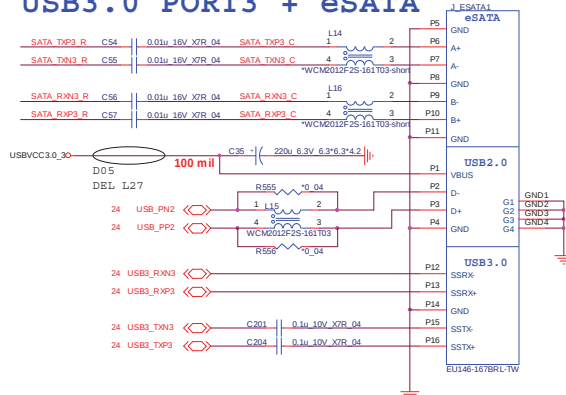
B.Schematic Diagrams

USB+eSATA, USB Charging

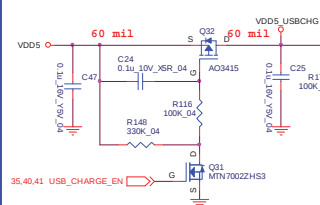
Sheet 29 of 61
USB+eSATA, USB
Charging



USB3.0 PORT3 + eSATA

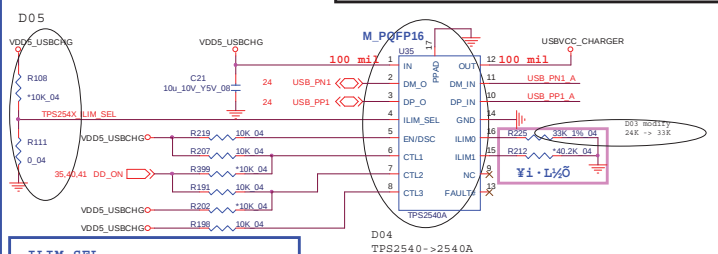


TPS2540 USB Charging PORT



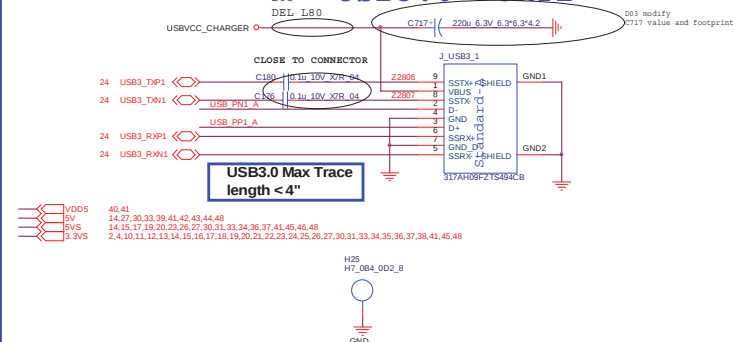
		(DD_ON) CTL1	(VDD5) CTL2	(VDD5) CTL3
Mode 1	Power off & Discharge	0	0	0
Mode 2	Power off & Charge	0	1	1
Mode 3	Power off & Charge	1	0	1
Mode 4	Power on & Charge	1	1	1

CTL1	CTL2	CTL3:	0	0	0----> Out discharge, power switch Off
CTL1	CTL2	CTL3:	0	x	1----> Dedicated charging port, auto-detect
CTL1	CTL2	CTL3:	1	0	1----> Dedicated charging port, Divider Mode only
CTL1	CTL2	CTL3:	1	1	1----> Charging downstream port, BC12.

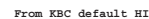


```
ILIM_SEL
( FOR TPS2543/TPS2540 | 304P)
ILIM_SEL=HI , FOR TPS2543
ILIM_SEL=LOW, FOR TPS2540A
```

USB3.0 PORT1



USB2.0 PORT



LID SWITCH IC

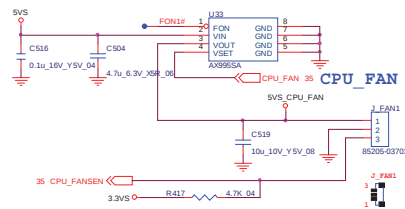


Schematic Diagrams

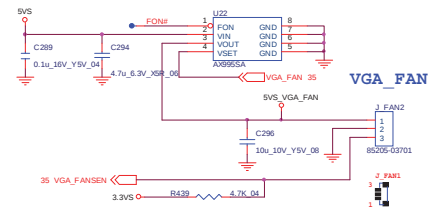
<http://hobi-elektronika.net>

LED, Hotkey, LID SW, Fan

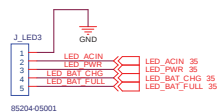
CPU FAN CONTROL



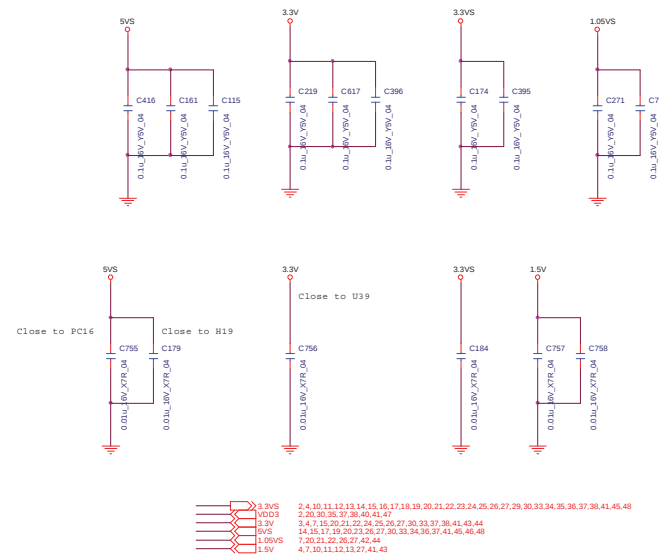
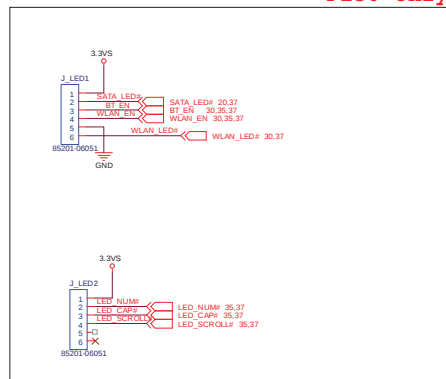
VGA FAN CONTROL



Sheet 31 of 61
LED, Hotkey, LID
SW, Fan

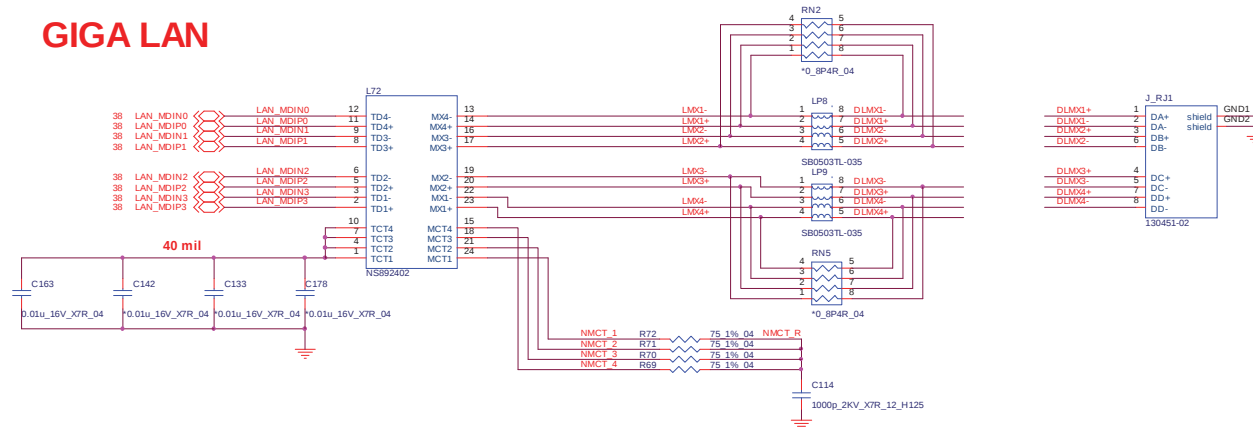


P150 only

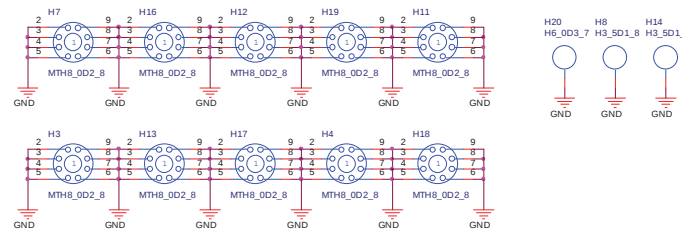
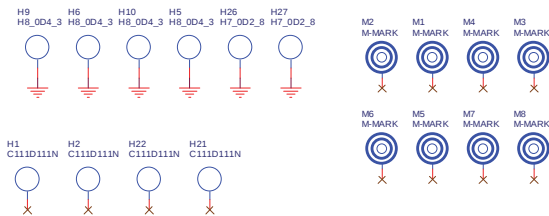


RJ 45

GIGA LAN

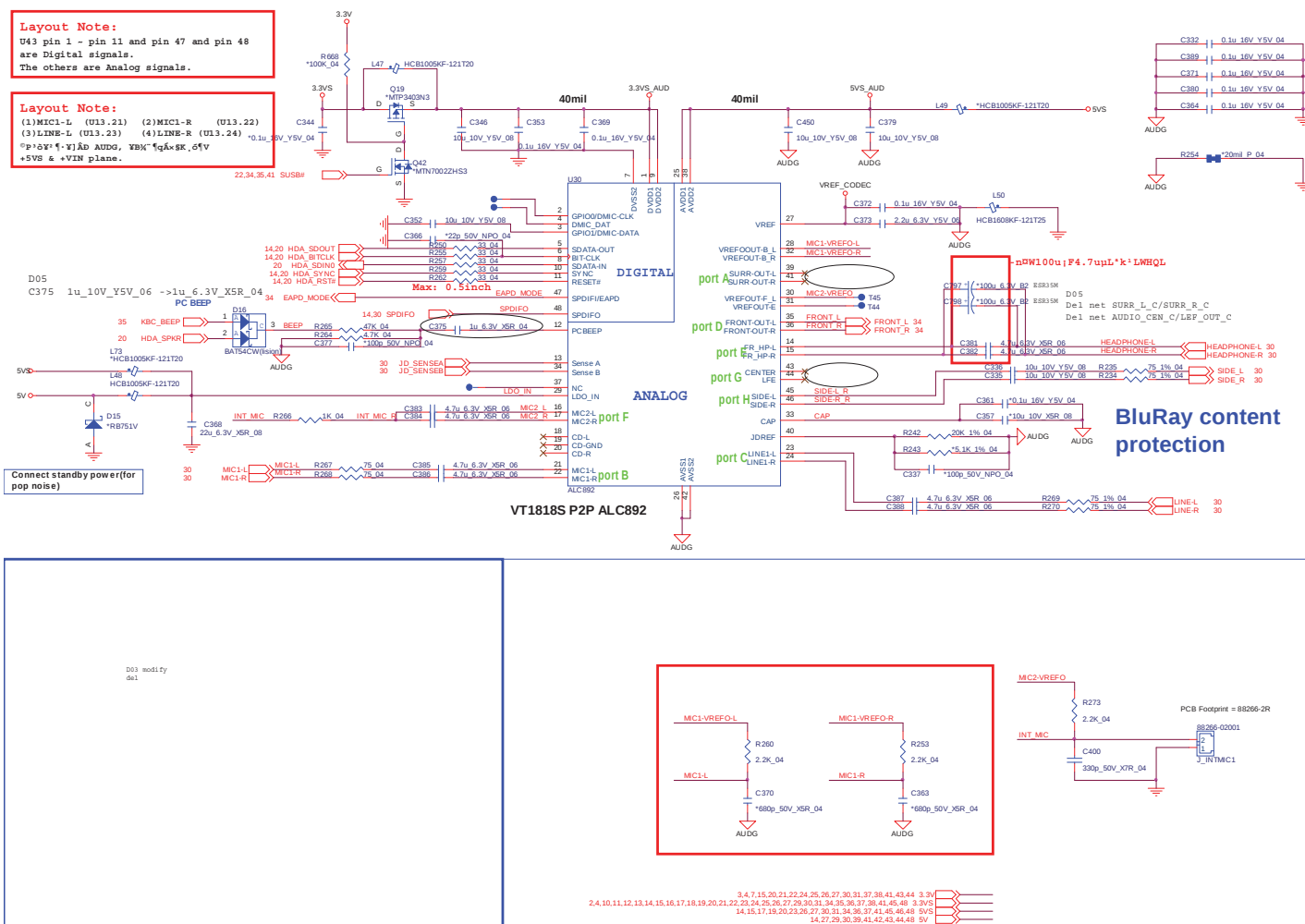


Sheet 32 of 61
RJ 45

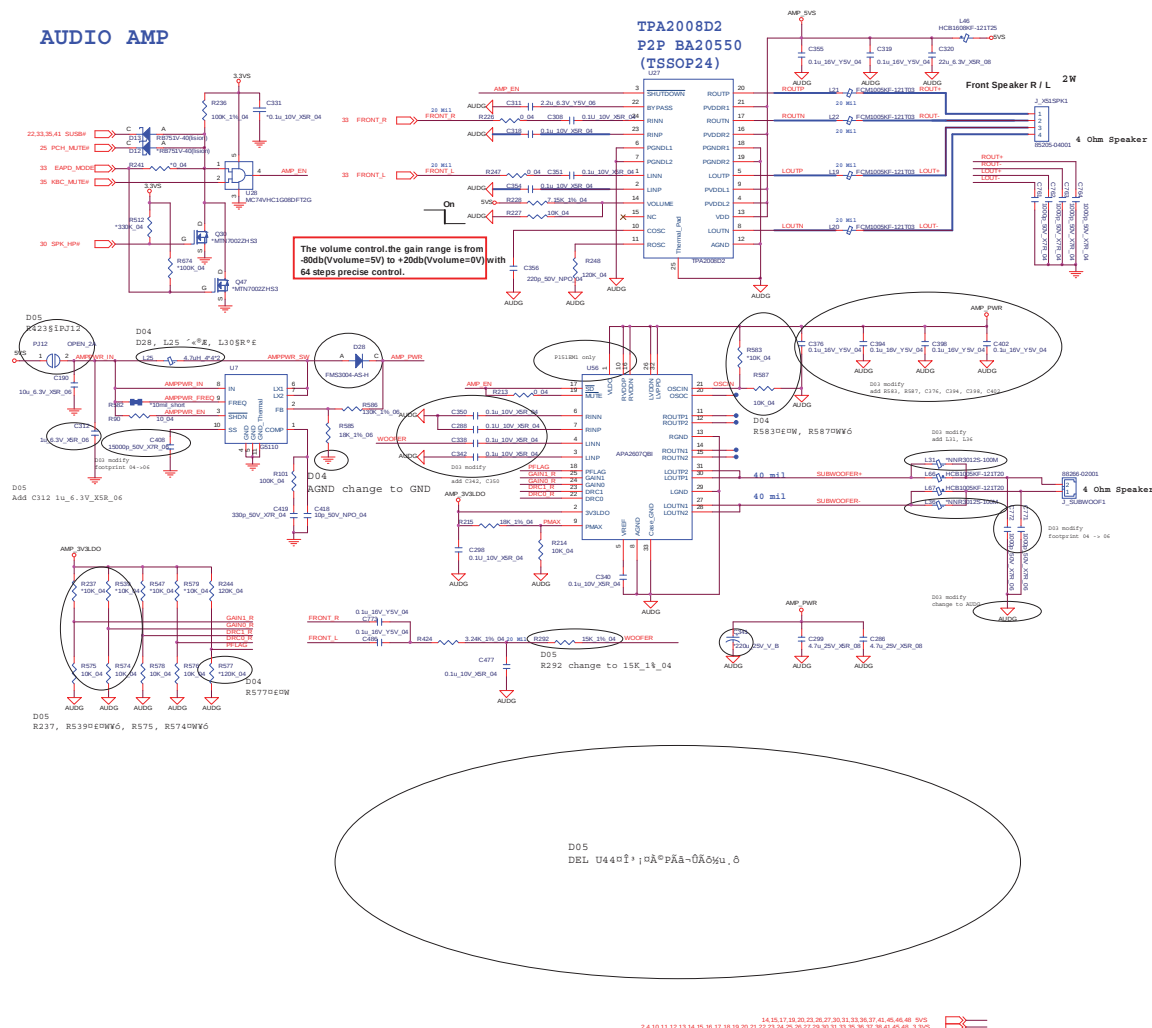


3.3V 3, 4, 7, 15, 20, 21, 22, 24, 25, 26, 27, 30, 31, 33, 37, 38, 41, 43, 44

Sheet 33 of 61
Codec Realtek
ALC892



APA2607-TPA2008D2



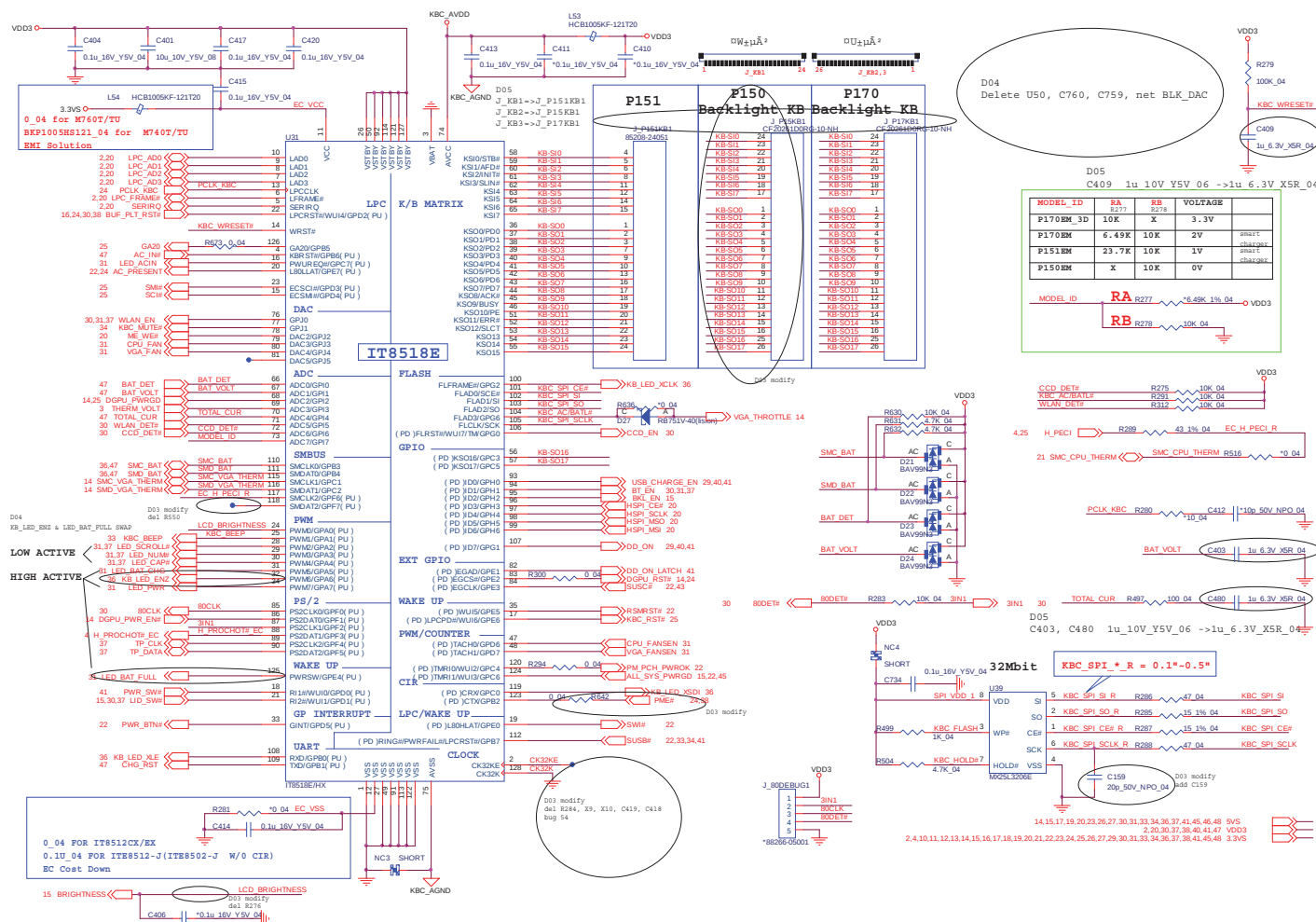
Sheet 34 of 61
APA2607-
TPA2008D2

Schematic Diagrams

<http://hobi-elektronika.net>

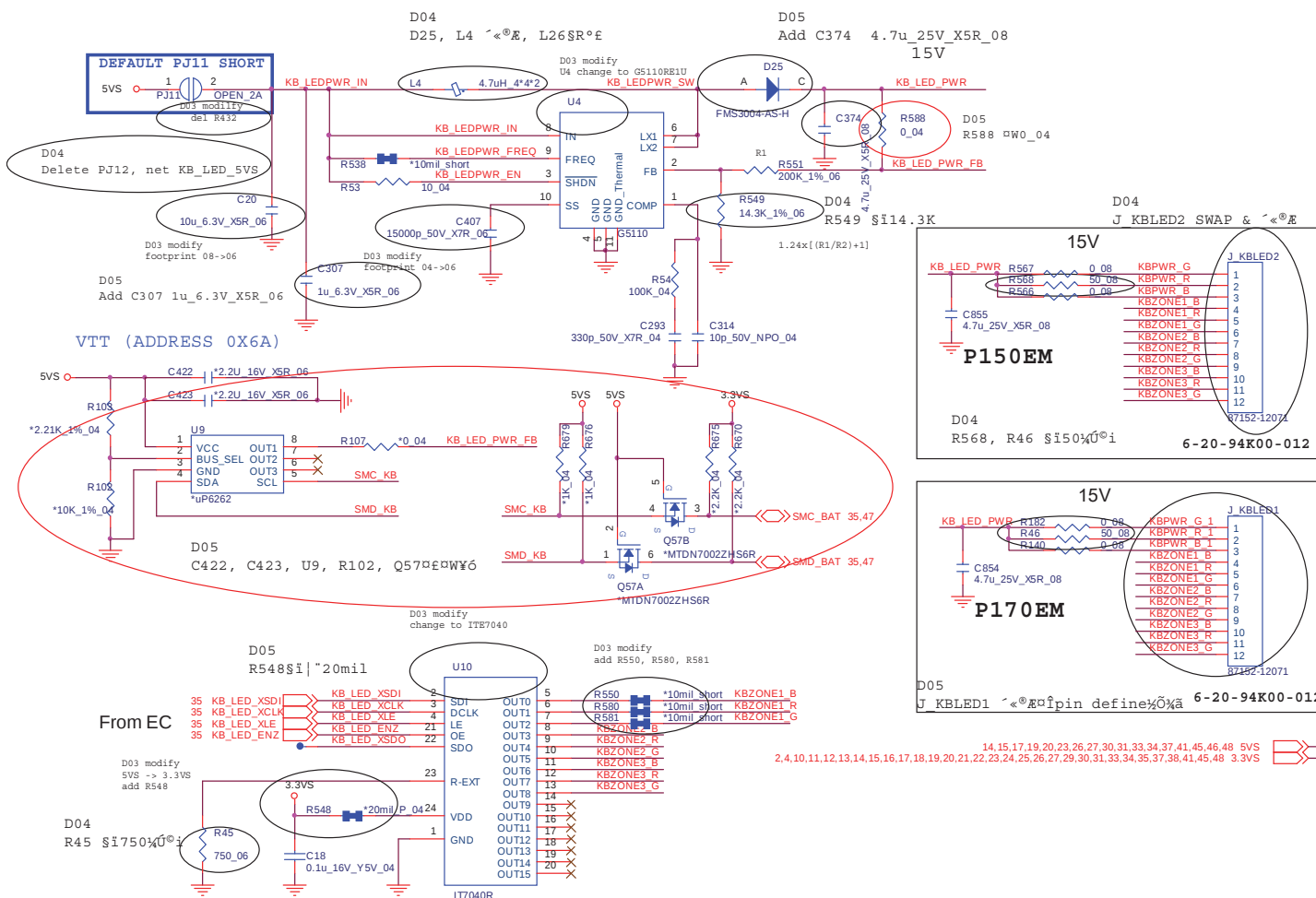
KBC-ITE IT8518E

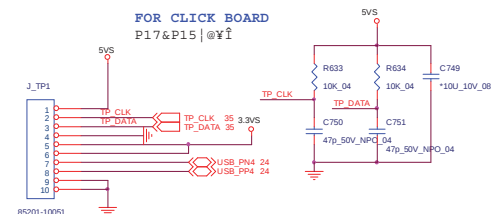
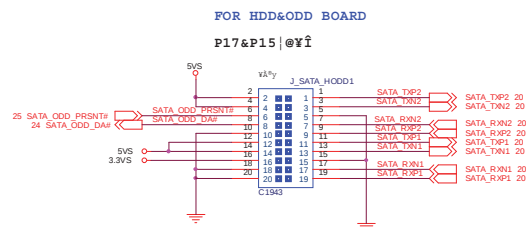
Sheet 35 of 61
KBC-ITE IT8518E



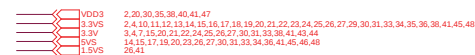
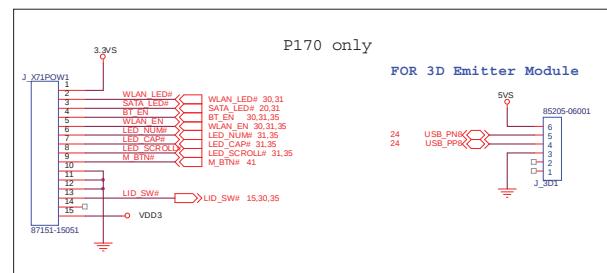
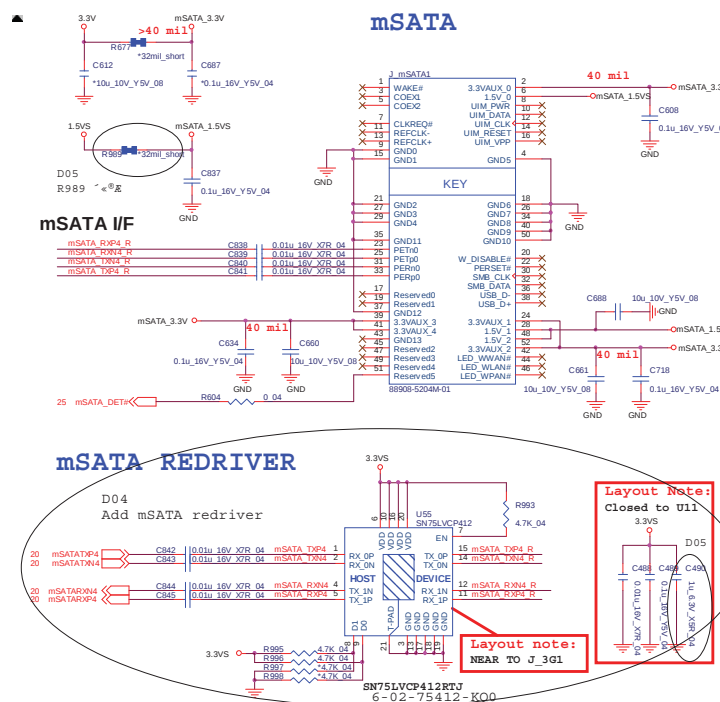
Backlight Keyboard

Sheet 36 of 61
Backlight
Keyboard





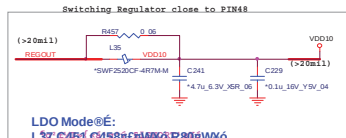
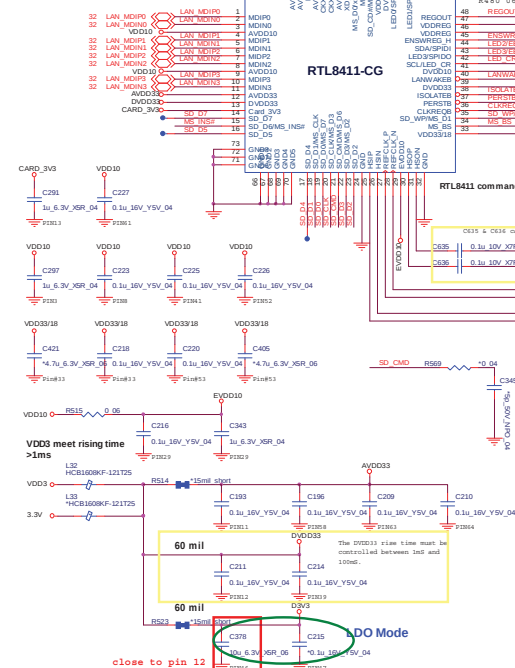
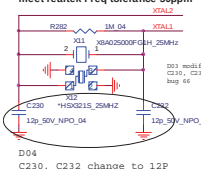
Sheet 37 of 61
mSATA, FAN, TP,
FP, MULTI-CON



Card Reader RTL8411

LAN (RTL8411)

Crystal 8045 & 3225 Co-4ay
meet realtek Freq tolerance 50ppm



Switching Regulator close to PIN48
(>20m11)
LDO Mode@E:
L37,C451,C458,C460,R80,W60



TO EC8518 PIN123 PME#.
TO SB PCH WAKE#.



RTL8411 command



CL35 & CL34 capacitors must be close to pin side.



60 mil



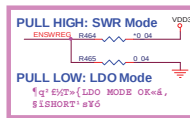
60 mil



DO Mode



close to pin 12



PULL HIGH: SWR Mode
PULL LOW: LDO Mode



Close to chip



SD_D0_RSD_D1MS_CLK_RSD_D2_RSD_D3MS_D2SD_CLKMS_D3_R



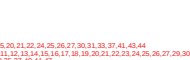
VCC_CARD



Near Cardreader CONN



meet falling time

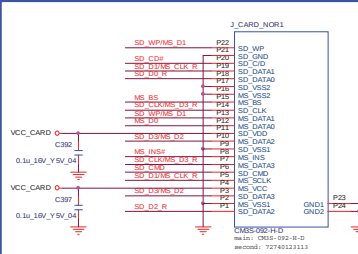


meet falling time



meet falling time

CARD READER



3.3V 3.4,7,15,20,21,22,24,25,26,27,30,31,33,37,41,43,44
3.3V 2,4,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,27,29,30,31,33,34,36,36,37,41,45,48
VDDIO 2,30,30,30,37,40,41,47

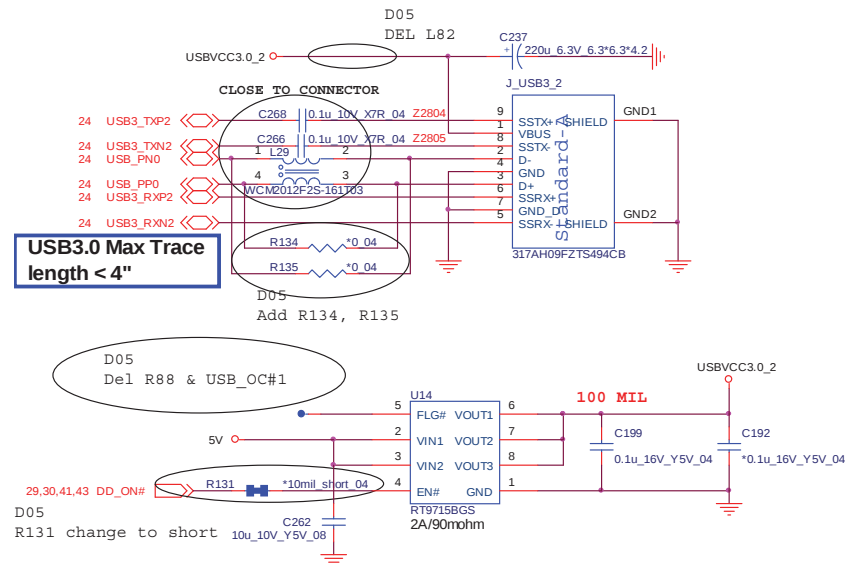
Schematic Diagrams

USB 3.0

<http://hobi-elektronika.net>

 Sheet 39 of 61
 USB 3.0

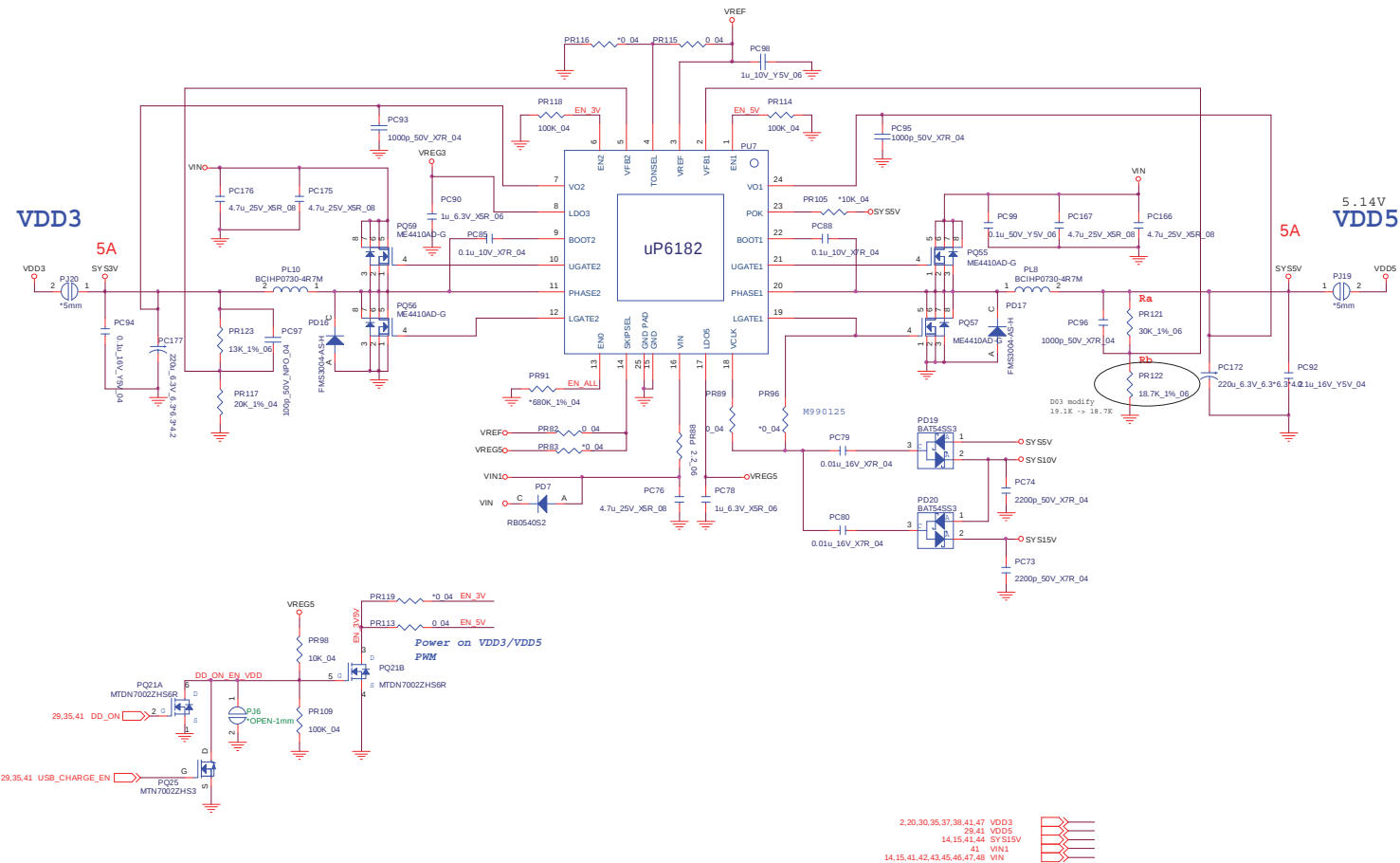
USB3.0 PORT2



14,27,29,30,33,41,42,43,44,48 5V

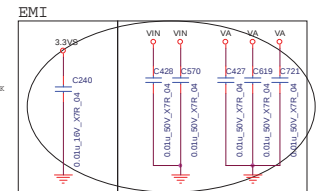
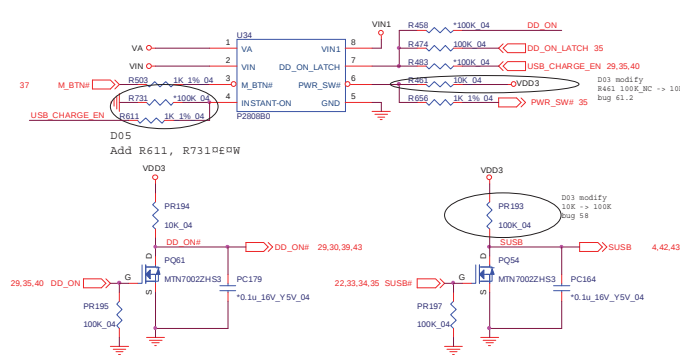
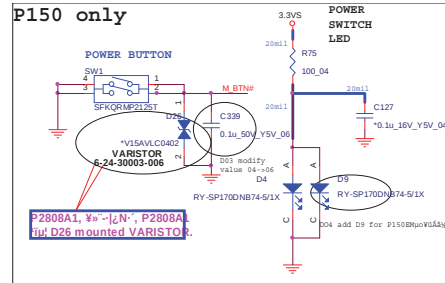


VDD3, VDD5

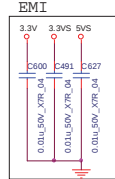


Sheet 40 of 61
VDD3, VDD5

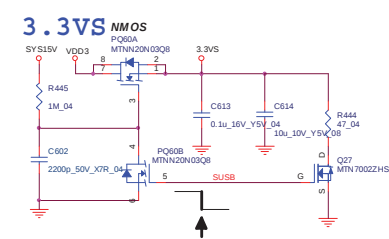
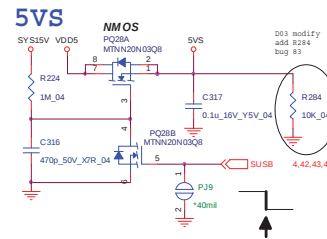
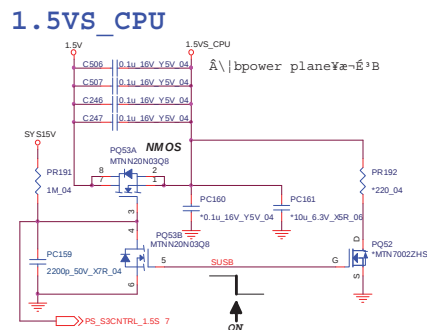
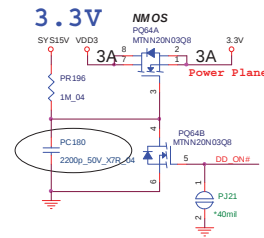
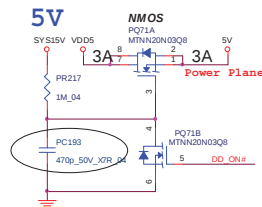
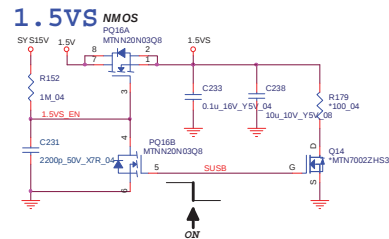
5VS, 3.3VS, 1.5VS



D05
C240: 0.1u 16V_Y5V_04 -> 0.01u 16V_X7R_04
C428, C570, C427, C619, C721:
0.1u 50V_Y5V_06 -> 0.01u 50V_X7R_04



D05
Add C600, C491, C627 0.01u 50V_X7R_04



40 VIN1
47 VIN
14,15,40,42,43,45,46,47,48 VIN
47 1.5VS CPU
4,7,10,11,12,13,27,31,43 1.5VS
26,37 1.5VS
14,15,40,44 SYS5V
29,40 VDD5
220,30,35,37,38,40,47 VDD3
14,15,17,19,20,22,26,27,30,31,33,34,36,37,45,46,48 5V
3,4,7,15,20,21,22,24,25,26,27,30,31,33,37,38,43,44 3.3V
2,4,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,27,29,30,31,33,34,35,36,37,38,45,48 3.3VS

B. Schematic Diagrams

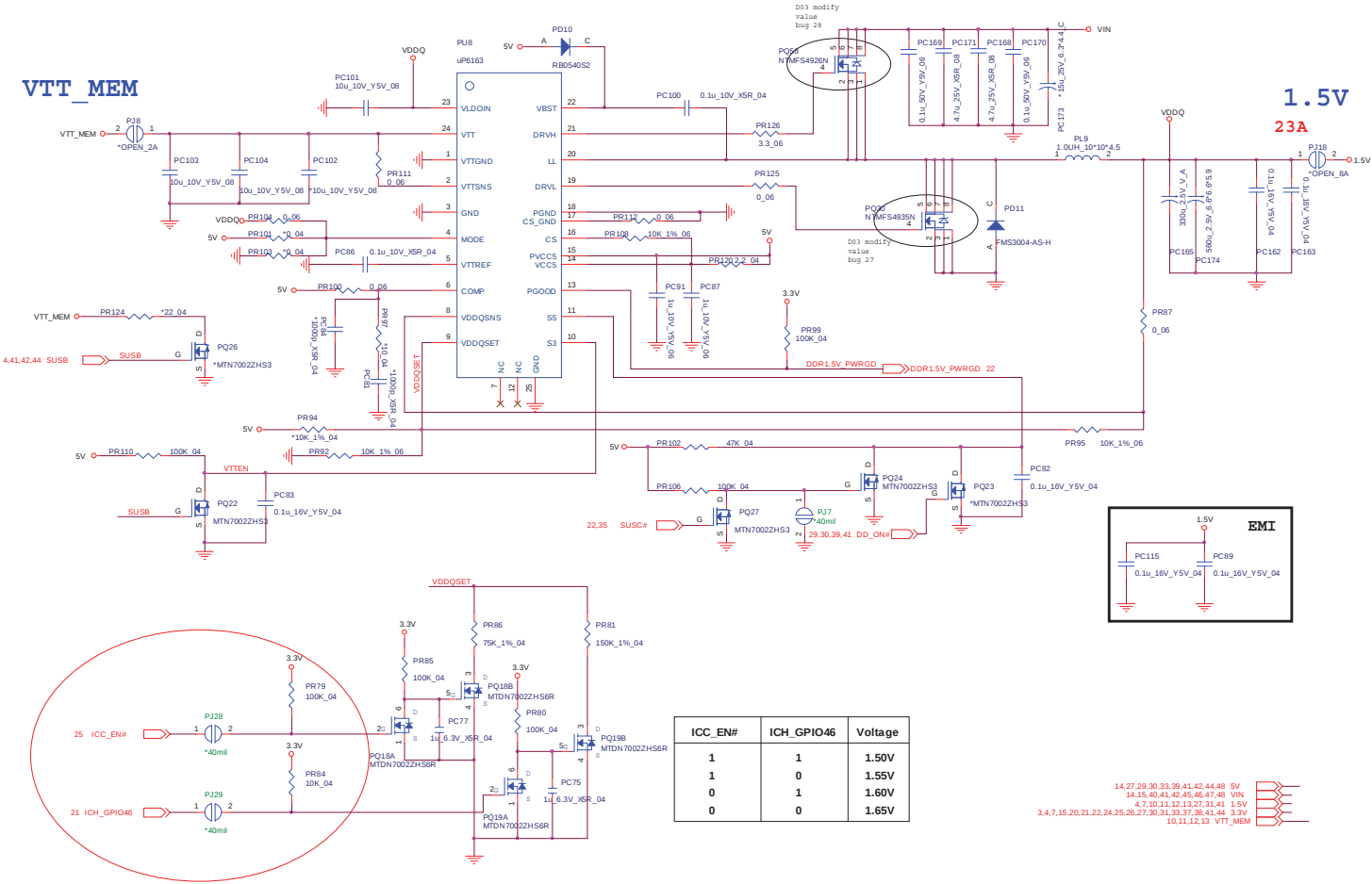


Schematic Diagrams

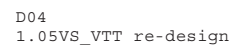
<http://hobi-elektronika.net>

Power 1.5V/VTT_MEM

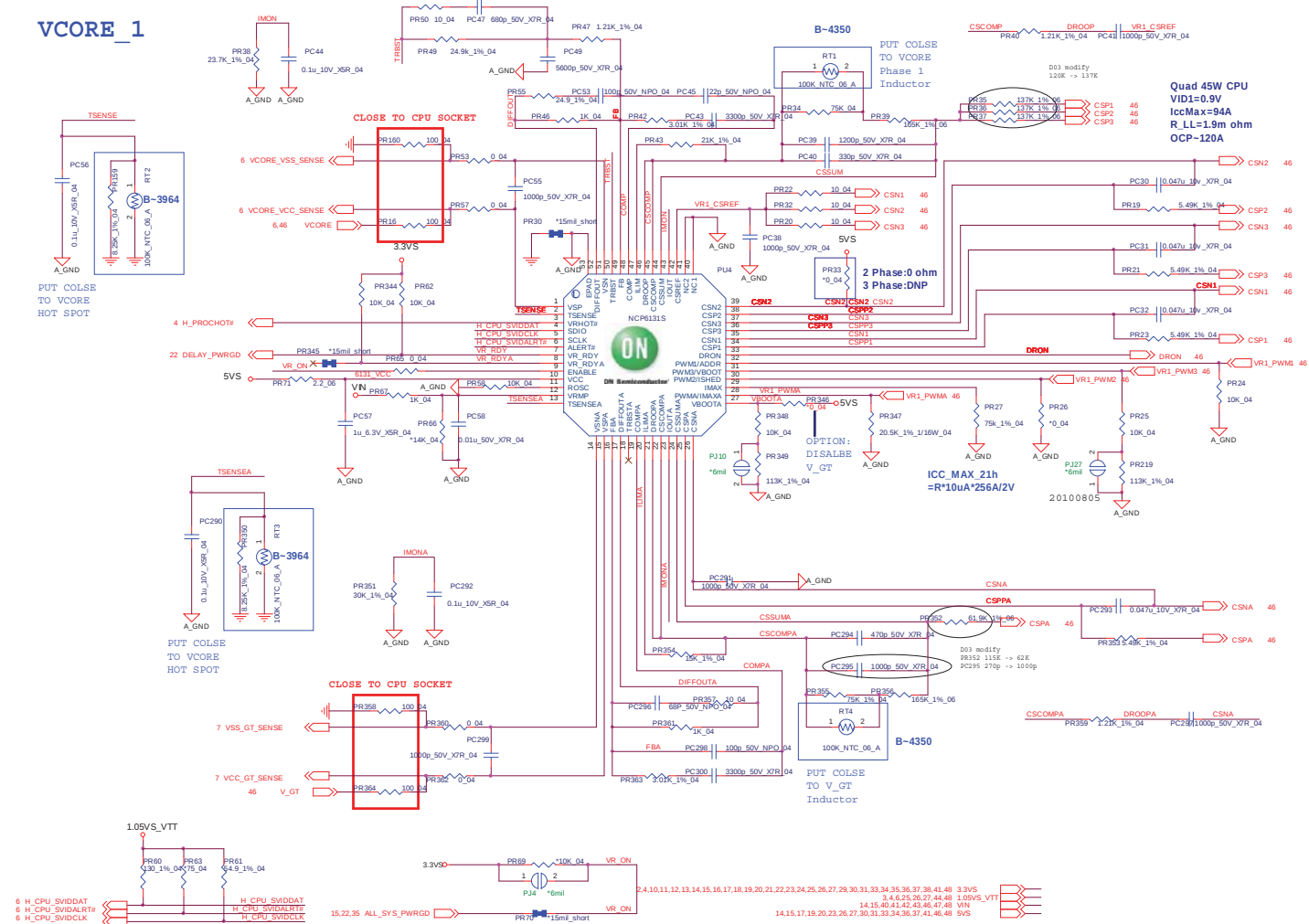
Sheet 43 of 61
Power 1.5V/
VTT_MEM



1.8VS



Power V-Core 1

Sheet 45 of 61
Power V-Core 1

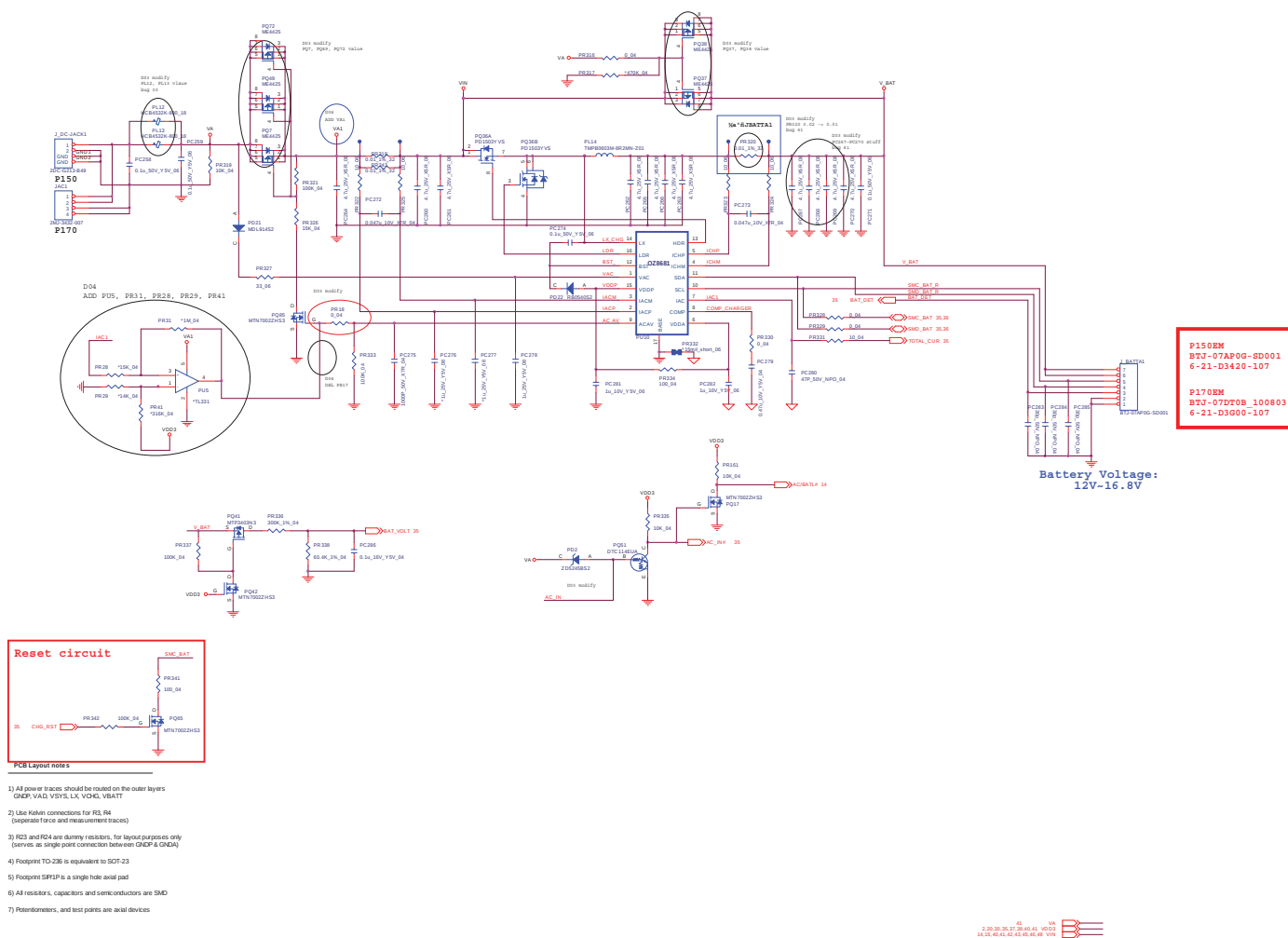
B.Schematic Diagrams



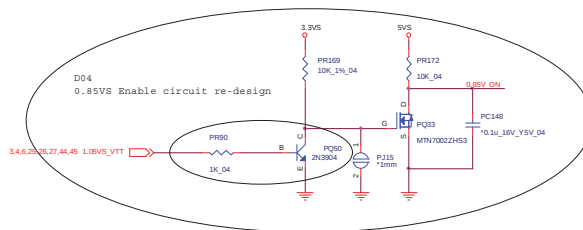
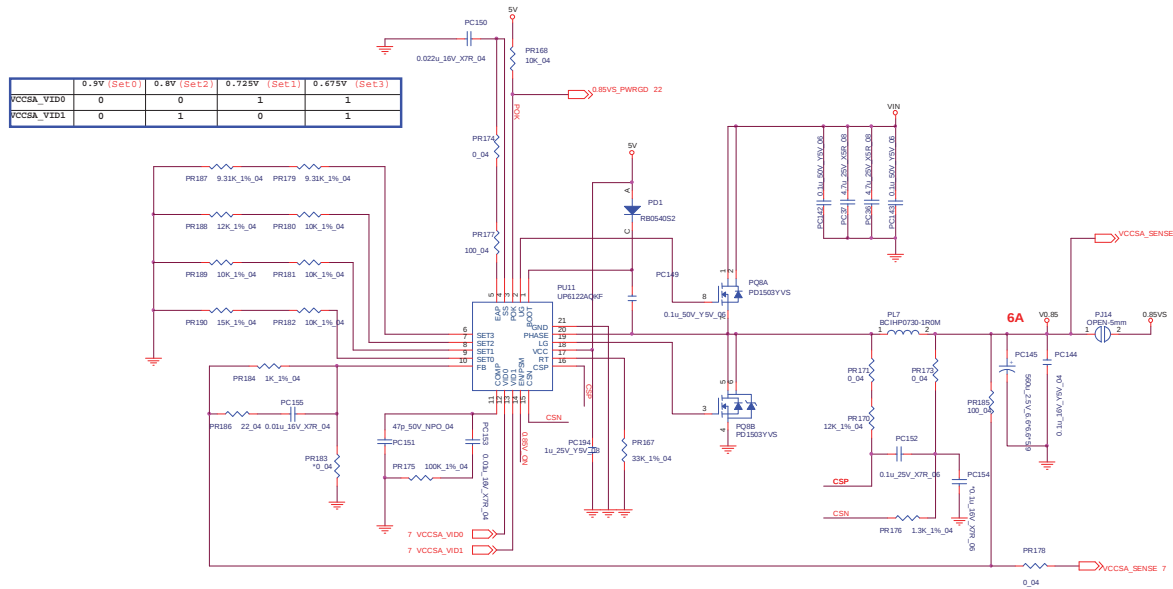
Schematic Diagrams

<http://hobi-elektronika.net>

AC_In, Charger

 Sheet 47 of 61
 AC_In, Charger


Power 0.85VS



5V 14,15,17,19,20,21,26,27,30,31,33,34,36,37,41,46,46
 3.3V 2,4,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,27,29,30,31,33,34,35,36,37,38,41,46
 0.85VS 7 14,27,29,30,33,38,41,42,43,44
 VIN 14,15,40,41,42,43,45,46,47

Sheet 48 of 61
Power 0.85VS

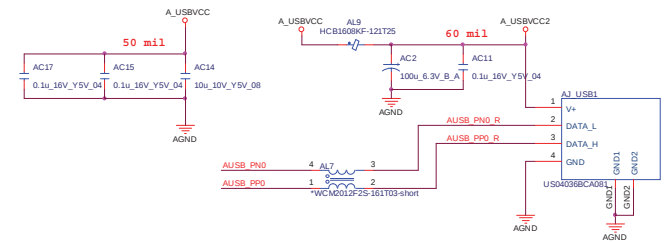
Schematic Diagrams

<http://hobi-elektronika.net>

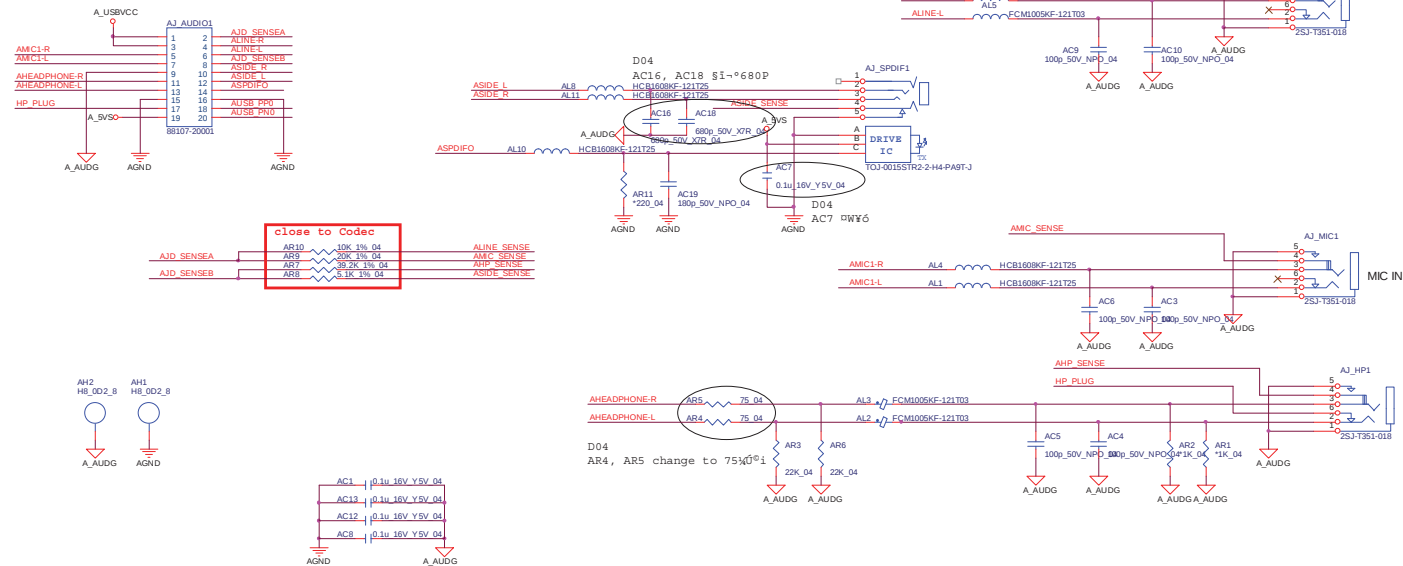
Audio Board

Sheet 49 of 61
Audio Board

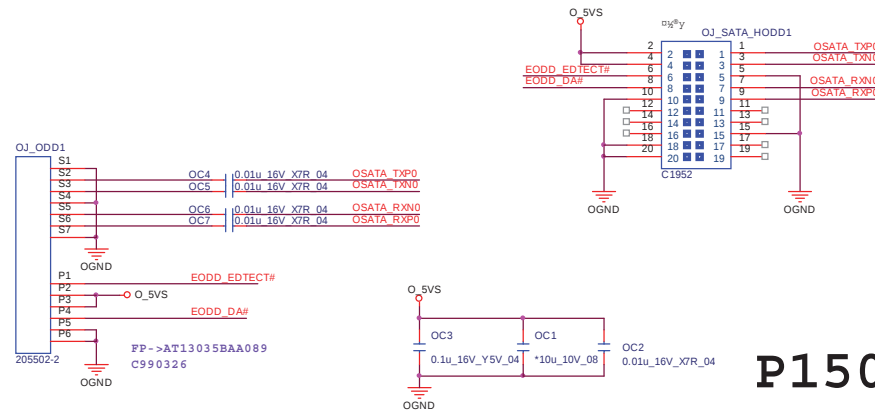
USB PORT



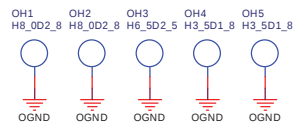
AUDIO JACK



P150 ODD Board



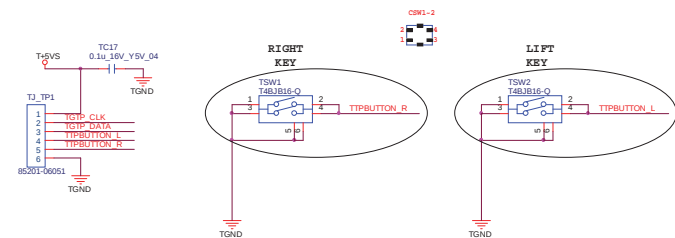
P150 ONLY



Sheet 50 of 61
P150 ODD Board

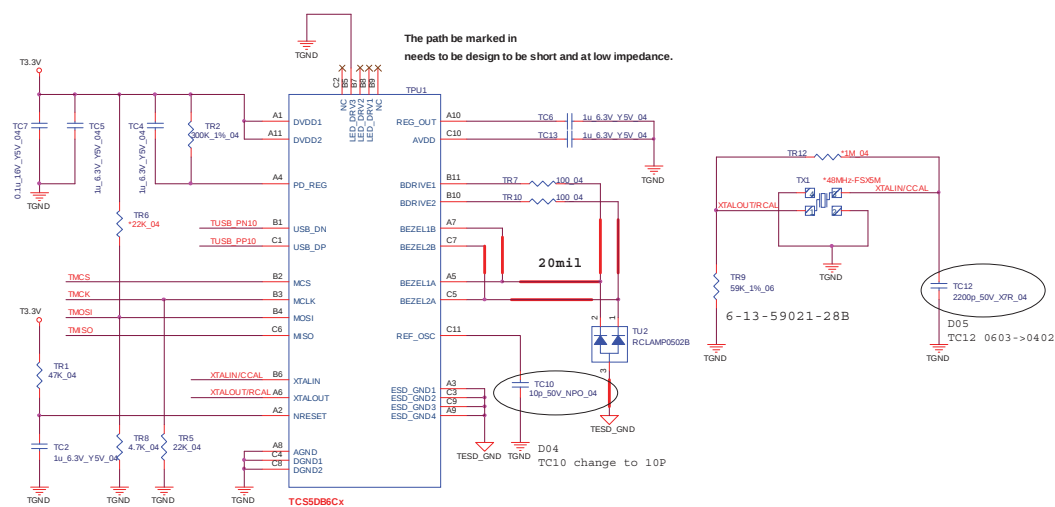
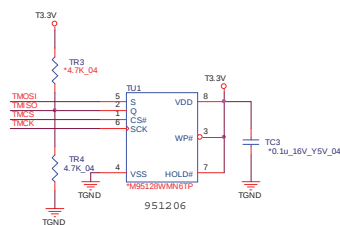
B.Schematic Diagrams

P150 Click Board



The TESD_GND trace has to be wide (> 20mil)

The path be marked in
needs to be design to be short and at low impedance.

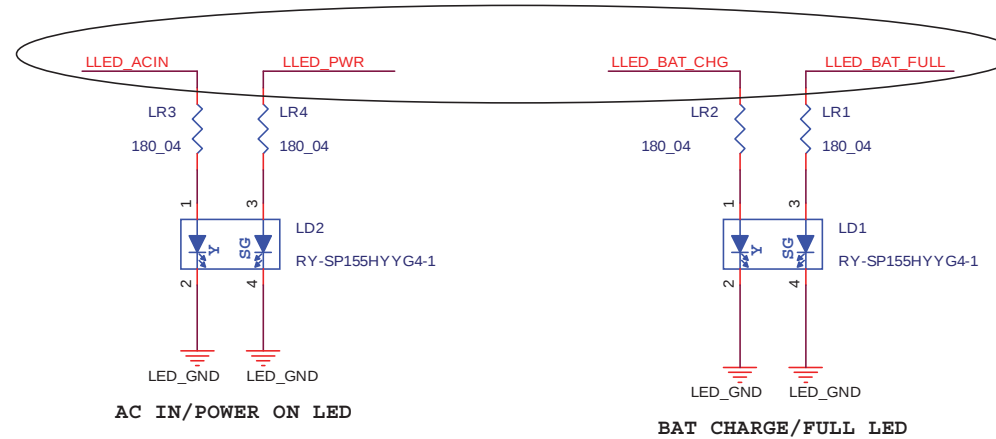
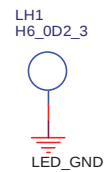
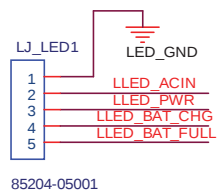


P150 ONLY

W/O finger printer P/N: 6-71-P15E2-D01-1

P150 LED 1 Board

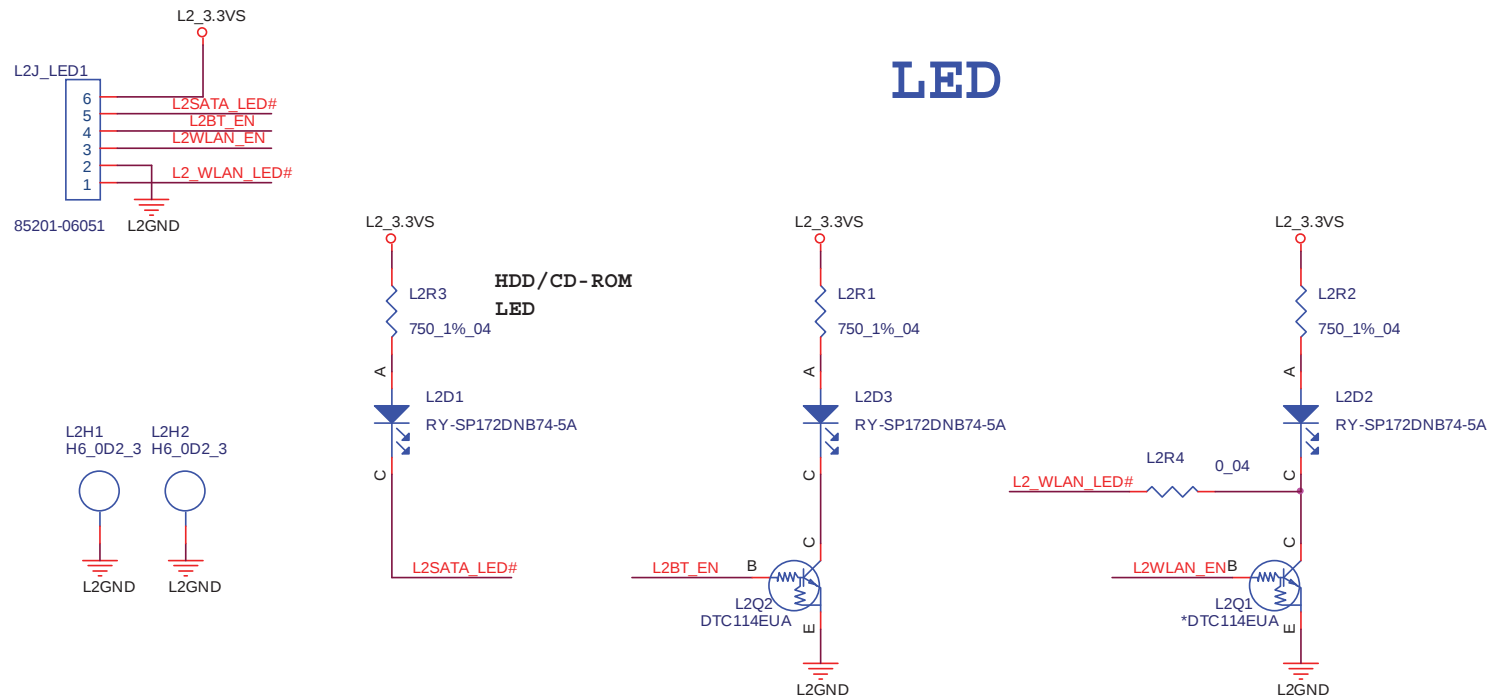
P17&P15



Sheet 52 of 61
P150 LED 1 Board

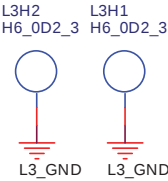
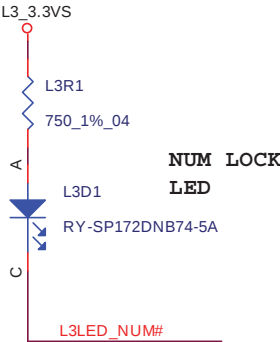
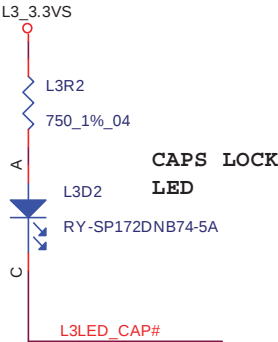
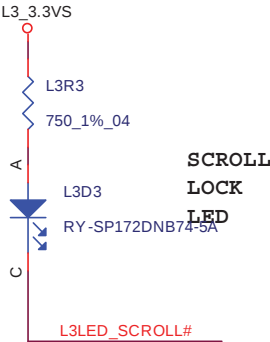
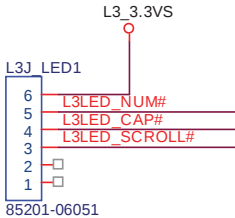
P150 LED 2 Board

Sheet 53 of 61
P150 LED 2 Board



LED

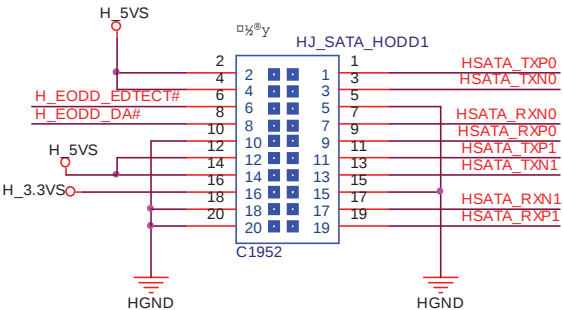
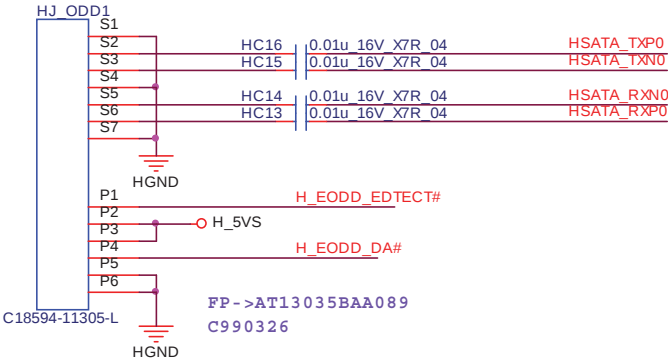
P150 LED 3 Board



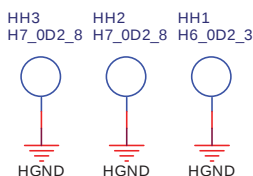
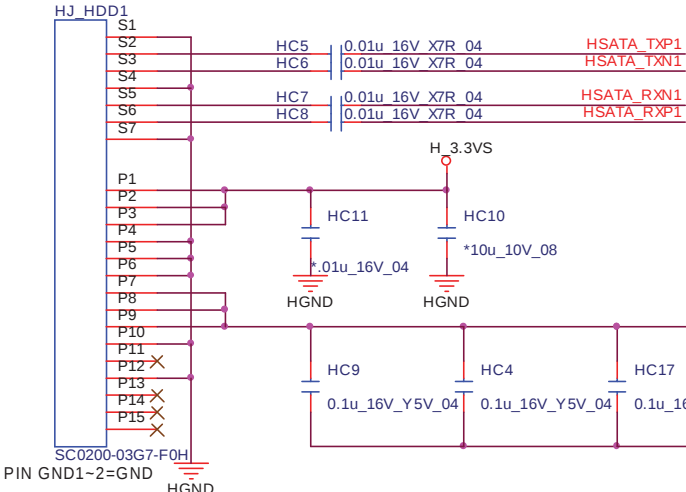
Sheet 54 of 61
P150 LED 3 Board

P170 HDD & ODD Board

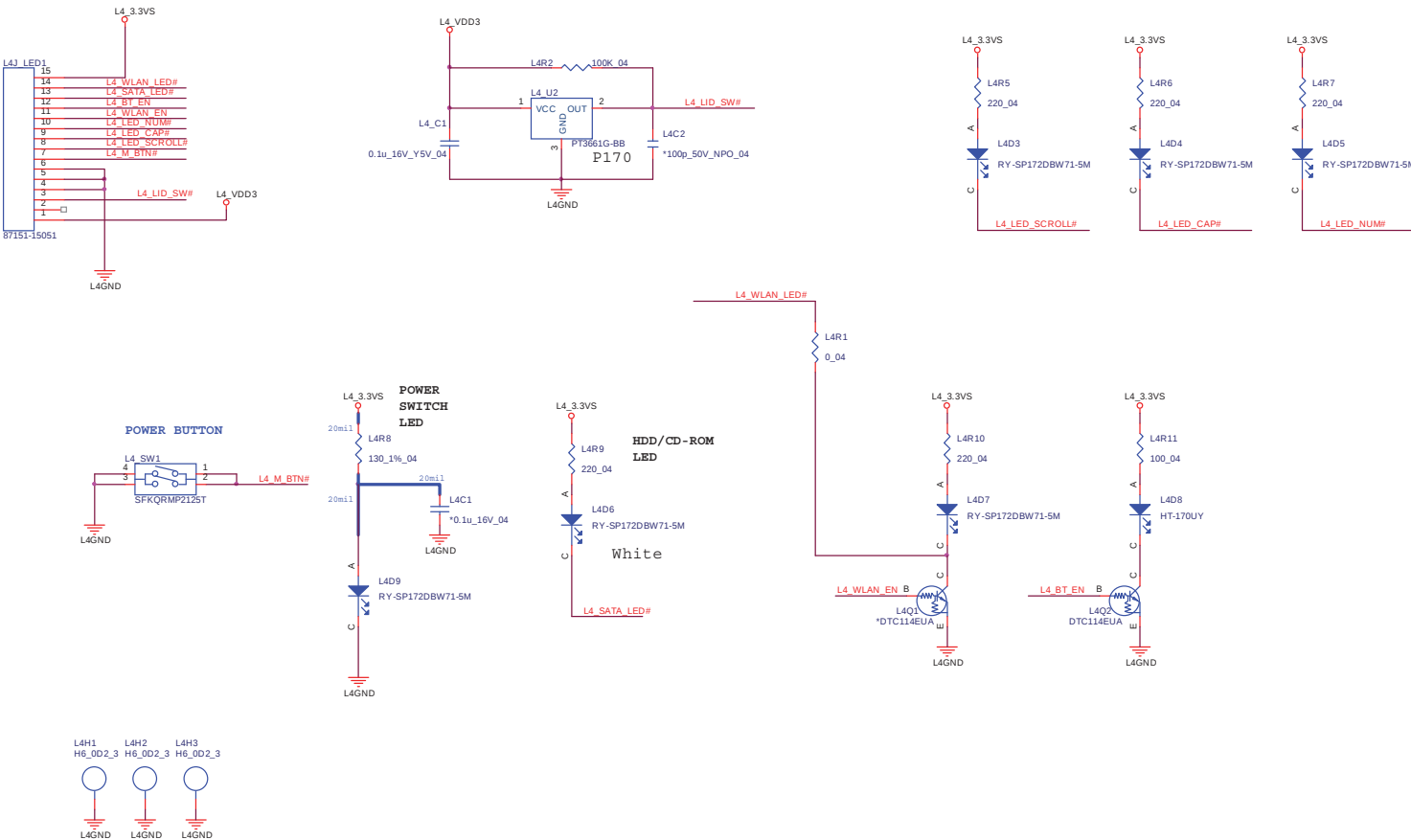
Sheet 55 of 61
P170 HDD& ODD
Board



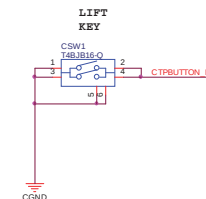
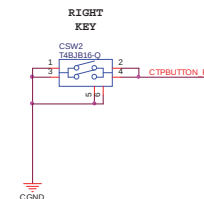
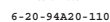
P170 ONLY



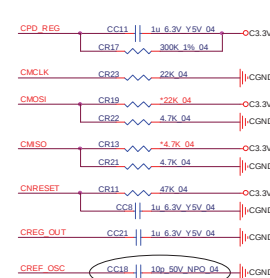
P170 LED Board



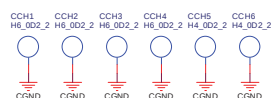
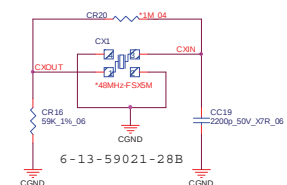
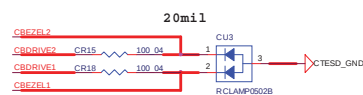
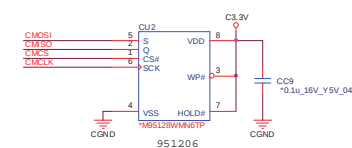
Sheet 56 of 61
P170 LED Board



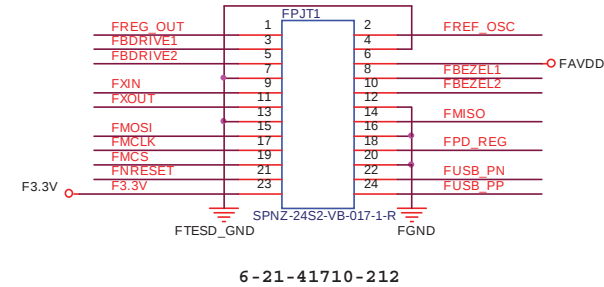
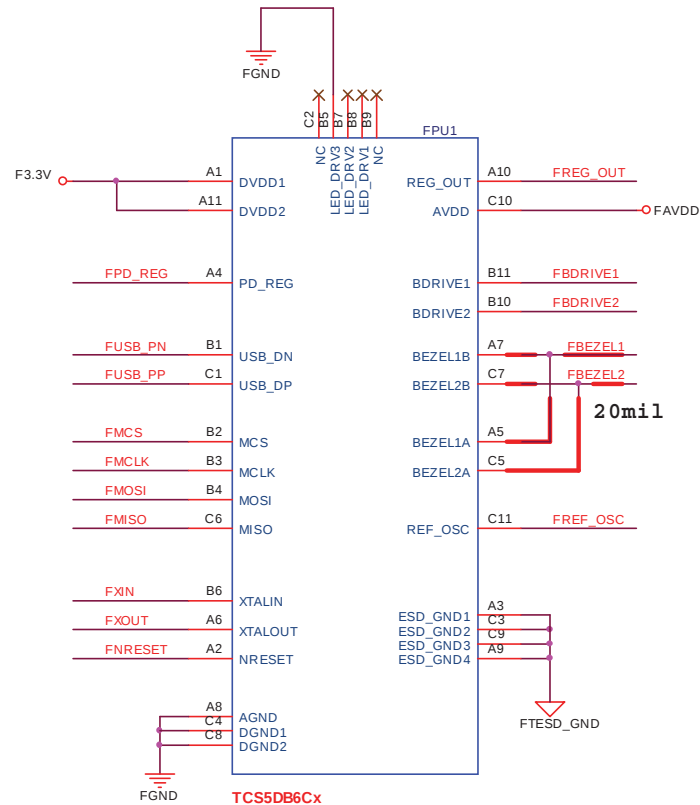
Sheet 57 of 61
P170 Click Board



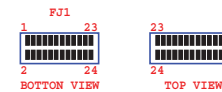
D04
CC18 change to 10P



P170 Fingerprint Board



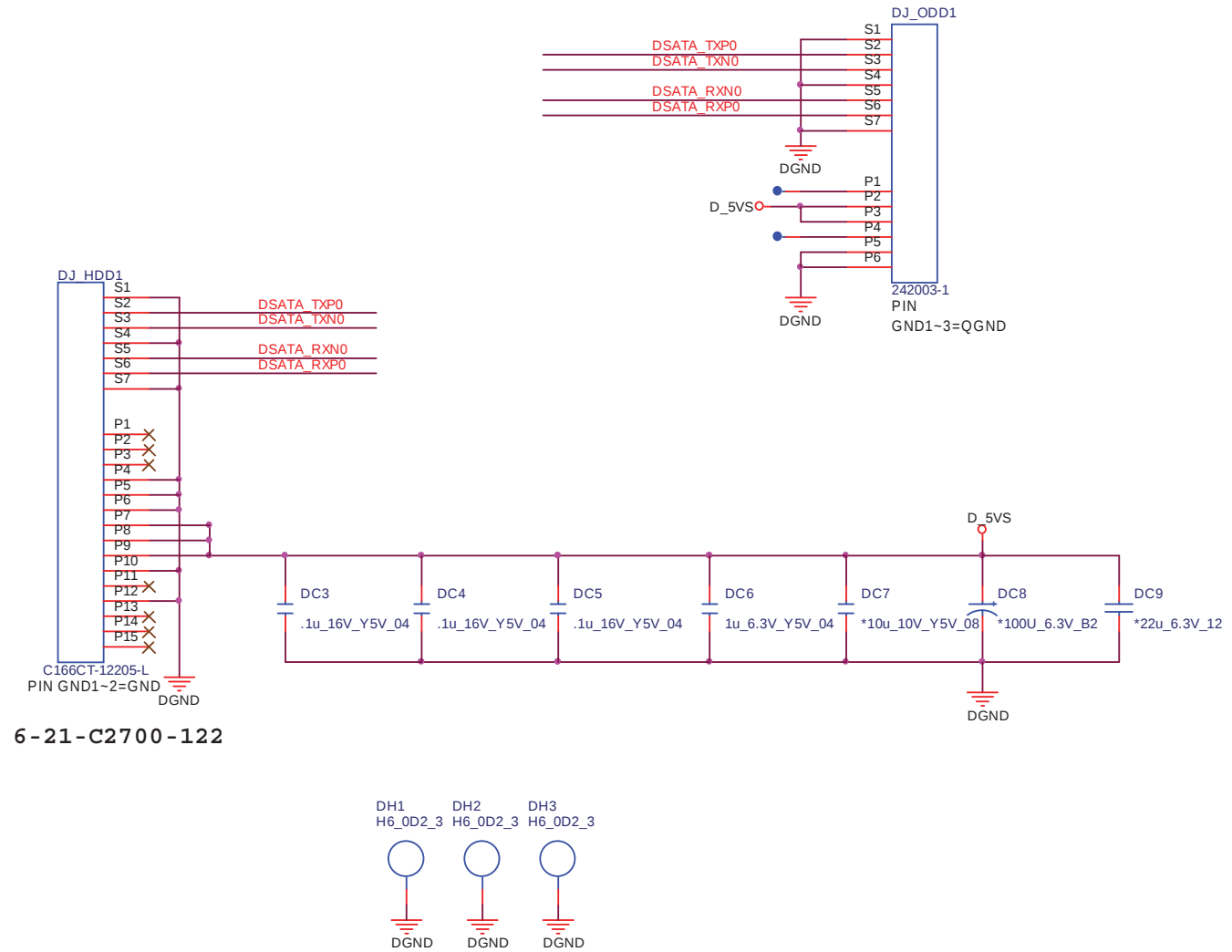
P170 ONLY



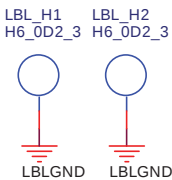
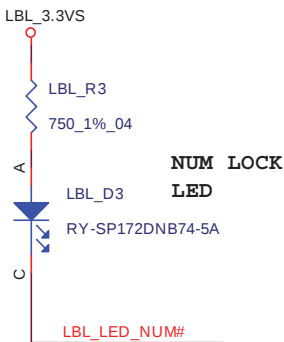
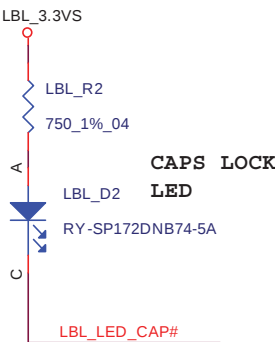
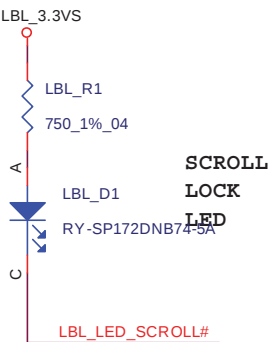
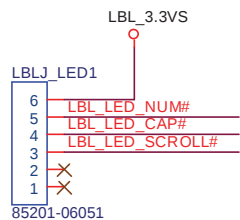
Sheet 58 of 61
P170 Fingerprint
Board

P150 HDD Board

Sheet 59 of 61
P150 HDD Board



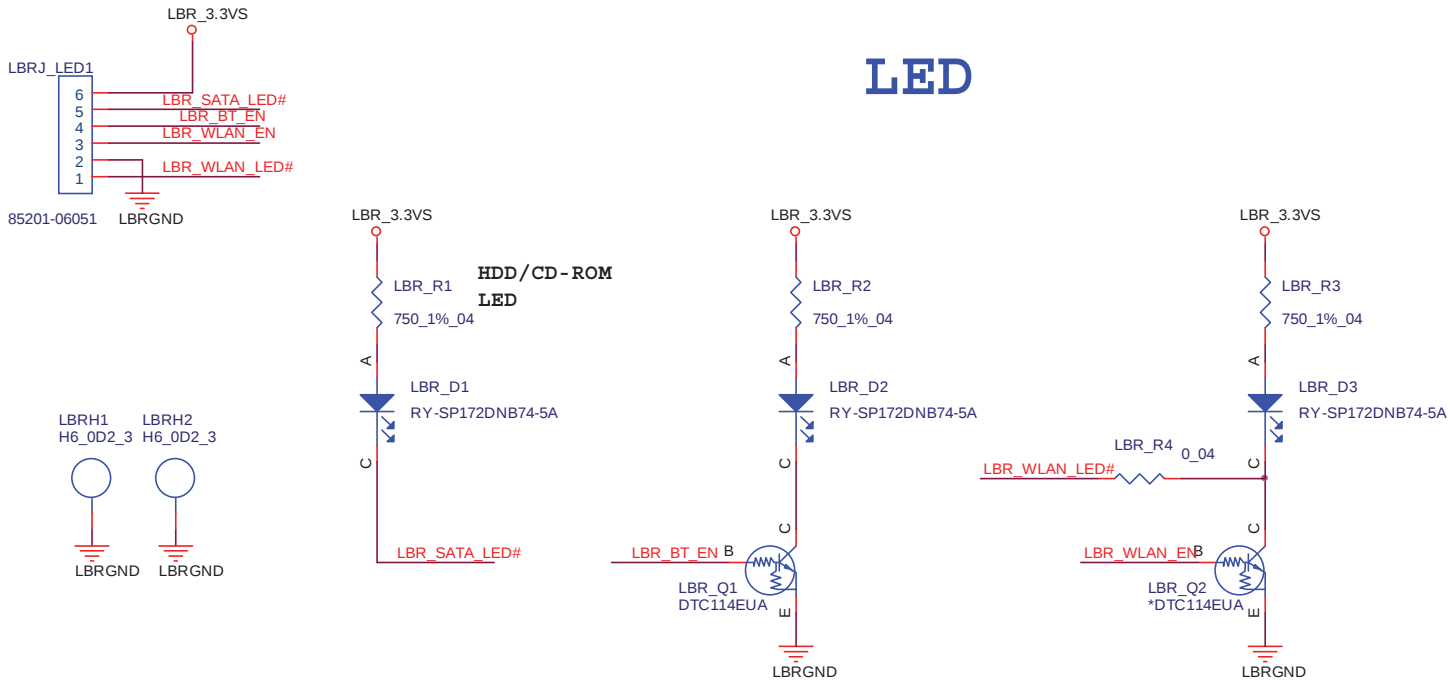
P150 LED Board_L



Sheet 60 of 61
P150 LED Board_L

P150 LED Board_R

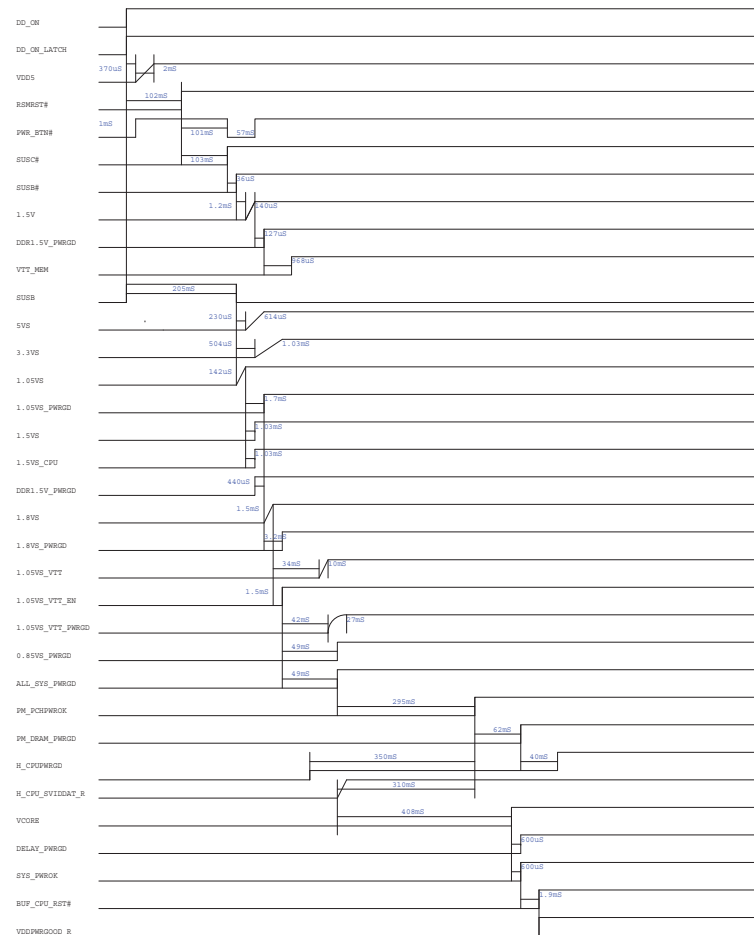
Sheet 61 of 61
P150 LED Board_R



LED

Power on Sequence

P150EM_D02 POWER on SEQUENCE



Sheet 62 of 61
Power on
Sequence

Appendix C: Updating the FLASH ROM BIOS

To update the FLASH ROM BIOS, you must:

- Download the BIOS update from the web site.
- Unzip the files onto a bootable CD/DVD/USB Flash Drive.
- Reboot your computer from an external CD/DVD/USB Flash Drive.
- Use the flash tools to update the flash BIOS using the commands indicated below.
- Restart the computer booting from the HDD and press **F2** at startup enter the BIOS.
- Load setup defaults from the BIOS and save the default settings and exit the BIOS to restart the computer.
- After rebooting the computer you may restart the computer again and make any required changes to the default BIOS settings.

Download the BIOS

1. Go to www.clevo.com.tw and point to **E-Services** and click **E-Channel**.
2. Use your user ID and password to access the appropriate download area (BIOS), and download the latest BIOS files (the BIOS file will be contained in a batch file that may be run directly once unzipped) for your computer model (see sidebar for important information on BIOS versions).

Unzip the downloaded files to a bootable CD/DVD/ or USB Flash drive

1. Insert a bootable CD/DVD/USB flash drive into the CD/DVD drive/USB port of the computer containing the downloaded files.
2. Use a tool such as Winzip or Winrar to unzip all the BIOS files and refresh tools to your bootable CD/DVD/USB flash drive (you may need to create a bootable CD/DVD with the files using a 3rd party software).

Set the computer to boot from the external drive

1. With the bootable CD/DVD/USB flash drive containing the BIOS files in your CD/DVD drive/USB port, restart the computer and press **F2** (in most cases) to enter the BIOS.
2. Use the arrow keys to highlight the **Boot** menu.
3. Use the “+” and “-” keys to move boot devices up and down the priority order.
4. Make sure that the CD/DVD drive/USB flash drive is set first in the boot priority of the BIOS.
5. Press **F4** to save any changes you have made and exit the BIOS to restart the computer.



BIOS Version

Make sure you download the latest correct version of the BIOS appropriate for the computer model you are working on.

You should only download BIOS versions that are **V1.01.XX or higher** as appropriate for your computer model.

Note that BIOS versions are not backward compatible and therefore **you may not downgrade your BIOS to an older version** after upgrading to a later version (e.g if you upgrade a BIOS to ver 1.01.05, you **MAY NOT** then go back and flash the BIOS to ver 1.01.04).

BIOS Update

<http://hobi-elektronika.net>

Use the flash tools to update the BIOS

1. Make sure you are not loading any memory management programs such as HIMEM by holding the **F8** key as you see the message “**Starting MS-DOS**”. You will then be prompted to give “**Y**” or “**N**” responses to the programs being loaded by DOS. Choose “**N**” for any memory management programs.
2. You should now be at the DOS prompt e.g: **DISK C:\>** (C is the designated drive letter for the CD/DVD drive/USB flash drive).
3. **Type the following command** at the DOS prompt:

C:\> Flash.bat

4. The utility will then proceed to flash the BIOS.
5. You should then be prompted to press any key to restart the system or turn the power off, and then on again but make sure you remove the CD/DVD/USB flash drive from the CD/DVD drive/USB port before the computer restarts.

Restart the computer (booting from the HDD)

1. With the CD/DVD/USB flash drive removed from the CD/DVD drive/USB port the computer should restart from the HDD.
2. Press **F2** as the computer restarts to enter the BIOS.
3. Use the arrow keys to highlight the **Exit** menu.
4. Select **Load Setup Defaults** (or press **F3**) and select “**Yes**” to confirm the selection.
5. Press **F4** to save any changes you have made and exit the BIOS to restart the computer.

Your computer is now running normally with the updated BIOS

You may now enter the BIOS and make any changes you require to the default settings.