

[illegible]

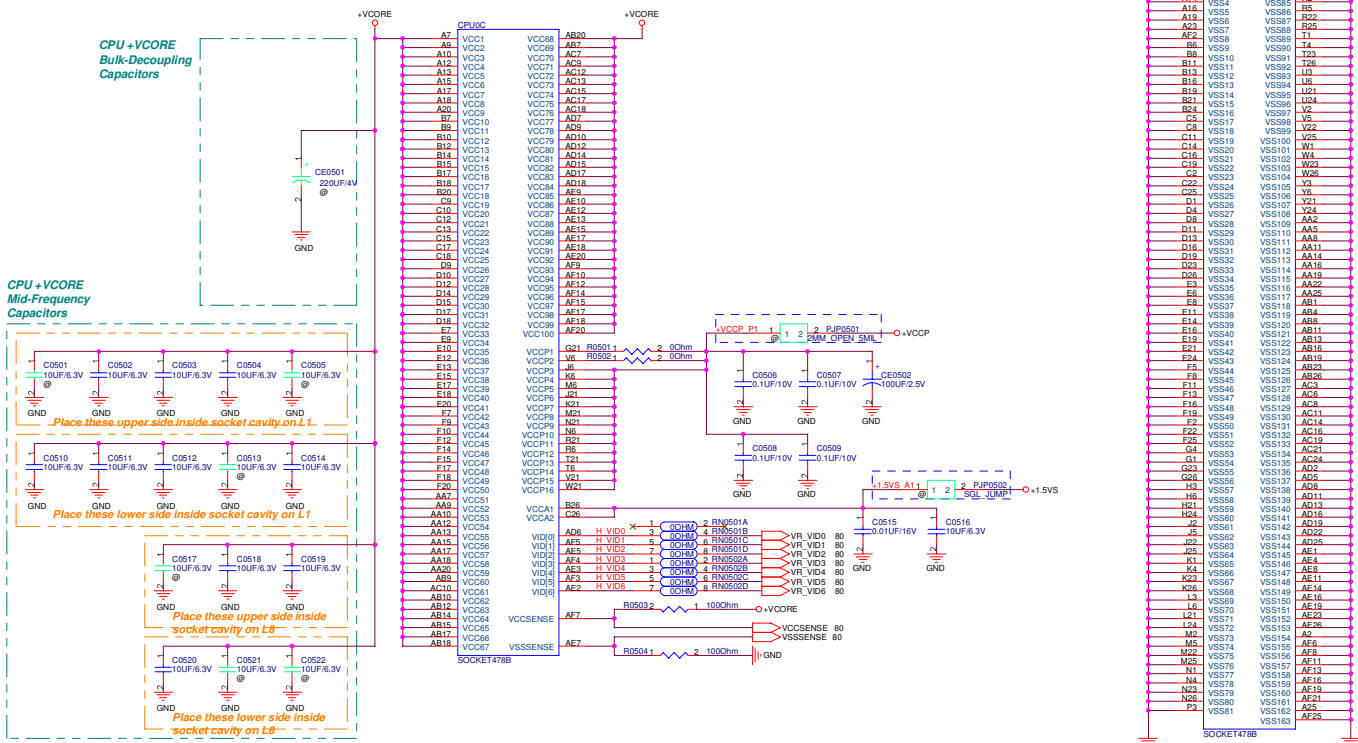
		Title : <u>System Setting</u>	
ASUSTek Computer INC. NB1		Engineer: <u>Gajun_Ho</u>	
Size Custom	Project Name F3Sc		
Date: <u>2007.08.21</u>		Sheet <u>2</u>	of <u>95</u>

<Variant Name>		
		Title : Schematic page name
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho
Size	Project Name	Rev
Custom	F3Sc	2.0
Date: 2008.08.21		Sheet 3 of 95

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+VCCORE Mid-Frequency Capacitor
Intel: 22UF *32
F3S: 10UF *16

+VCCP Decoupling Capacitor
Intel: 270UF *1, 0.1UF *6
F3S: 100UF *1, 0.1UF *4

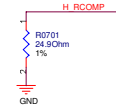
<< Kennedy_Zhang >>

<Variant Name>		Title : Schematic page name	
		Engineer: Gajun_Ho	
ASUSTeK COMPUTER INC			
Size	Project Name		Rev
Custom	F3Sc		2.0
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RCOMP

For Calibrating the FSB IO Buffer



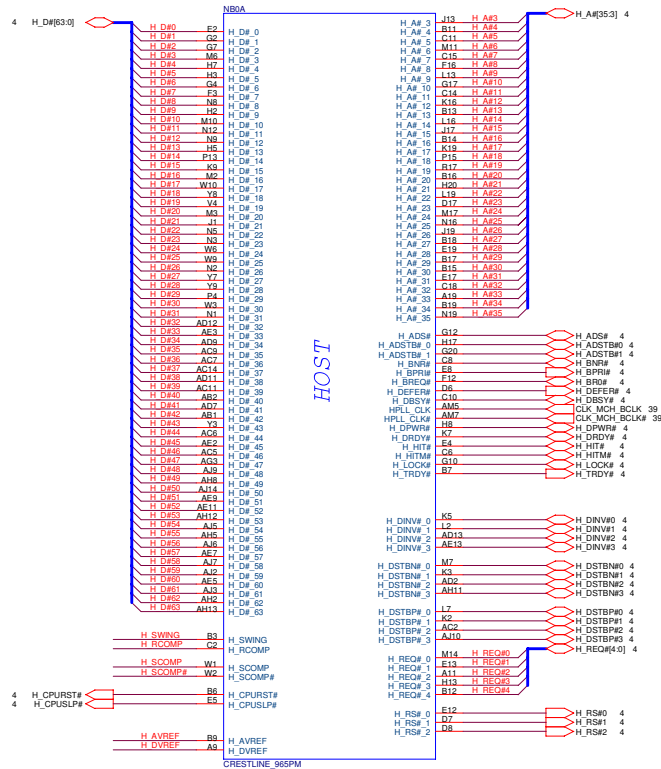
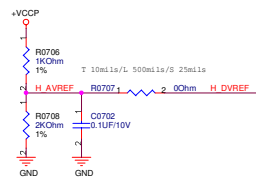
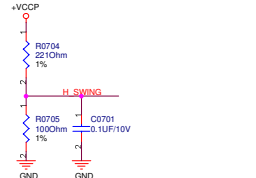
SCOMP

For Slew Rate Compensation on the FSB



Voltage Swing

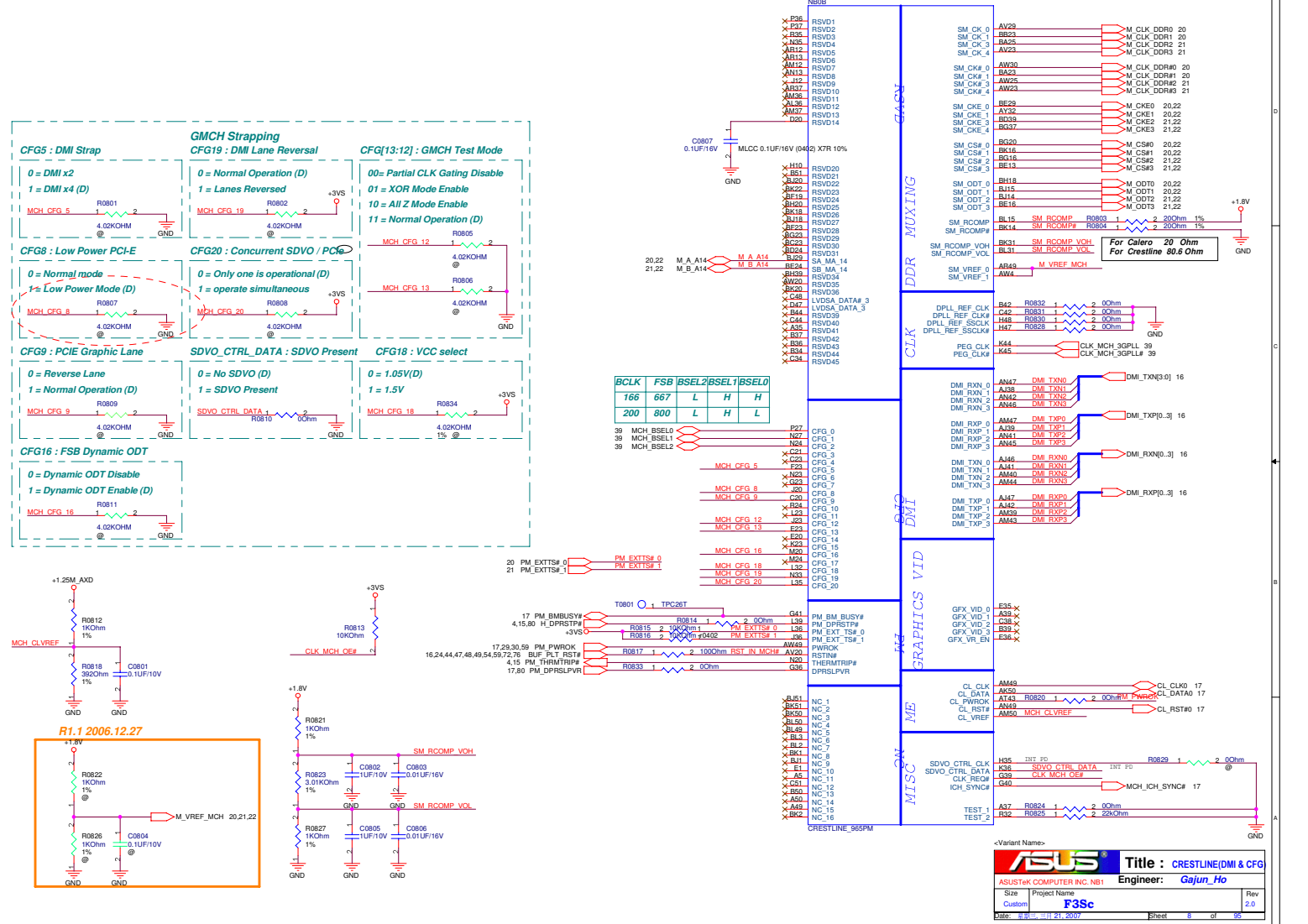
For Providing a Reference Voltage to The FSB RCOMP circuits



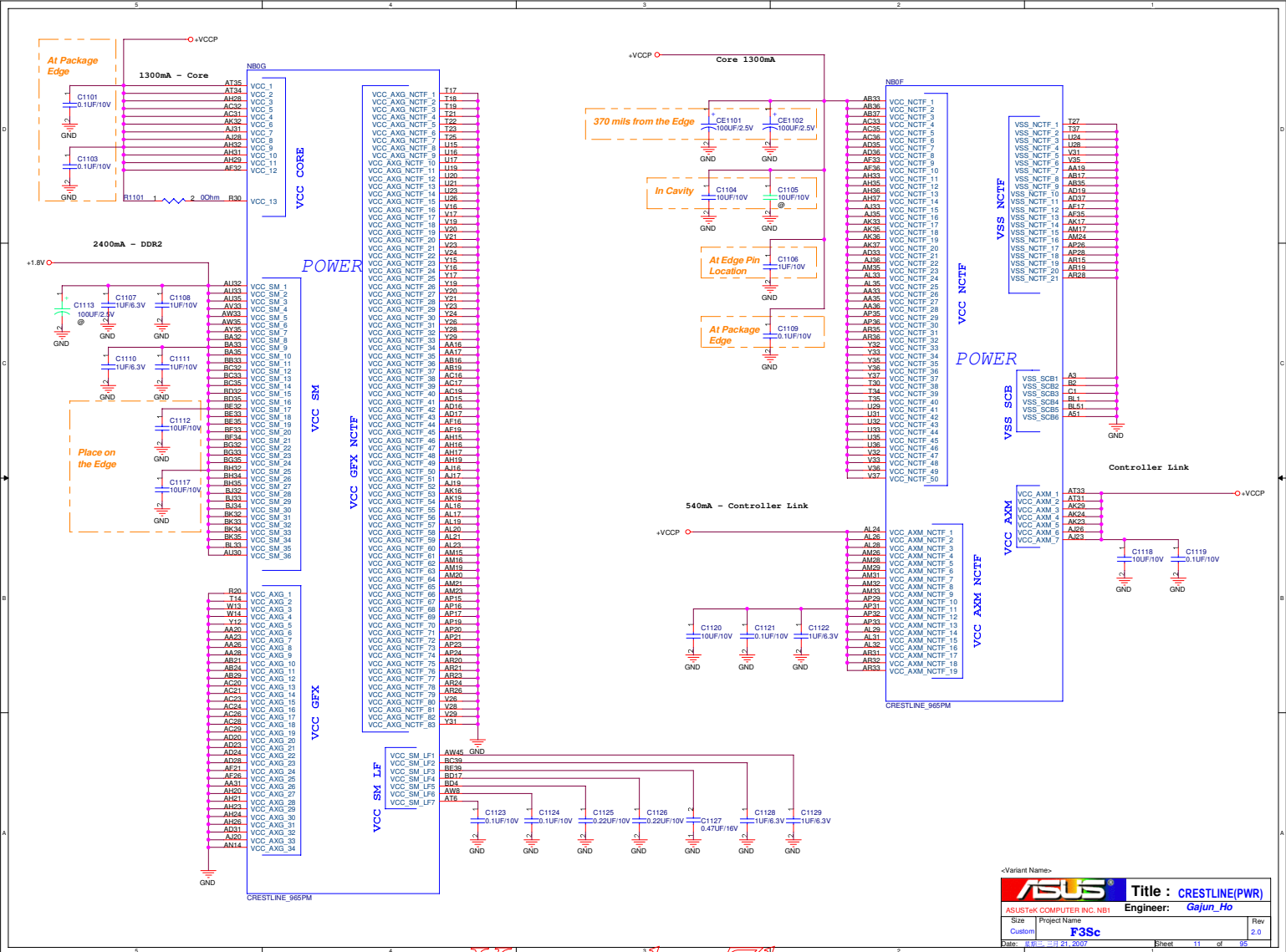
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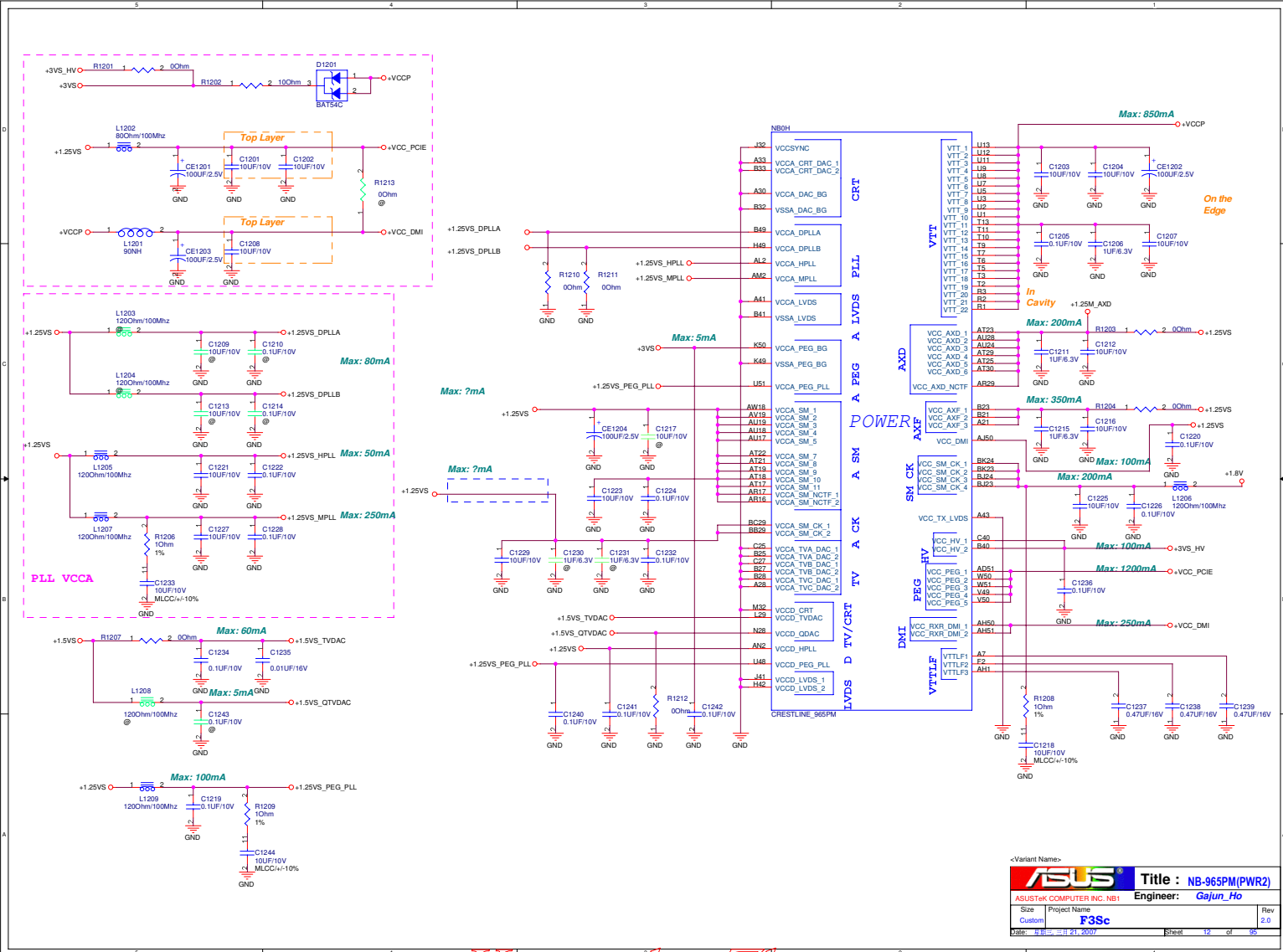
ASUS		Title : NB-965PM(HOST)	
ASUSTeK COMPUTER INC. NB1		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
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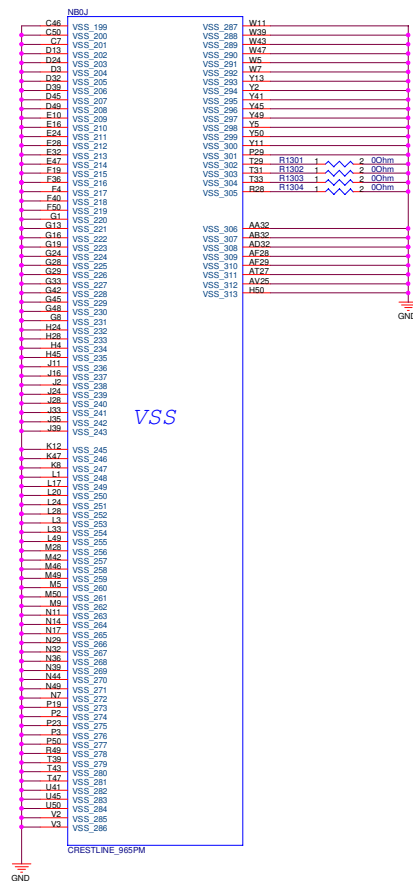
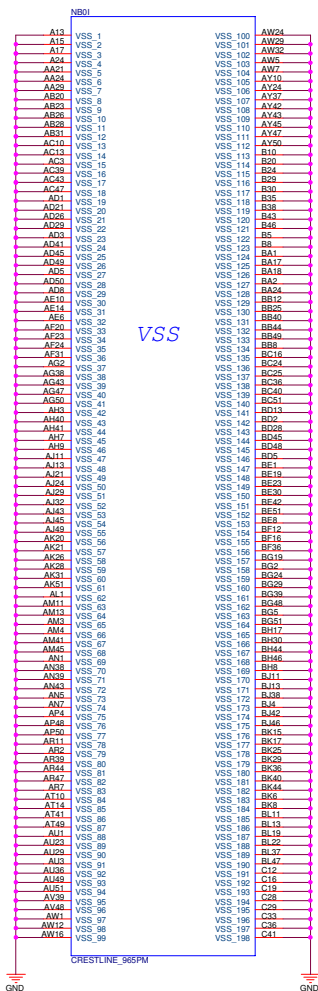


<< Kennedy_Zhang >>





<< Kennedy_Zhang >>



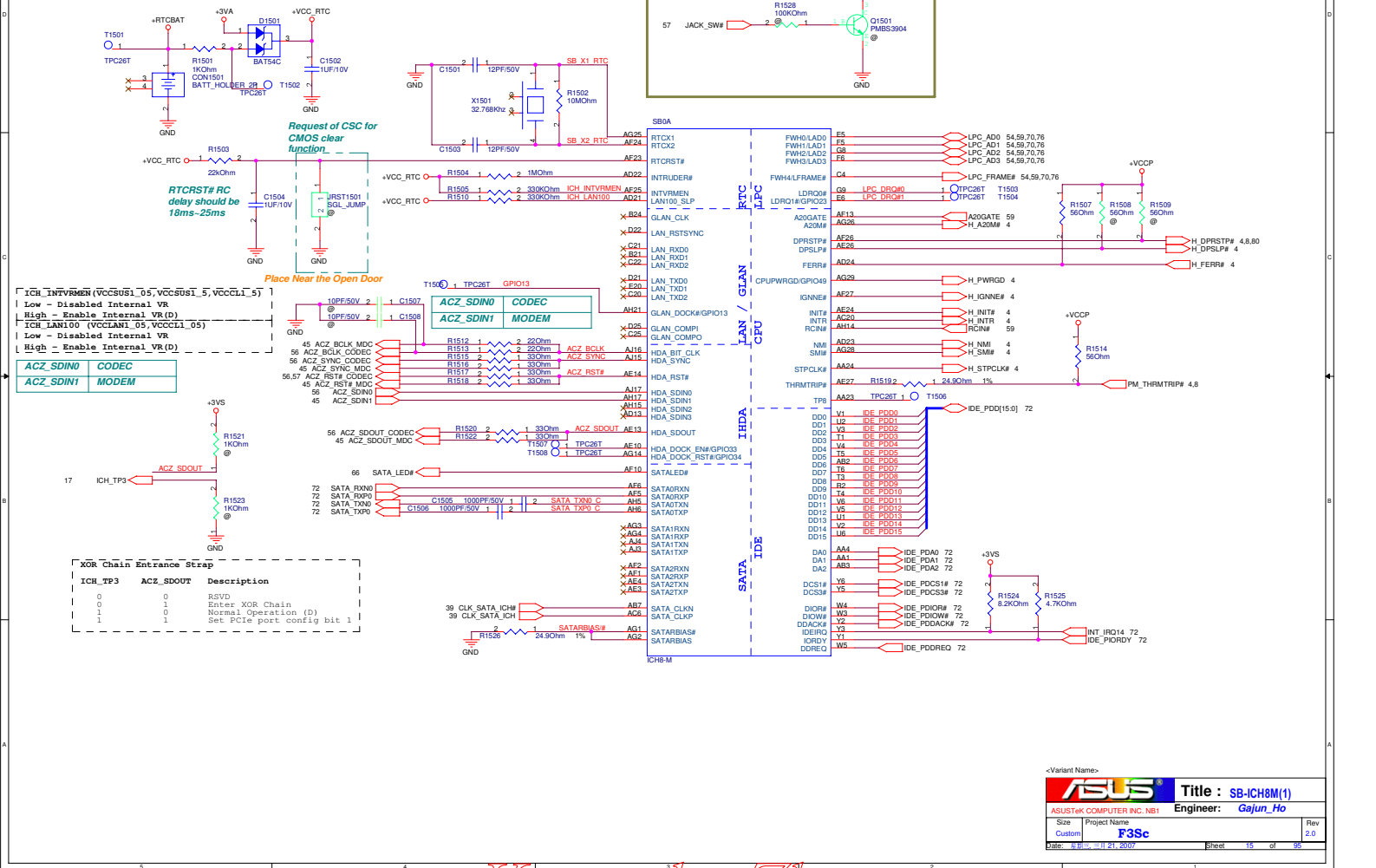
<Variant Name>

ASUS		Title : CRESTLINE(GND)	
ASUSTek COMPUTER INC. NE1		Engineer: <u>Gajun_Ho</u>	
Size	Project Name		Rev
Custom	F3Sc		2.0
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«« Kennedy_Zhang »»

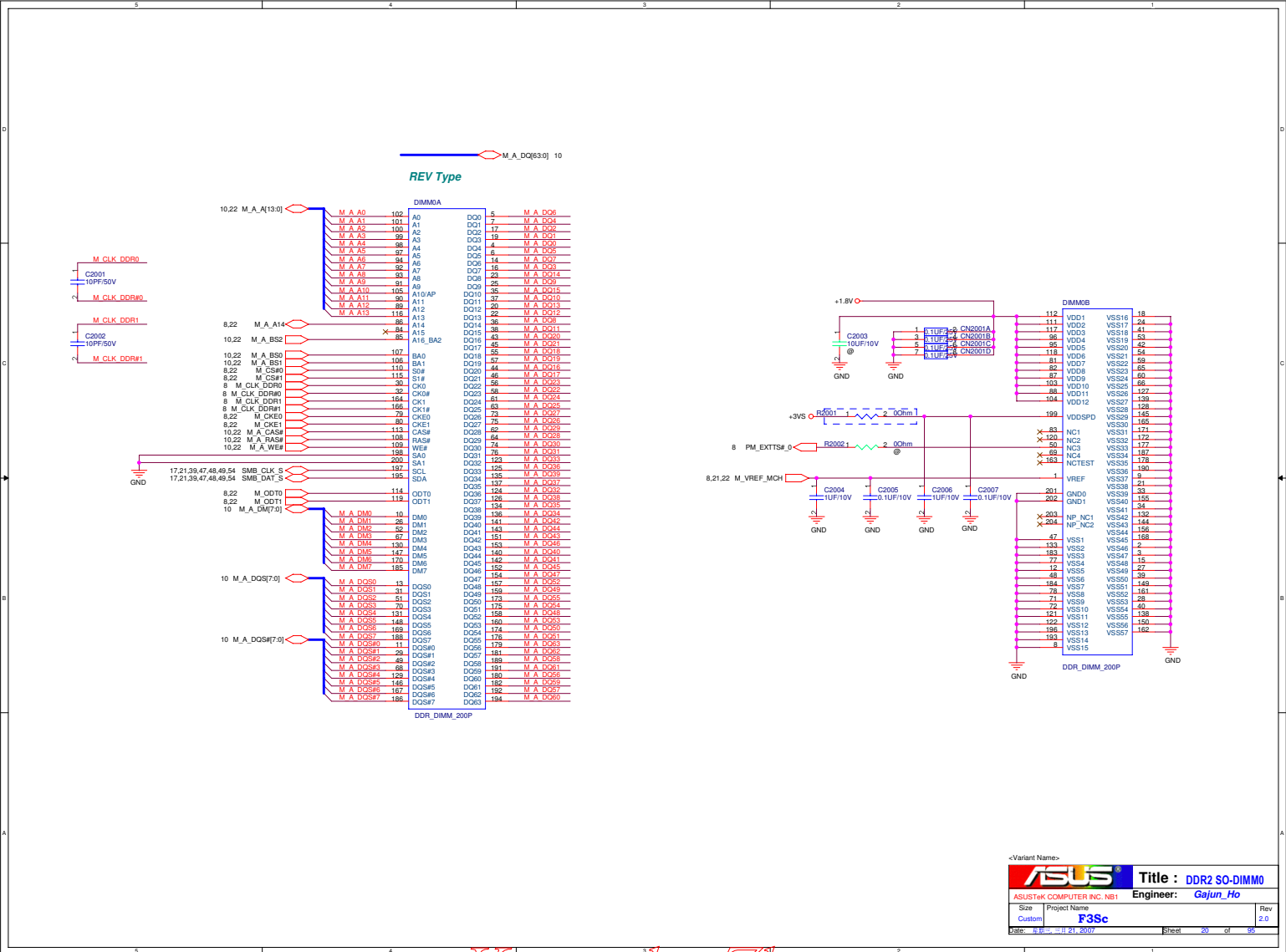
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ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho	
Size:	Project Name		Rev
C	F3Sc		0.0
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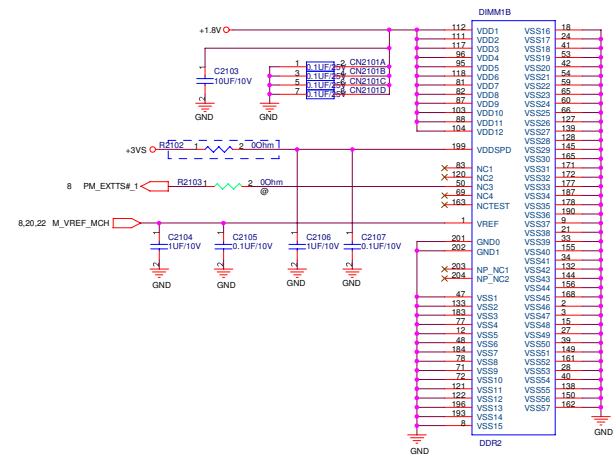
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<Variant Name>		
		Title : Schematic page name
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho
Size	Project Name	Rev
Custom	F3Sc	2.0
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<< Kennedy_Zhang >>



<Variant Name>



Title : DDR2 SO-DIMM1

ASUSTeK COMPUTER INC. NB1 Engineer: Gajun_Ho

Size	Project Name	Rev
Custom	F3Sc	2.0

Custom	FSSC	2.0
Date: 星期三, 三月 21, 2007	Sheet	21 of 95

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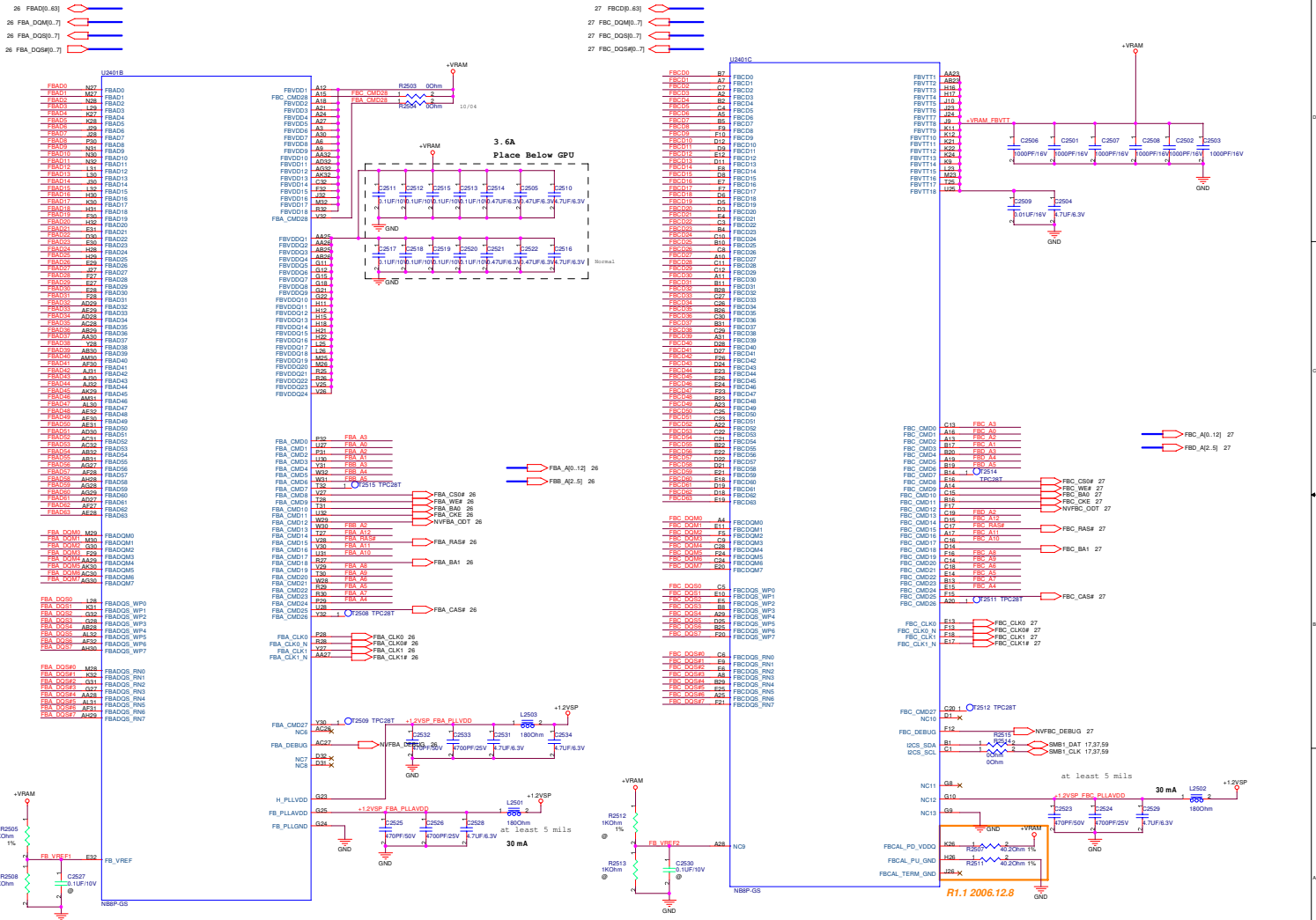


<< Kennedy_Zhang >>

<Variant Name>		ASUS		Title : DDR2 TERMINATION	
ASUSTek COMPUTER INC. NB1		Engineer: Gajun_Ho			
Size	Project Name			Rev	
Custom	F3Sc			2.0	
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5		4		3		2		1	
D									
C									
B									
A									
								</	





<< Kennedy_Zhang >>

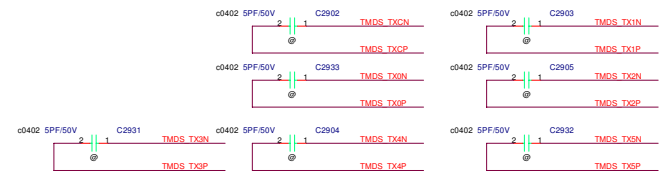
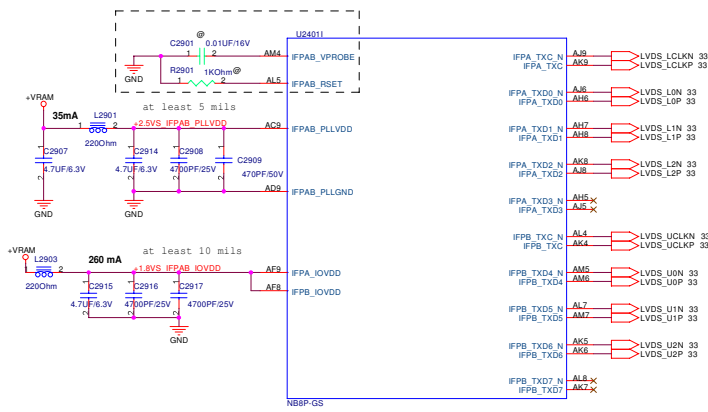
ASUS		Title : G8M2-Memory F
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho
Size	Project Name	Rev
C	F3Sc	2.0
Doc	100-10000000	ESD



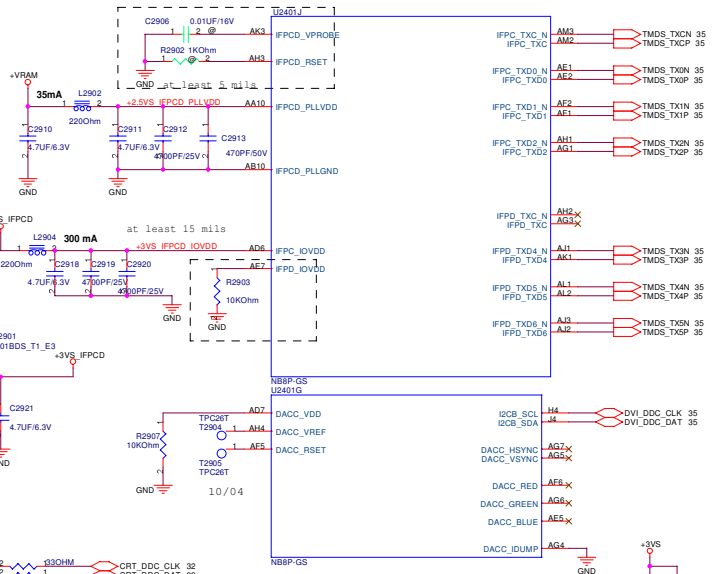
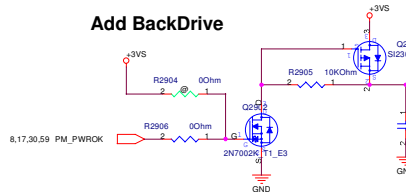


<< Kennedy_Zhang >>

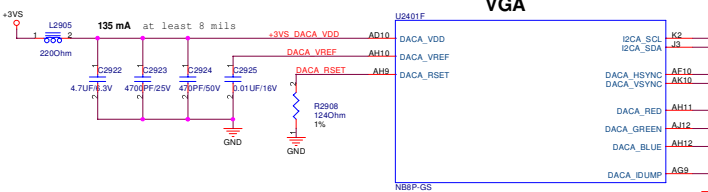
LVDS



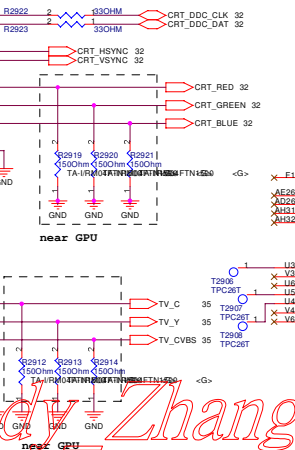
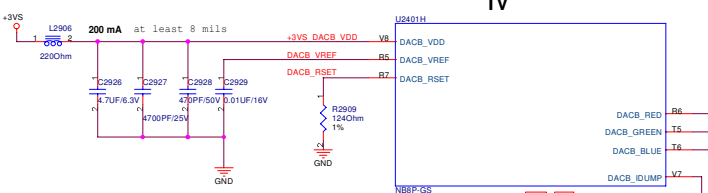
Add BackDrive



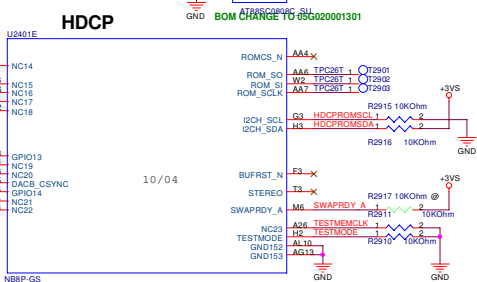
VGA



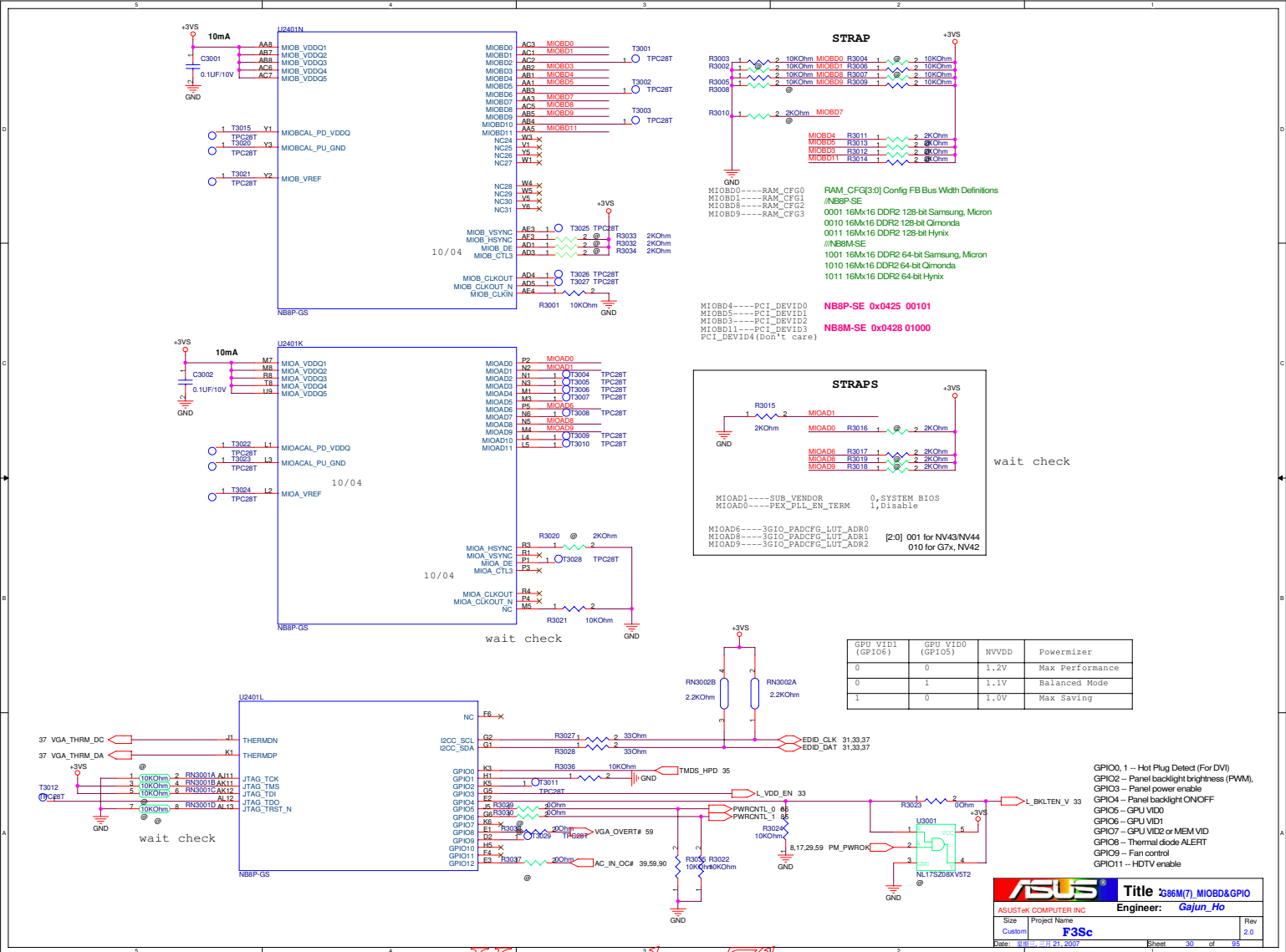
TV



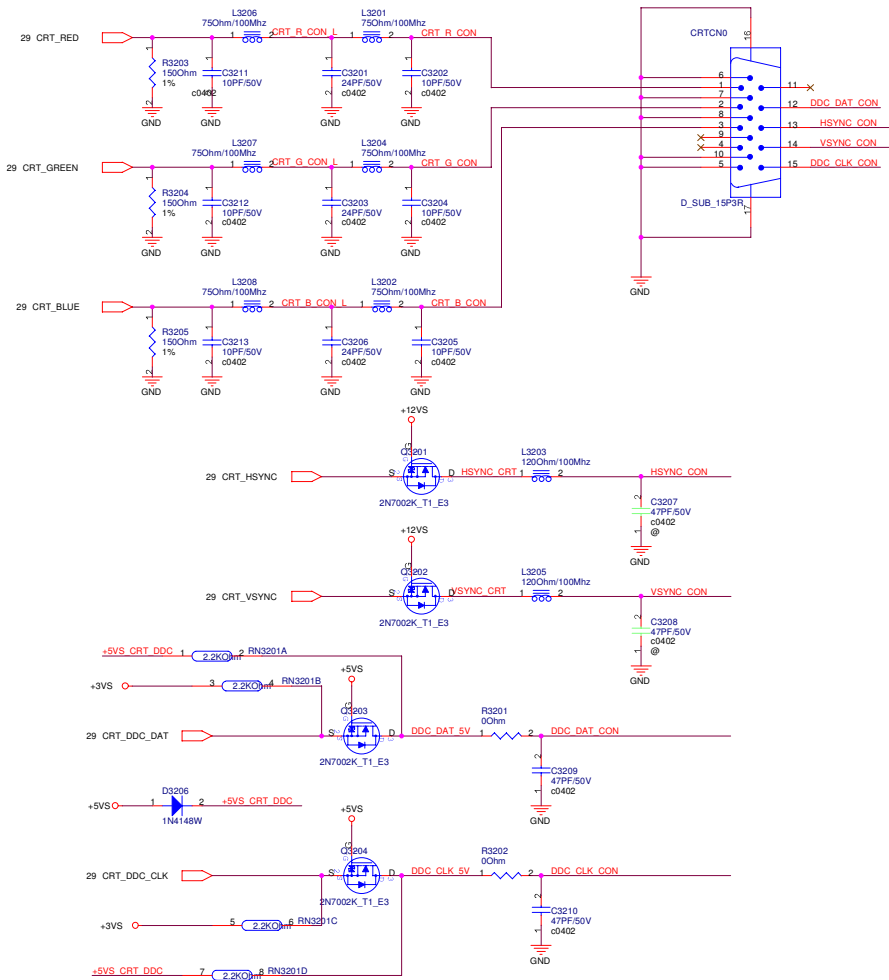
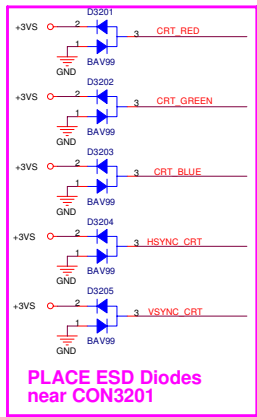
HDCP



<< Kennedy Zhang >>



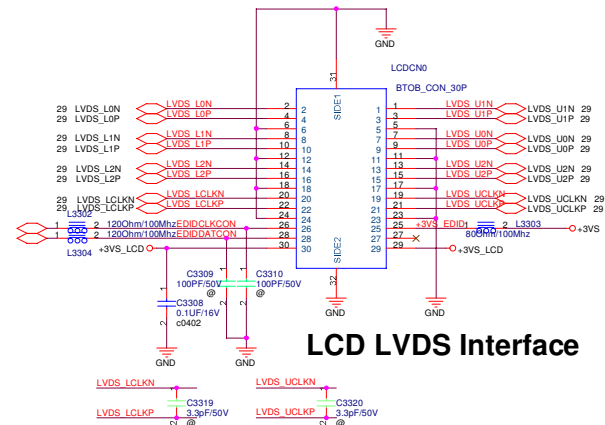
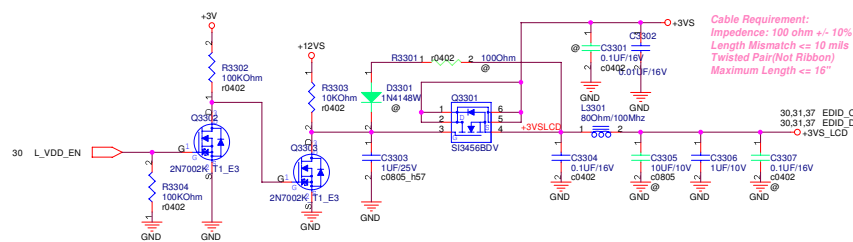
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ASUS		Title : CRT	
ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
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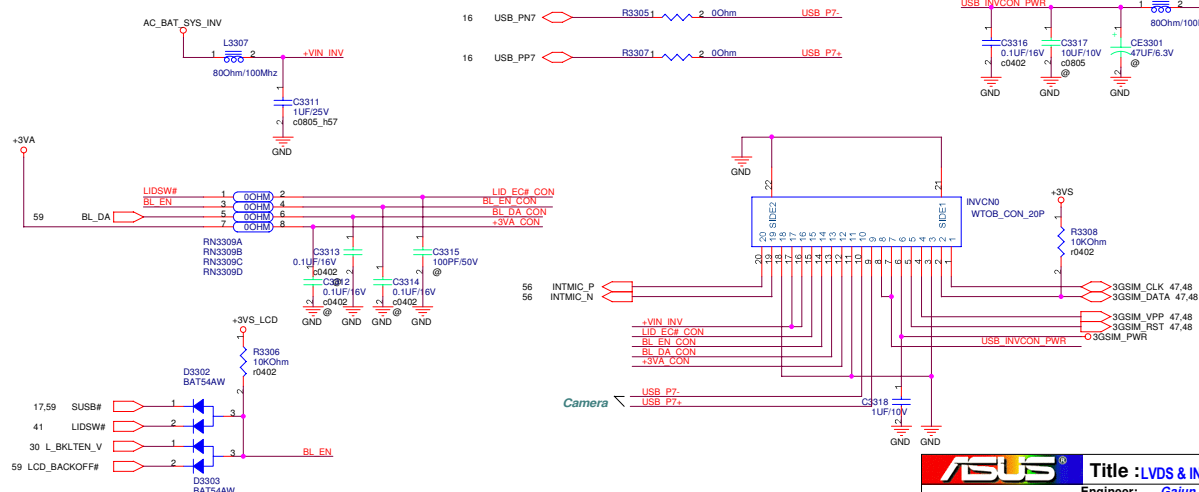
LCD Power



LCD LVDS Interface

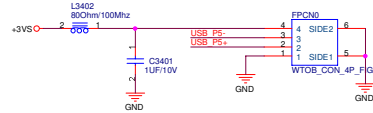
LCD Backlight Control

INVERTER Interface/Speaker CONN.



ASUS		Title : LVDS & INVERTER	
ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
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<Variant Name>

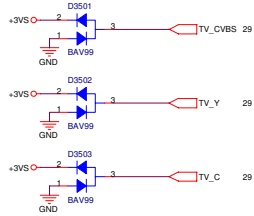
ASUS Title : FINGER PRINT

ASUSTeK COMPUTER INC. Engineer: Jack Chen

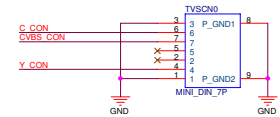
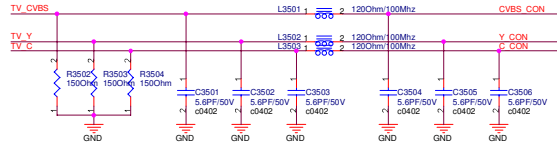
Size	Project Name	Rev
Custom	F3Sc	2.0
Date: 2007-01-21 21:20:07		Sheet 34 of 95

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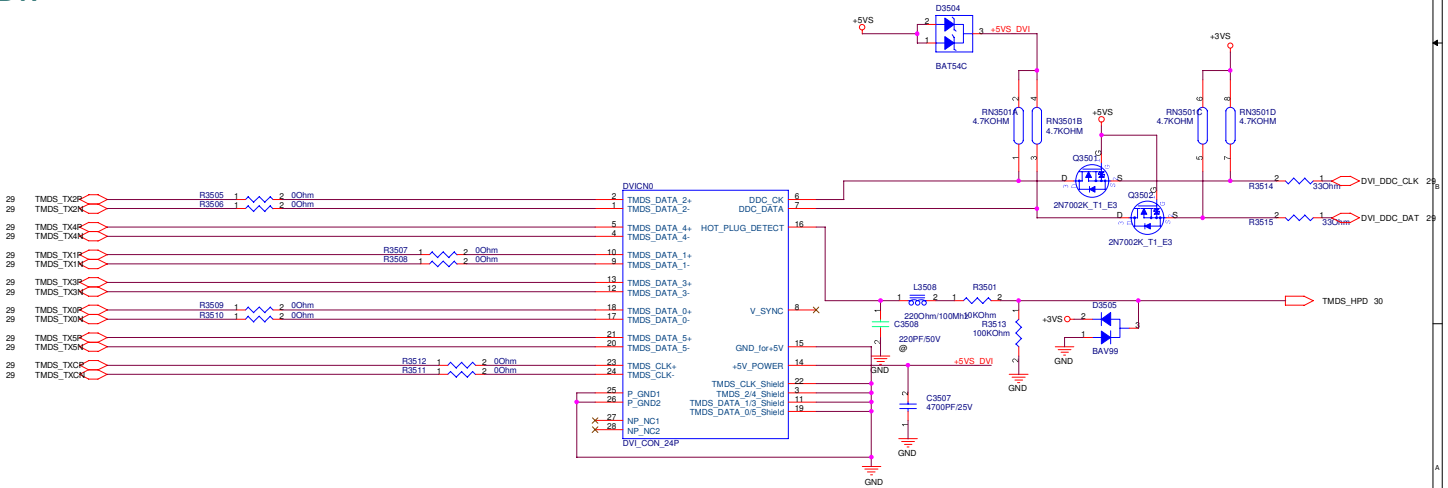
TV OUT



PLACE ESD
Diodes near
TV port

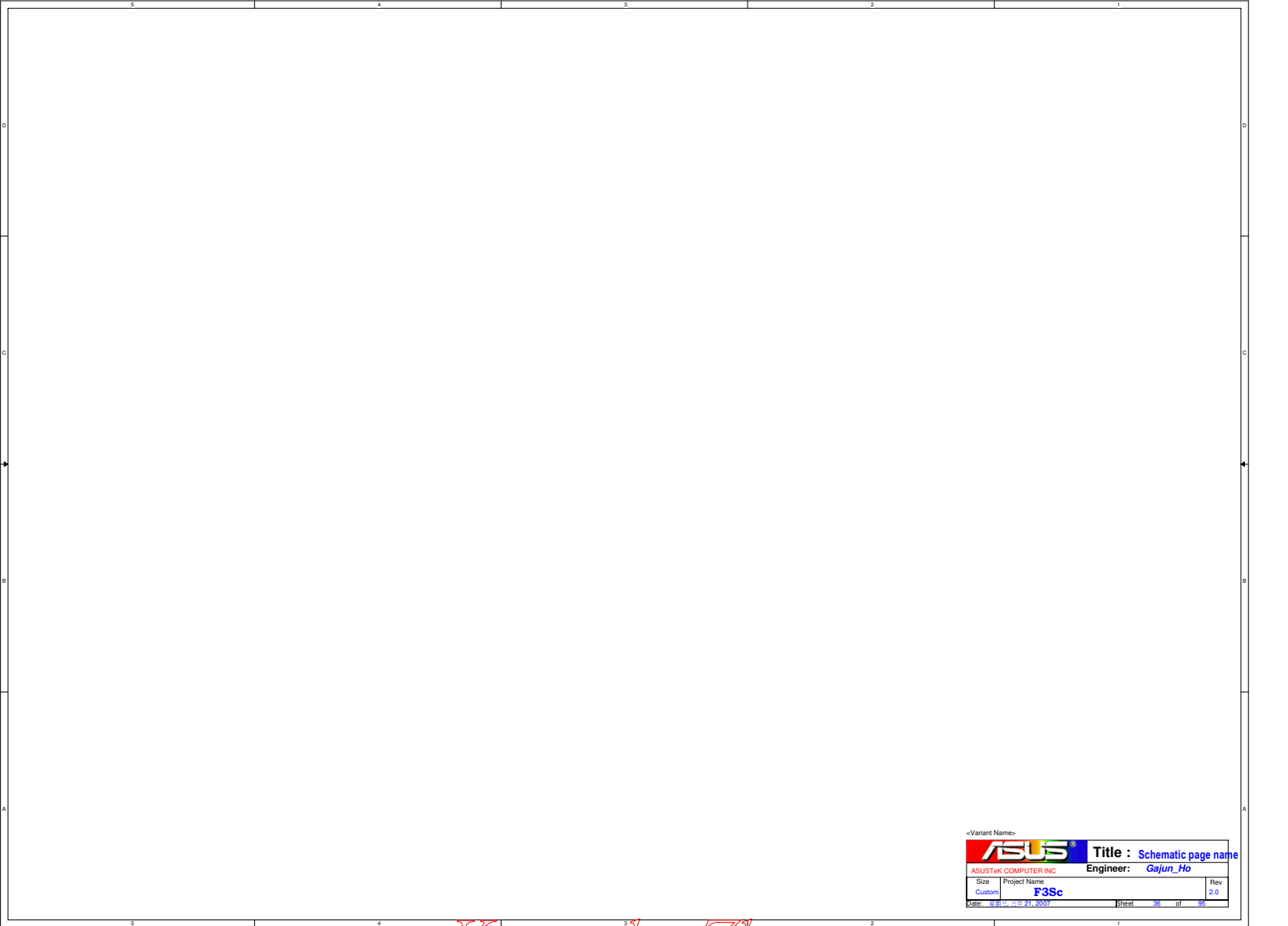


DVI



ASUS		Title : TV OUT & DVI CON.	
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
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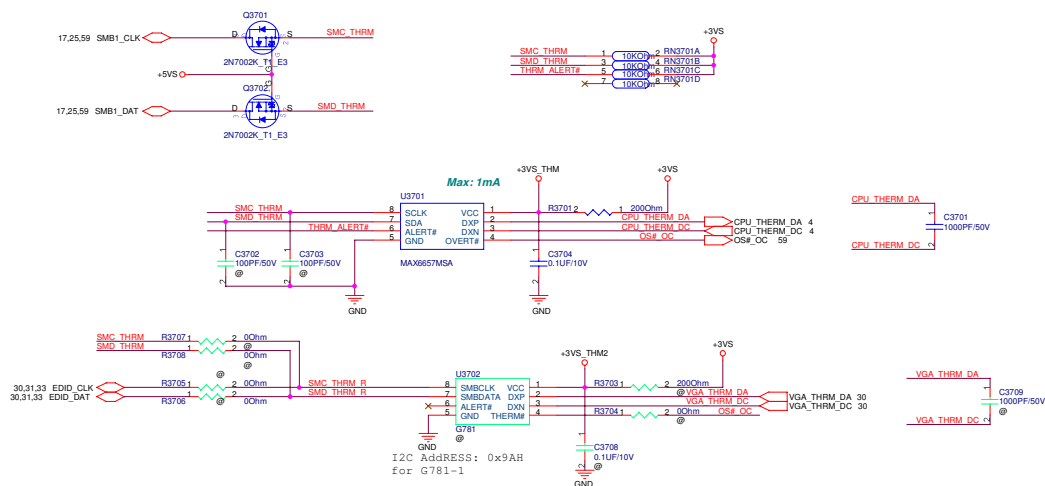
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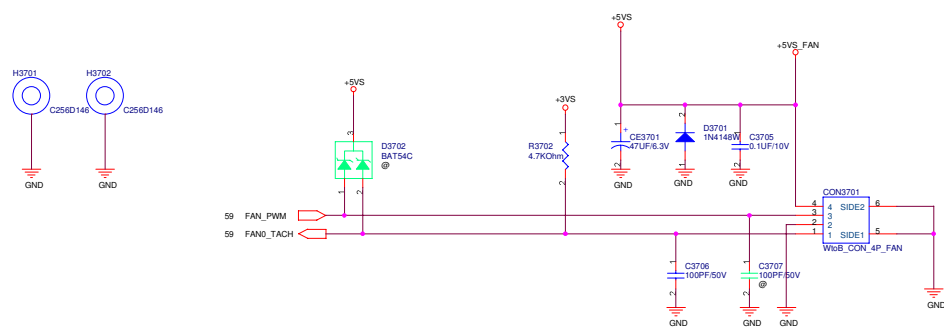
<Variant Name>			
		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
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Thermal Sensor



DC FAN Control

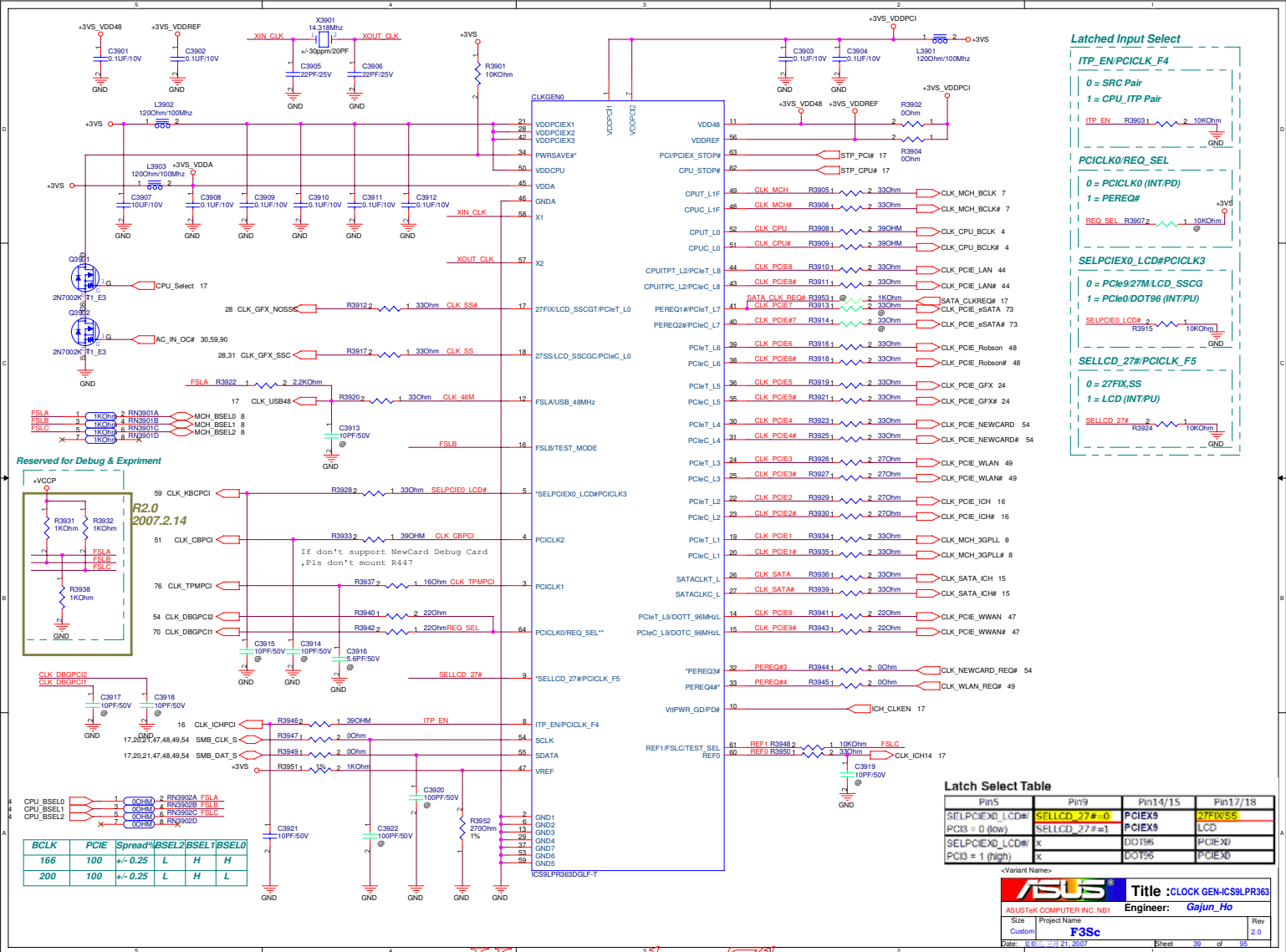


ASUS		Title : THER SENSOR & FAN	
ASUSTeK COMPUTER INC. NB1		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
Date: 2007.01.21	Sheet	87	of 95

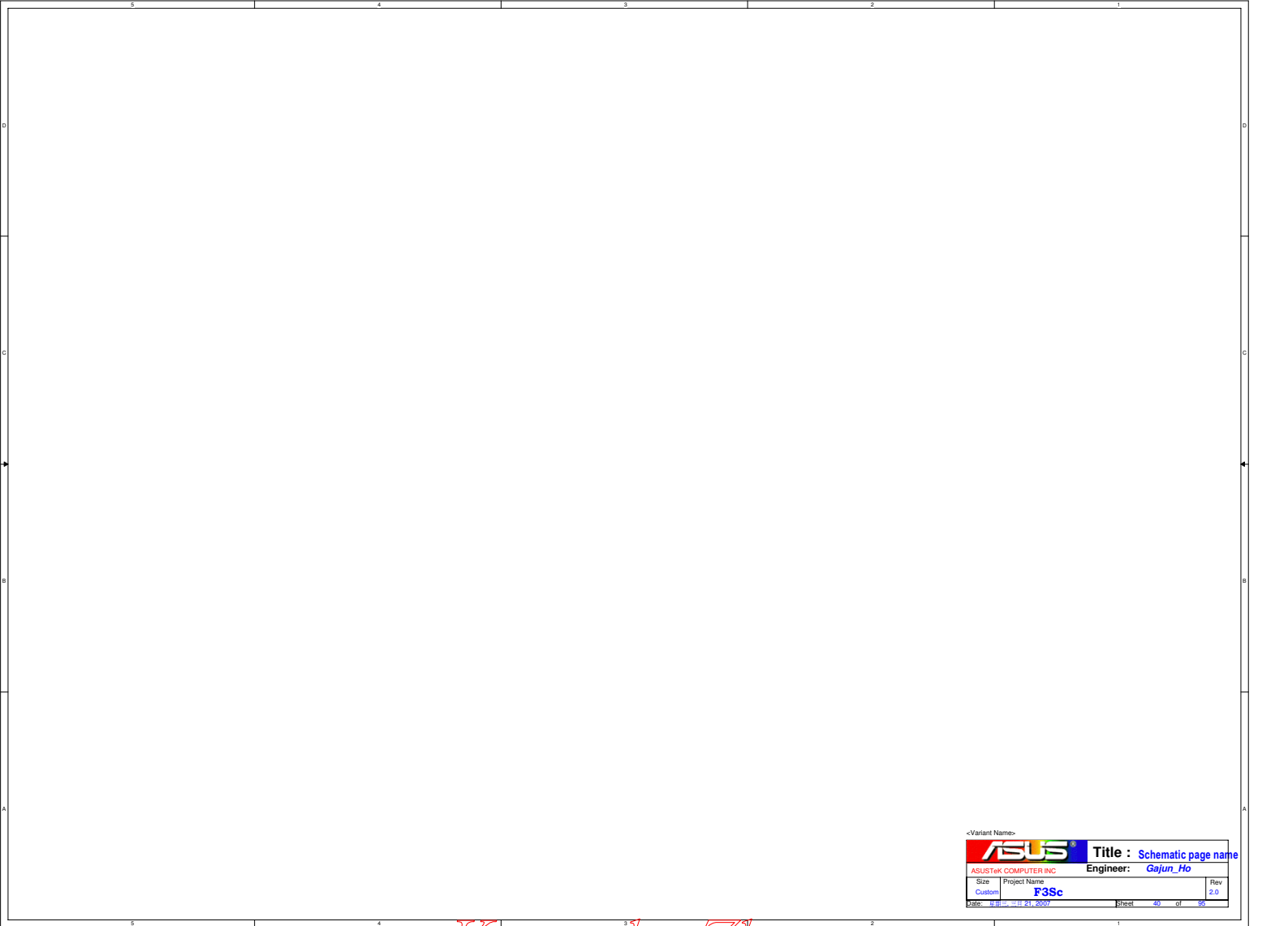
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<Variant Name>		
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ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho
Size	Project Name	Rev
Custom	F3Sc	2.0
Date: 2008-01-21 21:20:07		Sheet 38 of 95

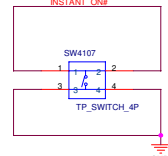
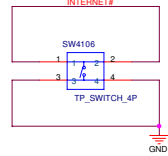
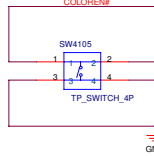
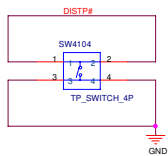
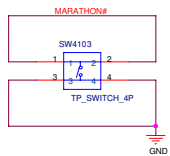
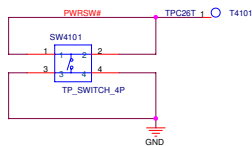
<< Kennedy_Zhang >>



<< Kennedy_Zhang >>

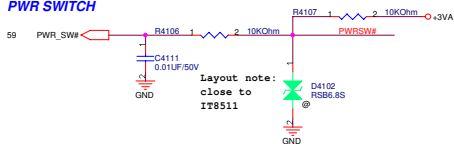


<< Kennedy_Zhang >>



R1.1 2006.12.8
For Switch

PWR SWITCH



LID SWITCH

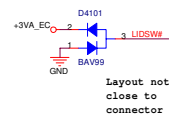


RF ON SWITCH

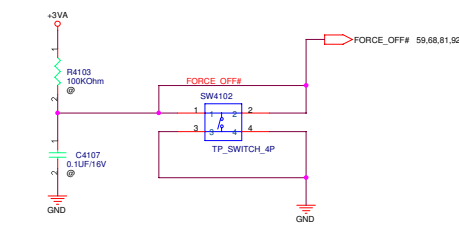


Note:

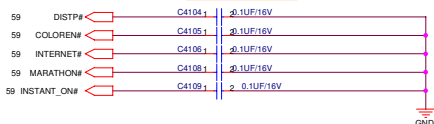
This LID_EC# is a signal from inverter board, it is easy to cause high voltage damage when plugging inverter board connector to M/B with AC present. It needed to add bidirectional diode to protect this pin.



SHUT_DOWN#



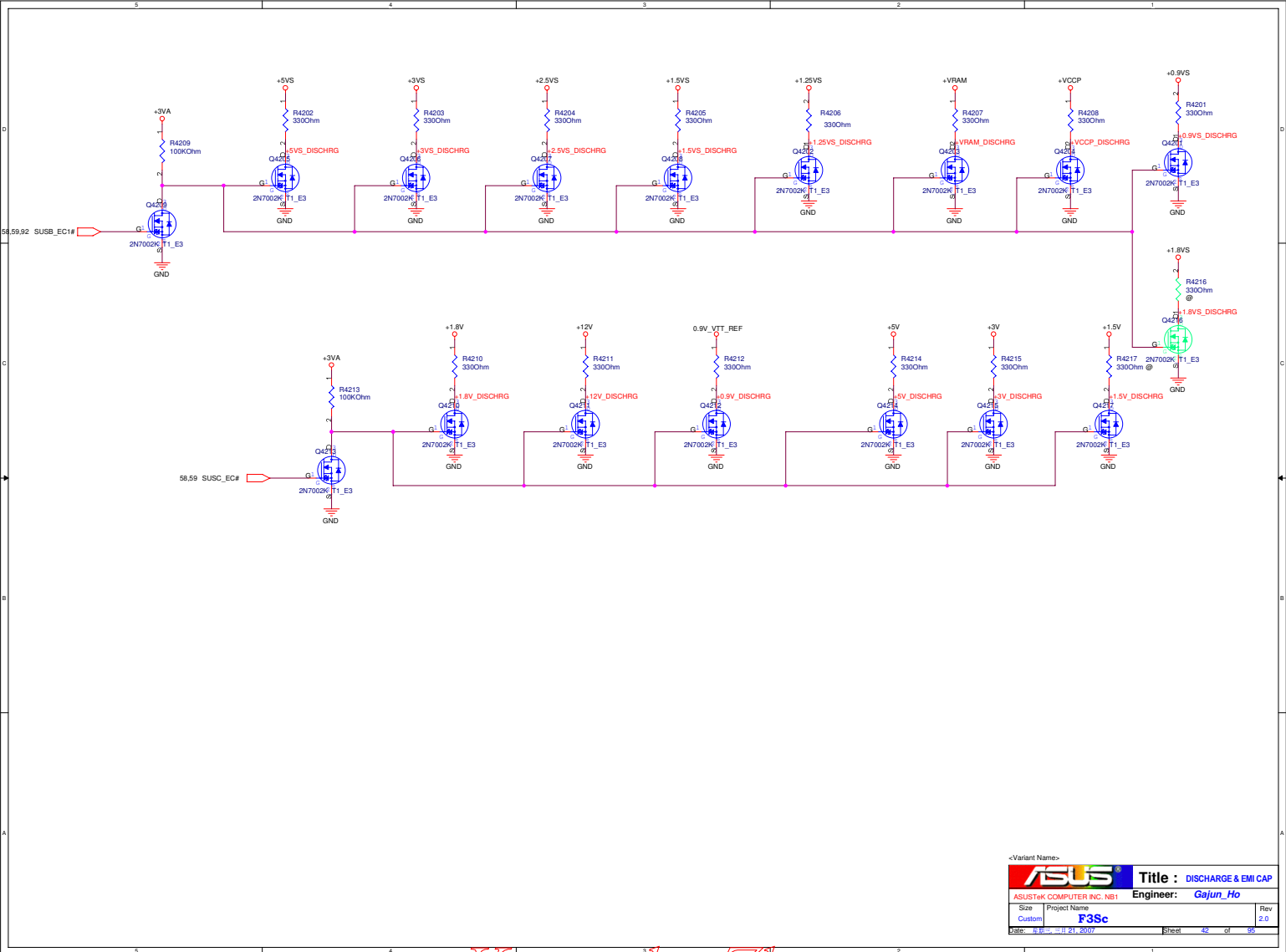
R1.1 2006.12.8



<Variant Name>

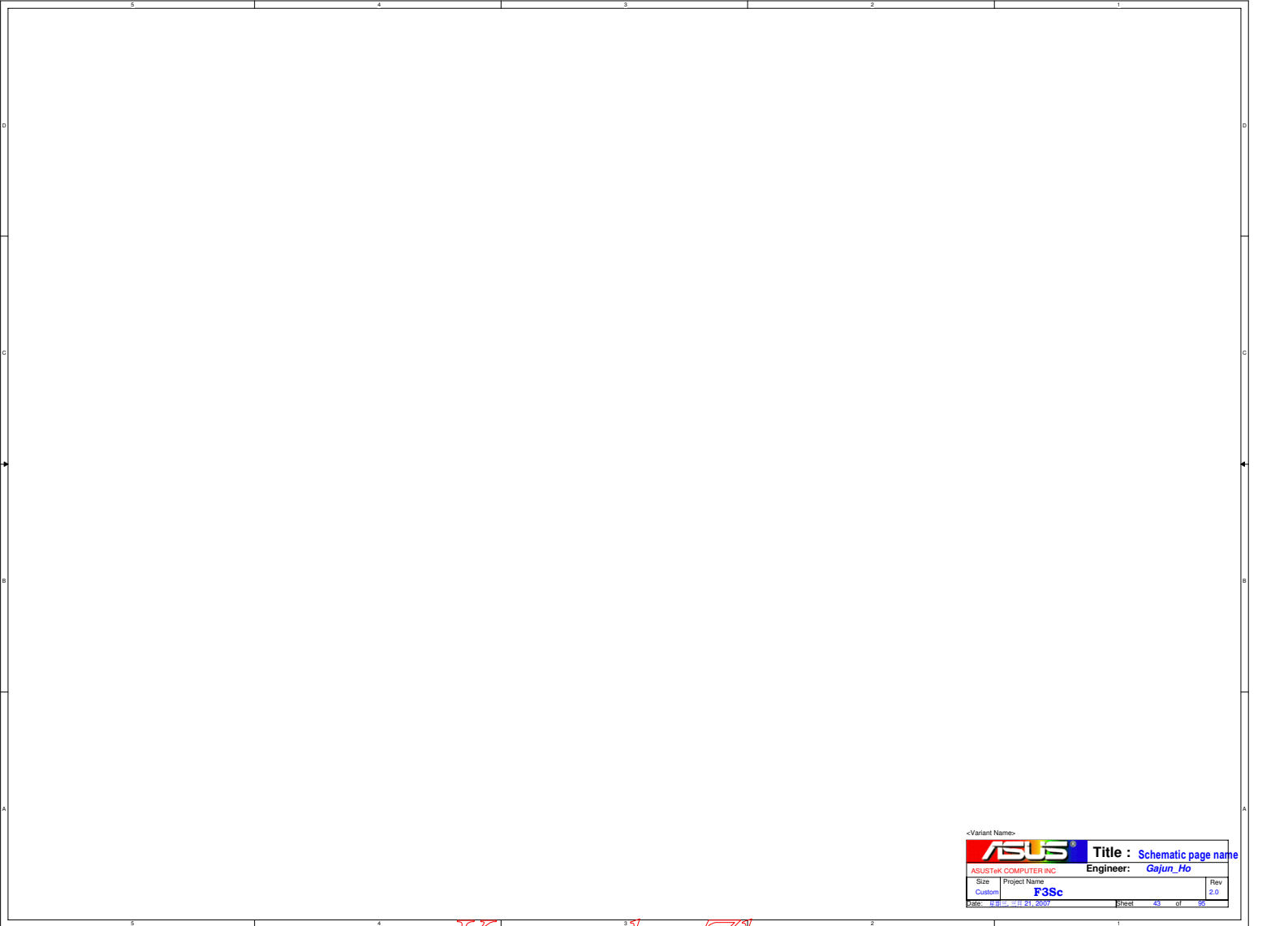
ASUS		Title :Power on & Res Freq	
ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2006.12.21	Sheet	41	of 95

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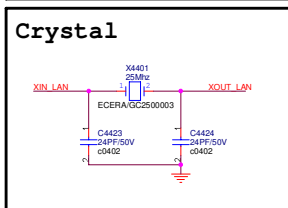
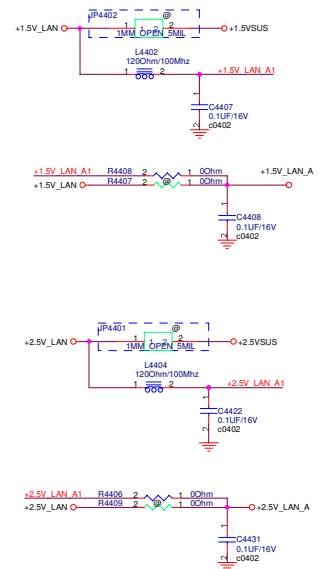
<Variant Name>			
ASUS		Title : DISCHARGE & EMI CAP	
ASUSTeK COMPUTER INC. NB1		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
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<Variant Name>			
		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2008-08-21 2007		Sheet 49	of 95

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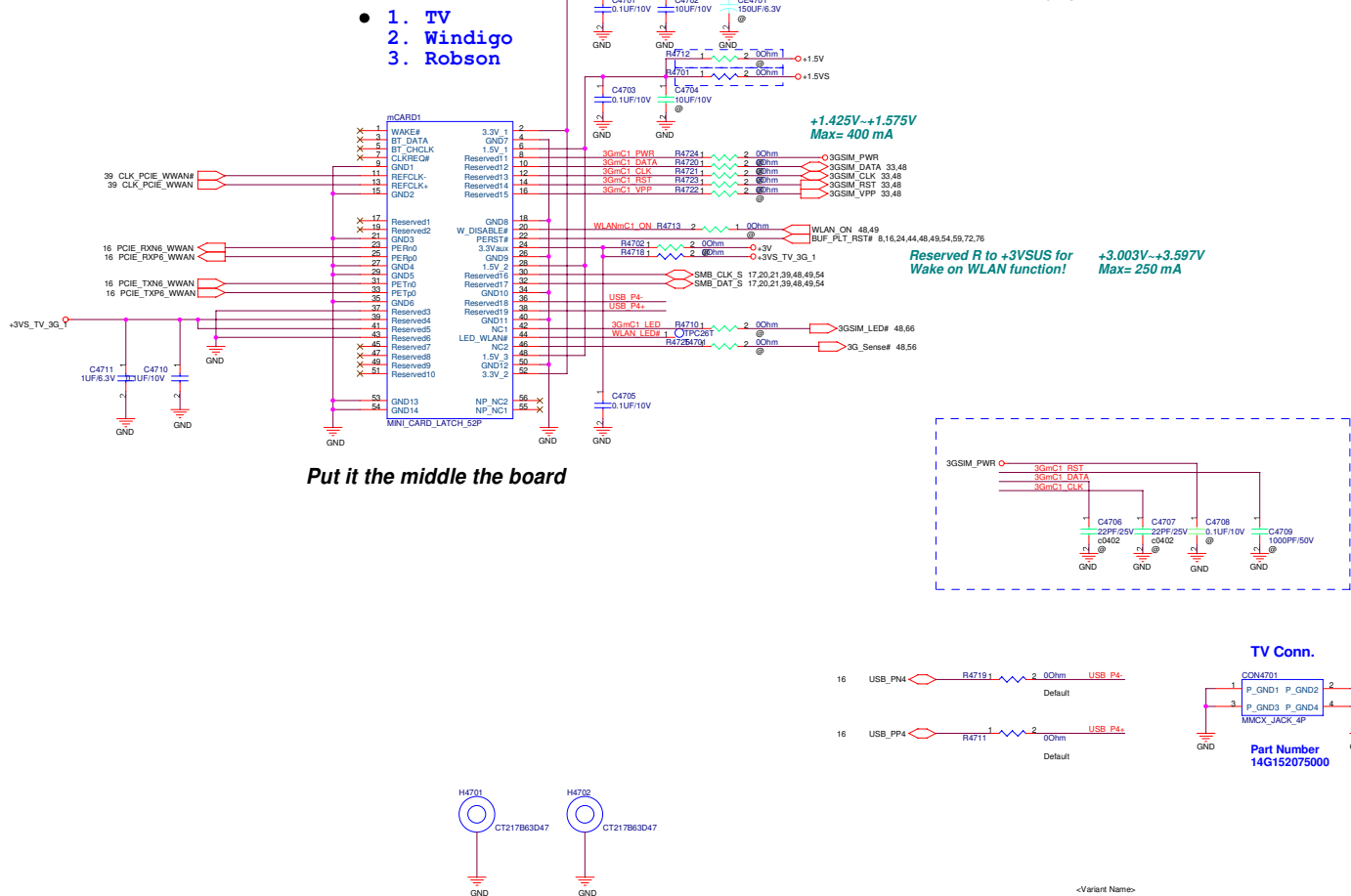


<Variant Name>		 Title : LAN-L1	
ASUSTeK COMPUTER INC. NB1		Engineer: <u>Gajun_Ho</u>	
Size Custom	Project Name F3Sc	Rev 2.0	
Date: 星期日, 三月 21, 2007		Sheet 44 of 95	

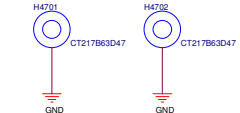
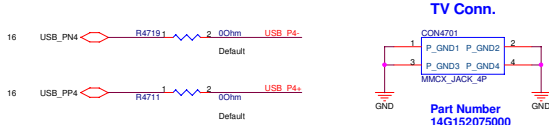
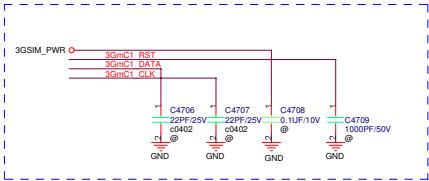
<< Kennedy_Zhang >>

<Variant Name>			
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ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2008-01-21 2007		Sheet	46 of 95

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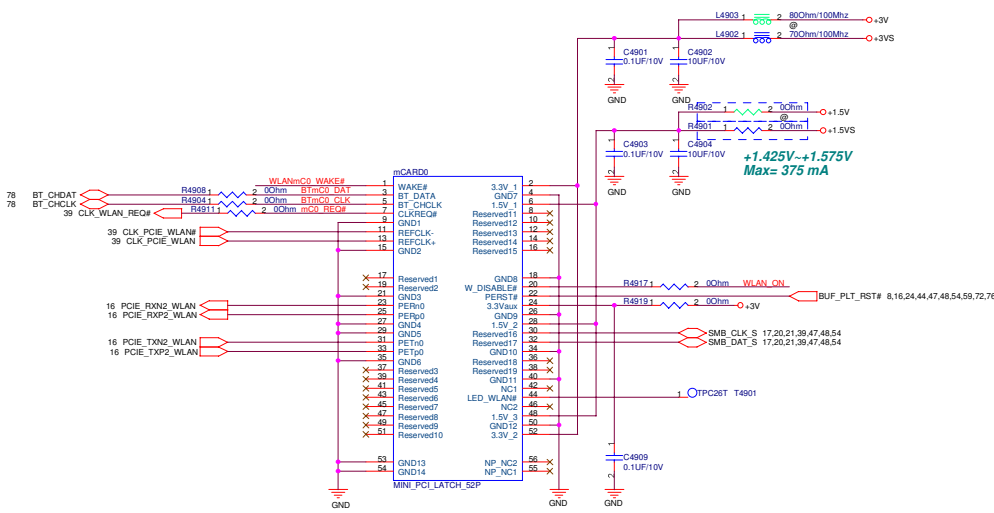
Put it the middle the board



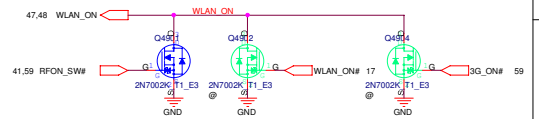
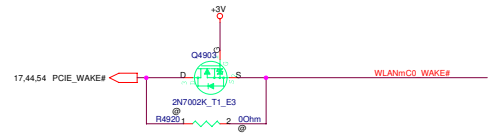
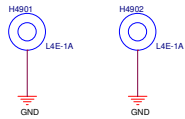
Change H4701, H4702 "13G021043011" from BOM file.

-Variant Name-		Title	
ASUS		MINI CARD-TV/Windigo	
ASUSTek COMPUTER INC. NBI		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
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<< Kennedy_Zhang >>

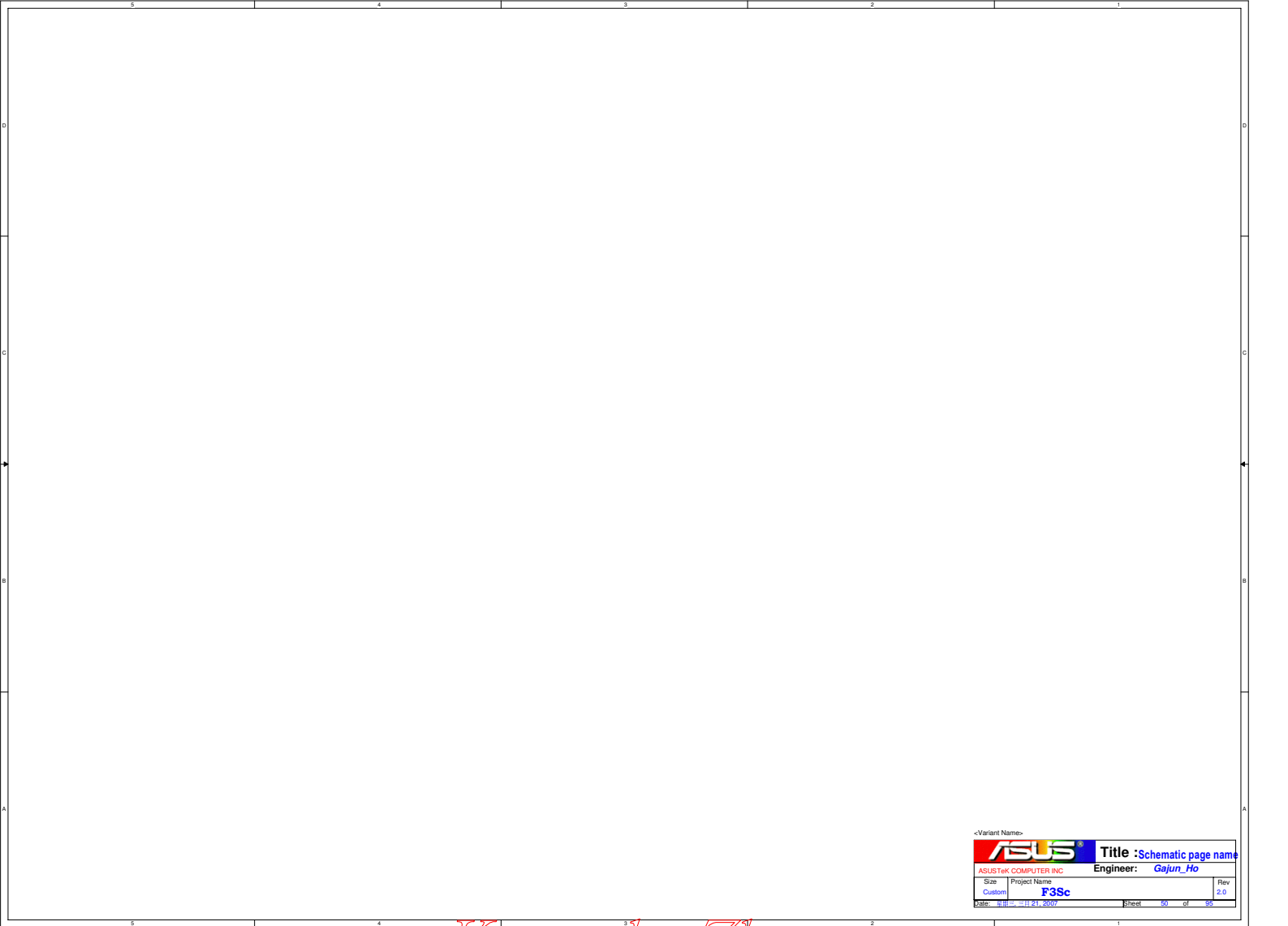


The left side depends on TV connect to put the board

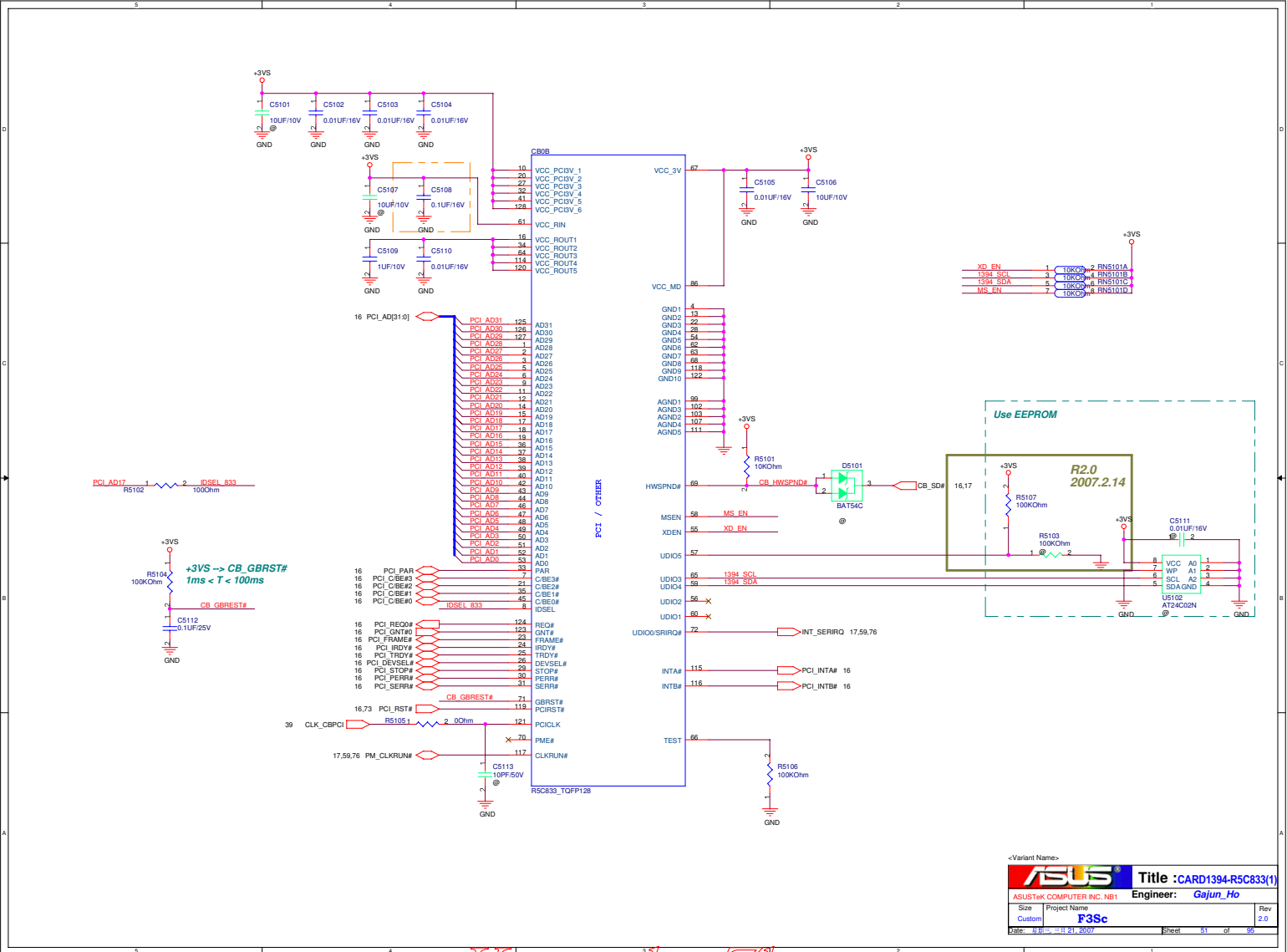


ASUS		Title : MINI CARD-Kedron	
ASUSTeK COMPUTER INC. NB1		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
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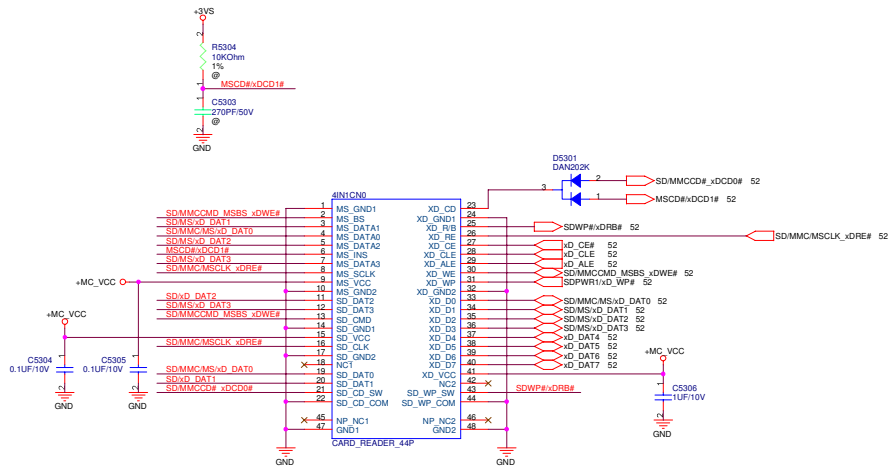
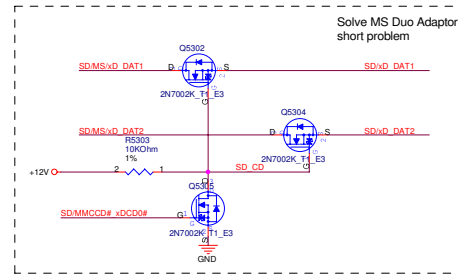
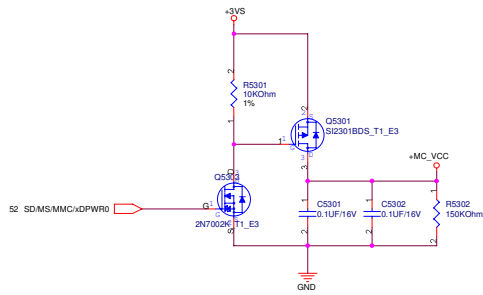
<< Kennedy_Zhang >>



<< Kennedy_Zhang >>

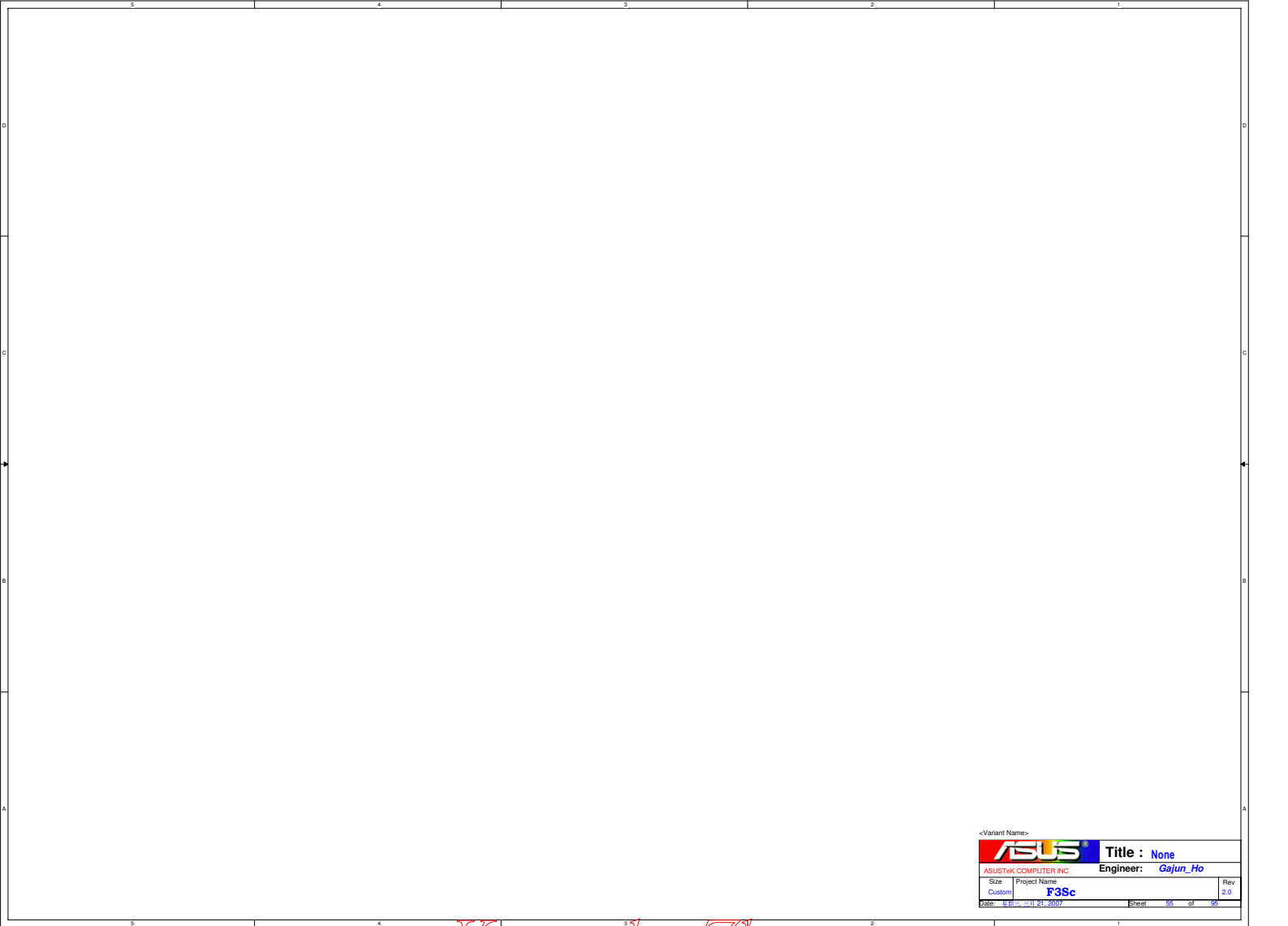


<< Kennedy_Zhang >>



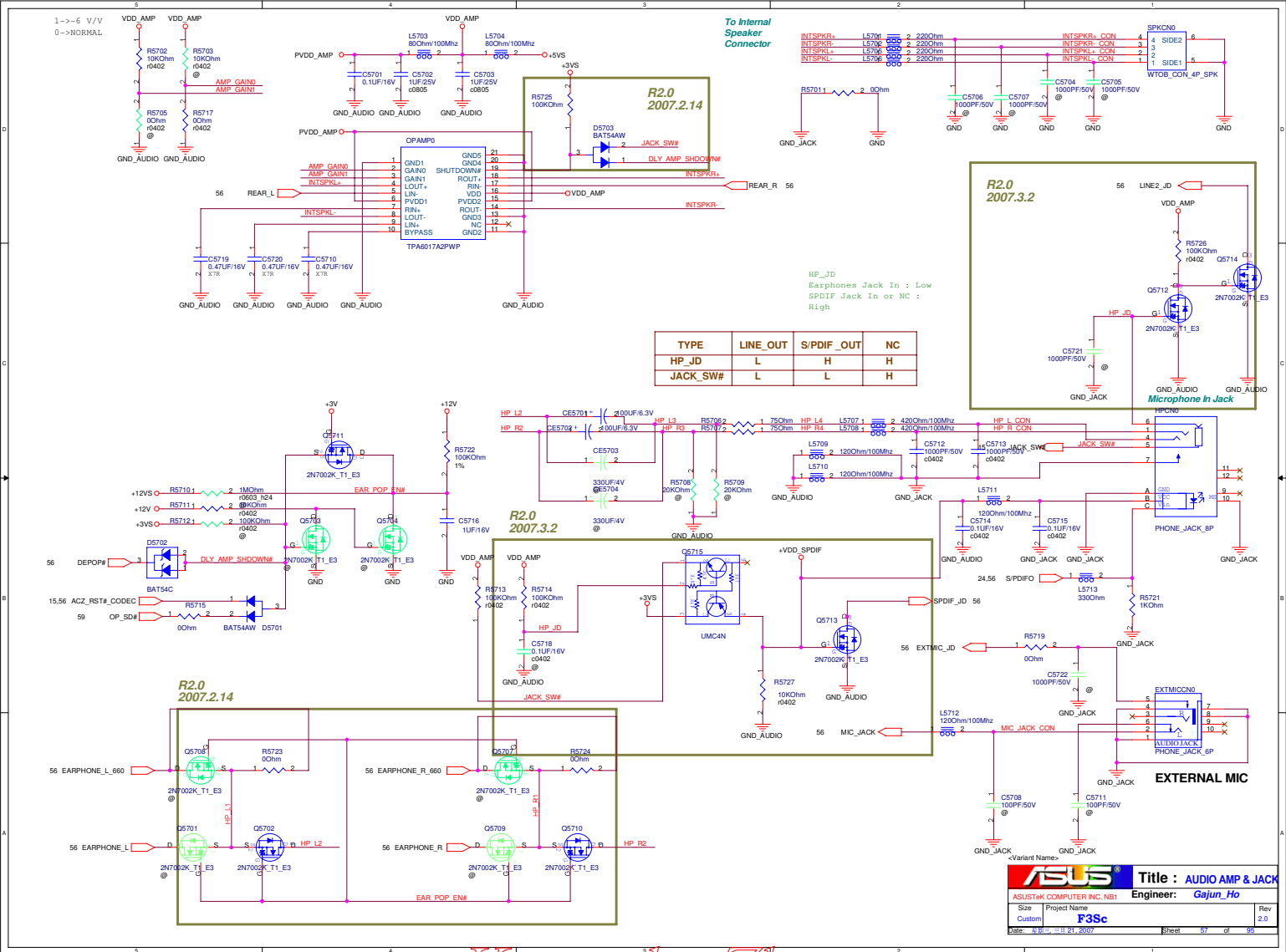
ASUS		Title 4 in 1 CARD READER	
ASUSTek COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
Date: 2007-01-21		Sheet	50 of 95

<< Kennedy_Zhang >>

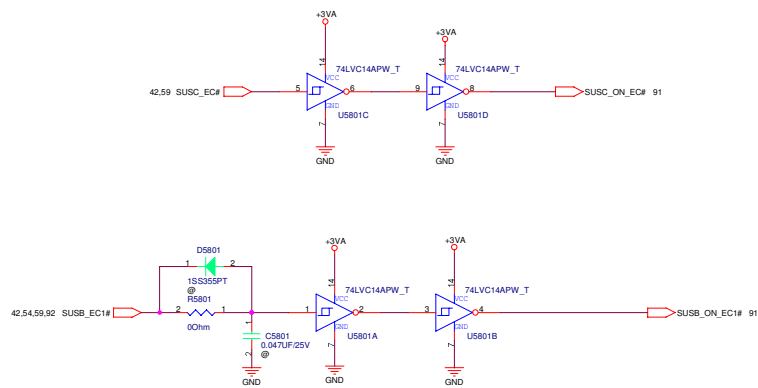


<Variant Name>			
		Title : None	
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2007-01-21 21:20:07		Sheet	66 of 95

<< Kennedy_Zhang >>

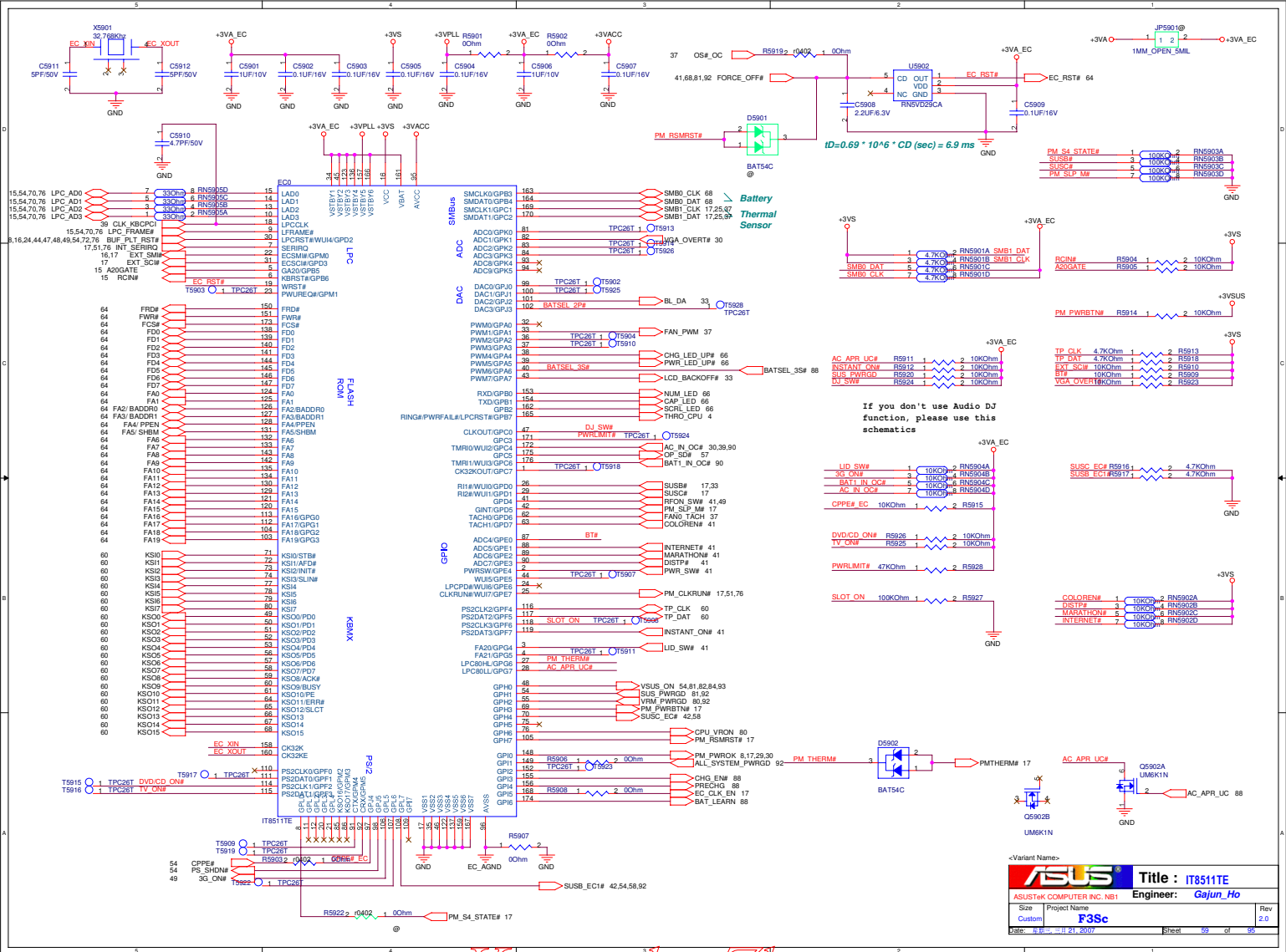


<< Kennedy_Zhang >>



<Variant Name>			
ASUS		Title Sequence Control Logic	
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
Date: 10/10/2007		Sheet 59 of 94	

<< Kennedy_Zhang >>

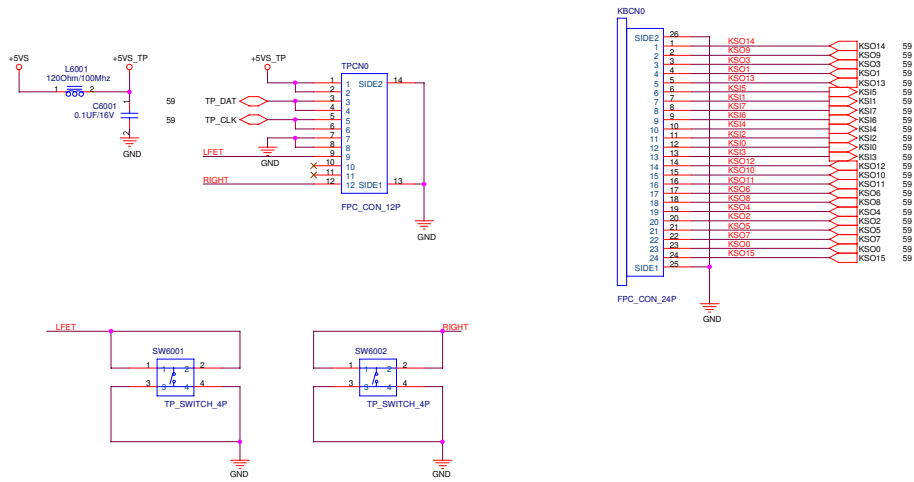


<< Kennedy_Zhang >>

ASUSTek COMPUTER INC. NE1		Title : IT8511TE	
Project Name		Engineer: Gajun_Ho	
Size		F3Sc	
Date: 2007.01.21		Rev: 2.0	
Sheet: 59		of 95	

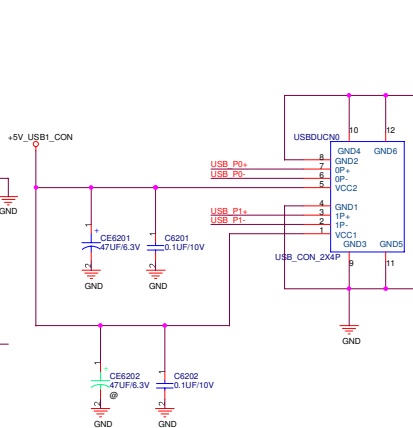
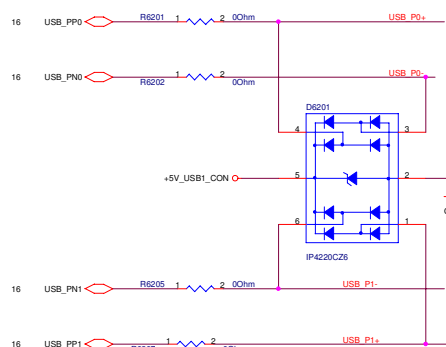
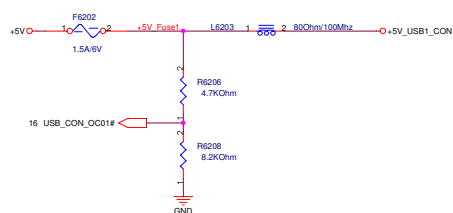
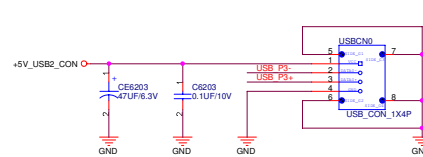
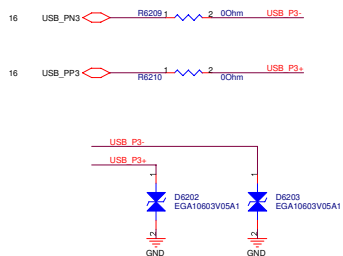
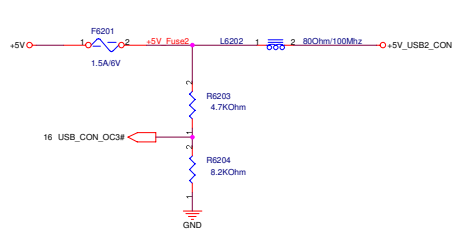
For Touch-Pad

For Keyboard(24Pin) Non KID



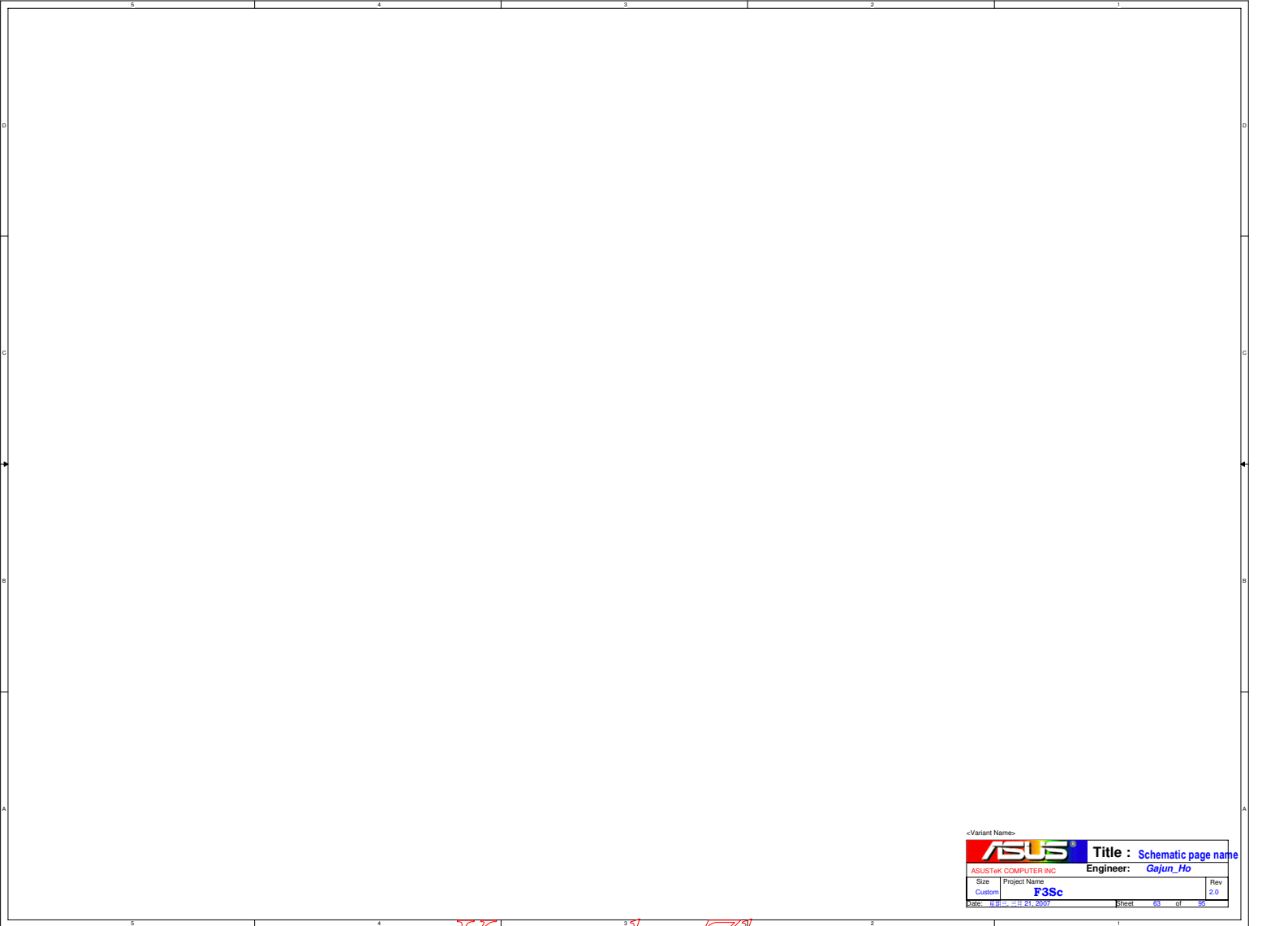
<< Kennedy_Zhang >>

5		4		3		2		1	
D									
C									
B									
A									
								</	



ASUS		Title : USB CONN	
ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
Date: 2007.01.21	Sheet	62	of 95

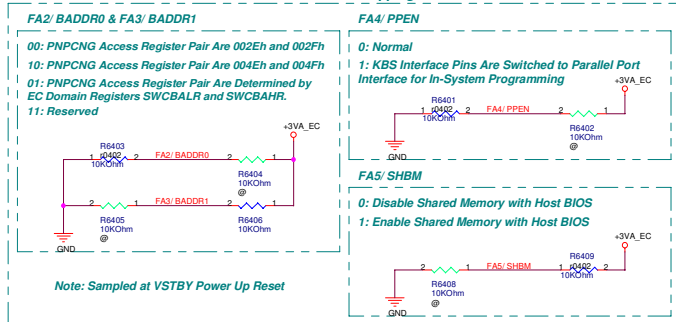
<< Kennedy_Zhang >>



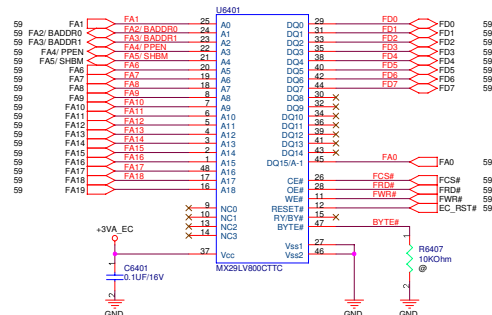
<< Kennedy_Zhang >>

ISA ROM

EC Hardware Strapping

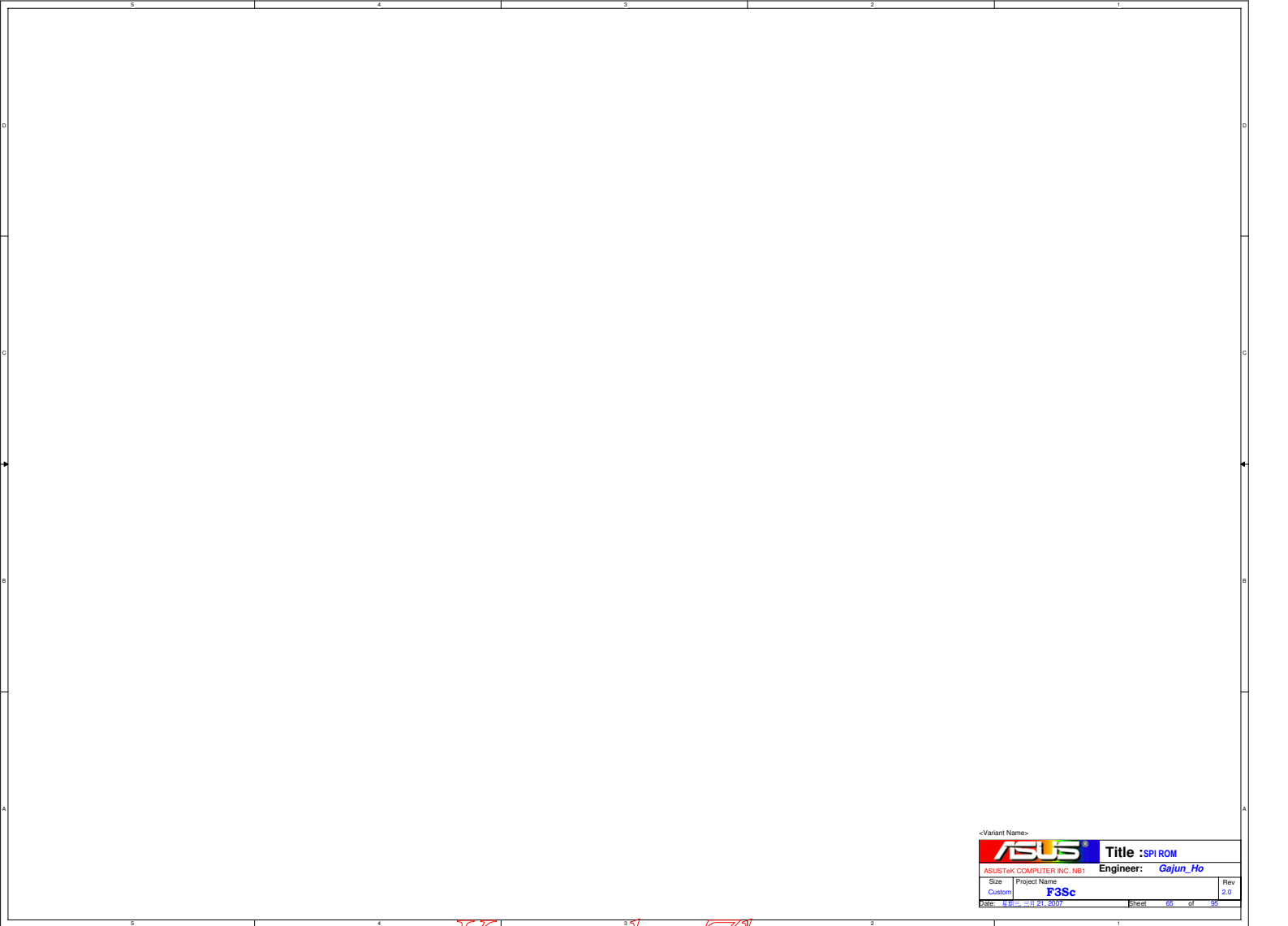


8M TSOP



ASUS		Title :ISA ROM	
ASUSTeK COMPUTER INC. NB1		Engineer: Gajun_Ho	
Size	Project Name	Rev	
Custom	F3Sc	2.0	
Date: 2007.01.21	Sheet	64	of 95

<< Kennedy_Zhang >>

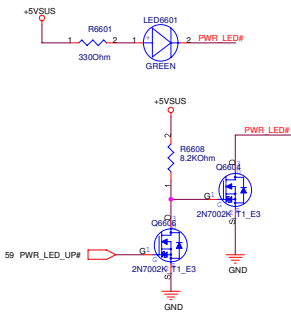


<Variant Name>			
		Title :SPI ROM	
ASUSTeK COMPUTER INC. NB1		Engineer: <u>Gajun_Ho</u>	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 10/10/2007		Sheet	65 of 95

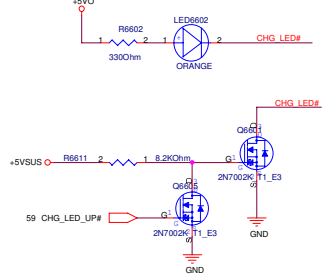
<< Kennedy_Zhang >>

For LED

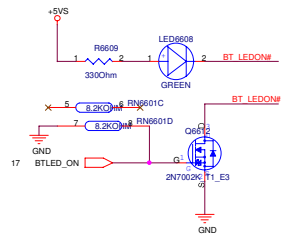
For POWER LED



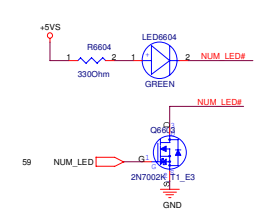
For BATTERY LED



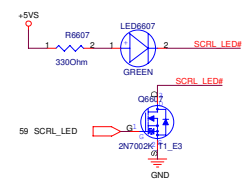
For BT LED



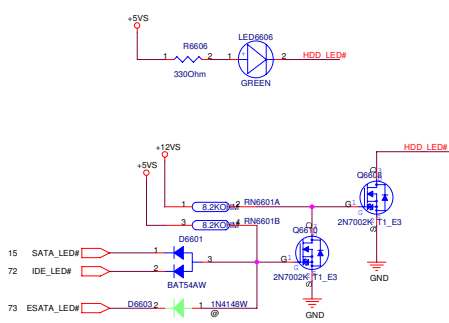
for Num Lock



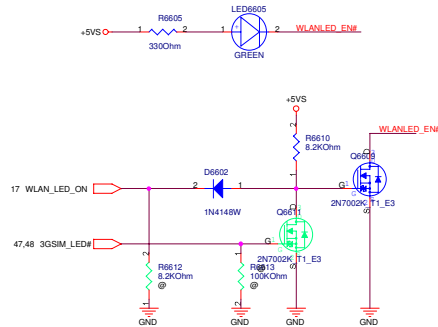
for Scroll Lock



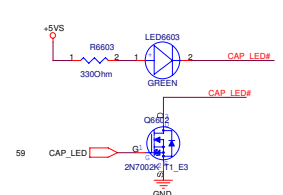
For SATA/IDE LED



For WireLess LED

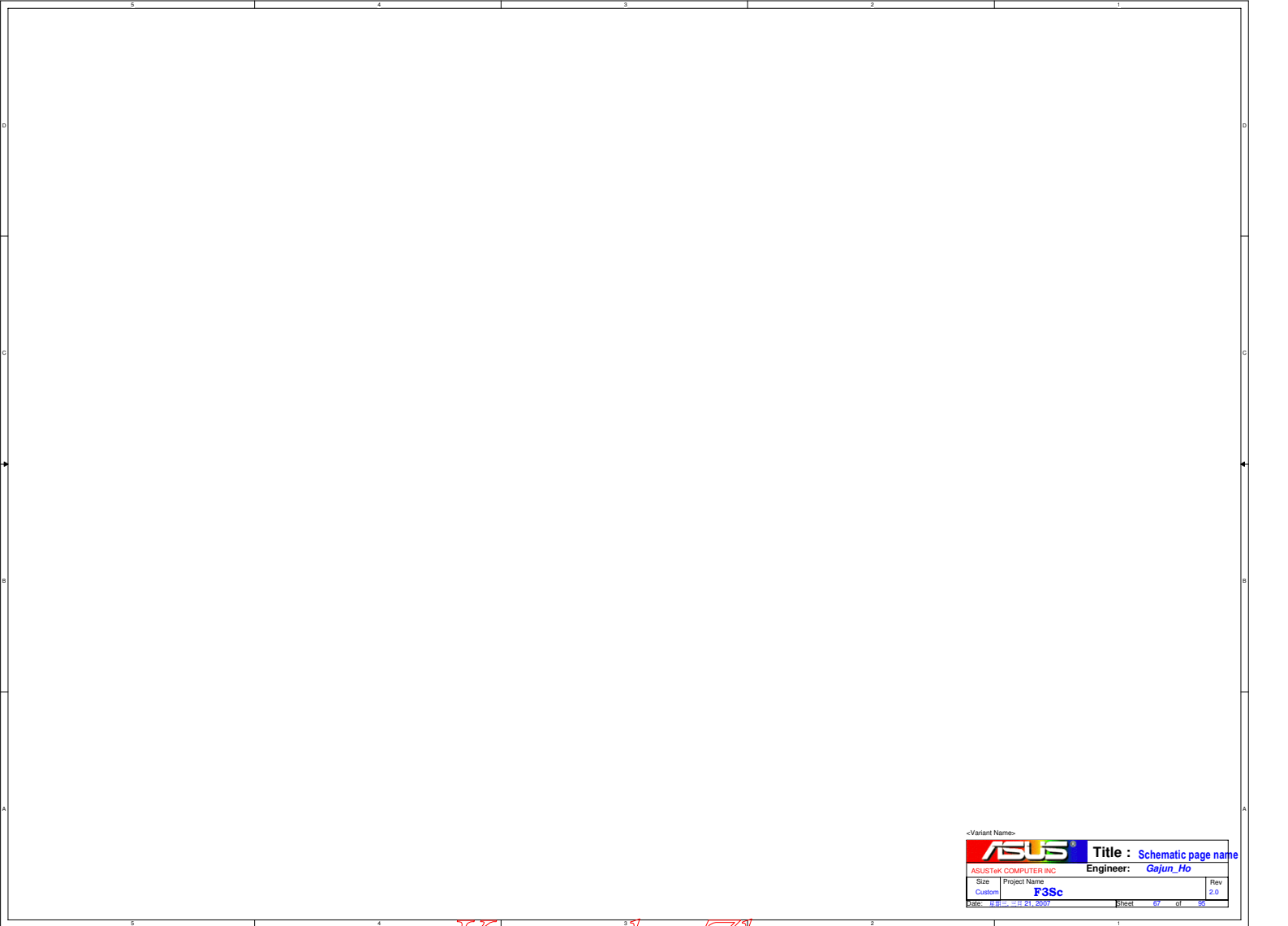


for Cap. Lock



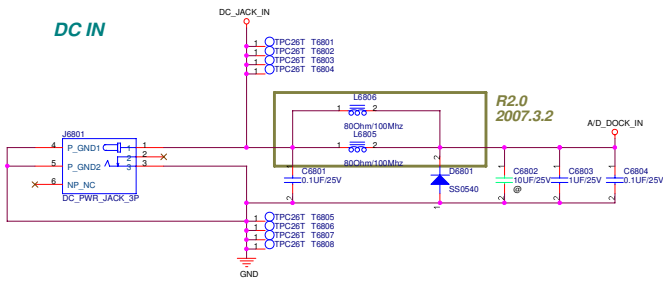
<Variant Name>			
ASUS		Title : LED	
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 11/11/2007		Sheet	66 of 95

<< Kennedy_Zhang >>

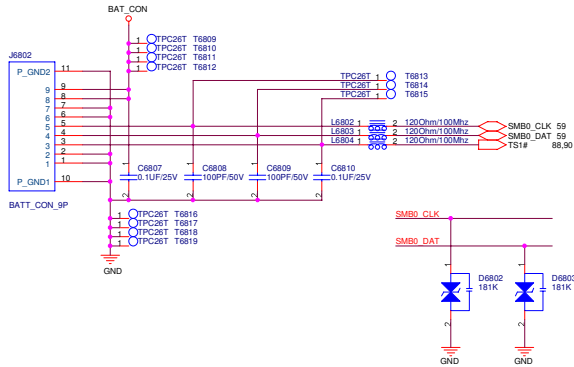


<< Kennedy_Zhang >>

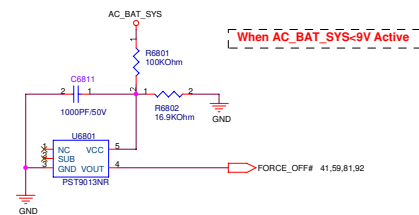
DC IN



BAT IN



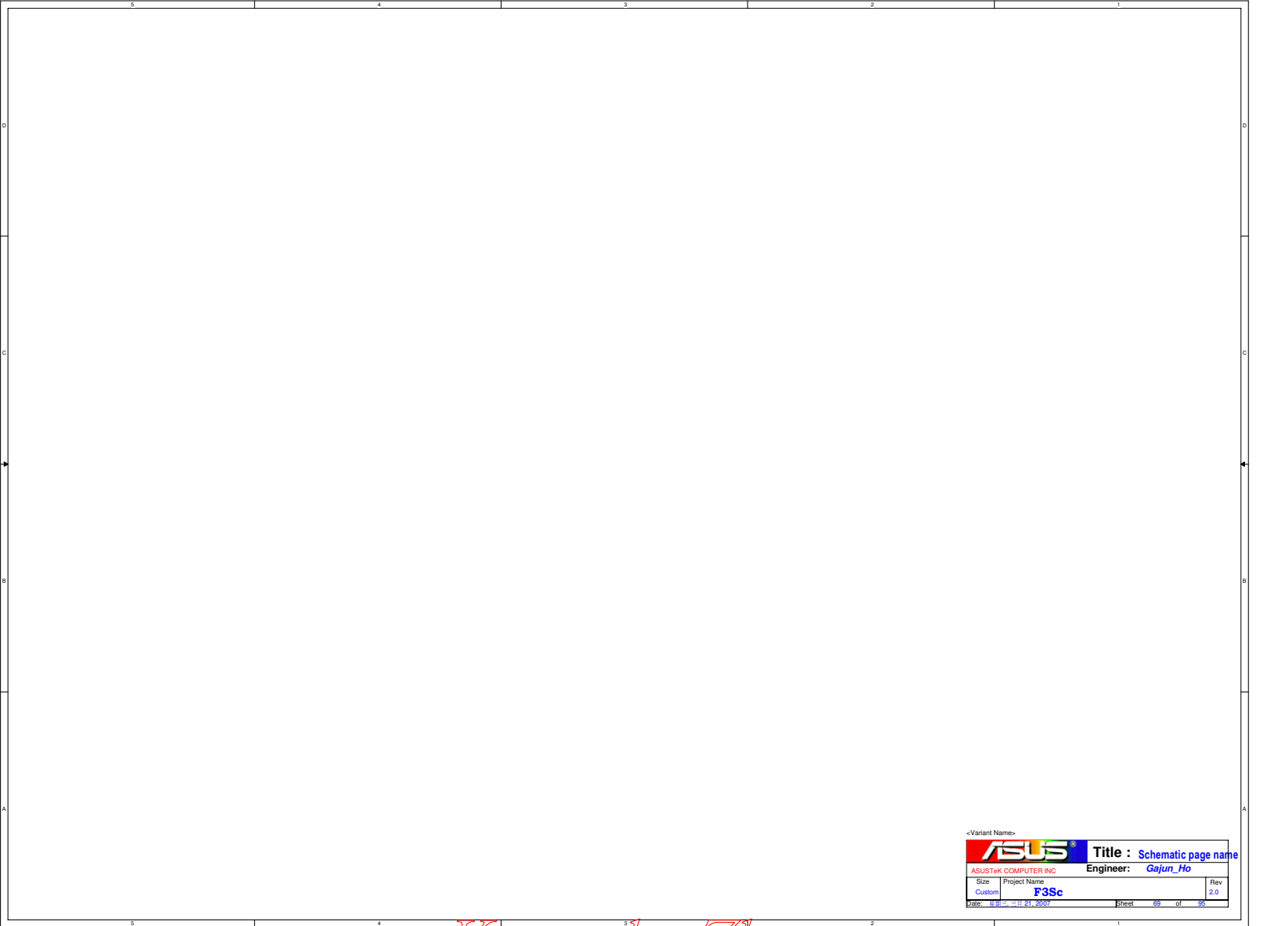
Without Battery & Pull out Adapter



<Variant Name>

ASUS		Title : DC & BAT IN	
ASUSTek COMPUTER INC. NB1		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 11/11/2007		Sheet	68 of 95

<< Kennedy_Zhang >>

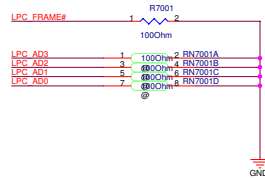
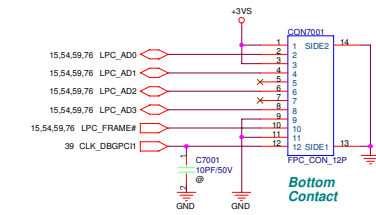


<Variant Name>			
		Title : Schematic page name	
ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2008-01-21 2007		Sheet 69	of 95

<< Kennedy_Zhang >>

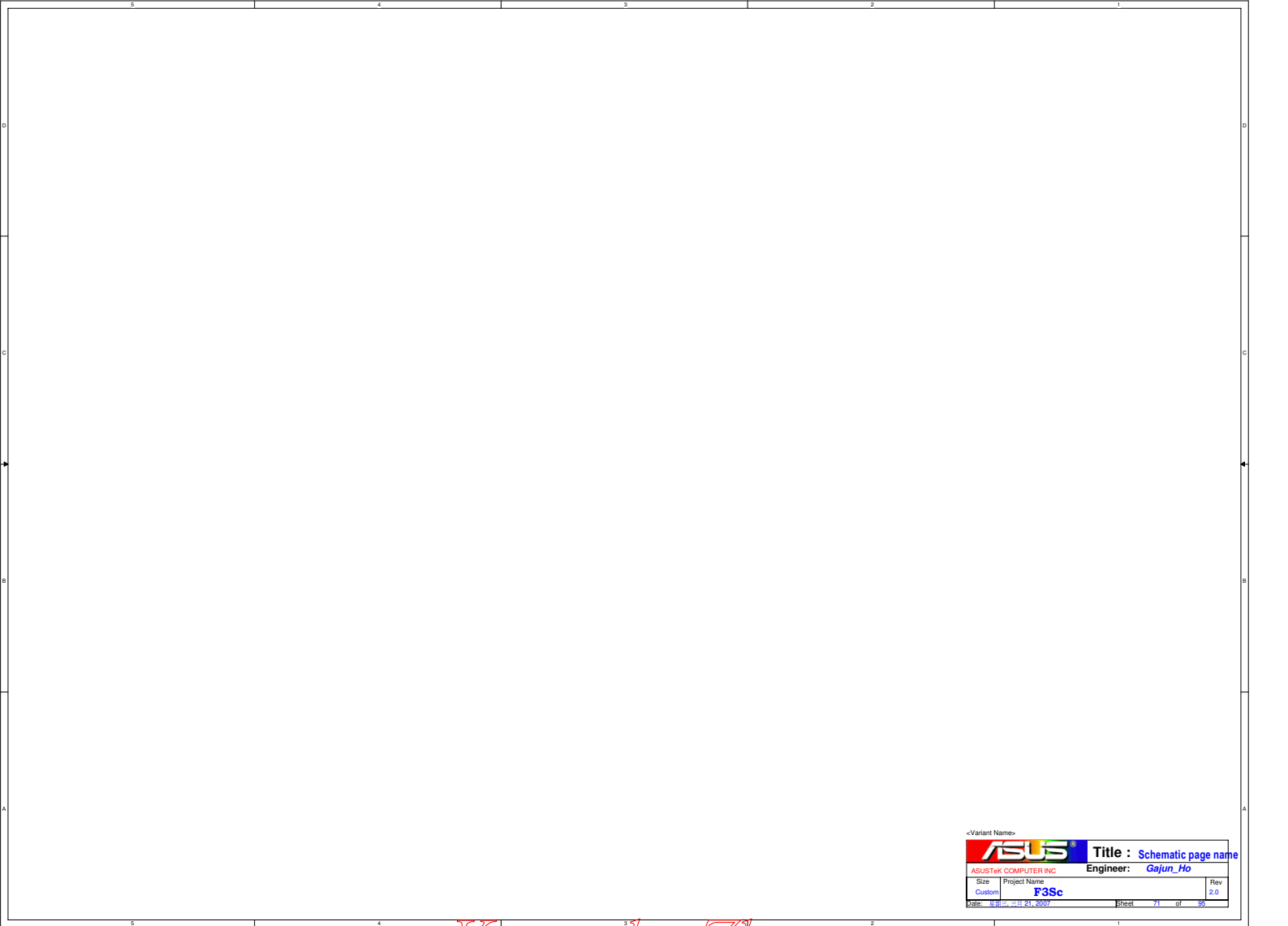
For Debug

If support NewCard Debug Card,
Pls don't mount all components.



<Variant Name>			
		Title : Debug CONN.	
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2007.01.21		Sheet 70	of 95

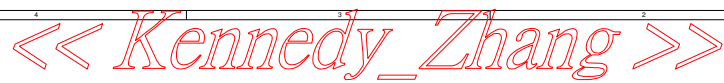
<< Kennedy_Zhang >>



<Variant Name>			
		Title : Schematic page name	
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2008-08-21 2007		Sheet	71 of 95

<< Kennedy_Zhang >>

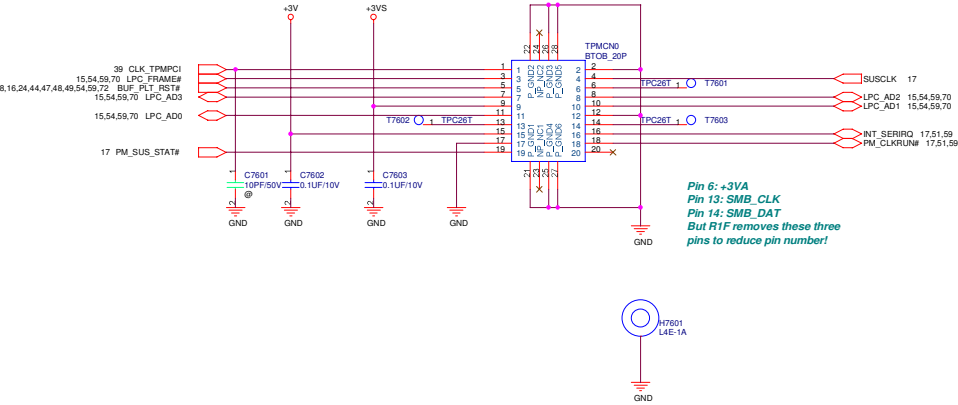
<< Kennedy_Zhang >>



<Variant Name>			
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ASUSTeK COMPUTER INC		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2008-01-21 2007		Sheet 75	of 95

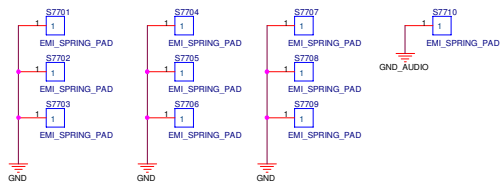
<< Kennedy_Zhang >>

For TPM Module



<Variant Name>			Title : TPM	
ASUS COMPUTER INC.			Engineer: Gajun_Ho	
Size	Project Name			Rev
Custom	F3Sc			2.0
Date: 2007.01.21		Sheet	76	of 95

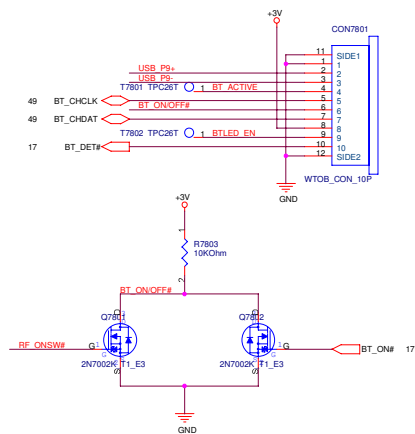
<< Kennedy_Zhang >>



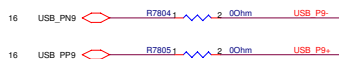
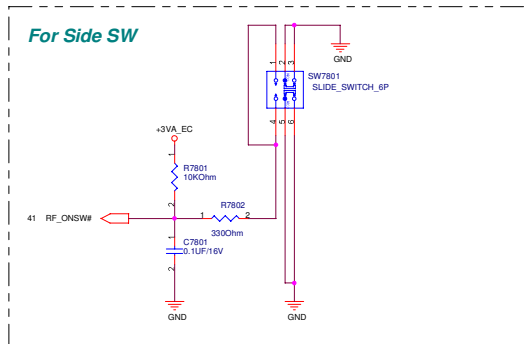
<Variant Name>			
		Title : SPRING_PAD	
ASUSTeK COMPUTER INC.		Engineer: Gajun_Ho	
Size	Project Name		Rev
Custom	F3Sc		2.0
Date: 2007.01.21		Sheet	77 of 95

<< Kennedy_Zhang >>

For Bluetooth



For Side SW

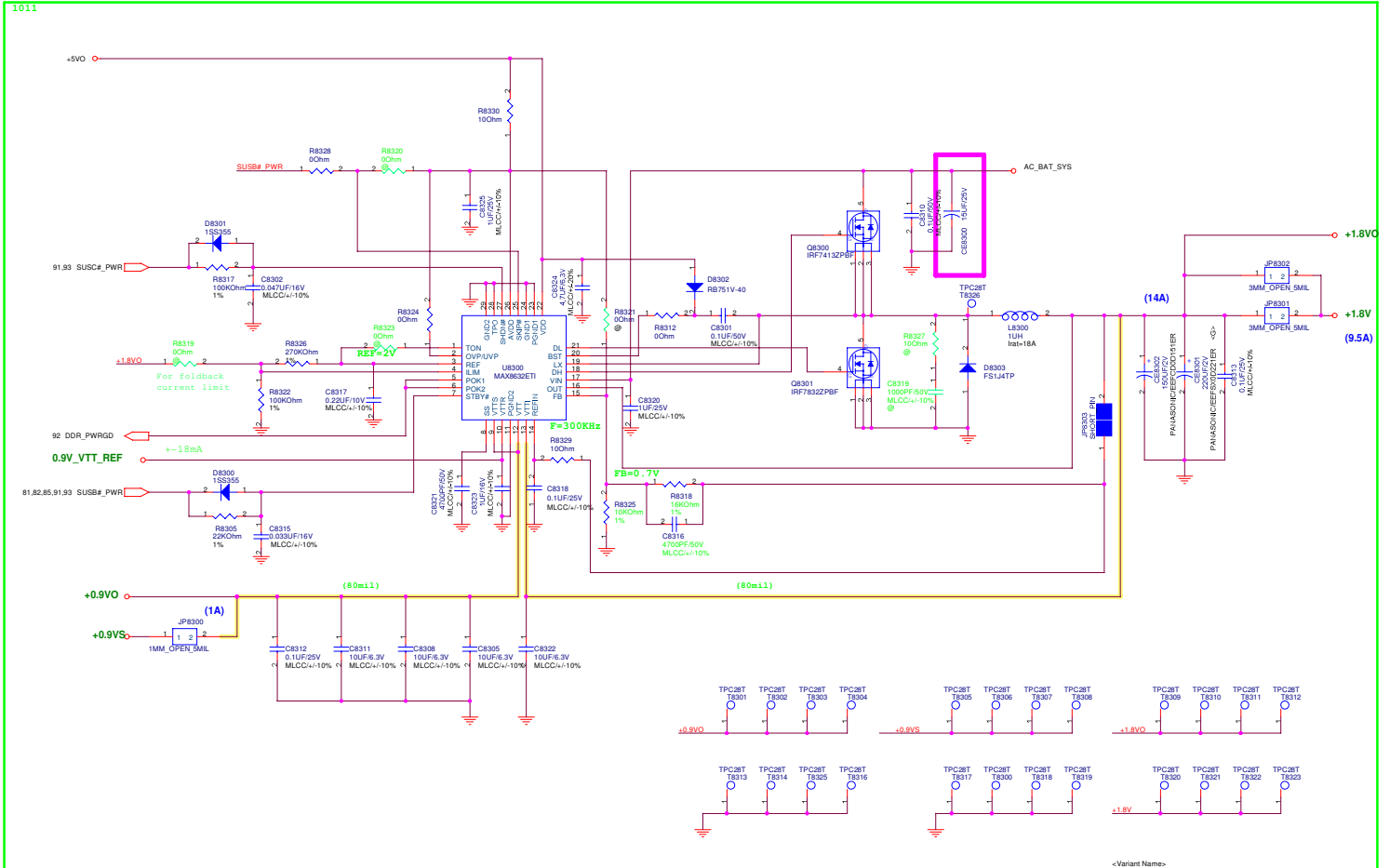


<Variant Name>	
	
Title : Blue Tooth	
ASUSTeK COMPUTER INC	
Engineer: Gajun_Ho	
Size	Project Name
Custom	F3Sc
Date: 2007.01.21	Sheet 78 of 95

<< Kennedy_Zhang >>





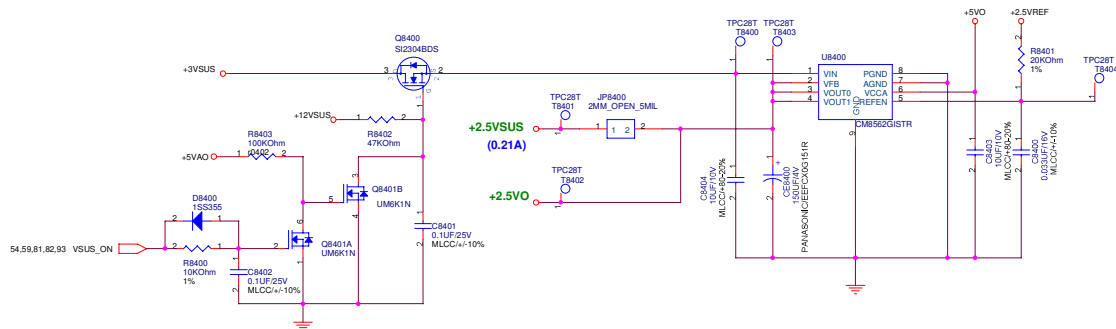


<Variant Name>

ASUS		Title : POWER_IO, DDR & VTT	
ASUSTek COMPUTER INC. NB		Engineer:	
Size	Project Name	Rev	
Custom	F38v/c	1.0	
Date	11/21/2007	Sheet	83 of 94

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+2.5VS



<Variant Name>		Title :POWER I/O, +2.5VS	
ASUSTeK COMPUTER INC. NB		Engineer:	
Size	Project Name	Rev	
Custom	F3Sv/c	1.0	
Date: 2007.01.21		Sheet 84 of 94	

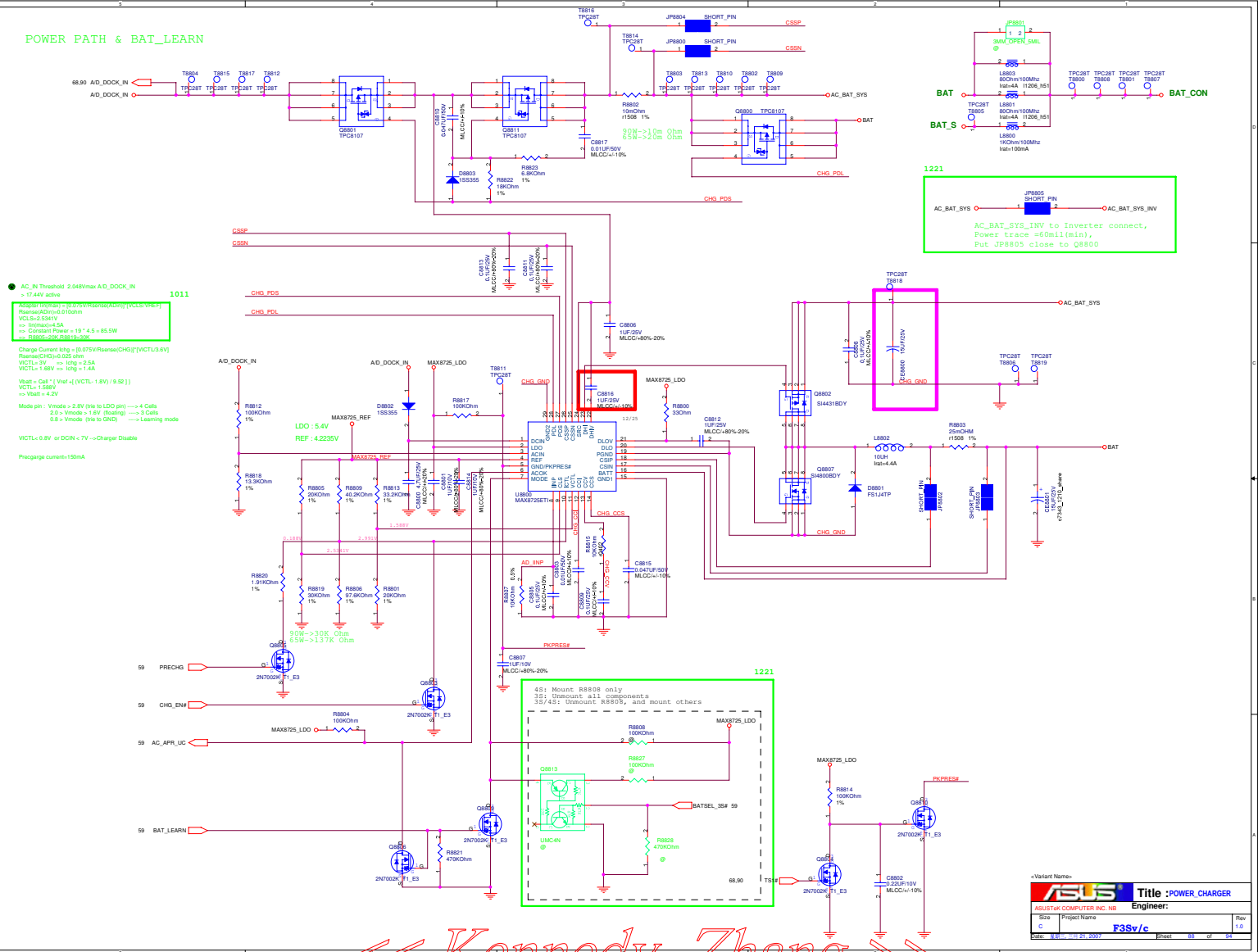
<< Kennedy_Zhang >>

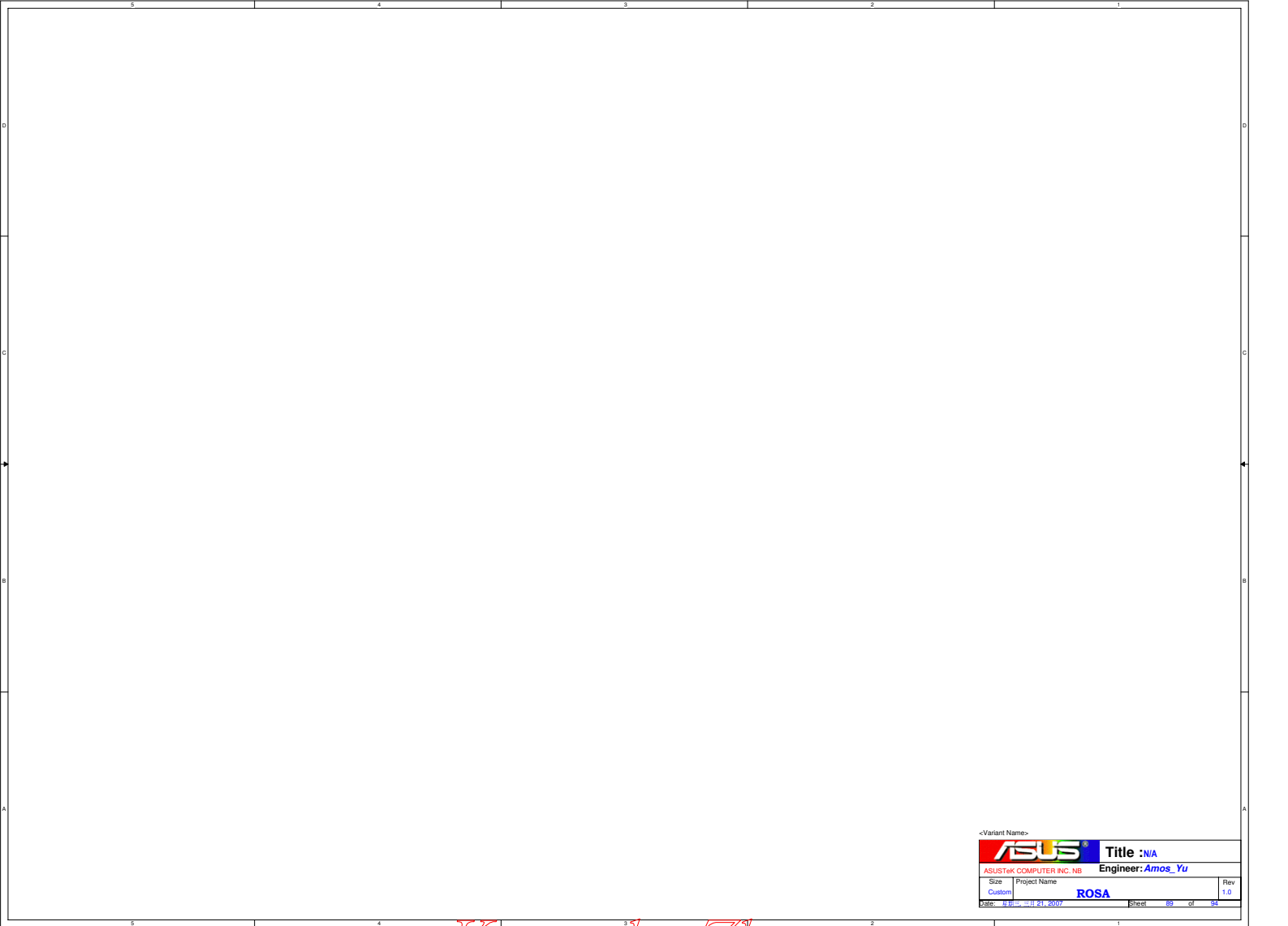
5					4					3					2					1				
D																								
C																								
B																								
A																								
<Variant Name>  Title :<i>N/A</i> ASUSTeK COMPUTER INC. NB Engineer: Size B Project Name <i>ROSA</i> Rev 1.0 Date: <i>11/21/2007</i> Sheet <i>66</i> of <i>94</i>																								
5					4					3					2					1				

<< *Kennedy_Zhang* >>

~Variant Name>

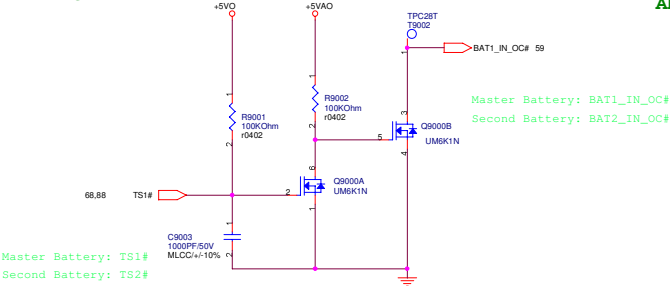
		Title :POWER_SHUTDOWN#	
ASUSTek COMPUTER INC. NB		Engineer:	
Size Custom	Project Name	ROSA	Rev 1.0
Date: 星期二, 三月 21, 2007			Sheet 87 of 94



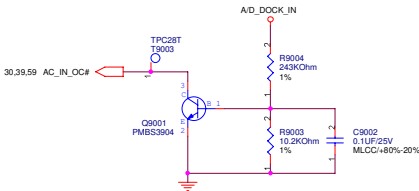


<< Kennedy_Zhang >>

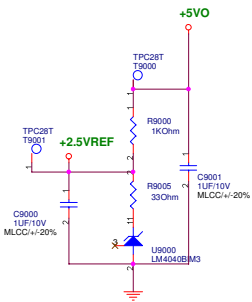
BATTERY IN DETECT



ADAPTER IN DETECT

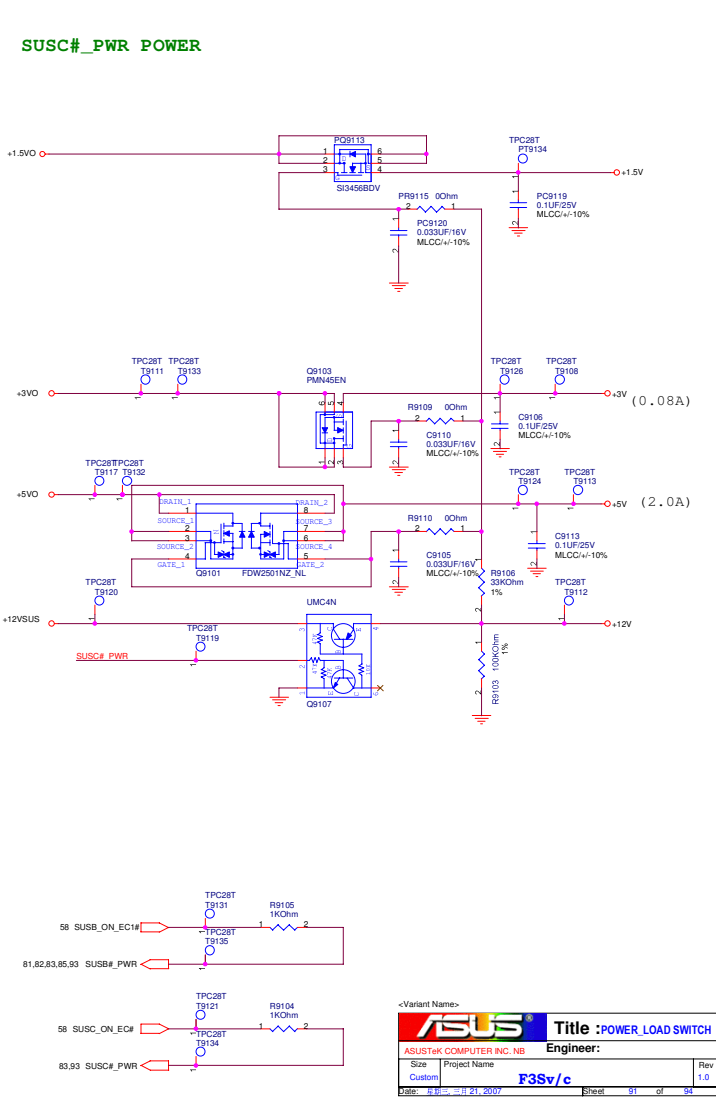
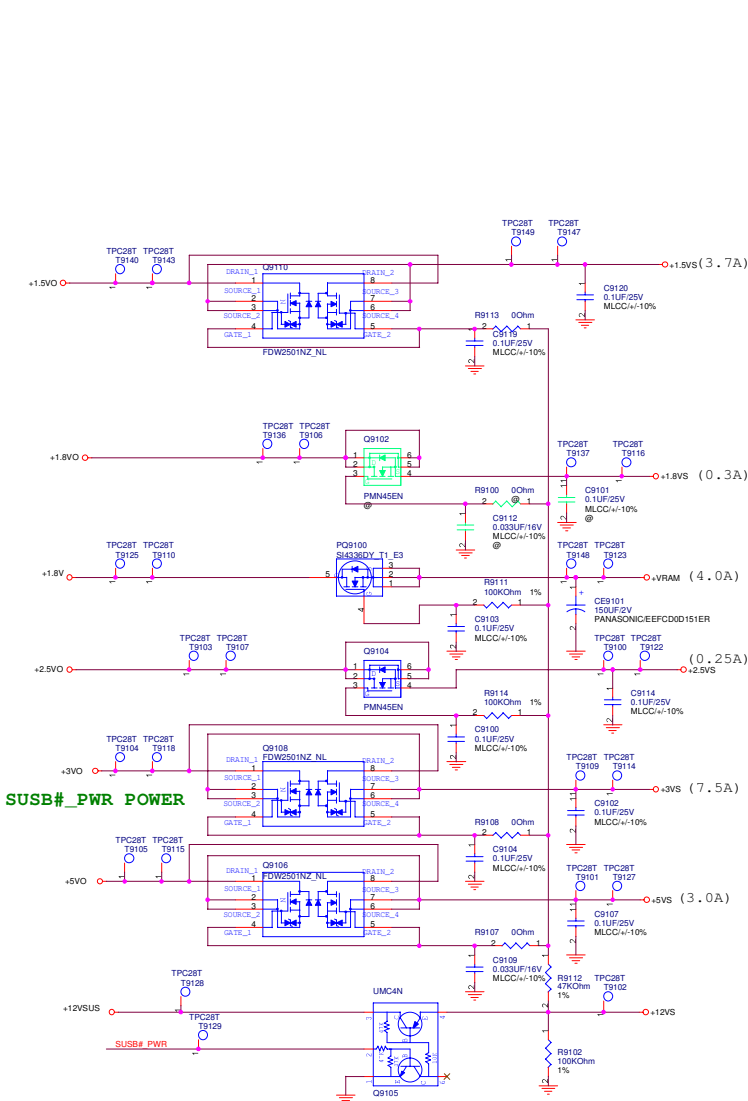


+2.5VREF



<Variant Name>			
		Title :POWER_DETECT	
ASUSTeK COMPUTER INC. NB		Engineer:	
Size	Project Name	Rev	
Custom	F3Sv/c	1.0	
Date: 11/11/2007	Sheet	60	of 94

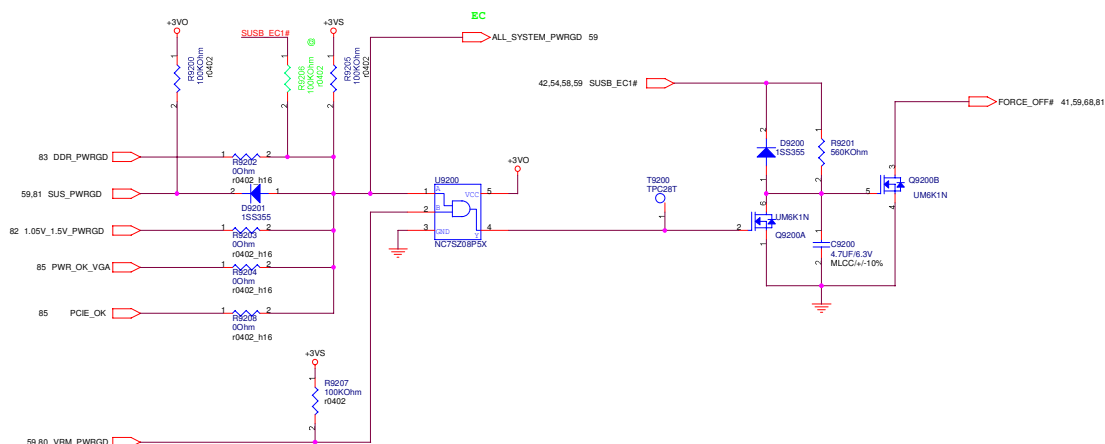
<< Kennedy_Zhang >>



<< Kennedy_Zhang >>

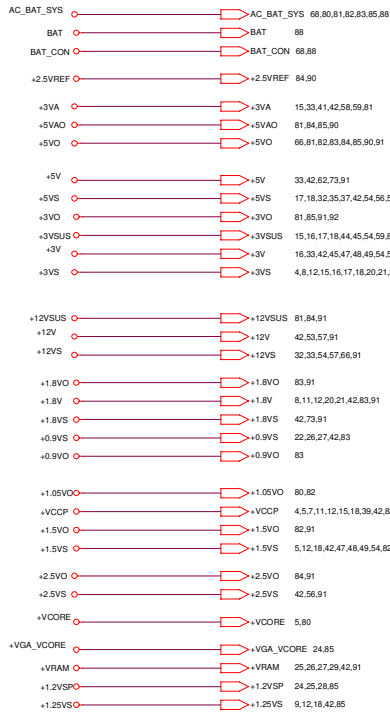
ASUS		Title :POWER_LOAD_SWITCH	
ASUSTeK COMPUTER INC. NE		Engineer:	
Size	Project Name	Rev	
Custom	F3Sv/c	1.0	
Date: 2007.01.21	Sheet	BT	of 94

POWER GOOD DETECTOR

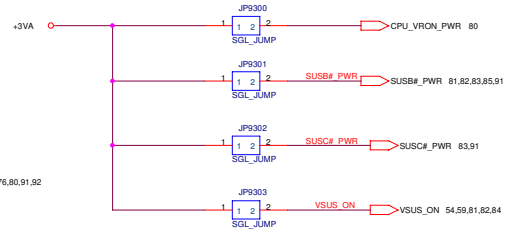


<Variant Name>			
ASUS		Title :POWER_PROTECT	
ASUSTek COMPUTER INC. NB		Engineer:	
Size	Project Name	Rev	
Custom	F3Sv/c	1.0	
Date: 10/10/2007	Sheet 62	of 94	

<< Kennedy_Zhang >>



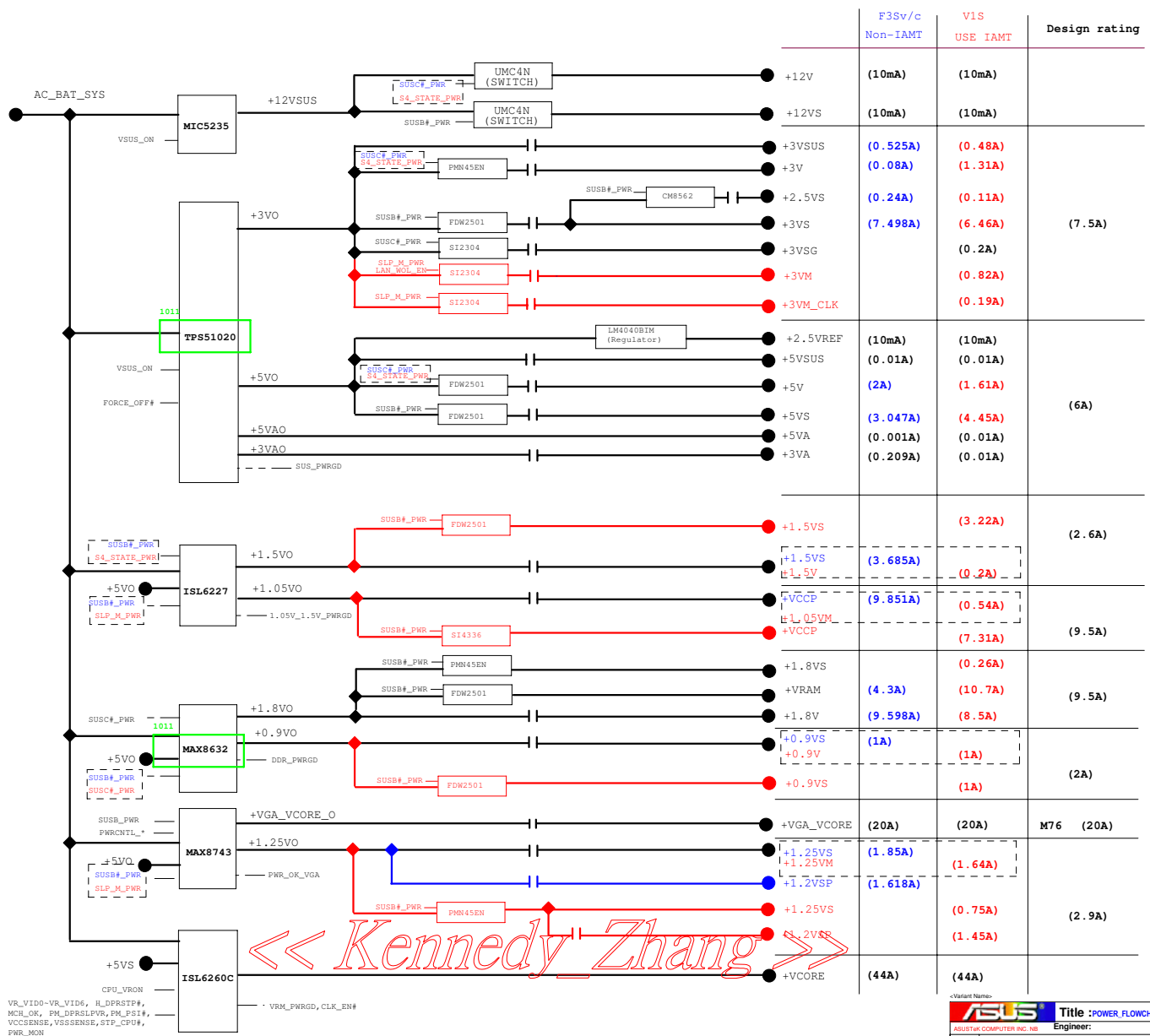
FOR POWER TEST



<Variant Name>

ASUS		Title :POWER SIGNAL	
ASUSTeK COMPUTER INC. NB		Engineer:	
Size	Project Name		Rev
Custom	F3Sv/c		1.0
Date: 10/21/2007	Sheet	89	of 94

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Rev	Date	Description
1.0	2006/10/	1. Initial release.
1.1	2006/12/28	1. Initial release.

Rev	Date	Description

<Variant Name>



Title : History

ASUSTek COMPUTER INC.

Engineer: Jack Chen

Size
Custom

Project Name
F3Sv

Rev
1.0

Date: 08/01/2007

Sheet 05 of 05

<< Kennedy_Zhang >>