1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD / DATE	ENG APPD / DATE
		454815	DVT RELEASE	?	?

SCHEM,YUBA(5V),M57

08/11/06

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
051-7165	1	SCHEM,YUBA,M57	SCH1	
820-2060	1	PCBF,YUBA,M57	PCB1	

DIMENSIONS ARE IN MILLIMETERS
XX ± _____
X.XX ± _____
X.XXX ± _____
ANGLES ° _____
DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION

METRIC

DRAFTER	/	DESIGN CK	/
ENG APPD	/	MFG APPD	/
QA APPD	/	DESIGNER	/
RELEASE	/	SCALE	NONE

MATERIAL/FINISH NOTED AS APPLICABLE

SIZE D

Apple Computer Inc.

TITLE
SCHEM,YUBA,M57

DRAWING NUMBER 051-7165 REV 3.0.0

SHT 1 OF 22

J8200
DC-In Jack
18V5_DCIN
U8290
AC Adapter Detect
18V5_DCIN
J8415
PBus/Battery Power Connector
PPBUS_G3H_DCIN_CONN
U7600
5V S3/S0 SUPPLY
3.3V S3/S0 Supply
3V3_S0
3V3_S3
5V_S0
5V_S3
J5110
Left USB Port2
U5100
J5100
Left USB Port1
USB Pwr Controller
5V_S3
J5500
LIO-MLB Flex Connector
PBUS
U5300
ExCard Pwr Switcher
3V3_S0
3V3_S3
J5300
PCI-ExpressCard 34 Connector
U6801
Audio 4.5V Supply
5V_S0
3V3_S0
J7350
Audio In/SPDIF Rx Jack
J7300
Audio Out/SPDIF Tx Jack
U6800
Audio Codec
U7100
HP AMP
1V5_S0
J5400
PCI-E miniCard (Airport) Connector
3V3_S0
3V3_S3
J7380
Mic Connector
5V_S0
J7382
Speaker Connector
U7210
Speaker Amp
U7220
Speaker Amp
J7381
Speaker Connector
U7230
Speaker Amp
U7240
Speaker Amp
U6401
LEFT ALS
U6402
THERMAL SENSOR

Block Diagram

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 2 OF 22	

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7707	PCBA,YUBA,M57	COMMON,ALTERNATE

BAR CODE LABEL / EEE#'S

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
000-0041	1	PLACEHOLDER FOR EEE/CCC INFO	[EEE:W3B]	CRITICAL	

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
815-8851	1	ALS SPACER, M9	SP6401	CRITICAL	

BOM CONFIGURATION

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0

SCALE	SHT	OF
NONE	3	22

POWER & GROUNDS

PPBUS_G3H_DCIN_CONN
MAKE_BASE=TRUE
VOLTAGE=18.5V
MIN_LINE_WIDTH=1mm
MIN_NECK_WIDTH=0.25mm

PP1V5_S0_LIO
MAKE_BASE=TRUE
VOLTAGE=1.5V
MIN_LINE_WIDTH=0.6MM
MIN_NECK_WIDTH=0.20mm

PP3V3_S3_LIO
MAKE_BASE=TRUE
VOLTAGE=3.3V
MIN_LINE_WIDTH=0.50MM
MIN_NECK_WIDTH=0.20mm

PP3V3_S0_LIO
MAKE_BASE=TRUE
VOLTAGE=3.3V
MIN_LINE_WIDTH=0.30MM
MIN_NECK_WIDTH=0.20MM

PP5V_S3_LIO
VOLTAGE=5V MAKE_BASE=TRUE
MIN_LINE_WIDTH=0.5mm FIX CONSTRAINTS,S5 TITLE
MIN_NECK_WIDTH=0.2mm

PP5V_S0_AUDIO_LIO
MAKE_BASE=TRUE
VOLTAGE=5V
MIN_LINE_WIDTH=0.60 MM
MIN_NECK_WIDTH=0.20 MM

PP5V_S0_AUDIO_AMP_LIO
MAKE_BASE=TRUE
VOLTAGE=5V
MIN_LINE_WIDTH=0.60 MM
MIN_NECK_WIDTH=0.20 MM

GND_AUDIO_AMP
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm

GND_AUDIO
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=1MM
MIN_NECK_WIDTH=0.2MM

SL1
SL-3.05X3.30

CHASSIS_GND1
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm

ZH1
118R98

CHASSIS_GND2
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm

ZH4
118R98

ZH2
118R98

CHASSIS_GND3
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm

ZH5
118R98

CHASSIS_GND4
MAKE_BASE=TRUE
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm

GND
VOLTAGE=0V
MIN_LINE_WIDTH=0.50mm
MIN_NECK_WIDTH=0.20mm

PCI-E EXPRESS CARD 34

USB2_EXCARD_P
USB2_EXCARD_N
PCIE_CLK100M_EXCARD_P
PCIE_CLK100M_EXCARD_N
PCIE_EXCARD_D2R_P
PCIE_EXCARD_D2R_N
PCIE_EXCARD_R2D_P
PCIE_EXCARD_R2D_N
EXCARD_CLKREQ_L
EXCARD_OC_L

MAKE_BASE=TRUE
NET_SPACING_TYPE=USB
NET_SPACING_TYPE=PCIE
NET_PHYSICAL_TYPE=USB
NET_PHYSICAL_TYPE=PCIE

PCIE_WAKE_L
PLT_RESET_L
LIO_P3V3S3_EN
PM_SLP_S3_LS5V

PCI-E MINICARD

PCIE_MINI_R2D_P
PCIE_MINI_R2D_N
PCIE_MINI_D2R_P
PCIE_MINI_D2R_N
PCIE_CLK100M_MINI_P
PCIE_CLK100M_MINI_N
MINI_CLKREQ_L

USB

USB_LEFT_P
USB_LEFT_N
USB_LEFT_OC_L
USB_LEFT2_P
USB_LEFT2_N
USB_LEFT2_OC_L
USB2_MINI_P
USB2_MINI_N
TP_USB2_MINIP
TP_USB2_MININ

SMBUS

SMBUS_SMC_SDA
SMBUS_SMC_SCL
SMBUS_SB_SDA
SMBUS_SB_SCL

Aliases

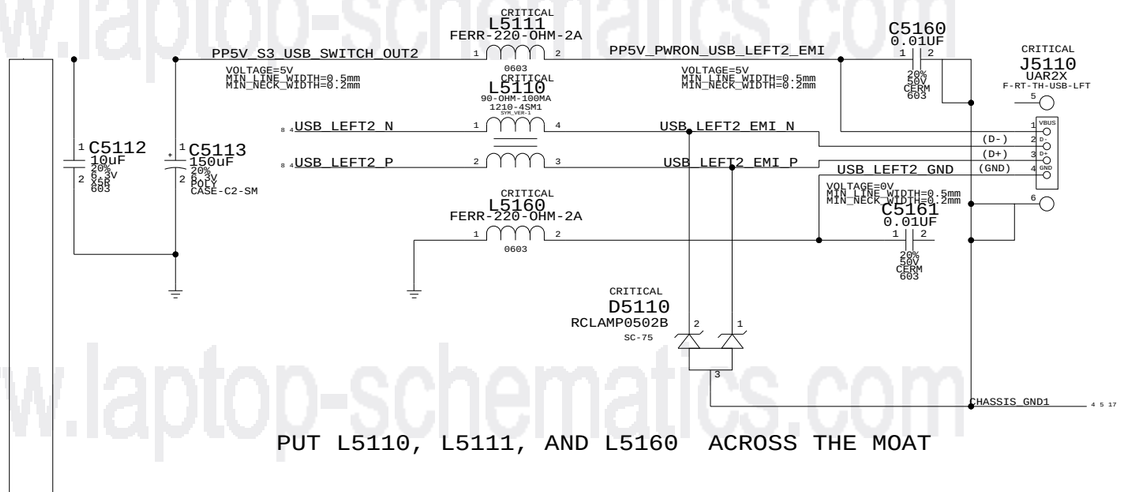
SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0

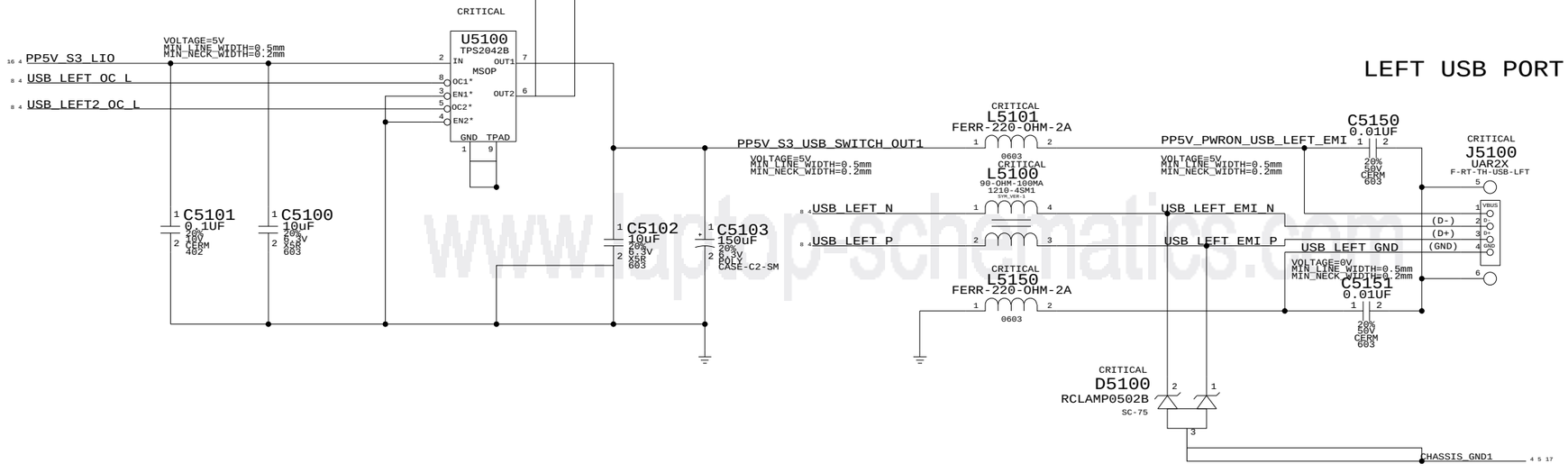
SCALE	SHT	OF
NONE	4	22

LEFT2 USB PORT



PUT L5110, L5111, AND L5160 ACROSS THE MOAT

LEFT USB PORT



PUT L5100, L5101, AND L5150 ACROSS THE MOAT

Left USB Port

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 5 OF 22	

EXPRESSCARD/34 TOP MOUNT CONNECTOR

INPUT DECOUPLING

16 9 7 6 4 PP3V3_S3_LIO
- C5330 0.1uF 20% 10V CERM 402
- C5331 10uF 20% 6.3V X5R 603

8 7 6 4 PP1V5_S0_LIO
- C5334 0.1uF 20% 10V CERM 402
- C5335 10uF 20% 6.3V X5R 603

CRITICAL

U5300 TPS2231 QFN

16 9 7 6 4 PP3V3_S3_LIO	17 AUXIN	AUXOUT 15	
16 15 14 10 7 6 4 PP3V3_S0_LIO	2 VIN3P3	VOUT3P3 3	
8 7 6 4 PP1V5_S0_LIO	12 VIN1P5	VOUT1P5 11	
8 SMC_EXCARD_PWR_EN	20 SHDN*	PERST* 8	
TP_EXCARD_STBY_L	1 STBY*	CPPE* 10	
8 7 4 PLT_RESET_L	6 SYSRST*	CPUSB* 9	
8 4 EXCARD_OC_L	19 OC*		
NC 4	NC0	RCLKEN 18	EXCARD_RCLKEN 6
NC 5	NC1		
NC 13	NC2		
NC 14	NC3		
NC 16	NC4	THRML_PAD 21	
	GND		

R5300 0 MF-LF 402 5% 1/16W
8 SMC_EXCARD_PWR_EN
TP_EXCARD_STBY_L

OUTPUT DECOUPLING

VOLTAGE=3.3V MIN_LINE_WIDTH=.3mm MIN_NECK_WIDTH=0.2mm
PP3V3_S3_EXCARD_SWITCH 6
- C5300 0.1uF 20% 10V CERM 402
- C5303 10uF 20% 6.3V X5R 603

VOLTAGE=3.3V MIN_LINE_WIDTH=.6mm MIN_NECK_WIDTH=0.2mm
PP3V3_S0_EXCARD_SWITCH 6
- C5301 0.1uF 10% 16V X5R 402
- C5304 10uF 20% 6.3V X5R 603

VOLTAGE=1.5V MIN_LINE_WIDTH=.6mm MIN_NECK_WIDTH=0.2mm
PP1V5_S0_EXCARD_SWITCH 6
- C5302 0.1uF 10% 16V X5R 402
- C5305 10uF 20% 6.3V X5R 603

PLT_RESET_SWITCH_L 6
EXCARD_CPPE_L 6
EXCARD_CPUSB_L 6

CARD CAGE CONNECTOR
CRITICAL
J5300 237-53007039903-2 F-RT-SM

15 14 6 4 CHASSIS_GND2
R5371 0 5% 1/10W MF-LF 402 1 2 EC_HOLE1

8 4 USB2_EXCARD_N	1	2
8 4 USB2_EXCARD_P	3	4
6 EXCARD_CPUSB_L	5	6
	NC RESERVED	NC RESERVED
8 4 SMBUS_SB_SCL	7	8
8 4 SMBUS_SB_SDA	9	10
6 PP1V5_S0_EXCARD_SWITCH	11	12
8 7 4 PCIE_WAKE_L	13	14
6 PP3V3_S3_EXCARD_SWITCH	15	16
6 PLT_RESET_SWITCH_L	17	18
6 PP3V3_S0_EXCARD_SWITCH	19	20
6 EXCARD_CLKREQ_CONN_L	21	22
6 EXCARD_CPPE_L	23	24
8 4 PCIE_CLK100M_EXCARD_N	25	26
8 4 PCIE_CLK100M_EXCARD_P		
8 4 PCIE_EXCARD_D2R_N	27	
8 4 PCIE_EXCARD_D2R_P	33	
8 4 PCIE_EXCARD_R2D_N		
8 4 PCIE_EXCARD_R2D_P		

R5370 0 5% 1/10W MF-LF 402
EC_HOLE2 1 2 CHASSIS_GND2 4 6 14 15
CHASSIS_GND4 4 8

16 9 7 6 4 PP3V3_S3_LIO
6 EXCARD_CPUSB_L
6 EXCARD_CPPE_L
U5351 MC74VHC1G00 SC70-5
SMC_EXCARD_CP 8
C5350 0.1uF 20% 10V CERM 402

16 15 14 10 7 6 4 PP3V3_S0_LIO
R5361 100K 1% 1/16W MF-LF 402
R5360 100K 1% 1/16W MF-LF 402
6 EXCARD_RCLKEN
EXCARD_CLKREQ_CONN
U5360 MC74VHC1G00 SC70-5
EXCARD_CLKREQ_L 4 8
6 EXCARD_CLKREQ_CONN_L
Q5360 2N7002 SOT23-LF
C5360 0.1uF 20% 10V CERM 402

ExpressCard Connector

SYNC_MASTER=M59_LIO SYNC_DATE=07/31/2006

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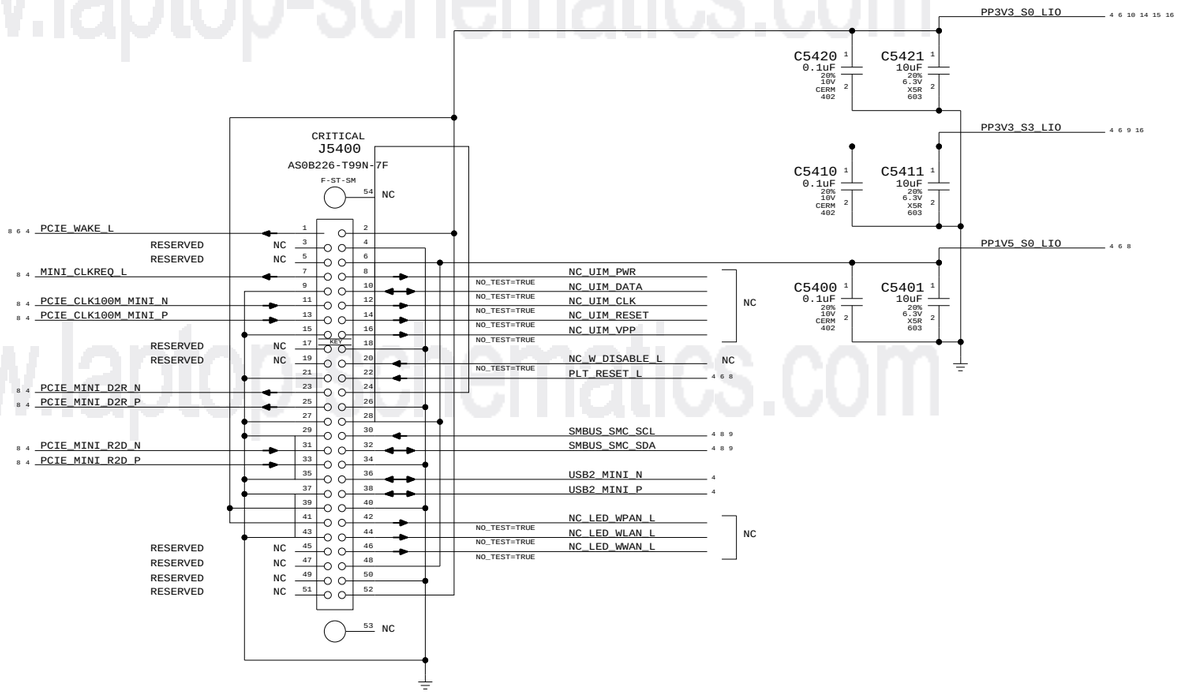
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 6 OF 22	

PCI-EXPRESS MINI CARD CONNECTOR



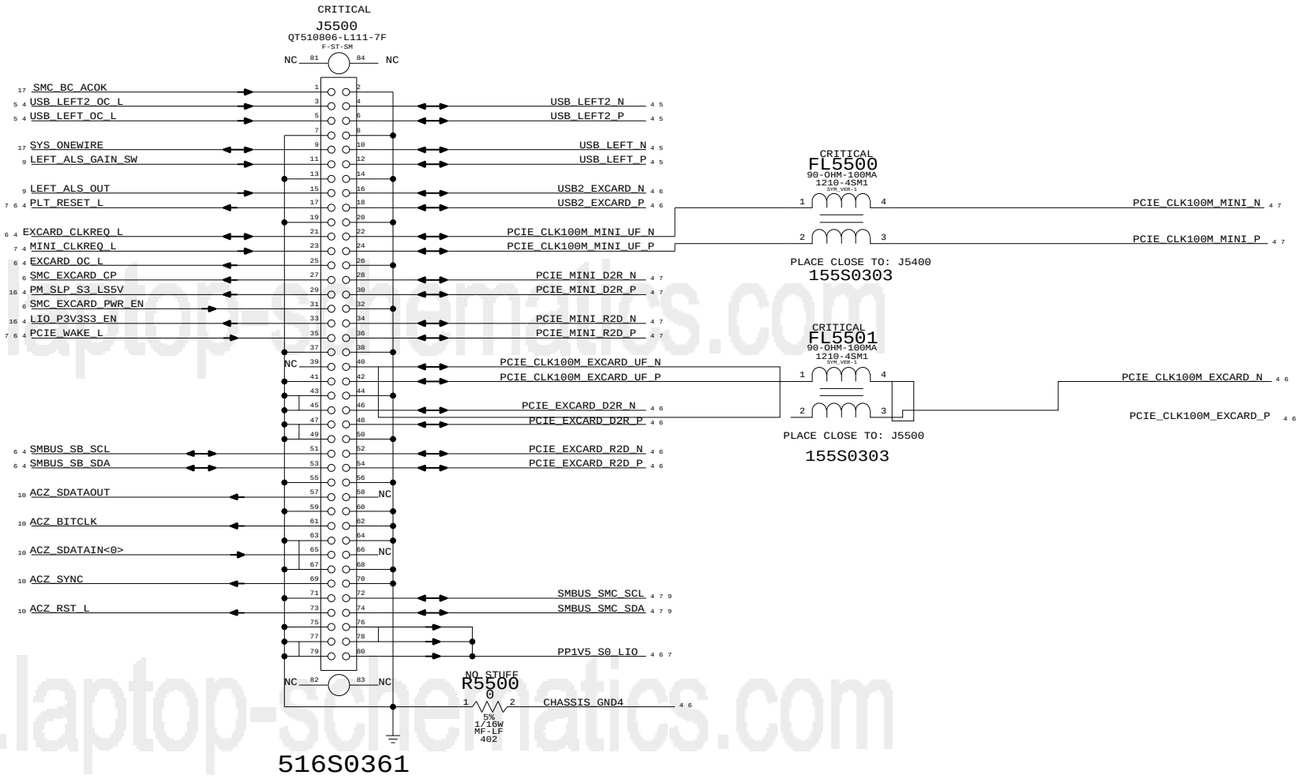
PCI-E MiniCard Connector

SYNC_MASTER=M59_LIO SYNC_DATE=07/31/2006

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 7 OF 22	

Left I/O Board Connector



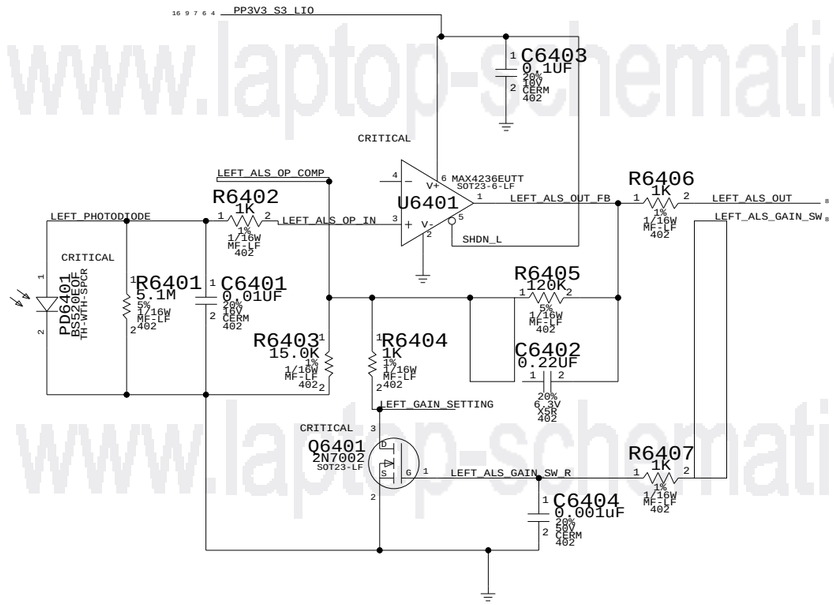
516S0361

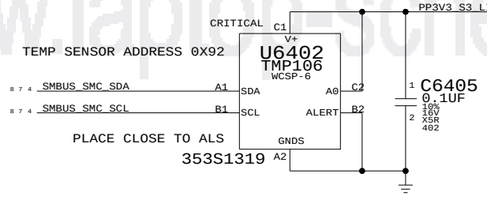
MLB I/O Board Connector

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 8 OF 22	





Left ALS

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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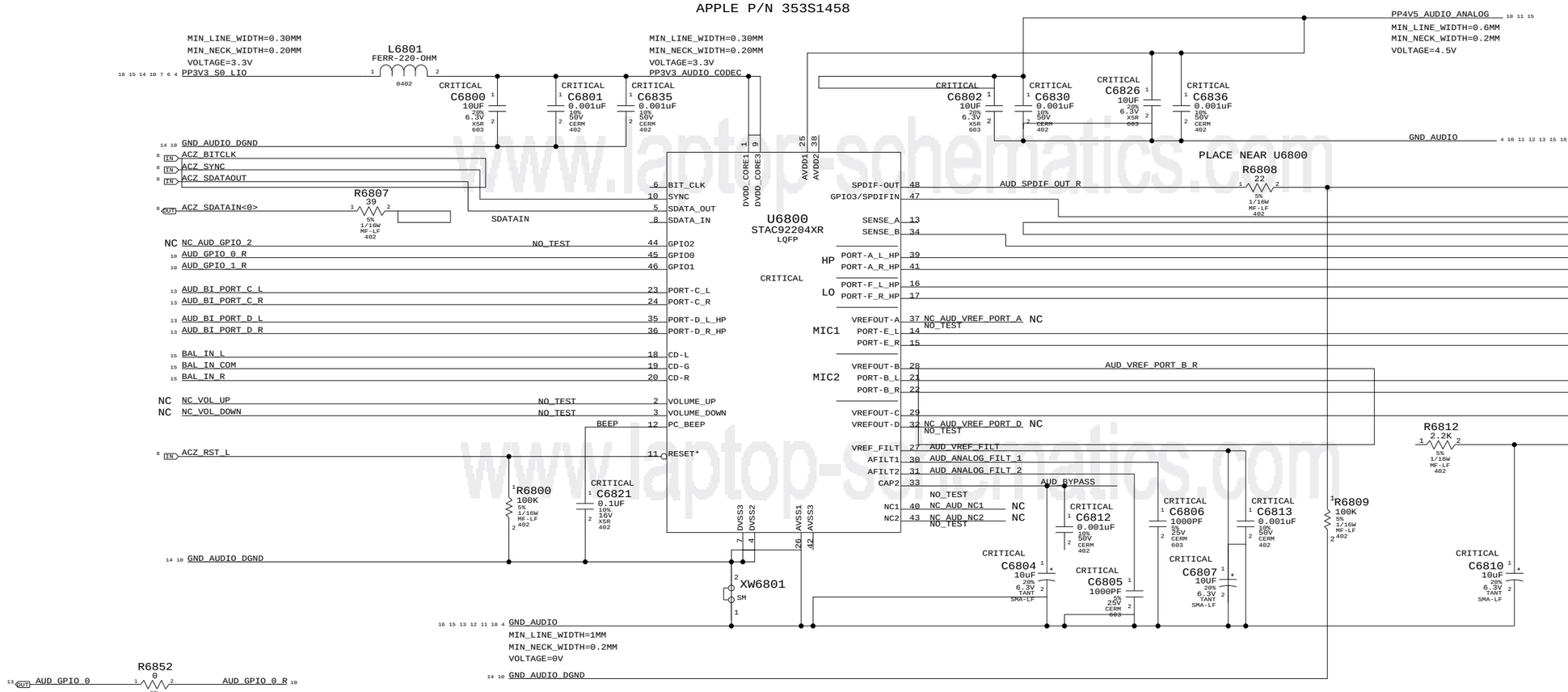
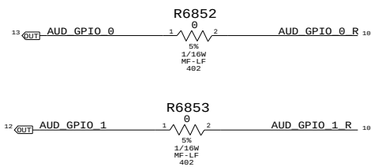
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 9 OF 22	

AUDIO CODEC

APPLE P/N 353S1458

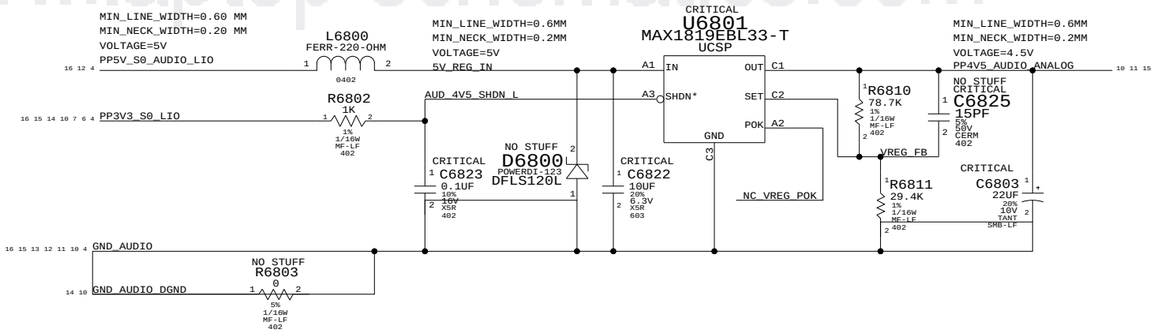




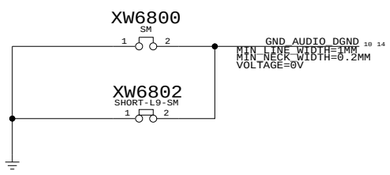
PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
353S1345	353S1458		U6800	DC OFFSET SCREENED

4.5V POWER SUPPLY FOR CODEC

APN: 353S1455



AUDIO DIGITAL GROUND ISOLATION



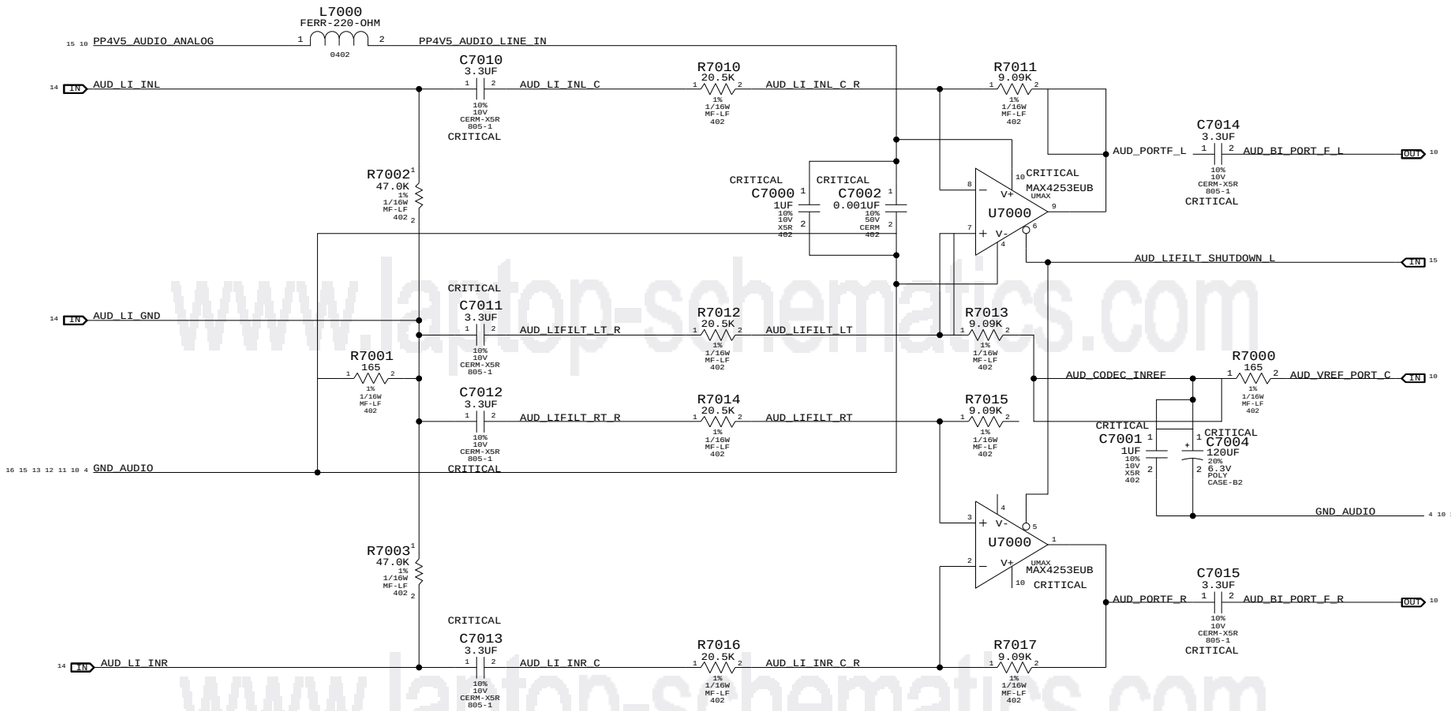
AUDIO: CODEC

SYNC_MASTER=LENGO_M57_AUDIO SYNC_DATE=07/31/2006

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 10 OF 22	

Pseudo-Diff Line-In Filter
GAIN = -7.1DB AV = 0.44
FC = 2.4 HZ



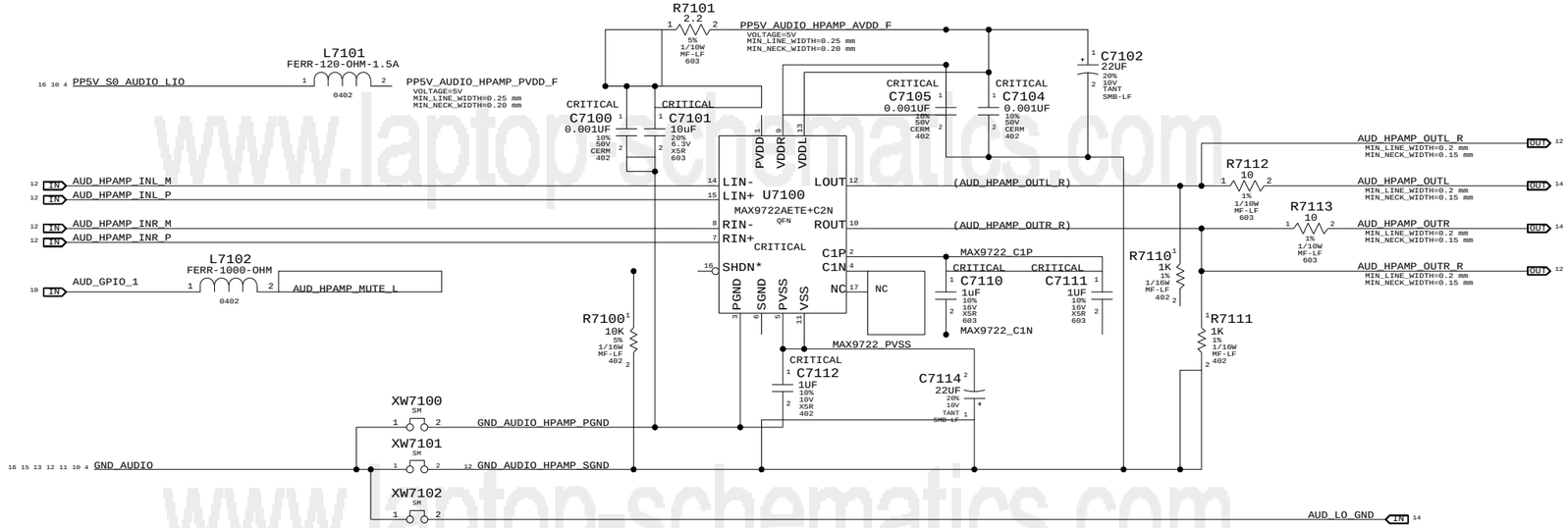
AUDIO: LINE IN

SYNC_MASTER=LENGO_M57_AUDIO SYNC_DATE=07/31/2006

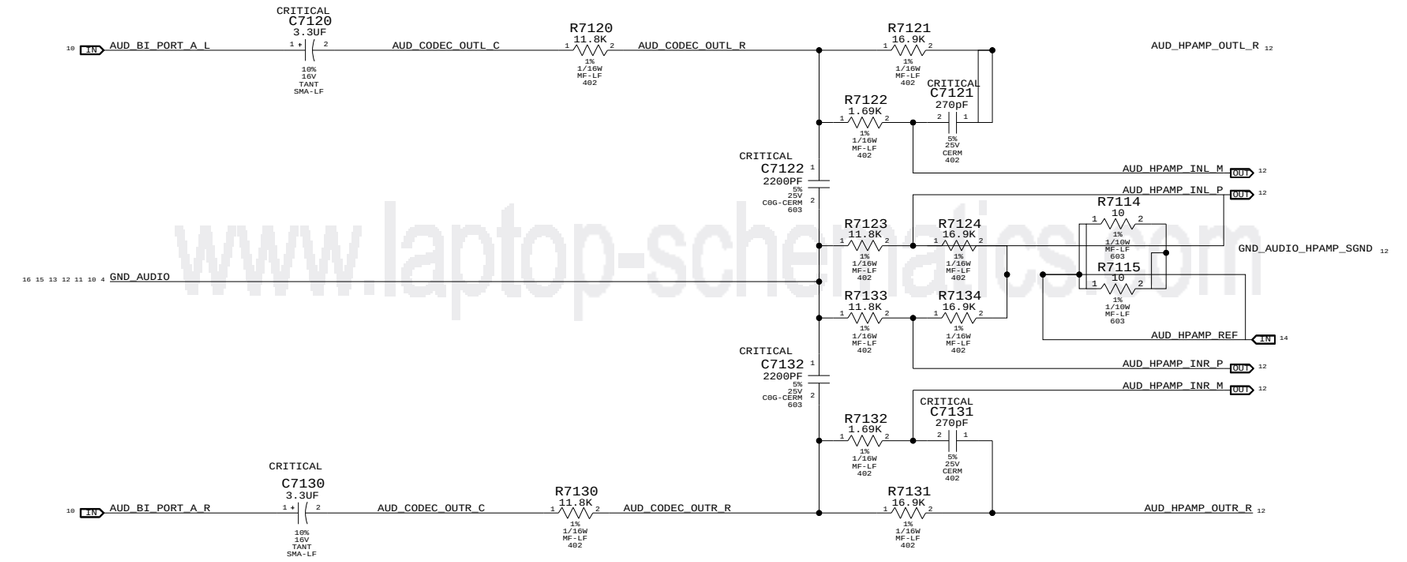
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 11 OF 22	

Headphone Amplifier (MAX9722)
APN:353S1536
VOLTAGE GAIN:1.43



2nd Order DAC Filter
HP:4.09 HZ LP:39 KHZ



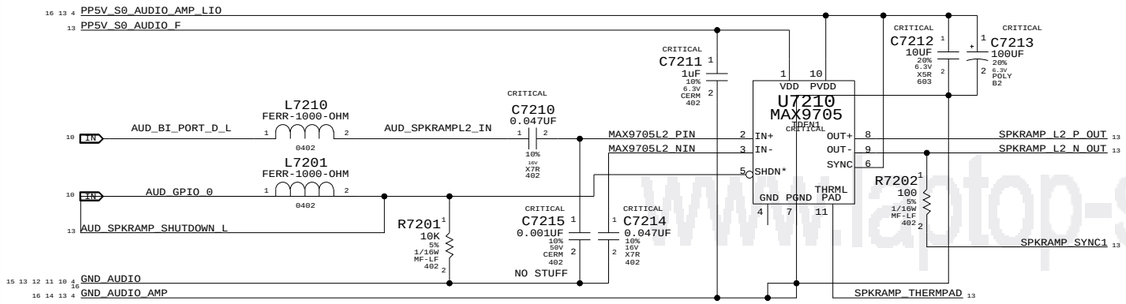
AUDIO: HEADPHONE AMP

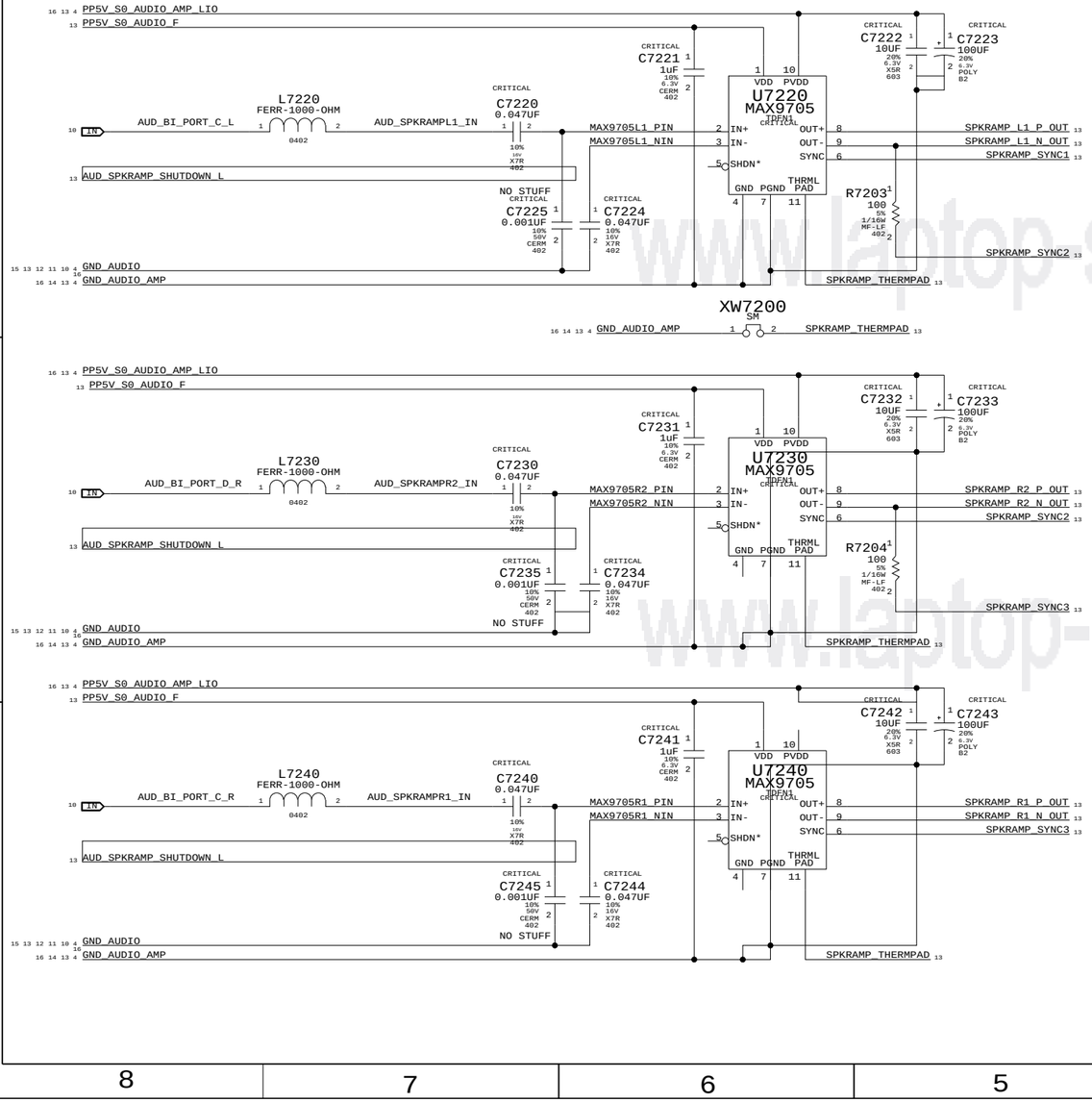
SYNC_MASTER=LENGO_M57_AUDIO SYNC_DATE=07/31/2006

APPLE COMPUTER INC.

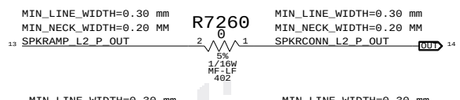
SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 12 OF 22	

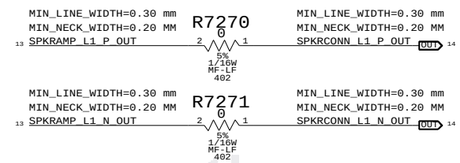
SPEAKER AMPLIFIERS (MAX9705) APN: 353S1595 TURN ON TIME: 30MS
GAIN = 12DB 170 < FC < 281HZ TURN ON DELAY: 60MS

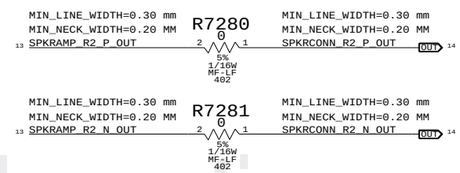


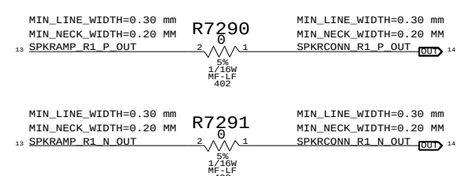


FANCY TESTPOINTS FOR SPEAKER OUTPUTS

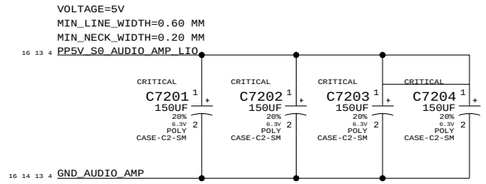




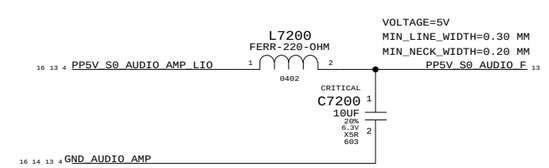




POWER AMPLIFIER SUPPLY BULK CAPS



ANALOG POWER RAIL



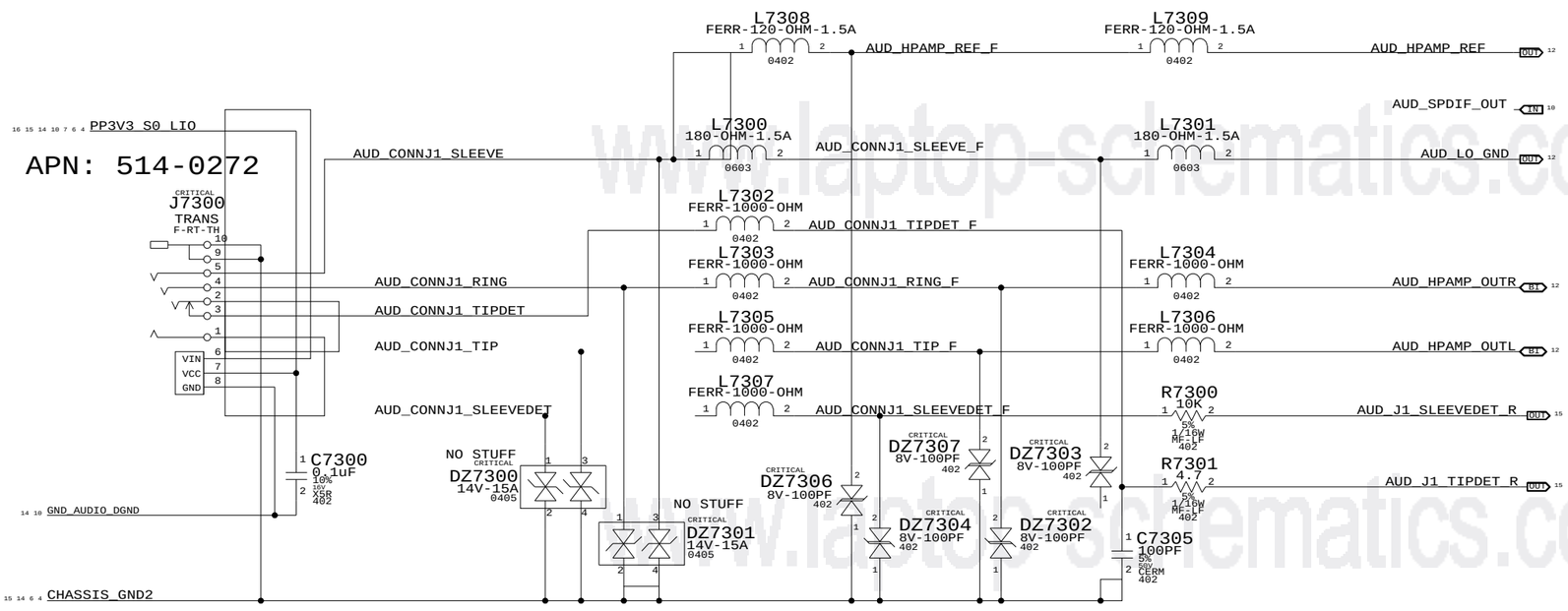
AUDIO: SPEAKER AMP

SYNC_MASTER=LENGO_M57_AUDIO SYNC_DATE=07/31/2006

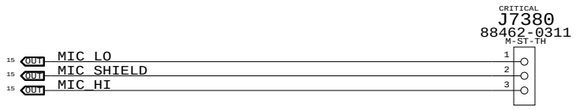
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 13 OF 22	

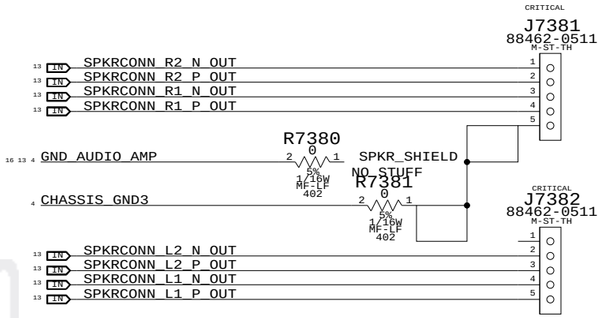
AUDIO JACK 1 LO/HP CONNECTOR, SPDIF TX



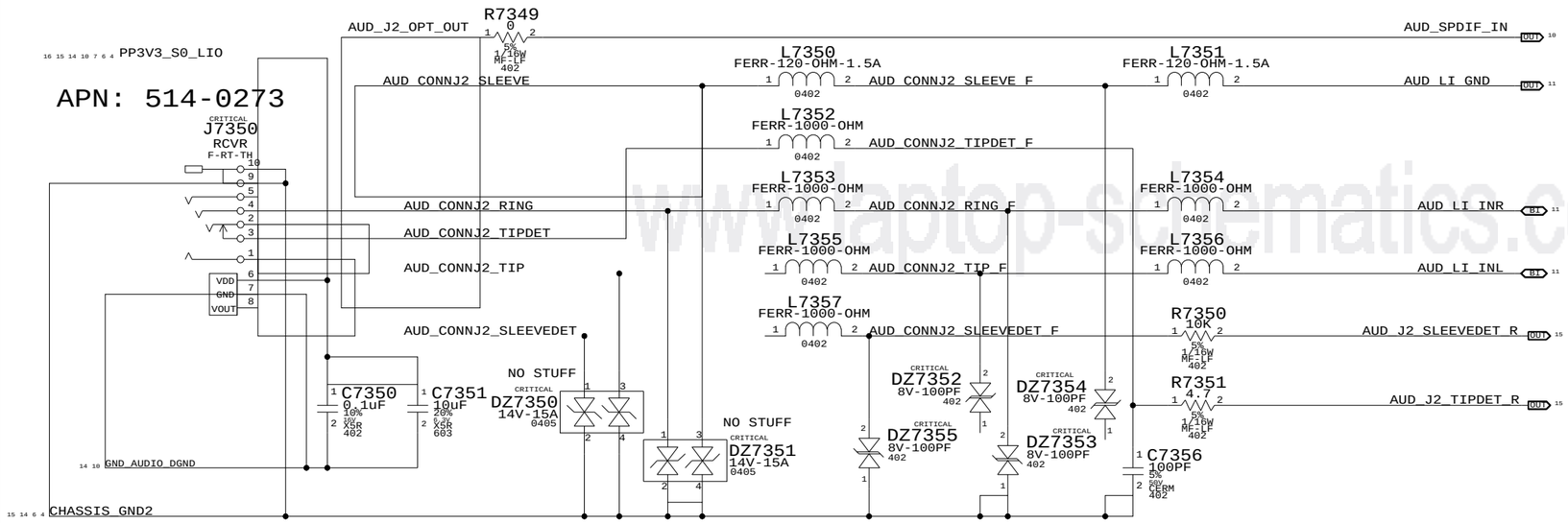
MIC CONNECTOR
APN: 518-0230

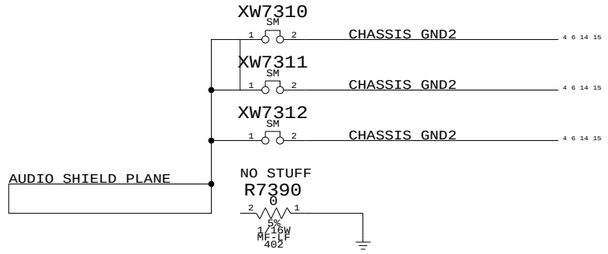


SPEAKER CONNECTORS
APN: 518-0229



AUDIO JACK 2 LINE IN CONNECTOR, SPDIF RX





AUDIO SHIELD
(FILLED SHAPE)

AUDIO: JACKS

SYNC_MASTER=LENGO_M57_AUDIO SYNC_DATE=07/31/2006

APPLE COMPUTER INC.

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D	051-7165	3.0.0
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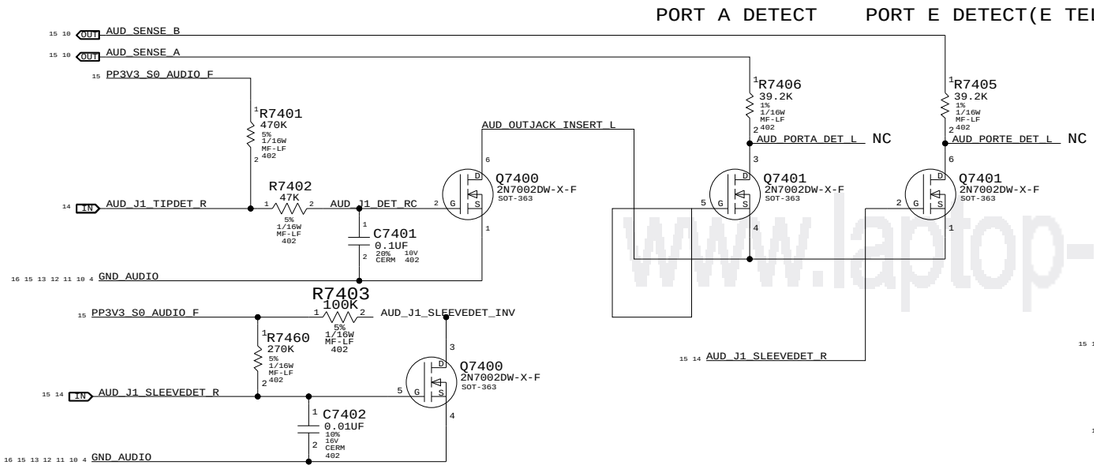
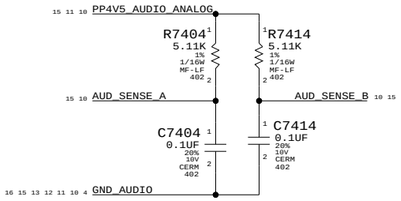
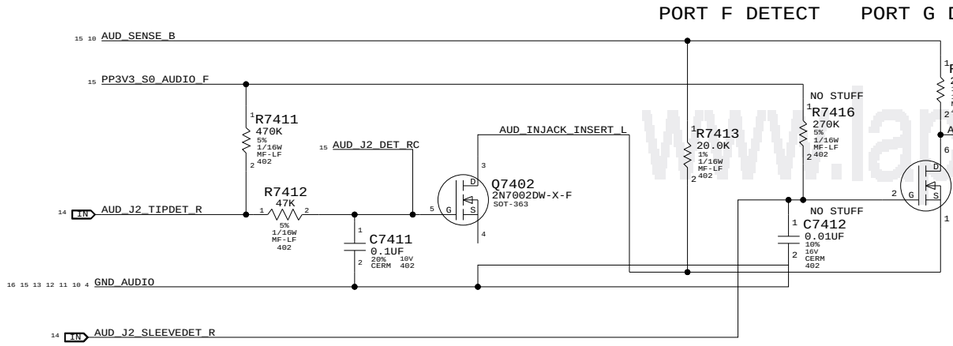
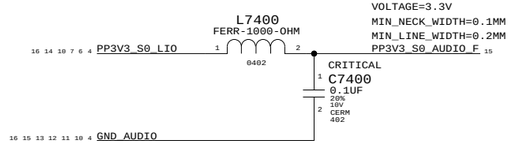






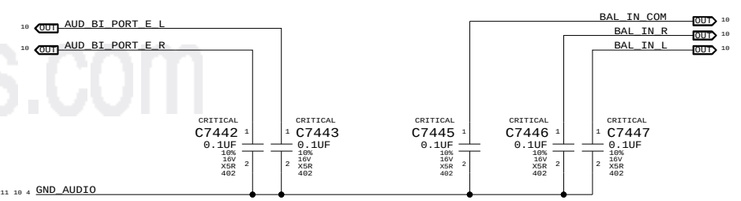


PLACE L7400/C7400 CLOSE TO Q7400

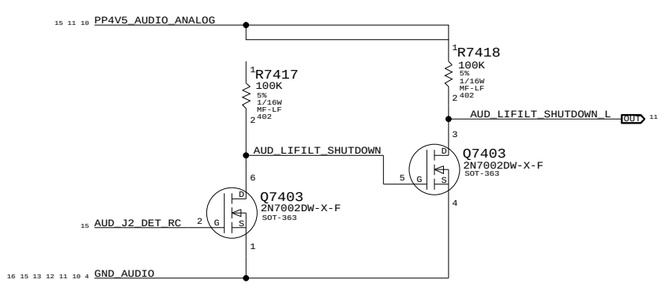
CODEC PORT ASSIGNMENTS

PORT A :HEADPHONE/LINE OUT
PORT B :MICROPHONE ON BOTH CH (ADC 0)
PORT C :TRANSDUCER 1 ON LEFT/RIGHT SPEAKER
PORT D :TRANSDUCER 2 ON LEFT/RIGHT SPEAKER
PORT E :SW USES TO TRIGGER DIGITAL OUT
PORT F :LINE IN (ADC 1)
CD INPUT :UNUSED

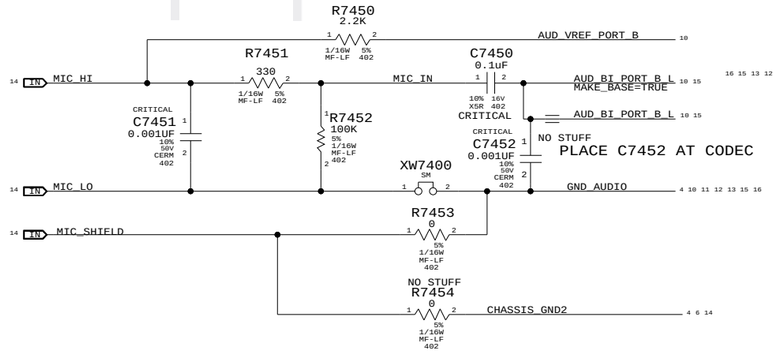
UNUSED CODEC ANALOG PORT TERMINATIONS



LINE IN FILTER SHUTDOWN CONTROL



MIC INPUT CIRCUITRY



AUDIO: JACK TRANSLATORS

SYNC_MASTER=LENGO_M57_AUDIO SYNC_DATE=07/31/2006

APPLE COMPUTER INC.

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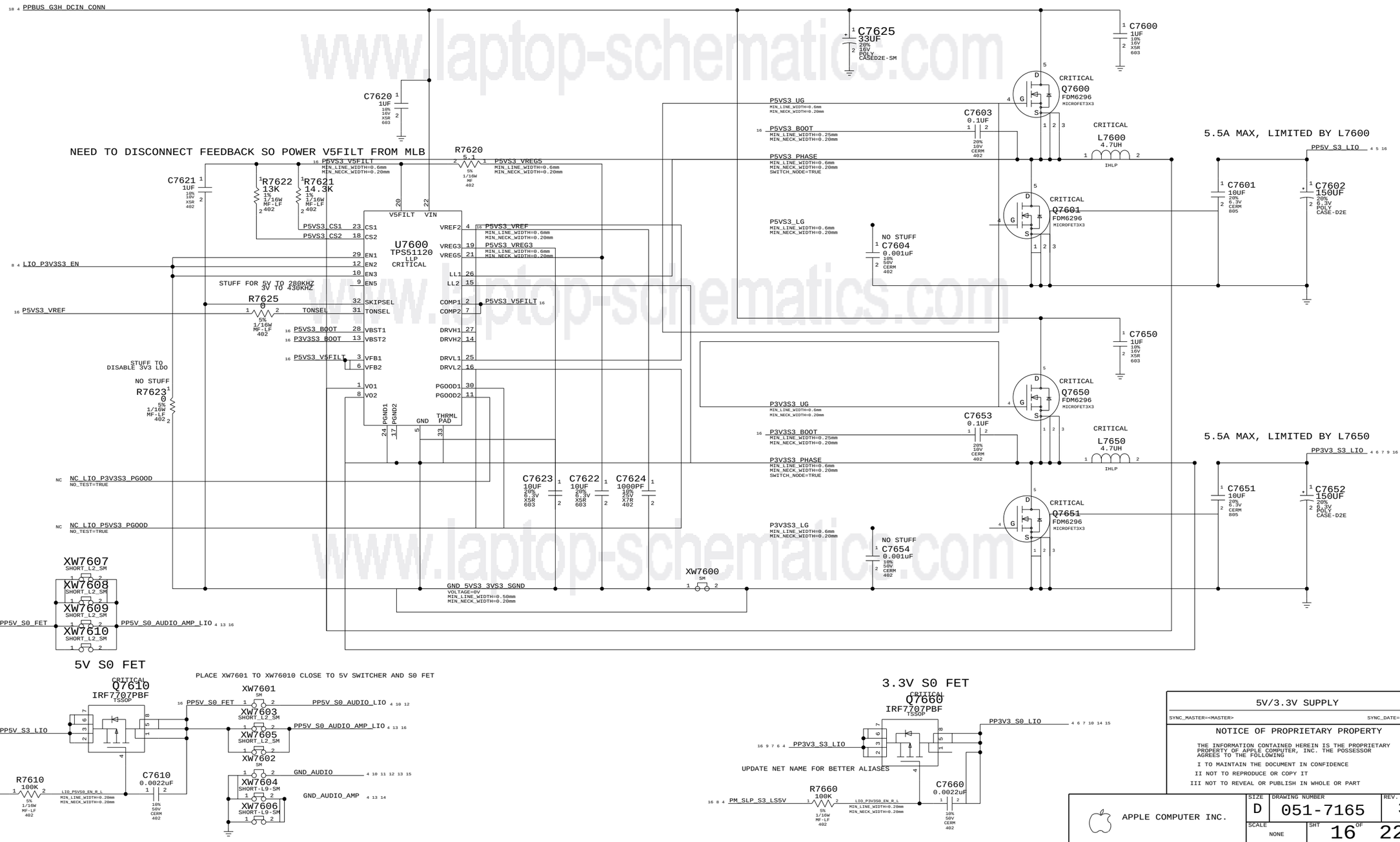
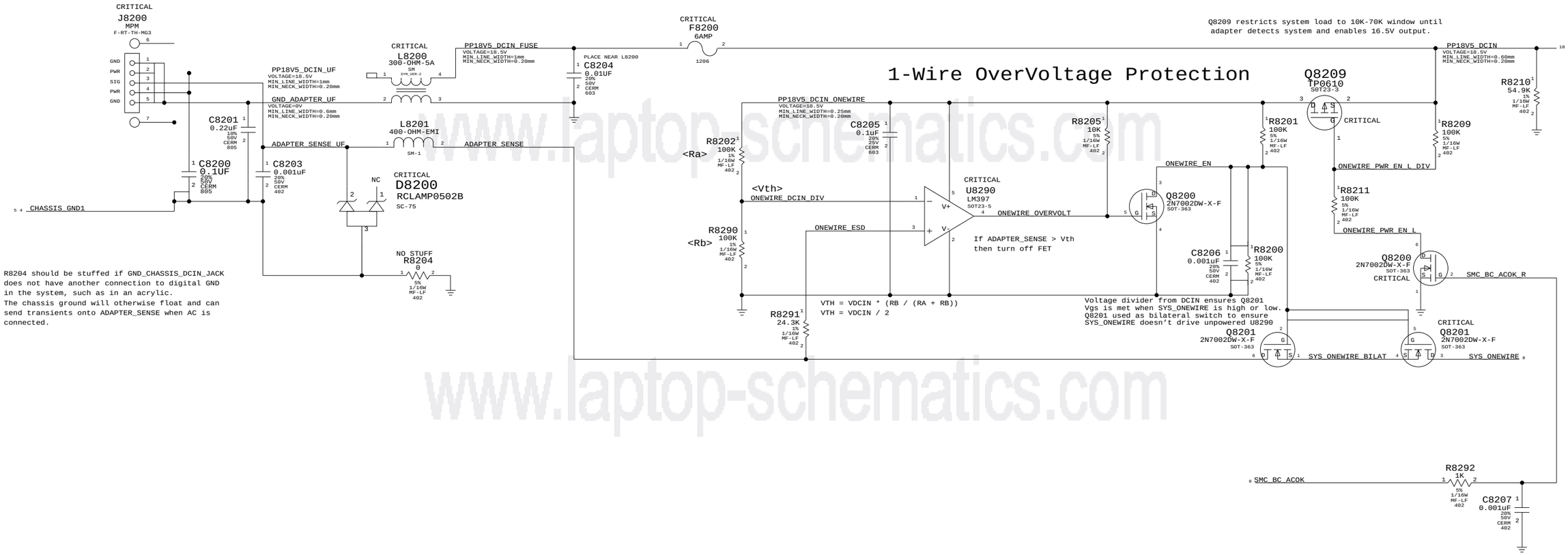
5V/3.3V POWER SUPPLY



DC Power Jack



DC-In & Battery Connectors

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
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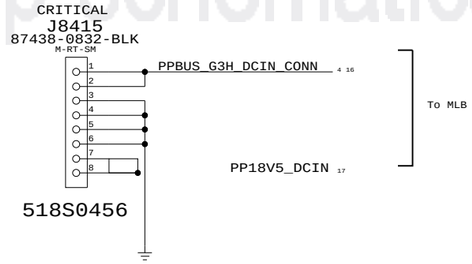
Left I/O Power Connector



LEFT I/O POWER CONNECTOR

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7165	3.0.0
SCALE: NONE	SHT 18 OF 22	

CHANGE LIST

05/04/06:
INITIAL M57 YUBA SCHEMATIC CREATED. LATEST AUDIO CIRCUITRY SYNCED INTO SCHEMATIC.

05/05/06:
PAGE 4: UPDATED ALIASES
PAGE 8: UPDATED LIO-MLB CONNECTOR WITH SMBUS ALS TEMP SIGNALS
PAGE 9: ADDED LEFT ALS TEMP SENSOR(U6402)
PAGE 18: CHANGED DC-IN CONNECTOR TO BLACK
PAGE 6: REMOVED UNNEEDED EXPRESSCARD COMPONENTS (AND GATE FOR SMC INPUT)
PAGE 16: REMOVED D7800,R7802; CHANGED TO ISL6269A

05/05/06:
PAGE 6: CHANGED EXPRESSCARD CONNECTOR TO BLACK
PAGE 18: REMOVED UNEEDED BOM OPTIONS AND COMPONENTS IN ONE-WIRE CIRCUIT

FUTURE UPDATES LOGGED IN PERFORCE

HISTORY: NON-AUDIO

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
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CHANGE LIST

MAY 03 2006:FIRST RELEASE
MAY 04 2006:REMOVED U7350
MAY 10 2006:CONNECT XW7200.1 TO GND_AUDIO_PWR INSTEAD OF 5V, REPLACED C7301/02/04/52/53/55 WITH ZAP DIODES
MAY 25 2006:REPLACED R6801, R7000, R7200 WITH 155S0138.CHANGED C6803 TO 22UF ROHS COMPLIANT CAP
MAY 31 2006:CHANGE U6800 TO 353S1458, DC SCREENED VERSION. CHANGED NETNAME "=GND_AUDIO_PWR" TO "=GND_AUDIO_AMP"
CHANGED NETNAME "=PP5V_S0_AUDIO_PWR" TO "=PP5V_S0_AUDIO_AMP"
JUNE 1 2006:ADDED NETNAMES TO NC PINS ON U6800. ADDED "NO_TEST=TRUE" ATTRIBUTES TO NC NETS.
JUNE 28 2006:ADDED CRITICAL TO U6800. REMOVE "?" FROM BOMOPTION TABLE

APPLE COMPUTER INC.

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Title: Basenet Report
Design: lio
Date: Aug 11 13:24:18 2006

Base nets and synonyms for lio_lib.LIO(@lio_lib.lio(sch_1))

Base Signal	Synonyms	Location([Zone][dir])
5V_REG_IN	5V_REG_IN - @lio_lib.LIO	10A4
ACZ_BITCLK	ACZ_BITCLK - @lio_lib.LIO	8B6 10D7
ACZ_RST_L	ACZ_RST_L - @lio_lib.LIO	8B6 10C7
ACZ_SDATAIN<0>	ACZ_SDATAIN<0> - @lio_lib.LIO	8B6 10C7
ACZ_SDATAOUT	ACZ_SDATAOUT - @lio_lib.LIO	8B6 10D7
ACZ_SYNC	ACZ_SYNC - @lio_lib.LIO	8B6 10D7
ADAPTER_SENSE	ADAPTER_SENSE - @lio_lib.LIO	17D6
ADAPTER_SENSE_UF	ADAPTER_SENSE_UF - @lio_lib.LIO	17D7
AUDIO_SHIELD_PLANE	AUDIO_SHIELD_PLANE - @lio_lib.LIO	14B3
AUD_4V5_SHDN_L	AUD_4V5_SHDN_L - @lio_lib.LIO	10A5
AUD_ANALOG_FILT_1	AUD_ANALOG_FILT_1 - @lio_lib.LIO	10C4
AUD_ANALOG_FILT_2	AUD_ANALOG_FILT_2 - @lio_lib.LIO	10C4
AUD_BI_PORT_A_L	AUD_BI_PORT_A_L - @lio_lib.LIO	10C1 12B7
AUD_BI_PORT_A_R	AUD_BI_PORT_A_R - @lio_lib.LIO	10C1 12A7
AUD_BI_PORT_B_L	AUD_BI_PORT_B_L - @lio_lib.LIO	10C1 10C1 15A4 15A4
AUD_BI_PORT_C_L	AUD_BI_PORT_C_L - @lio_lib.LIO	10C7 13C8
AUD_BI_PORT_C_R	AUD_BI_PORT_C_R - @lio_lib.LIO	10C7 13A8
AUD_BI_PORT_D_L	AUD_BI_PORT_D_L - @lio_lib.LIO	10C7 13D8
AUD_BI_PORT_D_R	AUD_BI_PORT_D_R - @lio_lib.LIO	10C7 13B8
AUD_BI_PORT_E_L	AUD_BI_PORT_E_L - @lio_lib.LIO	10C1 15B3
AUD_BI_PORT_E_R	AUD_BI_PORT_E_R - @lio_lib.LIO	10C1 15B3
AUD_BI_PORT_F_L	AUD_BI_PORT_F_L - @lio_lib.LIO	10C1 11C2
AUD_BI_PORT_F_R	AUD_BI_PORT_F_R - @lio_lib.LIO	10C1 11B2
AUD_BYPASS	AUD_BYPASS - @lio_lib.LIO	10C4
AUD_CODEC_INREF	AUD_CODEC_INREF - @lio_lib.LIO	11B3
AUD_CODEC_OUTL_C	AUD_CODEC_OUTL_C - @lio_lib.LIO	12B6
AUD_CODEC_OUTL_R	AUD_CODEC_OUTL_R - @lio_lib.LIO	12B5
AUD_CODEC_OUTR_C	AUD_CODEC_OUTR_C - @lio_lib.LIO	12A6
AUD_CODEC_OUTR_R	AUD_CODEC_OUTR_R - @lio_lib.LIO	12A5
AUD_CONNJ1_RING	AUD_CONNJ1_RING - @lio_lib.LIO	14C7
AUD_CONNJ1_RING_F	AUD_CONNJ1_RING_F - @lio_lib.LIO	14C6
AUD_CONNJ1_SLEEVE	AUD_CONNJ1_SLEEVE - @lio_lib.LIO	14D7
AUD_CONNJ1_SLEEVEDET	AUD_CONNJ1_SLEEVEDET - @lio_lib.LIO	14C7
AUD_CONNJ1_SLEEVEDET_F	AUD_CONNJ1_SLEEVEDET_F - @lio_lib.LIO	14C6
AUD_CONNJ1_SLEEVE_F	AUD_CONNJ1_SLEEVE_F - @lio_lib.LIO	14D6
AUD_CONNJ1_TIP	AUD_CONNJ1_TIP - @lio_lib.LIO	14C7
AUD_CONNJ1_TIPDET	AUD_CONNJ1_TIPDET - @lio_lib.LIO	14C7
AUD_CONNJ1_TIPDET_F	AUD_CONNJ1_TIPDET_F - @lio_lib.LIO	14C6
AUD_CONNJ1_TIP_F	AUD_CONNJ1_TIP_F - @lio_lib.LIO	14C6
AUD_CONNJ2_RING	AUD_CONNJ2_RING - @lio_lib.LIO	14B7
AUD_CONNJ2_RING_F	AUD_CONNJ2_RING_F - @lio_lib.LIO	14B5
AUD_CONNJ2_SLEEVE	AUD_CONNJ2_SLEEVE - @lio_lib.LIO	14B7
AUD_CONNJ2_SLEEVEDET	AUD_CONNJ2_SLEEVEDET - @lio_lib.LIO	14A7
AUD_CONNJ2_SLEEVEDET_F	AUD_CONNJ2_SLEEVEDET_F - @lio_lib.LIO	14A5
AUD_CONNJ2_SLEEVE_F	AUD_CONNJ2_SLEEVE_F - @lio_lib.LIO	14B5
AUD_CONNJ2_TIP	AUD_CONNJ2_TIP - @lio_lib.LIO	14A7
AUD_CONNJ2_TIPDET	AUD_CONNJ2_TIPDET - @lio_lib.LIO	14B7
AUD_CONNJ2_TIPDET_F	AUD_CONNJ2_TIPDET_F - @lio_lib.LIO	14B5
AUD_CONNJ2_TIP_F	AUD_CONNJ2_TIP_F - @lio_lib.LIO	14A5
AUD_GPIO_0	AUD_GPIO_0 - @lio_lib.LIO	10B8 13D8
AUD_GPIO_0_R	AUD_GPIO_0_R - @lio_lib.LIO	10B7 10C7
AUD_GPIO_1	AUD_GPIO_1 - @lio_lib.LIO	10B8 12C7
AUD_GPIO_1_R	AUD_GPIO_1_R - @lio_lib.LIO	10B7 10C7
AUD_HPAMP_INL_M	AUD_HPAMP_INL_M - @lio_lib.LIO	12B3 12D7
AUD_HPAMP_INL_P	AUD_HPAMP_INL_P - @lio_lib.LIO	12B3 12D7
AUD_HPAMP_INR_M	AUD_HPAMP_INR_M - @lio_lib.LIO	12A3 12C7
AUD_HPAMP_INR_P	AUD_HPAMP_INR_P - @lio_lib.LIO	12A3 12C7
AUD_HPAMP_MUTE_L	AUD_HPAMP_MUTE_L - @lio_lib.LIO	12C6
AUD_HPAMP_OUTL	AUD_HPAMP_OUTL - @lio_lib.LIO	12D2 14C3
AUD_HPAMP_OUTL_R	AUD_HPAMP_OUTL_R - @lio_lib.LIO	12B3 12D2
AUD_HPAMP_OUTR	AUD_HPAMP_OUTR - @lio_lib.LIO	12C2 14C3
AUD_HPAMP_OUTR_R	AUD_HPAMP_OUTR_R - @lio_lib.LIO	12A3 12C2
AUD_HPAMP_REF	AUD_HPAMP_REF - @lio_lib.LIO	12A3 14D3
AUD_HPAMP_REF_F	AUD_HPAMP_REF_F - @lio_lib.LIO	14D5
AUD_INJACK_INSERT_L	AUD_INJACK_INSERT_L - @lio_lib.LIO	15C7
AUD_J1_DET_RC	AUD_J1_DET_RC - @lio_lib.LIO	15D7
AUD_J1_SLEEVEDET_INV	AUD_J1_SLEEVEDET_INV - @lio_lib.LIO	15C7
AUD_J1_SLEEVEDET_R	AUD_J1_SLEEVEDET_R - @lio_lib.LIO	14C3 15C6 15C8
AUD_J1_TIPDET_R	AUD_J1_TIPDET_R - @lio_lib.LIO	14C3 15D8
AUD_J2_DET_RC	AUD_J2_DET_RC - @lio_lib.LIO	15A8 15C7
AUD_J2_OPT_OUT	AUD_J2_OPT_OUT - @lio_lib.LIO	14B7
AUD_J2_SLEEVEDET_R	AUD_J2_SLEEVEDET_R - @lio_lib.LIO	14A3 15B8
AUD_J2_TIPDET_R	AUD_J2_TIPDET_R - @lio_lib.LIO	14A3 15B8
AUD_LIFILT_LT	AUD_LIFILT_LT - @lio_lib.LIO	11C4
AUD_LIFILT_LT_R	AUD_LIFILT_LT_R - @lio_lib.LIO	11C5
AUD_LIFILT_RT	AUD_LIFILT_RT - @lio_lib.LIO	11B4
AUD_LIFILT_RT_R	AUD_LIFILT_RT_R - @lio_lib.LIO	11B5
AUD_LIFILT_SHUTDOWN	AUD_LIFILT_SHUTDOWN - @lio_lib.LIO	15A8
AUD_LIFILT_SHUTDOWN_L	AUD_LIFILT_SHUTDOWN_L - @lio_lib.LIO	11C2 15A6
AUD_LI_GND	AUD_LI_GND - @lio_lib.LIO	11C7 14B3
AUD_LI_INL	AUD_LI_INL - @lio_lib.LIO	11C7 14A3
AUD_LI_INL_C	AUD_LI_INL_C - @lio_lib.LIO	11C5
AUD_LI_INL_C_R	AUD_LI_INL_C_R - @lio_lib.LIO	11C4
AUD_LI_INR	AUD_LI_INR - @lio_lib.LIO	11B7 14B3
AUD_LI_INR_C	AUD_LI_INR_C - @lio_lib.LIO	11B5
AUD_LI_INR_C_R	AUD_LI_INR_C_R - @lio_lib.LIO	11B4
AUD_LO_GND	AUD_LO_GND - @lio_lib.LIO	12C2 14D3
AUD_OUTJACK_INSERT_L	AUD_OUTJACK_INSERT_L - @lio_lib.LIO	15D7
AUD_PORTA_DET_L	AUD_PORTA_DET_L - @lio_lib.LIO	15D5
AUD_PORTE_DET_L	AUD_PORTE_DET_L - @lio_lib.LIO	15D5
AUD_PORTF_L	AUD_PORTF_L - @lio_lib.LIO	11C3
AUD_PORTF_R	AUD_PORTF_R - @lio_lib.LIO	11B3
AUD_PORTG_DET_L	AUD_PORTG_DET_L - @lio_lib.LIO	15C5
AUD_SENSE_A	AUD_SENSE_A - @lio_lib.LIO	10C1 15C5 15D8
AUD_SENSE_B	AUD_SENSE_B - @lio_lib.LIO	10C1 15C4 15C8 15D8
AUD_SPDIF_IN	AUD_SPDIF_IN - @lio_lib.LIO	10C1 14B3
AUD_SPDIF_OUT	AUD_SPDIF_OUT - @lio_lib.LIO	10D1 14D3
AUD_SPDIF_OUT_R	AUD_SPDIF_OUT_R - @lio_lib.LIO	10D4
AUD_SPKRAMPL1_IN	AUD_SPKRAMPL1_IN - @lio_lib.LIO	13C7
AUD_SPKRAMPL2_IN	AUD_SPKRAMPL2_IN - @lio_lib.LIO	13D7
AUD_SPKRAMPR1_IN	AUD_SPKRAMPR1_IN - @lio_lib.LIO	13A7
AUD_SPKRAMPR2_IN	AUD_SPKRAMPR2_IN - @lio_lib.LIO	13B7
AUD_SPKRAMP_SHUTDOWN_L	AUD_SPKRAMP_SHUTDOWN_L - @lio_lib.LIO	13A8 13B8 13C8 13D8
AUD_VREF_FILT	AUD_VREF_FILT - @lio_lib.LIO	10C4
AUD_VREF_PORT_B	AUD_VREF_PORT_B - @lio_lib.LIO	10C1 15A4
AUD_VREF_PORT_B_R	AUD_VREF_PORT_B_R - @lio_lib.LIO	10C4
AUD_VREF_PORT_C	AUD_VREF_PORT_C - @lio_lib.LIO	10C1 11B2
BAL_IN_COM	BAL_IN_COM - @lio_lib.LIO	10C7 15B1
BAL_IN_L	BAL_IN_L - @lio_lib.LIO	10C7 15B1
BAL_IN_R	BAL_IN_R - @lio_lib.LIO	10C7 15B1
BEEP	BEEP - @lio_lib.LIO	10C6
CHASSIS_GND1	CHASSIS_GND1 - @lio_lib.LIO	4A7 4A7 4A8 5A2 5C2 17C8
CHASSIS_GND2	CHASSIS_GND2 - @lio_lib.LIO	4A7 4A7 4A7 4A8 6B1 6D3
		14A8 14B1 14B1 14B1 14B8 15A4
CHASSIS_GND3	CHASSIS_GND3 - @lio_lib.LIO	4A7 4A8 14C3
CHASSIS_GND4	CHASSIS_GND4 - @lio_lib.LIO	4A7 4A7 4A8 6B2 8B3
EC_HOLE1	EC_HOLE1 - @lio_lib.LIO	6D3
EC_HOLE2	EC_HOLE2 - @lio_lib.LIO	6B2
EXCARD_CLKREQ_CONN	EXCARD_CLKREQ_CONN - @lio_lib.LIO	6B3
EXCARD_CLKREQ_CONN_L	EXCARD_CLKREQ_CONN_L - @lio_lib.LIO	6A4 6C3
EXCARD_CLKREQ_L	EXCARD_CLKREQ_L - @lio_lib.LIO	4C4 4C5 6B2 8C6
EXCARD_CPPE_L	EXCARD_CPPE_L - @lio_lib.LIO	6A8 6C3 6C3
EXCARD_CPUSB_L	EXCARD_CPUSB_L - @lio_lib.LIO	6A8 6C3 6C3
EXCARD_OC_L	EXCARD_OC_L - @lio_lib.LIO	4C4 4C5 6C7 8C6
EXCARD_RCLKEN	EXCARD_RCLKEN - @lio_lib.LIO	6B3 6C5
EXCARD_SHDN_L_R	EXCARD_SHDN_L_R - @lio_lib.LIO	6C6
GND_5VS3_3VS3_SGND	GND_5VS3_3VS3_SGND - @lio_lib.LIO	16A6
GND_ADAPTER_UF	GND_ADAPTER_UF - @lio_lib.LIO	17D7
GND_AUDIO	GND_AUDIO - @lio_lib.LIO	4A7 4A7 4A8 10A6 10B6 10D2 11B2 11B7 12B6 12C6 13A8 13B8 13C8 13C8 15A3 15A4 15A8 15B5 15B8 15C5 15C8 15D8 16A6
GND_AUDIO_AMP	GND_AUDIO_AMP - @lio_lib.LIO	4B7 4B8 13A8 13B2 13B6 13B8 13C2 13C8 13C8 14C3 16A6
GND_AUDIO_DGND	GND_AUDIO_DGND - @lio_lib.LIO	10A6 10A7 10B6 10B7 10D7 14A8 14C8
GND_AUDIO_HPAMP_PGND	GND_AUDIO_HPAMP_PGND - @lio_lib.LIO	12C5
GND_AUDIO_HPAMP_SGND	GND_AUDIO_HPAMP_SGND - @lio_lib.LIO	12B2 12C5
LEFT_ALS_GAIN_SW	LEFT_ALS_GAIN_SW - @lio_lib.LIO	8C6 9C4
LEFT_ALS_GAIN_SW_R	LEFT_ALS_GAIN_SW_R - @lio_lib.LIO	9C5
LEFT_ALS_OP_COMP	LEFT_ALS_OP_COMP - @lio_lib.LIO	9C6
LEFT_ALS_OP_IN	LEFT_ALS_OP_IN - @lio_lib.LIO	9C5
LEFT_ALS_OUT	LEFT_ALS_OUT - @lio_lib.LIO	8C6 9C4
LEFT_ALS_OUT_FB	LEFT_ALS_OUT_FB - @lio_lib.LIO	9C5
LEFT_GAIN_SETTING	LEFT_GAIN_SETTING - @lio_lib.LIO	9C5
LEFT_PHOTODIODE	LEFT_PHOTODIODE - @lio_lib.LIO	9C6
LIO_P3V3S0_EN_R_L	LIO_P3V3S0_EN_R_L - @lio_lib.LIO	16A3
LIO_P3V3S3_EN	LIO_P3V3S3_EN - @lio_lib.LIO	4C3 4C4 8C6 16C8
LIO_P5VS0_EN_R_L	LIO_P5VS0_EN_R_L - @lio_lib.LIO	16A8
MAX9705L1_NIN	MAX9705L1_NIN - @lio_lib.LIO	13C6
MAX9705L1_PIN	MAX9705L1_PIN - @lio_lib.LIO	13C6
MAX9705L2_NIN	MAX9705L2_NIN - @lio_lib.LIO	13D6
MAX9705L2_PIN	MAX9705L2_PIN - @lio_lib.LIO	13D6
MAX9705R1_NIN	MAX9705R1_NIN - @lio_lib.LIO	13A6
MAX9705R1_PIN	MAX9705R1_PIN - @lio_lib.LIO	13A6
MAX9705R2_NIN	MAX9705R2_NIN - @lio_lib.LIO	13B6
MAX9705R2_PIN	MAX9705R2_PIN - @lio_lib.LIO	13B6
MAX9722_C1N	MAX9722_C1N - @lio_lib.LIO	12C4
MAX9722_C1P	MAX9722_C1P - @lio_lib.LIO	12C4
MAX9722_PVSS	MAX9722_PVSS - @lio_lib.LIO	12C4
MIC_HI	MIC_HI - @lio_lib.LIO	14D3 15A6
MIC_IN	MIC_IN - @lio_lib.LIO	15A5
MIC_LO	MIC_LO - @lio_lib.LIO	14D3 15A6
MIC_SHIELD	MIC_SHIELD - @lio_lib.LIO	14D3 15A6
MINI_CLKREQ_L	MINI_CLKREQ_L - @lio_lib.LIO	4B4 4B5 7C6 8C6
NC_AUD_GPIO_2	NC_AUD_GPIO_2 - @lio_lib.LIO	10C7
NC_AUD_NC1	NC_AUD_NC1 - @lio_lib.LIO	10C4
NC_AUD_NC2	NC_AUD_NC2 - @lio_lib.LIO	10B4
NC_AUD_VREF_PORT_A	NC_AUD_VREF_PORT_A - @lio_lib.LIO	10C4
NC_AUD_VREF_PORT_D	NC_AUD_VREF_PORT_D - @lio_lib.LIO	10C4
NC_LED_WLAN_L	NC_LED_WLAN_L - @lio_lib.LIO	7B3
NC_LED_WPAN_L	NC_LED_WPAN_L - @lio_lib.LIO	7B3
NC_LED_WWAN_L	NC_LED_WWAN_L - @lio_lib.LIO	7B3
NC_LIO_P3V3S3_PGOOD	NC_LIO_P3V3S3_PGOOD - @lio_lib.LIO	16B7
NC_LIO_P5VS3_PGOOD	NC_LIO_P5VS3_PGOOD - @lio_lib.LIO	16B7
NC_UIM_CLK	NC_UIM_CLK - @lio_lib.LIO	7C3
NC_UIM_DATA	NC_UIM_DATA - @lio_lib.LIO	7C3
NC_UIM_PWR	NC_UIM_PWR - @lio_lib.LIO	7C3
NC_UIM_RESET	NC_UIM_RESET - @lio_lib.LIO	7C3
NC_UIM_VPP	NC_UIM_VPP - @lio_lib.LIO	7C3
NC_VOL_DOWN	NC_VOL_DOWN - @lio_lib.LIO	10C7
NC_VOL_UP	NC_VOL_UP - @lio_lib.LIO	10C7
NC_VREG_POK	NC_VREG_POK - @lio_lib.LIO	10A4
NC_W_DISABLE_L	NC_W_DISABLE_L - @lio_lib.LIO	7C3
ONEWIRE_DCIN_DIV	ONEWIRE_DCIN_DIV - @lio_lib.LIO	17C5
ONEWIRE_EN	ONEWIRE_EN - @lio_lib.LIO	17D3
ONEWIRE_ESD	ONEWIRE_ESD - @lio_lib.LIO	17C4
ONEWIRE_OVEROVLT	ONEWIRE_OVEROVLT - @lio_lib.LIO	17C4
ONEWIRE_PWR_EN_L	ONEWIRE_PWR_EN_L - @lio_lib.LIO	17C2
ONEWIRE_PWR_EN_L_DIV	ONEWIRE_PWR_EN_L_DIV - @lio_lib.LIO	17D2
P3V3S3_BOOT	P3V3S3_BOOT - @lio_lib.LIO	16B4 16B6
P3V3S3_LG	P3V3S3_LG - @lio_lib.LIO	16B4
P3V3S3_PHASE	P3V3S3_PHASE - @lio_lib.LIO	16B4
P3V3S3_UG	P3V3S3_UG - @lio_lib.LIO	16B4
P5VS3_BOOT	P5VS3_BOOT - @lio_lib.LIO	16C4 16C6
P5VS3_CS1	P5VS3_CS1 - @lio_lib.LIO	16C6
P5VS3_CS2	P5VS3_CS2 - @lio_lib.LIO	16C6
P5VS3_LG	P5VS3_LG - @lio_lib.LIO	16C4
P5VS3_PHASE	P5VS3_PHASE - @lio_lib.LIO	16C4
P5VS3_UG	P5VS3_UG - @lio_lib.LIO	16C4
P5VS3_V5FILT	P5VS3_V5FILT - @lio_lib.LIO	16B6 16C5 16C6
P5VS3_VREF	P5VS3_VREF - @lio_lib.LIO	16C5 16C8
P5VS3_VREG3	P5VS3_VREG3 - @lio_lib.LIO	16C5
P5VS3_VREG5	P5VS3_VREG5 - @lio_lib.LIO	16C5
PCIE_CLK100M_EXCARD_N	PCIE_CLK100M_EXCARD_N - @lio_lib.LIO	4D2 4D5 6C3 8B1
PCIE_CLK100M_EXCARD_P	PCIE_CLK100M_EXCARD_P - @lio_lib.LIO	4D2 4D5 6C3 8B1
PCIE_CLK100M_EXCARD_UF_N	PCIE_CLK100M_EXCARD_UF_N - @lio_lib.LIO	8C4
PCIE_CLK100M_EXCARD_UF_P	PCIE_CLK100M_EXCARD_UF_P - @lio_lib.LIO	8B4
PCIE_CLK100M_MINI_N	PCIE_CLK100M_MINI_N - @lio_lib.LIO	4B2 4B5 7C6 8C1
PCIE_CLK100M_MINI_P	PCIE_CLK100M_MINI_P - @lio_lib.LIO	4B2 4B5 7C6 8C1
PCIE_CLK100M_MINI_UF_N	PCIE_CLK100M_MINI_UF_N - @lio_lib.LIO	8C4
PCIE_CLK100M_MINI_UF_P	PCIE_CLK100M_MINI_UF_P - @lio_lib.LIO	8C4
PCIE_EXCARD_D2R_N	PCIE_EXCARD_D2R_N - @lio_lib.LIO	4D2 4D5 6C3 8B3
PCIE_EXCARD_D2R_P	PCIE_EXCARD_D2R_P - @lio_lib.LIO	4D2 4D5 6C3 8B3
PCIE_EXCARD_R2D_N	PCIE_EXCARD_R2D_N - @lio_lib.LIO	4D2 4D5 6C3 8B3
PCIE_EXCARD_R2D_P	PCIE_EXCARD_R2D_P - @lio_lib.LIO	4D2 4D5 6C3 8B3
PCIE_MINI_D2R_N	PCIE_MINI_D2R_N - @lio_lib.LIO	4B2 4B5 7C6 8C3
PCIE_MINI_D2R_P	PCIE_MINI_D2R_P - @lio_lib.LIO	4B2 4B5 7C6 8C3
PCIE_MINI_R2D_N	PCIE_MINI_R2D_N - @lio_lib.LIO	4B2 4B5 7B6 8C3
PCIE_MINI_R2D_P	PCIE_MINI_R2D_P - @lio_lib.LIO	4C2 4C5 7B6 8C3
PCIE_WAKE_L	PCIE_WAKE_L - @lio_lib.LIO	4C3 4C3 4C4 6C3 7C6 8C6
PLT_RESET_L	PLT_RESET_L - @lio_lib.LIO	4C3 4C4 6C7 7C3 8C6
PLT_RESET_SWITCH_L	PLT_RESET_SWITCH_L - @lio_lib.LIO	6C3 6C3
PM_SLP_S3_LS5V	PM_SLP_S3_LS5V - @lio_lib.LIO	4C3 4C3 4C4 8C6 16A4 16A8
PP1V5_S0_EXCARD_SWITCH	PP1V5_S0_EXCARD_SWITCH - @lio_lib.LIO	6C3 6C3
PP1V5_S0_LIO	PP1V5_S0_LIO - @lio_lib.LIO	4C7 4D7 4D7 4D8 6C7 6C8 7C2 8B3
PP3V3_AUDIO_CODEC	PP3V3_AUDIO_CODEC - @lio_lib.LIO	10D6
PP3V3_S0_AUDIO_F	PP3V3_S0_AUDIO_F - @lio_lib.LIO	15B4 15C8 15C8 15D8
PP3V3_S0_EXCARD_SWITCH	PP3V3_S0_EXCARD_SWITCH - @lio_lib.LIO	6C3 6C3
PP3V3_S0_LIO	PP3V3_S0_LIO - @lio_lib.LIO	4C7 4C7 4C7 4C7 4C8 6B4 6C7 7D2 10A6 10D7 14B8 14D8 15B5 16A2
PP3V3_S3_EXCARD_SWITCH	PP3V3_S3_EXCARD_SWITCH - @lio_lib.LIO	6C3 6C3
PP3V3_S3_LIO	PP3V3_S3_LIO - @lio_lib.LIO	4C7 4C7 4C7 4C7 4C7 4C8 6B8 6C7 6C8 7C2 9B4 9D6 16A4 16B1
PP4V5_AUDIO_ANALOG	PP4V5_AUDIO_ANALOG - @lio_lib.LIO	10A2 10D2 11C7 15A8 15C5
PP4V5_AUDIO_LINE_IN	PP4V5_AUDIO_LINE_IN - @lio_lib.LIO	11C5
PP5V_AUDIO_HPAMP_AVDD_F	PP5V_AUDIO_HPAMP_AVDD_F - @lio_lib.LIO	12D4
PP5V_AUDIO_HPAMP_PVDD_F	PP5V_AUDIO_HPAMP_PVDD_F - @lio_lib.LIO	12D5
PP5V_PWRON_USB_LEFT2_EMI_EMI	PP5V_PWRON_USB_LEFT2_EMI - @lio_lib.LIO	5D4
PP5V_PWRON_USB_LEFT_EMI	PP5V_PWRON_USB_LEFT_EMI - @lio_lib.LIO	5B4
PP5V_S0_AUDIO_AMP_LIO	PP5V_S0_AUDIO_AMP_LIO - @lio_lib.LIO	4B7 4B8 13A8 13B2 13B8 13C2 13C8 13D8 16A6 16A7
PP5V_S0_AUDIO_F	PP5V_S0_AUDIO_F - @lio_lib.LIO	13A8 13B1 13B8 13C8 13D8
PP5V_S0_AUDIO_LIO	PP5V_S0_AUDIO_LIO - @lio_lib.LIO	4B7 4B8 10A6 12D7 16A6
PP5V_S0_FET	PP5V_S0_FET - @lio_lib.LIO	16A7 16A8
PP5V_S3_LIO	PP5V_S3_LIO - @lio_lib.LIO	4C7 4C7 4C7 4C8 5B8 16A8 16C1
PP5V_S3_USB_SWITCH_OUT1	PP5V_S3_USB_SWITCH_OUT1 - @lio_lib.LIO	5B5
PP5V_S3_USB_SWITCH_OUT2	PP5V_S3_USB_SWITCH_OUT2 - @lio_lib.LIO	5D5
PP18V5_DCIN	PP18V5_DCIN - @lio_lib.LIO	17D1 18C4
PP18V5_DCIN_FUSE	PP18V5_DCIN_FUSE - @lio_lib.LIO	17D6
PP18V5_DCIN_ONEWIRE	PP18V5_DCIN_ONEWIRE - @lio_lib.LIO	17D5
PP18V5_DCIN_UF	PP18V5_DCIN_UF - @lio_lib.LIO	17D7
PPBUS_G3H_DCIN_CONN	PPBUS_G3H_DCIN_CONN - @lio_lib.LIO	4D6 4D7 4D8 16D8 18C4
SDATAIN	SDATAIN - @lio_lib.LIO	10C6
SMBUS_SB_SCL	SMBUS_SB_SCL - @lio_lib.LIO	4A2 4A3 6C3 8B6
SMBUS_SB_SDA	SMBUS_SB_SDA - @lio_lib.LIO	4A4 4A5 6C3 8B6
SMBUS_SMC_SCL	SMBUS_SMC_SCL - @lio_lib.LIO	4A2 4A2 4A3 7B3 8B3 9A6
SMBUS_SMC_SDA	SMBUS_SMC_SDA - @lio_lib.LIO	4A4 4A4 4A5 7B3 8B3 9A6
SMC_BC_ACOK	SMC_BC_ACOK - @lio_lib.LIO	8C6 17B2
SMC_BC_ACOK_R	SMC_BC_ACOK_R - @lio_lib.LIO	17C1
SMC_EXCARD_CP	SMC_EXCARD_CP - @lio_lib.LIO	6A6 8C6
SMC_EXCARD_PWR_EN	SMC_EXCARD_PWR_EN - @lio_lib.LIO	6C7 8C6
SPKRAMP_L1_N_OUT	SPKRAMP_L1_N_OUT - @lio_lib.LIO	13C4 13C5
SPKRAMP_L1_P_OUT	SPKRAMP_L1_P_OUT - @lio_lib.LIO	13C4 13C5
SPKRAMP_L2_N_OUT	SPKRAMP_L2_N_OUT - @lio_lib.LIO	13D4 13D5
SPKRAMP_L2_P_OUT	SPKRAMP_L2_P_OUT - @lio_lib.LIO	13D4 13D5
SPKRAMP_R1_N_OUT	SPKRAMP_R1_N_OUT - @lio_lib.LIO	13A4 13A5
SPKRAMP_R1_P_OUT	SPKRAMP_R1_P_OUT - @lio_lib.LIO	13A4 13A5
SPKRAMP_R2_N_OUT	SPKRAMP_R2_N_OUT - @lio_lib.LIO	13B4 13B5
SPKRAMP_R2_P_OUT	SPKRAMP_R2_P_OUT - @lio_lib.LIO	13B4 13B5
SPKRAMP_SYNC1	SPKRAMP_SYNC1 - @lio_lib.LIO	13C5 13D5
SPKRAMP_SYNC2	SPKRAMP_SYNC2 - @lio_lib.LIO	13B5 13C5
SPKRAMP_SYNC3	SPKRAMP_SYNC3 - @lio_lib.LIO	13A5 13B5
SPKRAMP_THERMPAD	SPKRAMP_THERMPAD - @lio_lib.LIO	13A5 13B5 13B5 13C5 13C5
SPKRCONN_L1_N_OUT	SPKRCONN_L1_N_OUT - @lio_lib.LIO	13C3 14C3
SPKRCONN_L1_P_OUT	SPKRCONN_L1_P_OUT - @lio_lib.LIO	13C3 14C3
SPKRCONN_L2_N_OUT	SPKRCONN_L2_N_OUT - @lio_lib.LIO	13D3 14C3
SPKRCONN_L2_P_OUT	SPKRCONN_L2_P_OUT - @lio_lib.LIO	13D3 14C3
SPKRCONN_R1_N_OUT	SPKRCONN_R1_N_OUT - @lio_lib.LIO	13A3 14C3
SPKRCONN_R1_P_OUT	SPKRCONN_R1_P_OUT - @lio_lib.LIO	13A3 14C3
SPKRCONN_R2_N_OUT	SPKRCONN_R2_N_OUT - @lio_lib.LIO	13B3 14C3
SPKRCONN_R2_P_OUT	SPKRCONN_R2_P_OUT - @lio_lib.LIO	13B3 14C3
SPKR_SHIELD	SPKR_SHIELD - @lio_lib.LIO	14C2
SYS_ONEWIRE	SYS_ONEWIRE - @lio_lib.LIO	8C6 17C1
SYS_ONEWIRE_BILAT	SYS_ONEWIRE_BILAT - @lio_lib.LIO	17C2
TONSEL	TONSEL - @lio_lib.LIO	16C6
TP_EXCARD_STBY_L	TP_EXCARD_STBY_L - @lio_lib.LIO	6C7
TP_USB2_MININ	TP_USB2_MININ - @lio_lib.LIO	4A3
TP_USB2_MINIP	TP_USB2_MINIP - @lio_lib.LIO	4A3
USB2_EXCARD_N	USB2_EXCARD_N - @lio_lib.LIO	4D2 4D5 6C3 8C3
USB2_EXCARD_P	USB2_EXCARD_P - @lio_lib.LIO	4D2 4D5 6C3 8C3
USB_LEFT2_EMI_N	USB_LEFT2_EMI_N - @lio_lib.LIO	5C4
USB_LEFT2_EMI_P	USB_LEFT2_EMI_P - @lio_lib.LIO	5C4
USB_LEFT2_GND	USB_LEFT2_GND - @lio_lib.LIO	5C3
USB_LEFT2_N	USB_LEFT2_N - @lio_lib.LIO	4B3 4B5 5C5 8C3
USB_LEFT2_OC_L	USB_LEFT2_OC_L - @lio_lib.LIO	4A3 4A5 5B8 8C6
USB_LEFT2_P	USB_LEFT2_P - @lio_lib.LIO	4B3 4B5 5C5 8C3
USB_LEFT_EMI_N	USB_LEFT_EMI_N - @lio_lib.LIO	5B4
USB_LEFT_EMI_P	USB_LEFT_EMI_P - @lio_lib.LIO	5B4
USB_LEFT_GND	USB_LEFT_GND - @lio_lib.LIO	5B3
USB_LEFT_N	USB_LEFT_N - @lio_lib.LIO	4B3 4B5 5B5 8C3
USB_LEFT_OC_L	USB_LEFT_OC_L - @lio_lib.LIO	4B3 4B5 5B8 8C6
USB_LEFT_P	USB_LEFT_P - @lio_lib.LIO	4B3 4B5 5B5 8C3
VREG_FB	VREG_FB - @lio_lib.LIO	10A3

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C5100	CAP_603	lio[5B7]
C5101	CAP_402	lio[5B7]
C5102	CAP_603	lio[5B6]
C5103	CAP_P_CASE-C2-SM	lio[5B5]
C5112	CAP_603	lio[5C6]
C5113	CAP_P_CASE-C2-SM	lio[5C5]
C5150	CAP_603	lio[5B3]
C5151	CAP_603	lio[5A3]
C5160	CAP_603	lio[5D3]
C5161	CAP_603	lio[5C3]
C5300	CAP_402	lio[6C4]
C5301	CAP_402	lio[6C4]
C5302	CAP_402	lio[6C4]
C5303	CAP_603	lio[6C4]
C5304	CAP_603	lio[6C4]
C5305	CAP_603	lio[6C4]
C5330	CAP_402	lio[6C8]
C5331	CAP_603	lio[6C7]
C5334	CAP_402	lio[6C8]
C5335	CAP_603	lio[6C7]
C5350	CAP_402	lio[6A7]
C5360	CAP_402	lio[6A3]
C5400	CAP_402	lio[7C3]
C5401	CAP_603	lio[7C3]
C5410	CAP_402	lio[7C3]
C5411	CAP_603	lio[7C3]
C5420	CAP_402	lio[7C3]
C5421	CAP_603	lio[7C3]
C6401	CAP_402	lio[9C6]
C6402	CAP_402	lio[9C5]
C6403	CAP_402	lio[9D5]
C6404	CAP_402	lio[9B5]
C6405	CAP_402	lio[9A4]
C6800	CAP_603	lio[10D6]
C6801	CAP_402	lio[10D6]
C6802	CAP_603	lio[10D4]
C6803	CAP_P_SMB-LF	lio[10A3]
C6804	CAP_P_SMA-LF	lio[10B4]
C6805	CAP_603	lio[10B4]
C6806	CAP_603	lio[10B3]
C6807	CAP_P_SMA-LF	lio[10B3]
C6810	CAP_P_SMA-LF	lio[10B2]
C6812	CAP_402	lio[10B4]
C6813	CAP_402	lio[10B3]
C6821	CAP_402	lio[10C6]
C6822	CAP_603	lio[10A4]
C6823	CAP_402	lio[10A4]
C6825	CAP_402	lio[10A3]
C6826	CAP_603	lio[10D3]
C6830	CAP_402	lio[10D4]
C6835	CAP_402	lio[10D6]
C6836	CAP_402	lio[10D3]
C7000	CAP_402	lio[11C4]
C7001	CAP_402	lio[11B3]
C7002	CAP_402	lio[11C4]
C7004	CAP_P_CASE-B2	lio[11B3]
C7010	CAP_805-1	lio[11C5]
C7011	CAP_805-1	lio[11B5]
C7012	CAP_805-1	lio[11B5]
C7013	CAP_805-1	lio[11B5]
C7014	CAP_805-1	lio[11C3]
C7015	CAP_805-1	lio[11B3]
C7100	CAP_402	lio[12D5]
C7101	CAP_603	lio[12D5]
C7102	CAP_P_SMB-LF	lio[12D3]
C7104	CAP_402	lio[12D4]
C7105	CAP_402	lio[12D4]
C7110	CAP_603	lio[12C4]
C7111	CAP_603	lio[12C3]
C7112	CAP_402	lio[12C4]
C7114	CAP_P_SMB-LF	lio[12C4]
C7120	CAP_P_SMA-LF	lio[12B6]
C7121	CAP_402	lio[12B4]
C7122	CAP_603	lio[12B4]
C7130	CAP_P_SMA-LF	lio[12A6]
C7131	CAP_402	lio[12A4]
C7132	CAP_603	lio[12A4]
C7200	CAP_603	lio[13B1]
C7201	CAP_P_CASE-C2-SM	lio[13C2]
C7202	CAP_P_CASE-C2-SM	lio[13C2]
C7203	CAP_P_CASE-C2-SM	lio[13C1]
C7204	CAP_P_CASE-C2-SM	lio[13C1]
C7210	CAP_402	lio[13D7]
C7211	CAP_402	lio[13D6]
C7212	CAP_603	lio[13D5]
C7213	CAP_P_B2	lio[13D5]
C7214	CAP_402	lio[13D6]
C7215	CAP_402	lio[13D6]
C7220	CAP_402	lio[13C7]
C7221	CAP_402	lio[13C6]
C7222	CAP_603	lio[13C5]
C7223	CAP_P_B2	lio[13C5]
C7224	CAP_402	lio[13C6]
C7225	CAP_402	lio[13C6]
C7230	CAP_402	lio[13B7]
C7231	CAP_402	lio[13B6]
C7232	CAP_603	lio[13B5]
C7233	CAP_P_B2	lio[13B5]
C7234	CAP_402	lio[13B6]
C7235	CAP_402	lio[13B7]
C7240	CAP_402	lio[13A7]
C7241	CAP_402	lio[13A6]
C7242	CAP_603	lio[13A5]
C7243	CAP_P_B2	lio[13A5]
C7244	CAP_402	lio[13A6]
C7245	CAP_402	lio[13A7]
C7300	CAP_402	lio[14C7]
C7305	CAP_402	lio[14C5]
C7350	CAP_402	lio[14A7]
C7351	CAP_603	lio[14A7]
C7356	CAP_402	lio[14A5]
C7400	CAP_402	lio[15B4]
C7401	CAP_402	lio[15D7]
C7402	CAP_402	lio[15C7]
C7404	CAP_402	lio[15C4]
C7411	CAP_402	lio[15B7]
C7412	CAP_402	lio[15B6]
C7414	CAP_402	lio[15C4]
C7442	CAP_402	lio[15A3]
C7443	CAP_402	lio[15A2]
C7445	CAP_402	lio[15A2]
C7446	CAP_402	lio[15A1]
C7447	CAP_402	lio[15A1]
C7450	CAP_402	lio[15A4]
C7451	CAP_402	lio[15A5]
C7452	CAP_402	lio[15A4]
C7600	CAP_603	lio[16D2]
C7601	CAP_805	lio[16C2]
C7602	CAP_P_CASE-D2E	lio[16C1]
C7603	CAP_402	lio[16C3]
C7604	CAP_402	lio[16C3]
C7610	CAP_402	lio[16A7]
C7620	CAP_603	lio[16C6]
C7621	CAP_402	lio[16C7]
C7622	CAP_603	lio[16B5]
C7623	CAP_603	lio[16B5]
C7624	CAP_402	lio[16B5]
C7625	CAP_P_CASED2E-SM	lio[16D4]
C7650	CAP_603	lio[16B2]
C7651	CAP_805	lio[16B2]
C7652	CAP_P_CASE-D2E	lio[16B1]
C7653	CAP_402	lio[16B3]
C7654	CAP_402	lio[16B3]
C7660	CAP_402	lio[16A3]
C8200	CAP_805	lio[17D7]
C8201	CAP_805	lio[17D7]
C8203	CAP_402	lio[17D7]
C8204	CAP_603	lio[17D6]
C8205	CAP_603	lio[17D4]
C8206	CAP_402	lio[17C2]
C8207	CAP_402	lio[17B1]
D5100	DIODE_SCHOT_3P_A_SC-75	lio[5A3]
D5110	DIODE_SCHOT_3P_A_SC-75	lio[5C4]
D6800	DIODE_SCHOT_POWERDI-123	lio[10A4]
D8200	DIODE_SCHOT_3P_A_SC-75	lio[17C7]
DZ7300	SUPPR_TRANSIENT_4P1_0405	lio[14C6]
DZ7301	SUPPR_TRANSIENT_4P1_0405	lio[14C6]
DZ7302	SUPPR_TRANSIENT1_402	lio[14C5]
DZ7303	SUPPR_TRANSIENT1_402	lio[14C5]
DZ7304	SUPPR_TRANSIENT1_402	lio[14C5]
DZ7306	SUPPR_TRANSIENT1_402	lio[14C5]
DZ7307	SUPPR_TRANSIENT1_402	lio[14C5]
DZ7350	SUPPR_TRANSIENT_4P1_0405	lio[14A6]
DZ7351	SUPPR_TRANSIENT_4P1_0405	lio[14A6]
DZ7352	SUPPR_TRANSIENT1_402	lio[14A5]
DZ7353	SUPPR_TRANSIENT1_402	lio[14A5]
DZ7354	SUPPR_TRANSIENT1_402	lio[14A5]
DZ7355	SUPPR_TRANSIENT1_402	lio[14A5]
F8200	FUSE_1206	lio[17D5]
FL5500	FILTER_4P_1210-4SM1	lio[8C3]
FL5501	FILTER_4P_1210-4SM1	lio[8B3]
J5100	CON_F4RT_USB_S2MT_TH_F-RT-TH-USB-LFT	lio[5B2]
J5110	CON_F4RT_USB_S2MT_TH_F-RT-TH-USB-LFT	lio[5C2]
J5300	CON_F26RT_S2MT_SM_M59_F-RT-SM	lio[6D2]
J5400	CON_F52RT_D2MT_SM_F-ST-SM	lio[7C5]
J5500	CON_F80ST_D4MT_SM_F-ST-SM	lio[8C4]
J7300	CON_F8RT_SPDIFTRAN_TH2_F-RT-TH	lio[14C8]
J7350	CON_F8RT_SPDIFRCVR_TH2_F-RT-TH	lio[14B8]
J7380	CON_M3ST_S_TH_M-ST-TH	lio[14D1]
J7381	CON_M5ST_S_TH_M-ST-TH	lio[14C1]
J7382	CON_M5ST_S_TH_M-ST-TH	lio[14C1]
J8200	CON_F5RT_S2MT_TH3_F-RT-TH-MG3	lio[17D8]
J8415	CON_M8RT_S_SM_M-RT-SM	lio[18C5]
L5100	FILTER_4P_1210-4SM1	lio[5B4]
L5101	IND_0603	lio[5B4]
L5110	FILTER_4P_1210-4SM1	lio[5C4]
L5111	IND_0603	lio[5D4]
L5150	IND_0603	lio[5A4]
L5160	IND_0603	lio[5C4]
L6800	IND_0402	lio[10A5]
L6801	IND_0402	lio[10D6]
L7000	IND_0402	lio[11C6]
L7101	IND_0402	lio[12D6]
L7102	IND_0402	lio[12C6]
L7200	IND_0402	lio[13B2]
L7201	IND_0402	lio[13D7]
L7210	IND_0402	lio[13D7]
L7220	IND_0402	lio[13C7]
L7230	IND_0402	lio[13B7]
L7240	IND_0402	lio[13A7]
L7300	IND_0603	lio[14D6]
L7301	IND_0603	lio[14D4]
L7302	IND_0402	lio[14C6]
L7303	IND_0402	lio[14C6]
L7304	IND_0402	lio[14C4]
L7305	IND_0402	lio[14C6]
L7306	IND_0402	lio[14C4]
L7307	IND_0402	lio[14C6]
L7308	IND_0402	lio[14D6]
L7309	IND_0402	lio[14D4]
L7350	IND_0402	lio[14B6]
L7351	IND_0402	lio[14B4]
L7352	IND_0402	lio[14B6]
L7353	IND_0402	lio[14B6]
L7354	IND_0402	lio[14B4]
L7355	IND_0402	lio[14A6]
L7356	IND_0402	lio[14A4]
L7357	IND_0402	lio[14A6]
L7400	IND_0402	lio[15B4]
L7600	IND_IHLP	lio[16C2]
L7650	IND_IHLP	lio[16B2]
L8200	FILTER_4P_SM	lio[17D6]
L8201	IND_SM-1	lio[17D6]
PD6401	PHOTODIODE_2P_TH-WTH-SPCR	lio[9C6]
Q5360	TRA_2N7002_SOT23-LF	lio[6B3]
Q6401	TRA_2N7002_SOT23-LF	lio[9C5]
Q7400	TRA_2N7002DW_SOT-363	lio[15C7 15D7]
Q7401	TRA_2N7002DW_SOT-363	lio[15D5 15D6]
Q7402	TRA_2N7002DW_SOT-363	lio[15B7 15C5]
Q7403	TRA_2N7002DW_SOT-363	lio[15A7 15A8]
Q7600	TRA_FDM6296_MICROFET 3X3	lio[16C3]
Q7601	TRA_FDM6296_MICROFET 3X3	lio[16C3]
Q7610	TRA_IRF7707_TSSOP	lio[16A7]
Q7650	TRA_FDM6296_MICROFET 3X3	lio[16B3]
Q7651	TRA_FDM6296_MICROFET 3X3	lio[16B3]
Q7660	TRA_IRF7707_TSSOP	lio[16A3]
Q8200	TRA_2N7002DW_SOT-363	lio[17C1 17C3]
Q8201	TRA_2N7002DW_SOT-363	lio[17C1 17C2]
Q8209	TRA_TP0610_S0T23-3	lio[17D2]
R5300	RES_402	lio[6C6]
R5360	RES_402	lio[6B3]
R5361	RES_402	lio[6B3]
R5370	RES_402	lio[6B2]
R5371	RES_402	lio[6D3]
R5500	RES_402	lio[8B4]
R6401	RES_402	lio[9C6]
R6402	RES_402	lio[9C6]
R6403	RES_402	lio[9C5]
R6404	RES_402	lio[9C5]
R6405	RES_402	lio[9C5]
R6406	RES_402	lio[9C4]
R6407	RES_402	lio[9C4]
R6800	RES_402	lio[10C6]
R6802	RES_402	lio[10A5]
R6803	RES_402	lio[10A5]
R6807	RES_402	lio[10C7]
R6808	RES_402	lio[10D3]
R6809	RES_402	lio[10B3]
R6810	RES_402	lio[10A3]
R6811	RES_402	lio[10A3]
R6812	RES_402	lio[10C2]
R6852	RES_402	lio[10B8]
R6853	RES_402	lio[10B8]
R7000	RES_402	lio[11B3]
R7001	RES_402	lio[11B6]
R7002	RES_402	lio[11C5]
R7003	RES_402	lio[11B5]
R7010	RES_402	lio[11C4]
R7011	RES_402	lio[11C4]
R7012	RES_402	lio[11C4]
R7013	RES_402	lio[11C4]
R7014	RES_402	lio[11B4]
R7015	RES_402	lio[11B4]
R7016	RES_402	lio[11B4]
R7017	RES_402	lio[11B4]
R7100	RES_402	lio[12C5]
R7101	RES_603	lio[12D4]
R7110	RES_402	lio[12C3]
R7111	RES_402	lio[12C3]
R7112	RES_603	lio[12D3]
R7113	RES_603	lio[12C2]
R7114	RES_603	lio[12B3]
R7115	RES_603	lio[12B3]
R7120	RES_402	lio[12B5]
R7121	RES_402	lio[12B4]
R7122	RES_402	lio[12B4]
R7123	RES_402	lio[12B4]
R7124	RES_402	lio[12B4]
R7130	RES_402	lio[12A5]
R7131	RES_402	lio[12A4]
R7132	RES_402	lio[12A4]
R7133	RES_402	lio[12A4]
R7134	RES_402	lio[12A4]
R7201	RES_402	lio[13D7]
R7202	RES_402	lio[13D5]
R7203	RES_402	lio[13C5]
R7204	RES_402	lio[13B5]
R7260	RES_402	lio[13D4]
R7261	RES_402	lio[13D4]
R7270	RES_402	lio[13C4]
R7271	RES_402	lio[13C4]
R7280	RES_402	lio[13B4]
R7281	RES_402	lio[13B4]
R7290	RES_402	lio[13A4]
R7291	RES_402	lio[13A4]
R7300	RES_402	lio[14C4]
R7301	RES_402	lio[14C4]
R7349	RES_402	lio[14B7]
R7350	RES_402	lio[14A4]
R7351	RES_402	lio[14A4]
R7380	RES_402	lio[14C2]
R7381	RES_402	lio[14C1]
R7390	RES_402	lio[14B2]
R7401	RES_402	lio[15D8]
R7402	RES_402	lio[15D7]
R7403	RES_402	lio[15C7]
R7404	RES_402	lio[15C4]
R7405	RES_402	lio[15D5]
R7406	RES_402	lio[15D6]
R7411	RES_402	lio[15C8]
R7412	RES_402	lio[15B7]
R7413	RES_402	lio[15C6]
R7414	RES_402	lio[15C4]
R7415	RES_402	lio[15C5]
R7416	RES_402	lio[15C6]
R7417	RES_402	lio[15A8]
R7418	RES_402	lio[15A7]
R7450	RES_402	lio[15A5]
R7451	RES_402	lio[15A5]
R7452	RES_402	lio[15A5]
R7453	RES_402	lio[15A4]
R7454	RES_402	lio[15A4]
R7460	RES_402	lio[15C7]
R7610	RES_402	lio[16A8]
R7620	RES_402	lio[16C5]
R7621	RES_402	lio[16C6]
R7622	RES_402	lio[16C7]
R7623	RES_402	lio[16B7]
R7625	RES_402	lio[16C6]
R7660	RES_402	lio[16A4]
R8200	RES_402	lio[17C2]
R8201	RES_402	lio[17D2]
R8202	RES_402	lio[17D5]
R8204	RES_402	lio[17C6]
R8205	RES_402	lio[17D3]
R8209	RES_402	lio[17D1]
R8210	RES_402	lio[17D1]
R8211	RES_402	lio[17C2]
R8290	RES_402	lio[17C5]
R8291	RES_402	lio[17C4]
R8292	RES_402	lio[17B1]
SL1	SLOT_TH	lio[4A8]
U5100	SWI_TPS2042B_MSOP	lio[5B6]
U5300	PWR_CNTRL_TPS2231_QFN	lio[6C5]
U5351	MC74VHC1G00_SC70-5	lio[6A6]
U5360	MC74VHC1G00_SC70-5	lio[6B2]
U6401	OPAMP_MAX4236EUTT_SO-6	lio[9C5]
U6402	TMP106_WCSP-6	lio[9B5]
U6800	AUDIO_STAC92204XR_LQFP	lio[10D5]
U6801	LREG_MAX1819_UCSP	lio[10A4]
U7000	OPAMP_MAX4253_UMAX	lio[11B4 11C4]
U7100	MAX9722_QFN	lio[12D4]
U7210	MAX9705_TDFN1	lio[13D6]
U7220	MAX9705_TDFN1	lio[13C6]
U7230	MAX9705_TDFN1	lio[13B6]
U7240	MAX9705_TDFN1	lio[13A6]
U7600	TPS51120_LLP	lio[16C6]
U8290	COMPARATOR_LM397_SOT23-5	lio[17C4]
XW6800	SHORT_SM	lio[10A8]
XW6801	SHORT_SM	lio[10B5]
XW6802	SHORT_LAYER_9_SHORT-L9-SM	lio[10A8]
XW7100	SHORT_SM	lio[12C5]
XW7101	SHORT_SM	lio[12C5]
XW7102	SHORT_SM	lio[12C5]
XW7200	SHORT_SM	lio[13B6]
XW7310	SHORT_SM	lio[14B2]
XW7311	SHORT_SM	lio[14B2]
XW7312	SHORT_SM	lio[14B2]
XW7400	SHORT_SM	lio[15A4]
XW7600	SHORT_SM	lio[16A4]
XW7601	SHORT_SM	lio[16A7]
XW7602	SHORT_SM	lio[16A7]
XW7603	SHORT_LAYER_2_SHORT-L2_SM	lio[16A7]
XW7604	SHORT_LAYER_9_SHORT-L9-SM	lio[16A7]
XW7605	SHORT_LAYER_2_SHORT_L2_SM	lio[16A7]
XW7606	SHORT_LAYER_9_SHORT-L9-SM	lio[16A7]
XW7607	SHORT_LAYER_2_SHORT-L2_SM	lio[16A7]
XW7608	SHORT_LAYER_2_SHORT_L2_SM	lio[16A7]
XW7609	SHORT_LAYER_2_SHORT_L2_SM	lio[16A7]
XW7610	SHORT_LAYER_2_SHORT_L2_SM	lio[16A7]
ZH1	MTGHOLE	lio[4A8]
ZH2	MTGHOLE	lio[4A8]
ZH4	MTGHOLE	lio[4A8]
ZH5	MTGHOLE	lio[4A8]