

M38 - DVT

12/08/05

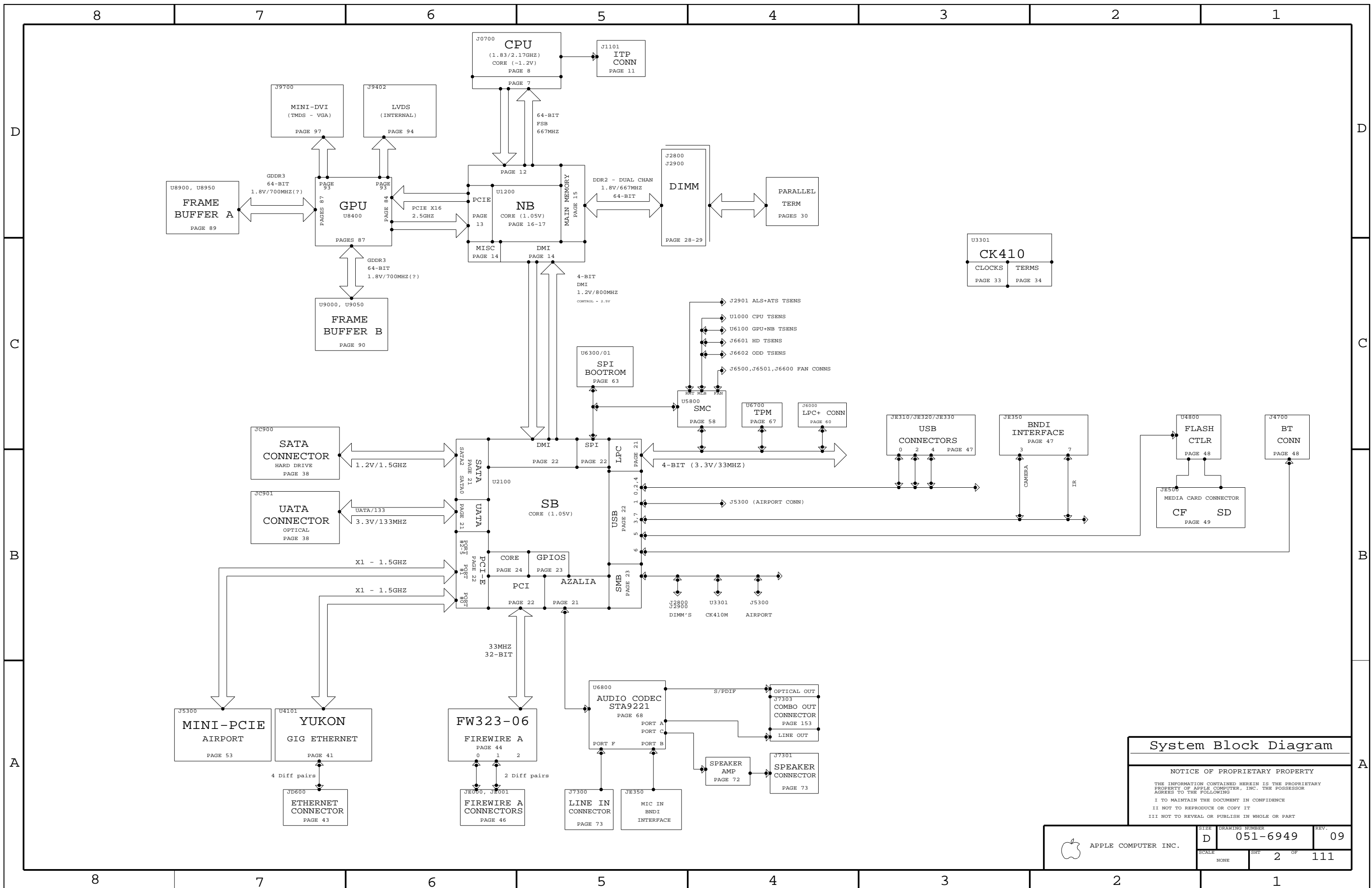
1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD	ENG APPD
				DATE	DATE
09		400372	ENGINEERING RELEASED	09/16/05	06/22/04

PAGE	DRI	PDF	CIRCUIT	
1	JD	JD	1	TABLE OF CONTENTS
2	JD	JD	2	SYSTEM BLOCK DIAGRAM
3	RT	RT	3	POWER BLOCK DIAGRAM
4	JD	JD	4	TABLE ITEMS & REVISION HISTORY
5	JD	JD	5	FUNC TEST
6	RT	RT	6	POWER CONNECTOR / POWER ALIAS
(M42) 7	MS	JD	7	CPU - BUS INTERFACE
(M42) 8	MS	JD	8	CPU - PWR & GND
9	MS	JD	9	CPU - DECAPS
(M42) 10	MS	JD	10	CPU - THERMAL SENSOR
M42 11	MS	JD	11	CPU - ITP CONN
M1 12	PS	JH	12	NB - CPU INTERFACE
M1 13	PS	JH	13	NB - VIDEO INTERFACE
14	PS	JH	14	NB - MISC INTERFACES
M1 15	PS	JH	15	NB - DDR2 INTERFACE
M1 16	PS	JH	16	NB - POWER 1
M1 17	PS	JH	17	NB - POWER 2
M1 18	PS	JH	18	NB - GROUNDS
19	PS	JH	19	NB - DECAPS
M1 20	PS	JH	20	NB - CONFIG STRAPS
21	JD	JD	21	SB - RTC, LAN, AUDIO, ATA, CPU, LPC
22	JD	JD	22	SB - PCIE, SPI, USB, DMI, PCI
23	JD	JD	23	SB - SMB, GPIO, PM, CLKS
24	JD	JD	24	SB - POWERS AND GROUNDS
25	JD	JD	25	SB - DECAPS
26	JD	JD	26	SB - MISC
27	JD	JD	27	SB - SMB BUS CONNECTIONS
28	PS	JD	28	DDR2 - SO-DIMM CONN A
29	PS	JD	29	DDR2 - SO-DIMM CONN B (REVERSED)
30	PS	JD	30	DDR2 - TERMINATION
M1 31	RT	RT	31	DDR2 - VTT SUPPLY
M42 33	JD	JD	32	CLOCKS - GENERATOR
34	JD	JD	33	CLOCKS - TERMINATIONS
38	JD	JD	34	ATA (SATA AND IDE) CONN'S
(M42) 41	JD	JD	35	LAN - YUKON'S PCIE INTERFACE
42	JD	JD	36	LAN - YUKON'S PWR, MISC
43	JD	JD	37	LAN - CONN
44	JD	JD	38	FIREWIRE - FW323-06
45	JD	JD	39	FIREWIRE - DECAPS
46	JD	JD	40	FIREWIRE - CONN'S
47	JD	JD	41	USB - CONN'S
49	JD	JD	42	USB - FLASH CONN

PAGE	DRI	PDF	CIRCUIT	
53	JD	JD	43	PCI-E - AIRPORT MINI-PCIE CONN
54	JD	JD	44	PCI-E - UNUSED PORTS
58	MS	MS	45	SMC - H8S2116
59	MS	MS	46	SMC - SMB BUSSES, MISC
60	MS	MS	47	SMC - LPC+ CONN
61	JH	JH	48	SMC - GPU/NB THERMAL SENSOR
RX 63	MS	JD	49	SMC - SPI BOOTROM
65	MS	MS	50	SMC - FANS
66	MS	MS	51	SMC - FANS
67	JD	JD	52	SMC - TPM
SO 68	PT	JD	53	AUDIO - CODEC, VREG, MIC BIAS
SO 72	PT	JD	54	AUDIO - INTERNAL SPEAKER AMP
SO 73	PT	JD	55	AUDIO - I/O CONN'S, EMC
SO 74	PT	JD	56	AUDIO - DETECT TRANSLATORS
RP 75	RT	RT	57	VR - CPU CORE
RP 76	RT	RT	58	VR - CPU I-V SENSE CKT
RP 77	RT	RT	59	VR - "S0" 1.2V & 2.5V (GRAFIX)
RP 78	RT	RT	60	VR - "S0" 1.8V
RP 79	RT	RT	61	VR - "S3" 1.8V
RP 80	RT	RT	62	VR - "S0" 1.5V
RP 81	RT	RT	63	VR - "S0" 1.05V
RP 83	RT	RT	64	VR - "S3" 3.3V AND 5V
JH 84	JH	JH	65	GPU - M56 PCI-E
M1 85	JH	JH	66	GPU - VCORE SUPPLY
M1 86	JH	JH	67	GPU - M56 CORE PWR
M1 87	JH	JH	68	GPU - M56 FRAME BUFFER
M1 88	JH	JH	69	GPU - MISC
M1 89	JH	JH	70	GPU - GDDR SDRAM A
M1 90	JH	JH	71	GPU - GDDR SDRAM B
M1 91	JH	JH	72	GPU - M56 GPIO, DVO, MISC
M1 92	JH	JH	73	GPU - M56 CLOCKS
M1 93	JH	JH	74	GPU - M56 VIDEO INTERFACES
JH 94	JH	JH	75	GPU - INTERNAL DISPLAY CONN'S
JH 95	JH	JH	76	GPU - TP'S
JH 96	JH	JH	77	GPU - TMDS, INVERTER, EXT VGA
JH 97	JH	JH	78	GPU - EXTERNAL DISPLAY CONN'S

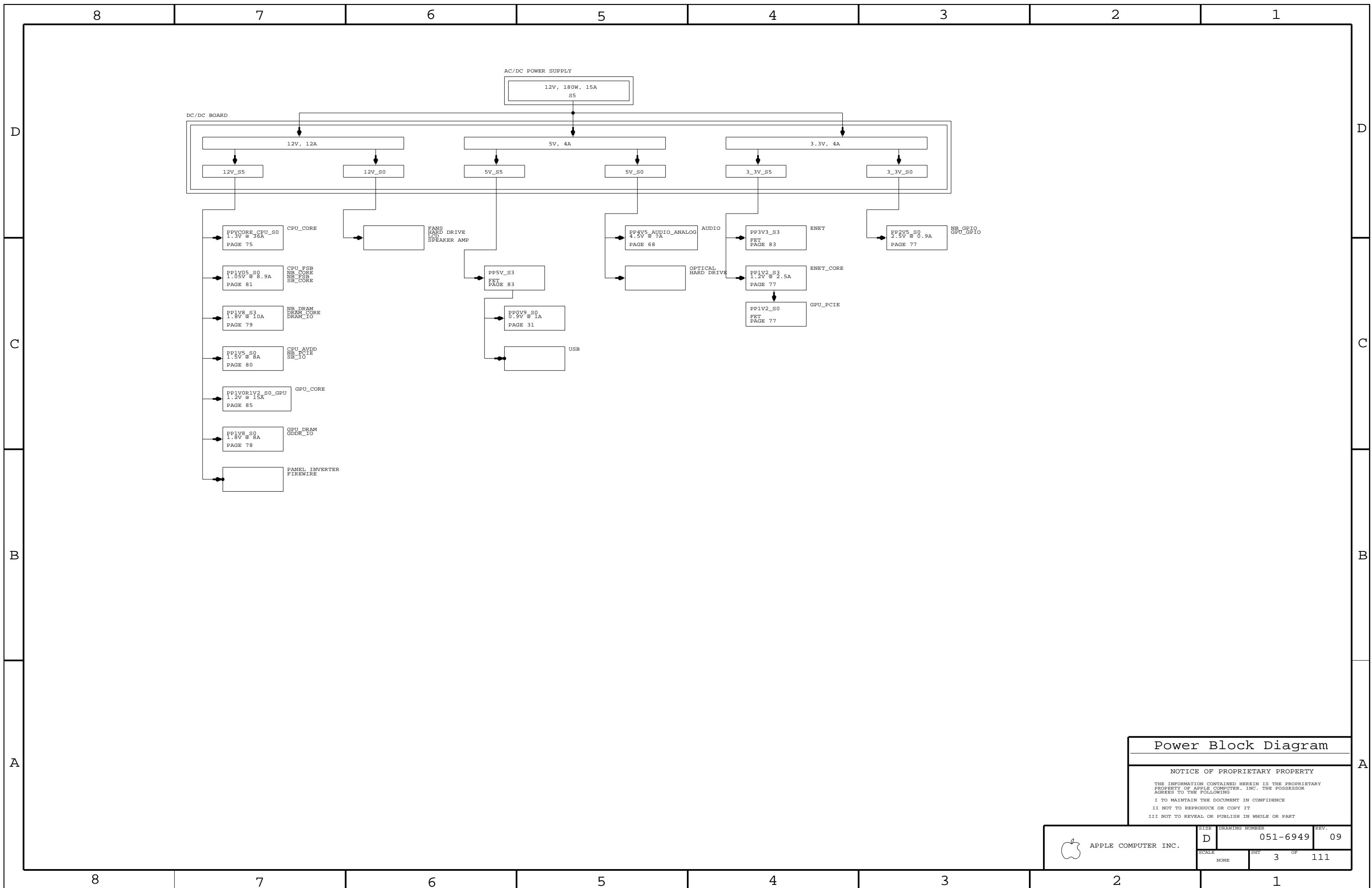
<p style="font-size: small;">DIMENSIONS ARE IN MILLIMETERS</p> <p>XX : _____</p> <p>X.XX : _____</p> <p>X.XXX : _____</p> <p>ANGLES : _____</p> <p style="text-align: center; font-size: x-small;">DO NOT SCALE DRAWING</p> <div style="text-align: center;"> <p style="font-size: x-small;">THIRD ANGLE PROJECTION</p> </div>	<p>METRIC</p>	<p>Apple Computer Inc.</p>
<p>NOTICE OF PROPRIETARY PROPERTY</p> <p>THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING</p> <p>I TO MAINTAIN THE DOCUMENT IN CONFIDENCE</p> <p>II NOT TO REPRODUCE OR COPY IT</p> <p>III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART</p>		
<p>RELEASE</p>		<p>SCALE</p> <p>NONE</p>
<p>MATERIAL/FINISH NOTED AS APPLICABLE</p>		<p>SIZE</p> <p>D</p>
<p>DRAWING NUMBER</p> <p style="font-size: large;">051-6949</p>		<p>REV.</p> <p style="font-size: large;">09</p>
<p>SHT 1 OF 111</p>		



System Block Diagram

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Power Block Diagram

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	SCALE NONE	SHEET 3	OF 111

8

7

6

5

4

3

2

1

COMMON

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
511S0025	1	IC,CPU-SKT,479BGA	J0700	CRITICAL	
338S0269	1	IC,945GM,NORTHBRIDGE	U1200	CRITICAL	
343S0385	1	IC,SB,652BGA	U2100	CRITICAL	
742-0048	1	BAT,COIN,3V,220MAH,CR2032	BT2600	CRITICAL	
359S0101	1	IC,CY28445-5,CLK GEN,68PIN QFP	U3301	CRITICAL	
338S0270	1	IC,888053,1GIGABIT ETH XVR,64P QFN,MD	U4101	CRITICAL	
(335S0382) 341S1797	1	IC,ENET LAN ROM	U4102	CRITICAL	
338S0279	1	IC,FW32306,1394A LINK,TQFP	U4400	CRITICAL	
338S0274	1	IC,SMC,HSS/2116,BLANK	U5800	CRITICAL	
341S1789	1	IC,TPM,TSSOP,28P	U6700	CRITICAL	LEMENU
353S1235	1	IC,CPU VREG,IMVP,TWO PHASE	U7500	CRITICAL	
338S0266	1	IC,ATI,M56P,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	ATI_B24
338S0305	1	IC,ATI,M56P,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	ATI_A24
128S0078	3	CAP,EL,AL,330UF,20A,16V,10X12.7MM,SMD,LF	C7517,C7518,C7910	CRITICAL	
825-6447	1	MLB LABEL,48.0X4.8	X14	CRITICAL	

M38

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-6949	1	PCB,SCHEM,MLB,M38	SCH1		17_INCH_LCD
820-1919	1	PCB,FAB,MLB,M38	MLB1		17_INCH_LCD
(335S0384) 341T0003	1	EFI ROM,M38	U6301	CRITICAL	17_INCH_LCD
114S0287	1	5.11K,1%,1/16W,402,MF-LF	R8522		17_INCH_LCD
337S3242	1	M00-SPEED CPU (QINZ)	CPU	CRITICAL	CPU_M00
337S3241	1	M38/M39 LOW-SPEED CPU (QINY)	CPU	CRITICAL	CPU_M38

M39

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-6950	1	PCB,SCHEM,MLB,M39	SCH1		20_INCH_LCD
820-1888	1	PCB,FAB,MLB,M39	MLB1		20_INCH_LCD
(335S0384) 341T0004	1	EFI ROM,M39	U6301	CRITICAL	20_INCH_LCD
114S0276	1	4.02K,1%,1/16W,402,MF-LF	R8522		20_INCH_LCD
337S3243	1	M39 HI-SPEED CPU (QHJ)	CPU	CRITICAL	CPU_M39

M38 / M39

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0354	4	IC,SURAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_SAMSUNG
333S0358	4	IC,SURAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_HYNIX

M39 - CTO

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0350	4	IC,SURAM,GDDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_SAMSUNG
333S0351	4	IC,SURAM,GDDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_HYNIX

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
126S0096	126S0076		C7801	SANYO W16C680EX 680UF 16V LFP
126S0086	126S0078		C699,C940,C1900,C1901,C1968	SANYO W6CE330F8 330UF 6.3V LFP
128S0080	128S0078		C7517,C7518,C7910	SANYO 160VP330W 330UF 16V SMD LFP
338S0309	338S0266		U8400	IC,ATI,M56LP,GRAFIX CTLR,880FBGA,LF
197S0177	197S0020		Y4101	XTAL,25MHZ,50PFM,16PF,3.220+-5 SMD,LF

124-0338 124-0333 C7501,C8014 CAP,AL,EL,680UF,16V,RAD,10X12.5MM

Table Items

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SIZE DRAWING NUMBER REV.

D 051-6949 09

SCALE NONE SHT 4 OF 111

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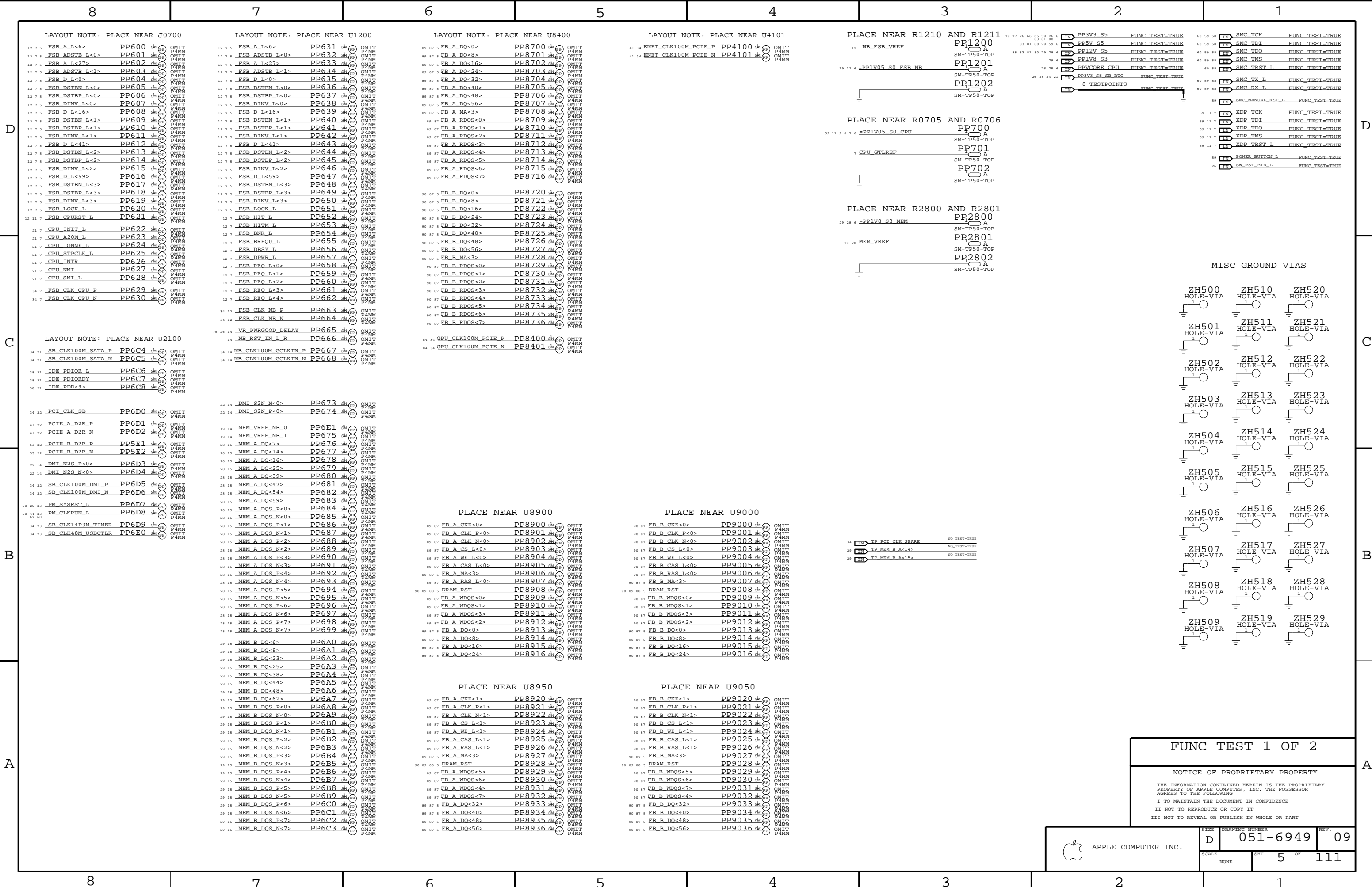
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1



LAYOUT NOTE: PLACE NEAR J0700

LAYOUT NOTE: PLACE NEAR U1200

LAYOUT NOTE: PLACE NEAR U8400

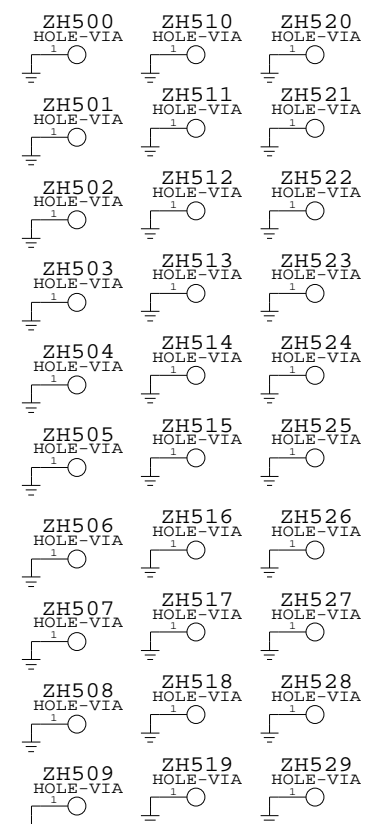
LAYOUT NOTE: PLACE NEAR U4101

PLACE NEAR R1210 AND R1211

PLACE NEAR R0705 AND R0706

PLACE NEAR R2800 AND R2801

MISC GROUND VIAS



FUNC TEST 1 OF 2

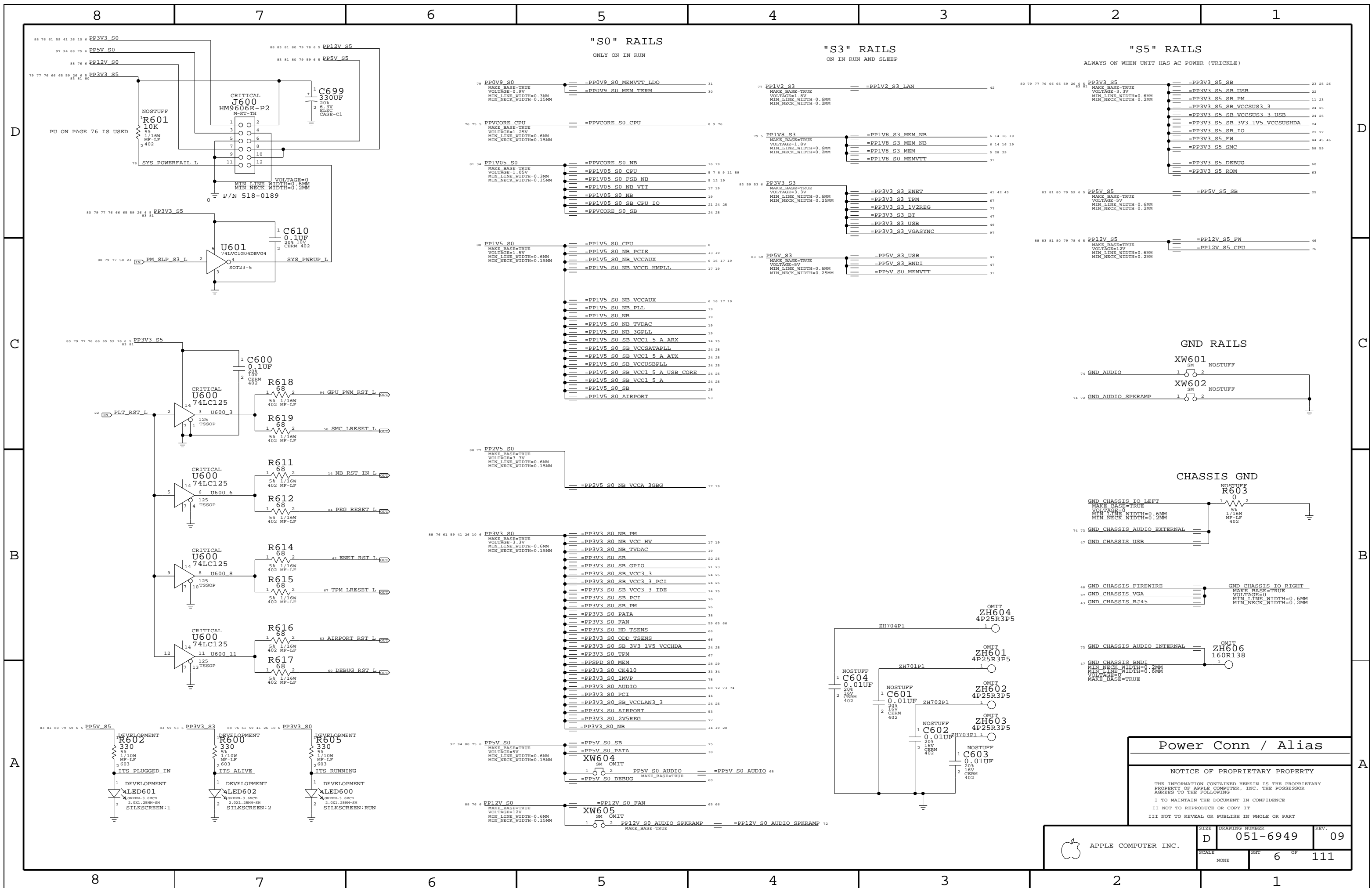
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SCALE	NONE	SHEET	5 OF 111



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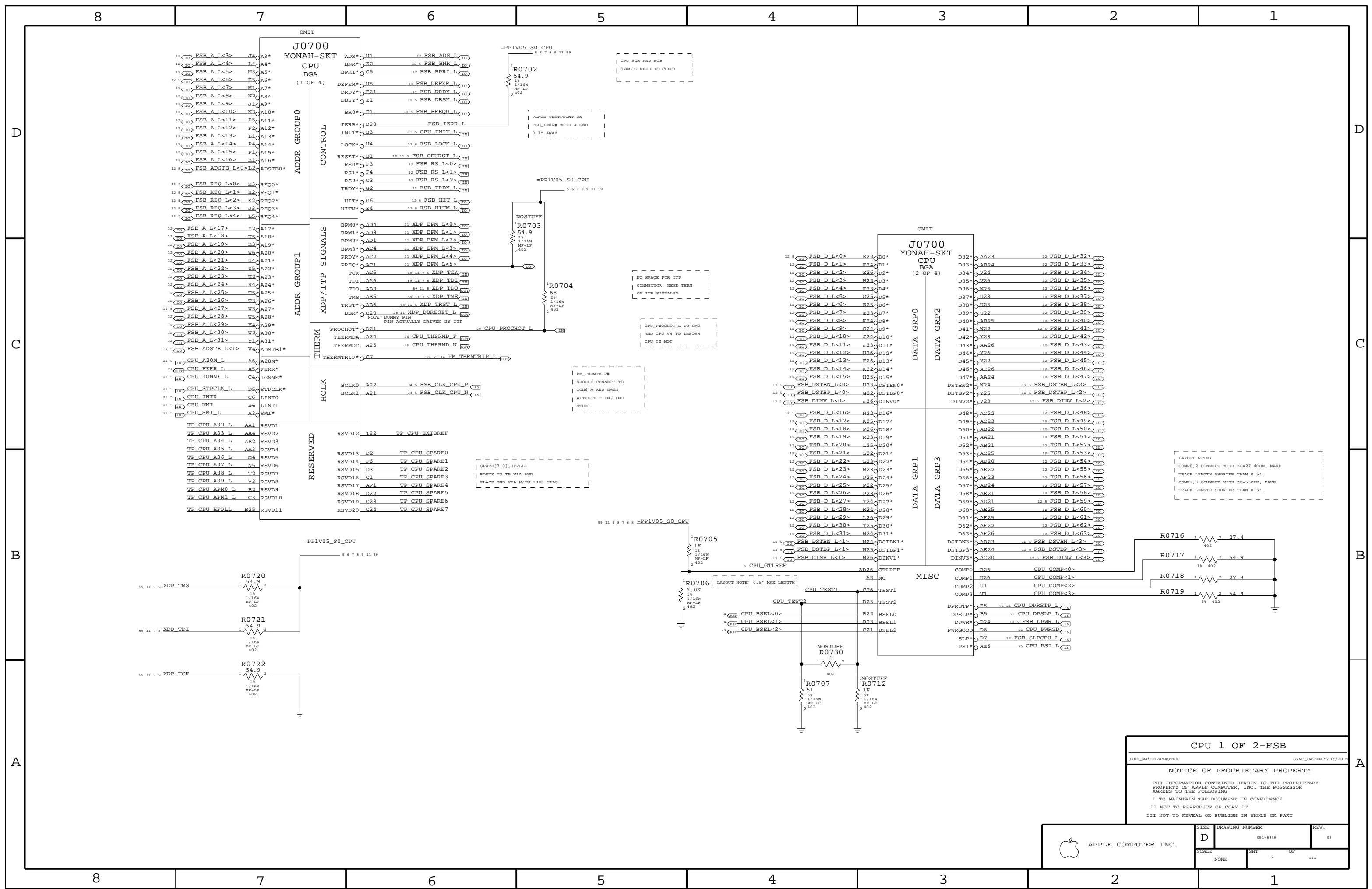


Power Conn / Alias

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SCALE	SHT	OF	
NONE	6	111	



CPU 1 OF 2-FSB

SYNC_MASTER=MASTER SYNC_DATE=05/03/2005

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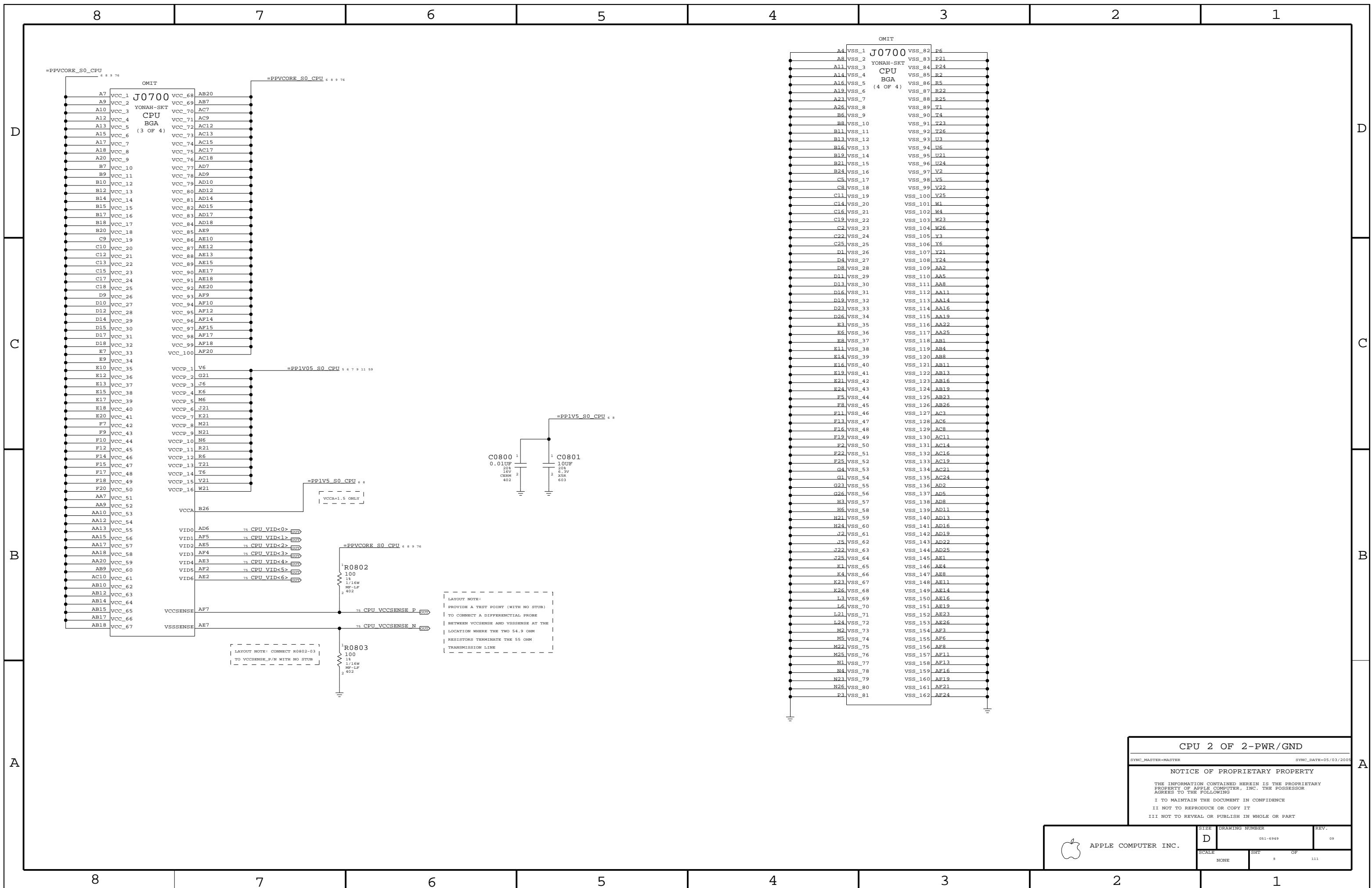
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NONE	7		



CPU 2 OF 2-PWR/GND

SYNC_MASTER=MASTER SYNC_DATE=05/03/2005

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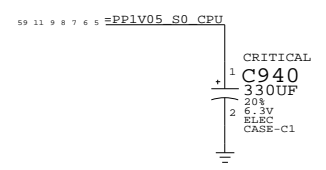
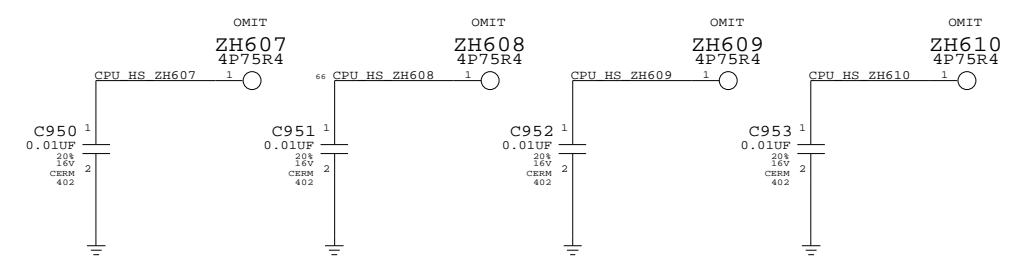
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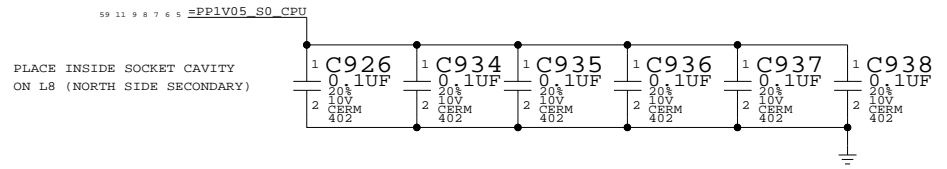
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	SCALE NONE	SHEET 8	OF 111

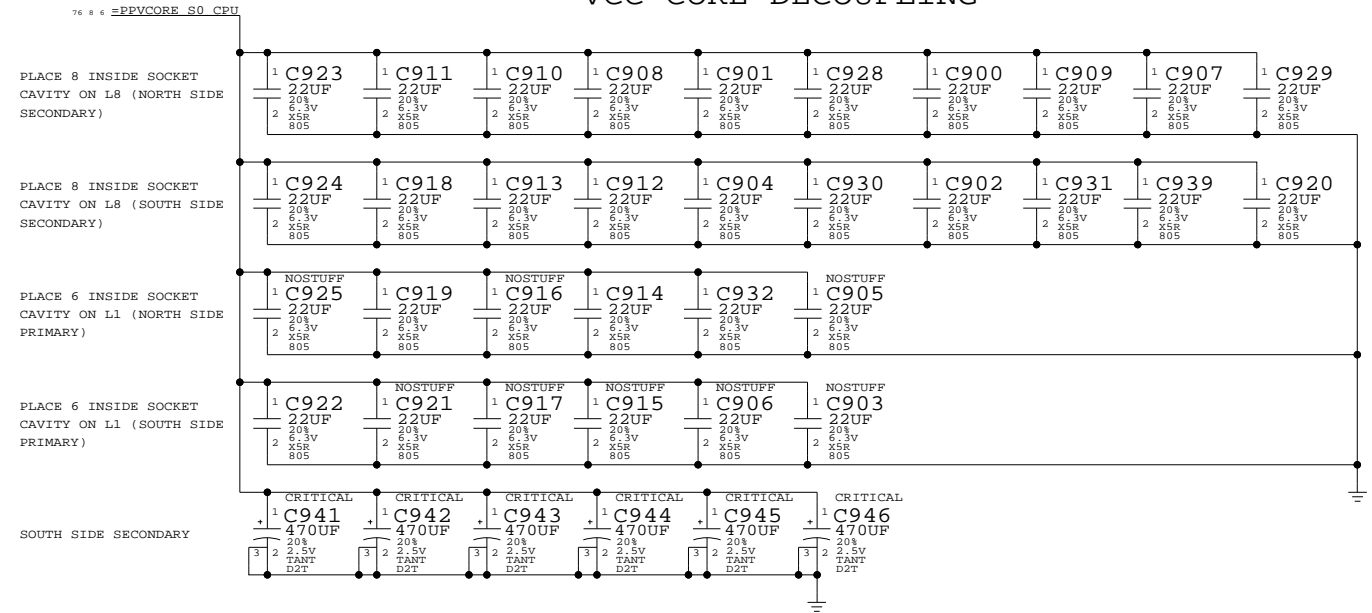
CPU HEATSINK MOUNTING HOLES



VCCP CORE DECOUPLING



VCC CORE DECOUPLING



CPU DECAPS & VID<>

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SCALE	SHT	9	OF 111
NONE			

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7

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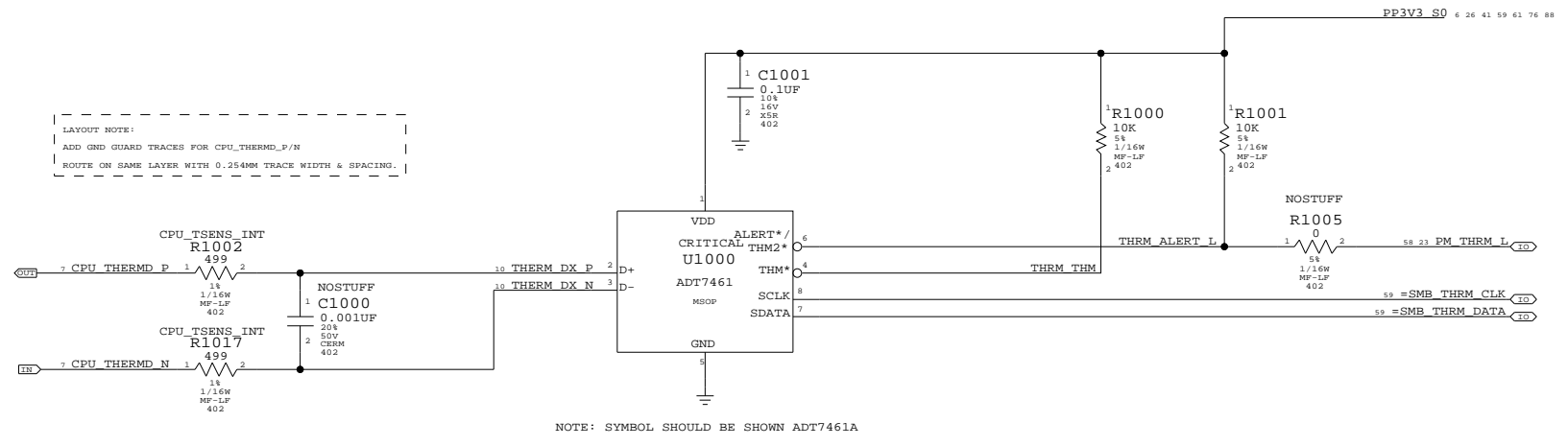
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CPU THERMAL SENSOR

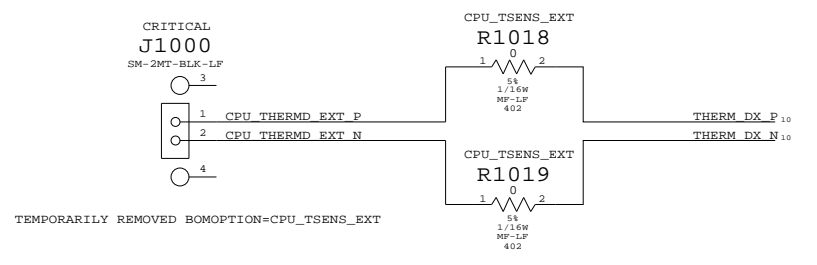
NOTE:
 IF CPU T DIODE TO BE READ IN OFF STATE,
 THEN THIS SHOULD BE S5

LAYOUT NOTE:
 ADD GND GUARD TRACES FOR CPU_THERMD_P/N
 ROUTE ON SAME LAYER WITH 0.254MM TRACE WIDTH & SPACING.



NOTE: SYMBOL SHOULD BE SHOWN ADT7461A

LAYOUT NOTE:
 PLACE R1002 AND R1018 SUCH THAT THEY SHARE ONE PAD
 PLACE R1017 AND R1019 SUCH THAT THEY SHARE ONE PAD



CPU TEMP SENSOR

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NONE	10		111

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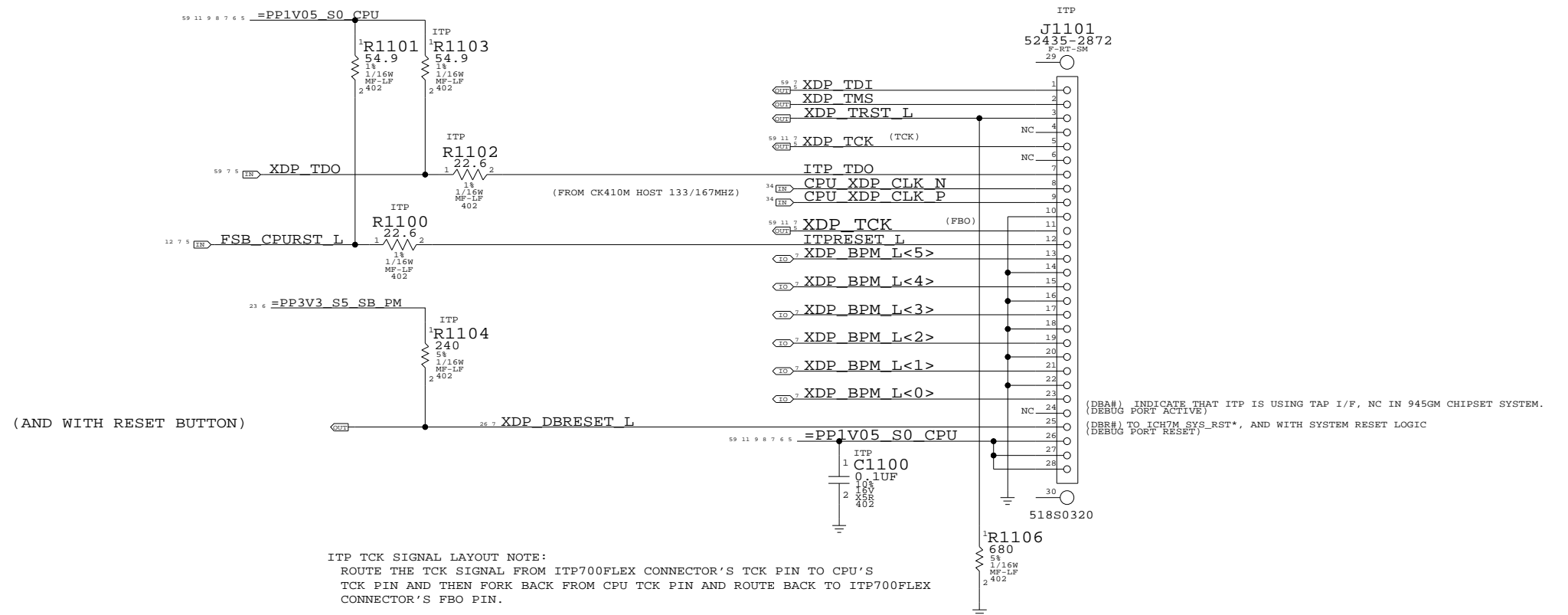
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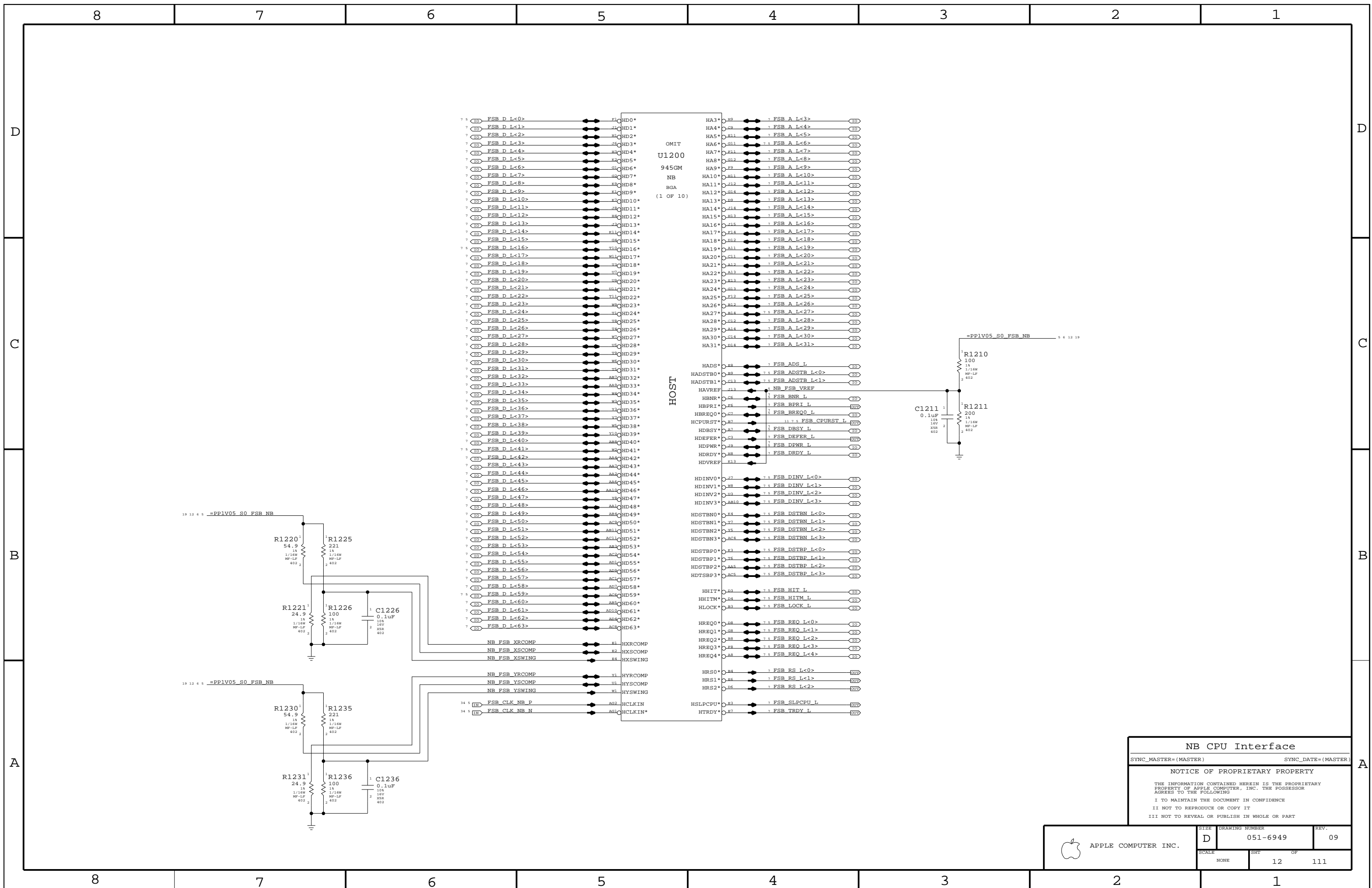
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CPU ITP700FLEX DEBUG SUPPORT



CPU ITP700FLEX DEBUG
 SYNC_MASTER=MASTER SYNC_DATE=5/23/05
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SCALE	SHT	OF	111
NONE	11		



NB CPU Interface

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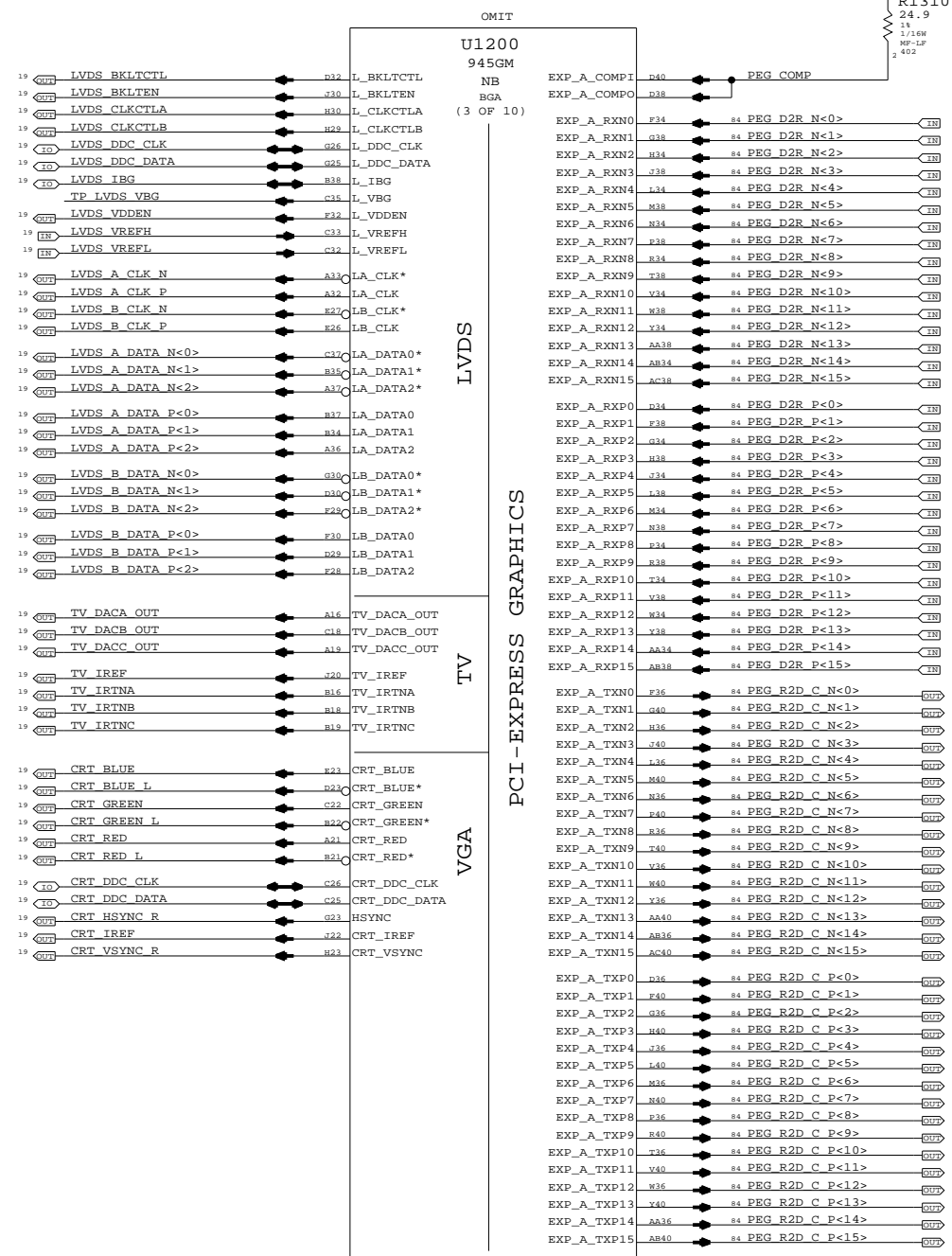
LVDS Disable
 Can leave all signals NC if LVDS is not implemented
 Tie VCC_TXLVDS and VCCA_LVDS to GND. If SDVO is used
 VCCD_LVDS must remain powered with proper decoupling.
 Otherwise, tie VCCD_LVDS to GND also.

TV-Out Signal Usage:
 Composite: DACA only
 S-Video: DACB & DACC only
 Component: DACA, DACB & DACC

Unused DAC outputs must remain powered, but can omit
 filtering components. Unused DAC outputs should
 connect to GND through 75-ohm resistors.

TV-Out Disable
 Tie DACx_OUT, IRTNx, and IREF to 1.5V power rail.
 Tie VCCD_TVDAC, VCCD_QTVDAC, VCCA_TVDACx, and
 VCCA_TVVBG to 1.5V power rail. Tie VSSA_TVVBG to GND.

CRT Disable
 Tie R/R#/G/G#/B/B# and IREF to VCC Core rail, tie
 HSYNC and VSYNC to GND. Tie VCCA_CRTDAC to VCC Core
 rail, and tie VSSA_CRTDAC and VCC_SYNC to GND.



SDVO Alternate Function

SDVO_TVCLKIN#
 SDVO_INT#
 SDVO_FLDSTALL#

SDVO_TVCLKIN
 SDVO_INT
 SDVO_FLDSTALL

SDVOB_RED#
 SDVOB_GREEN#
 SDVOB_BLUE#
 SDVOB_CLKN
 SDVOC_RED#
 SDVOC_GREEN#
 SDVOC_BLUE#
 SDVOC_CLKN

SDVOB_RED
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 SDVOB_BLUE
 SDVOB_CLKP
 SDVOC_RED
 SDVOC_GREEN
 SDVOC_BLUE
 SDVOC_CLKP

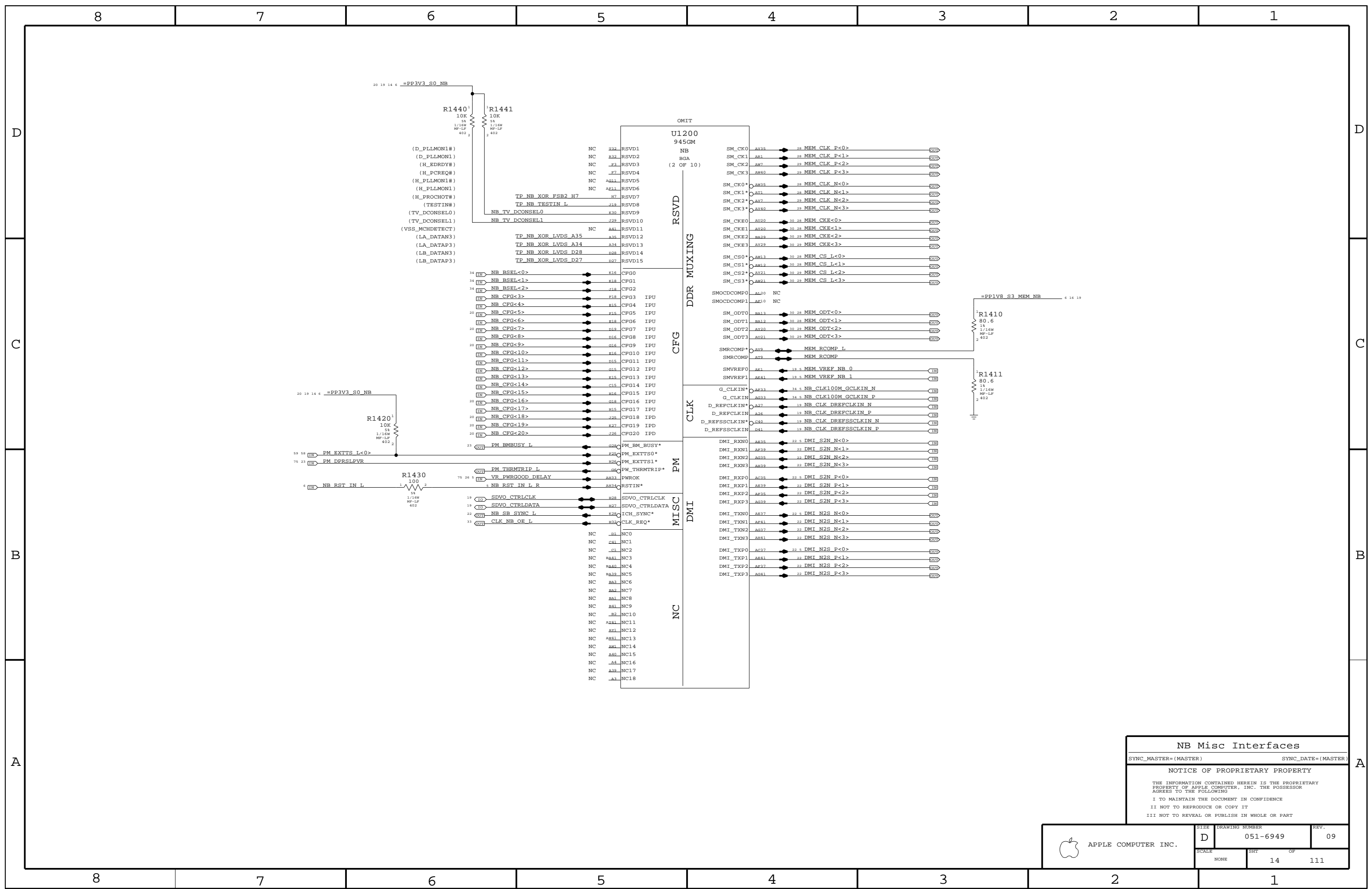
NB PEG / Video Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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		13	111



NB Misc Interfaces

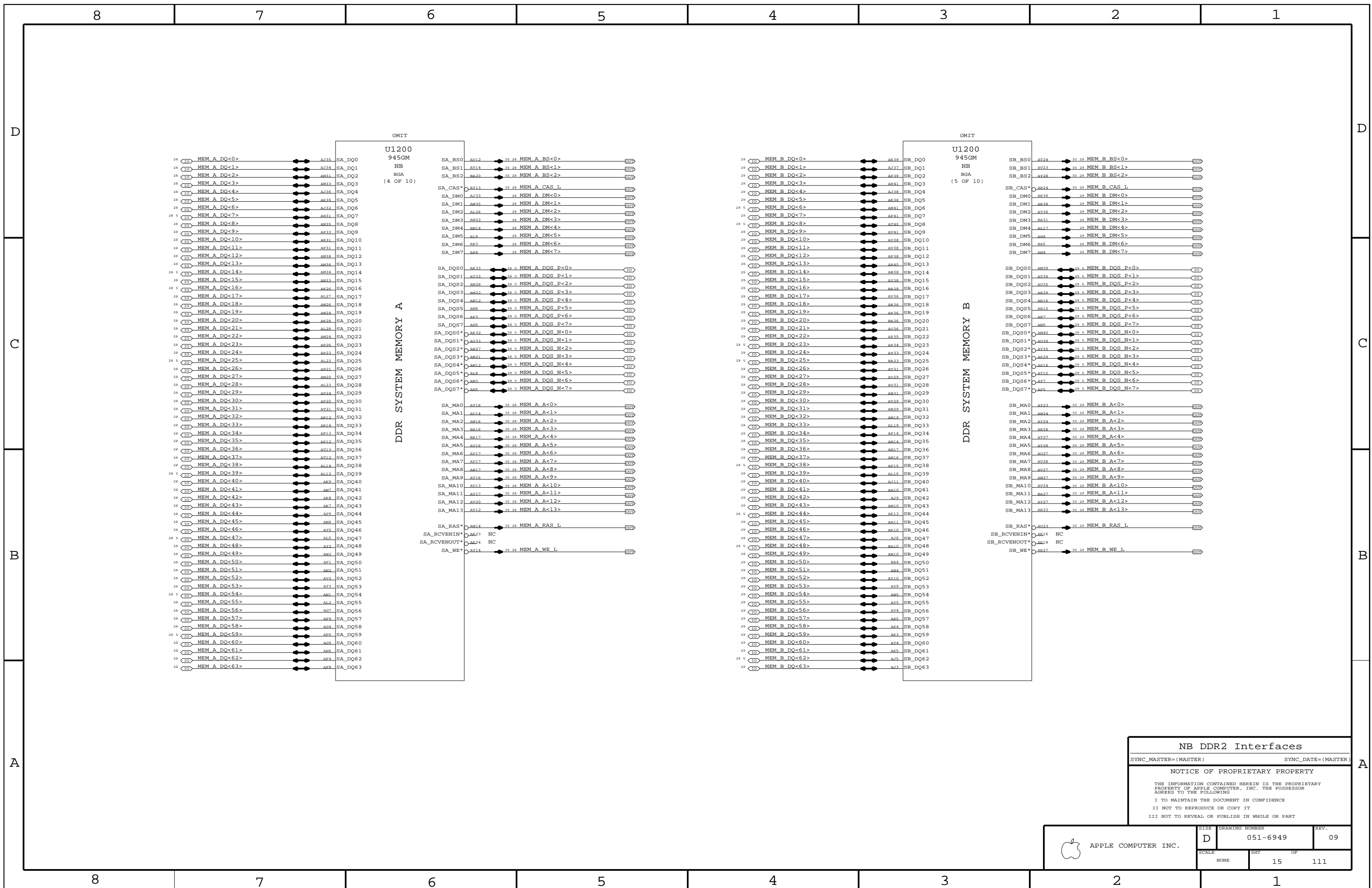
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SCALE	SHT OF		
NONE	14	111	



NB DDR2 Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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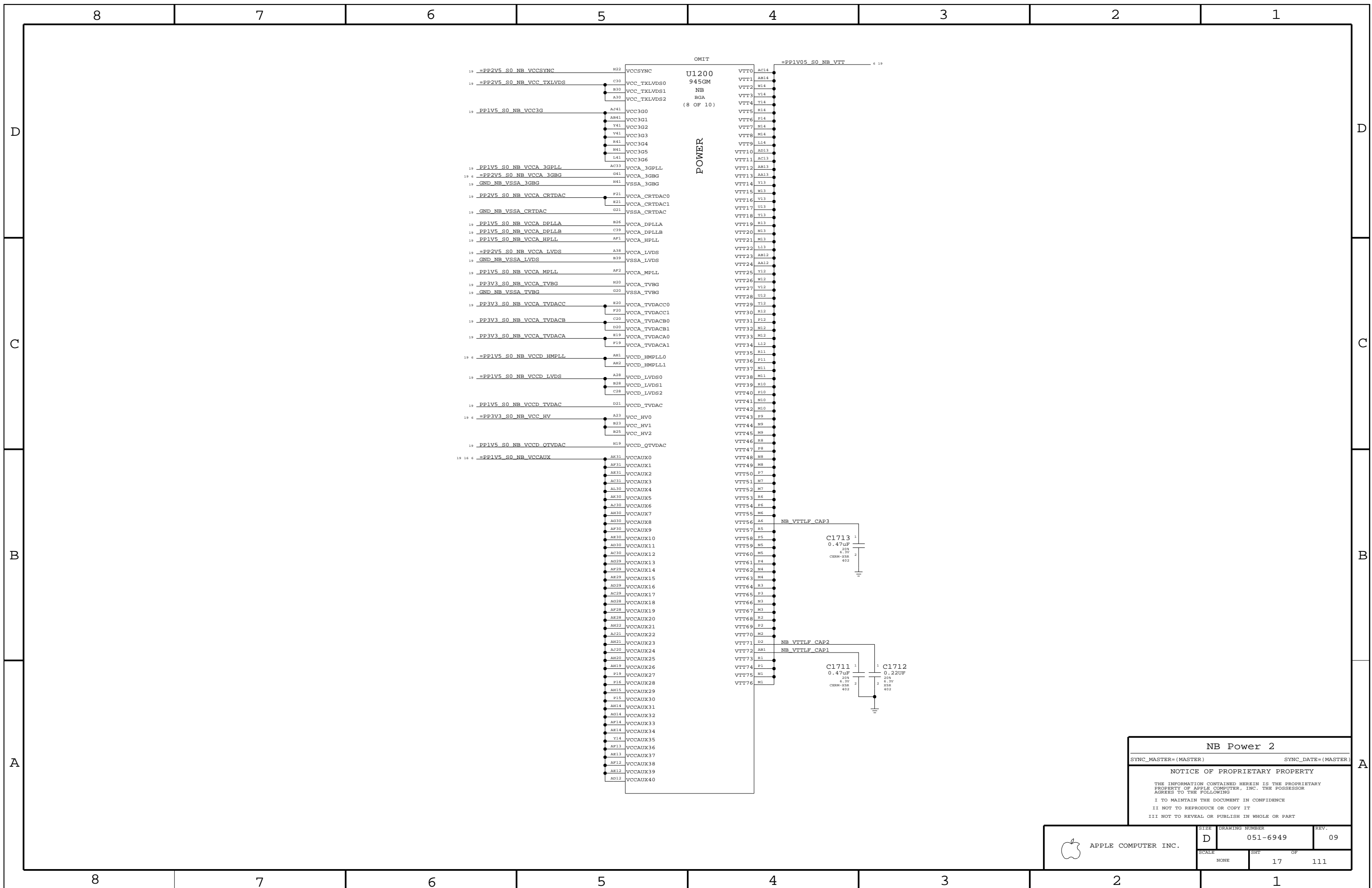
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	DRAWING NUMBER		REV.
	D	051-6949	09
SCALE		SHT	OF
NONE		15	111



NB Power 2

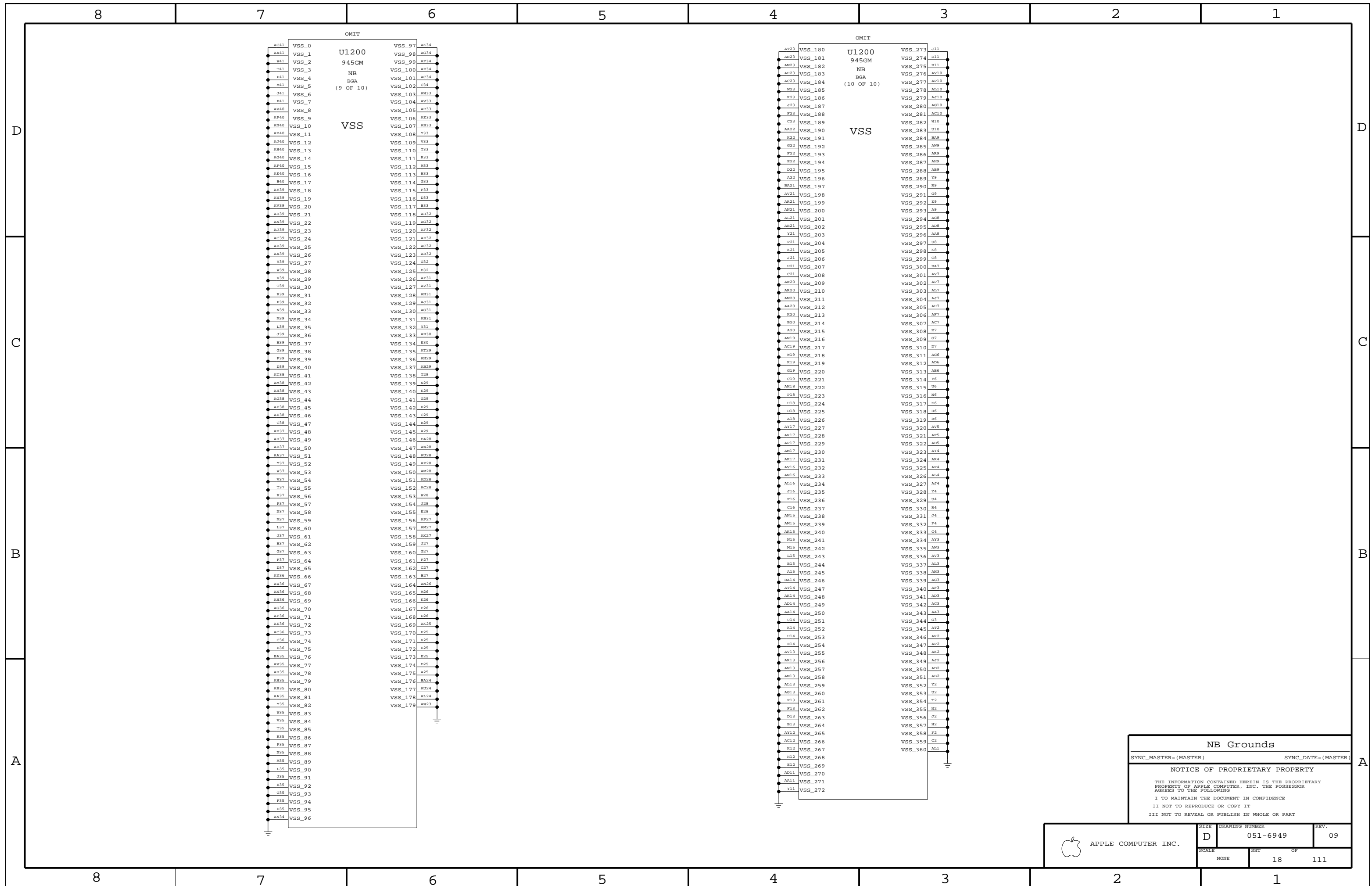
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NONE	17	111	



NB Grounds

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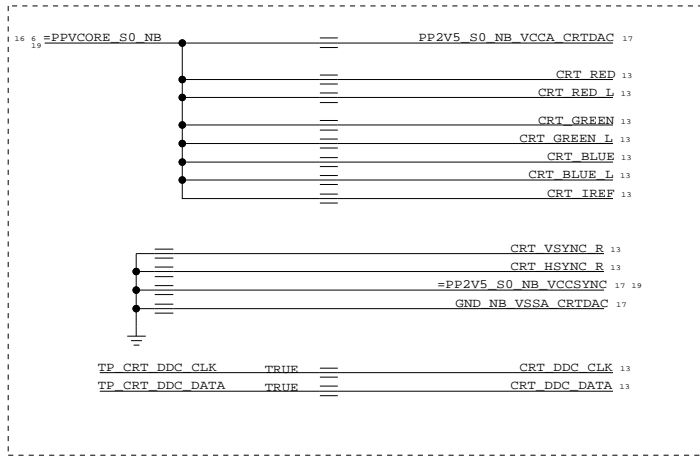
APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6949	REV. 09
	SCALE NONE	SHEET 18	OF 111

Power Interface

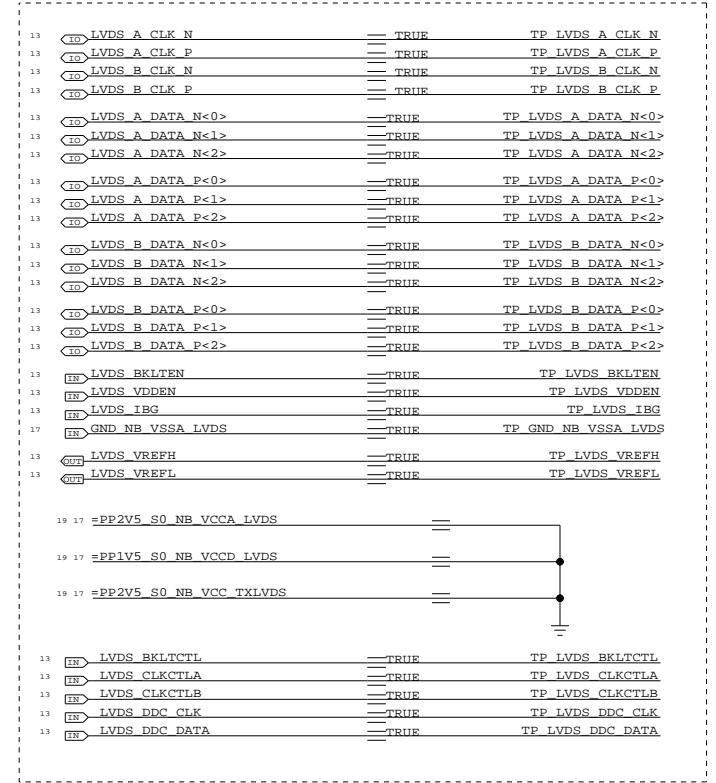
These are the power signals that leave the NB "block"

PP1V05_S0_FSB_NB	5 6 12
PPVCORE_S0_NB	6 16 19
PP1V05_S0_NB	6 16
PP1V05_S0_NB_VTT	6 17 19
PP1V5_S0_NB	6 19
PP1V5_S0_NB_PCIE	6 13
PP1V5_S0_NB_PLL	6 19
PP1V5_S0_NB_TVDAC	6 19
PP1V5_S0_NB_VCCD_HMPLL	6 19
PP1V5_S0_NB_VCCD_HV	6 17 19
PP1V5_S0_NB_VCCAUX	6 16 17 19
PP1V8_S3_MEM_NB	6 14 16 19
PP2V5_S0_NB_VCCSYNCR	17 19
PP2V5_S0_NB_VCC_TXLVDS	17 19
PP2V5_S0_NB_VCCA_3GBG	6 17 19
PP2V5_S0_NB_VCCA_LVDS	17 19
PP3V3_S0_NB	6 14 20
PP3V3_S0_NB_TVDAC	6
PP3V3_S0_NB_VCC_HV	6 17 19

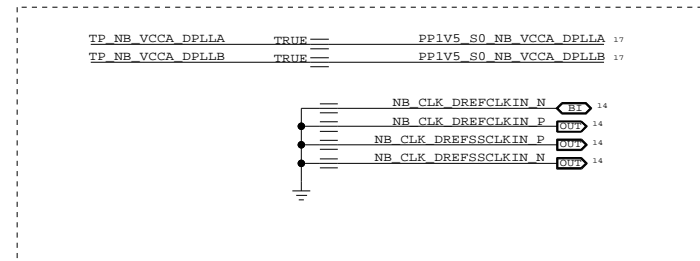
TVOUT DISABLE



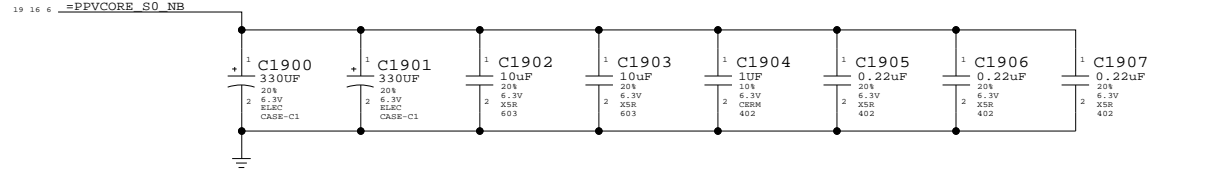
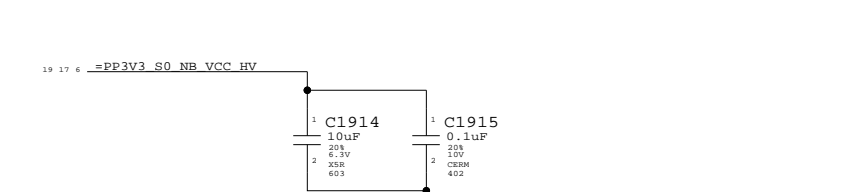
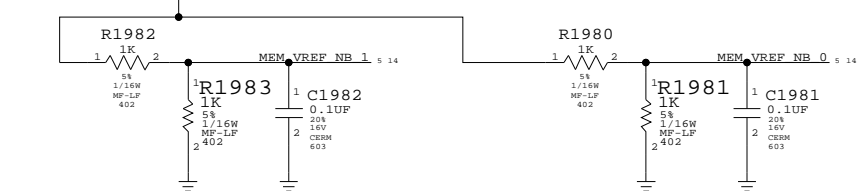
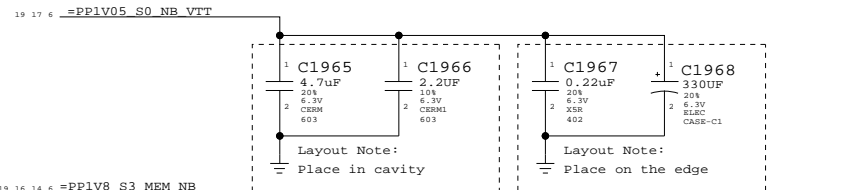
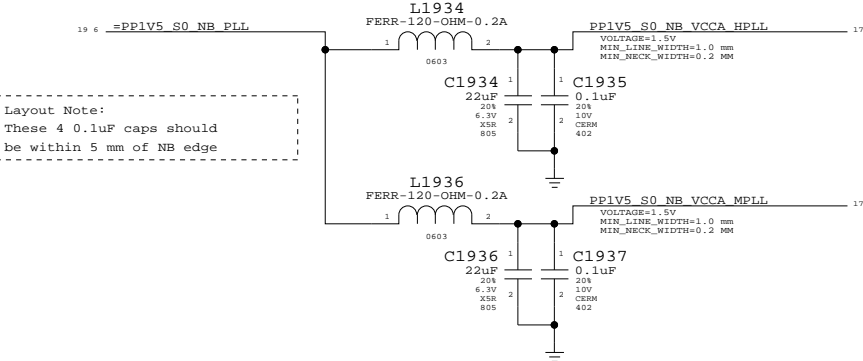
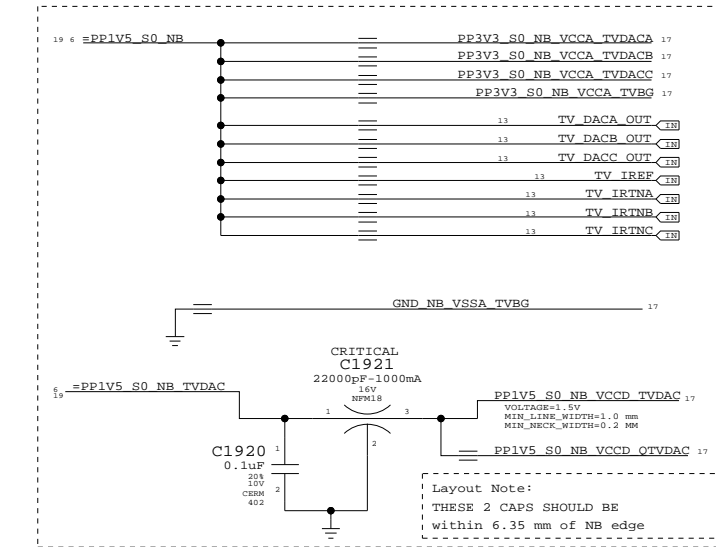
LVDS DISABLE



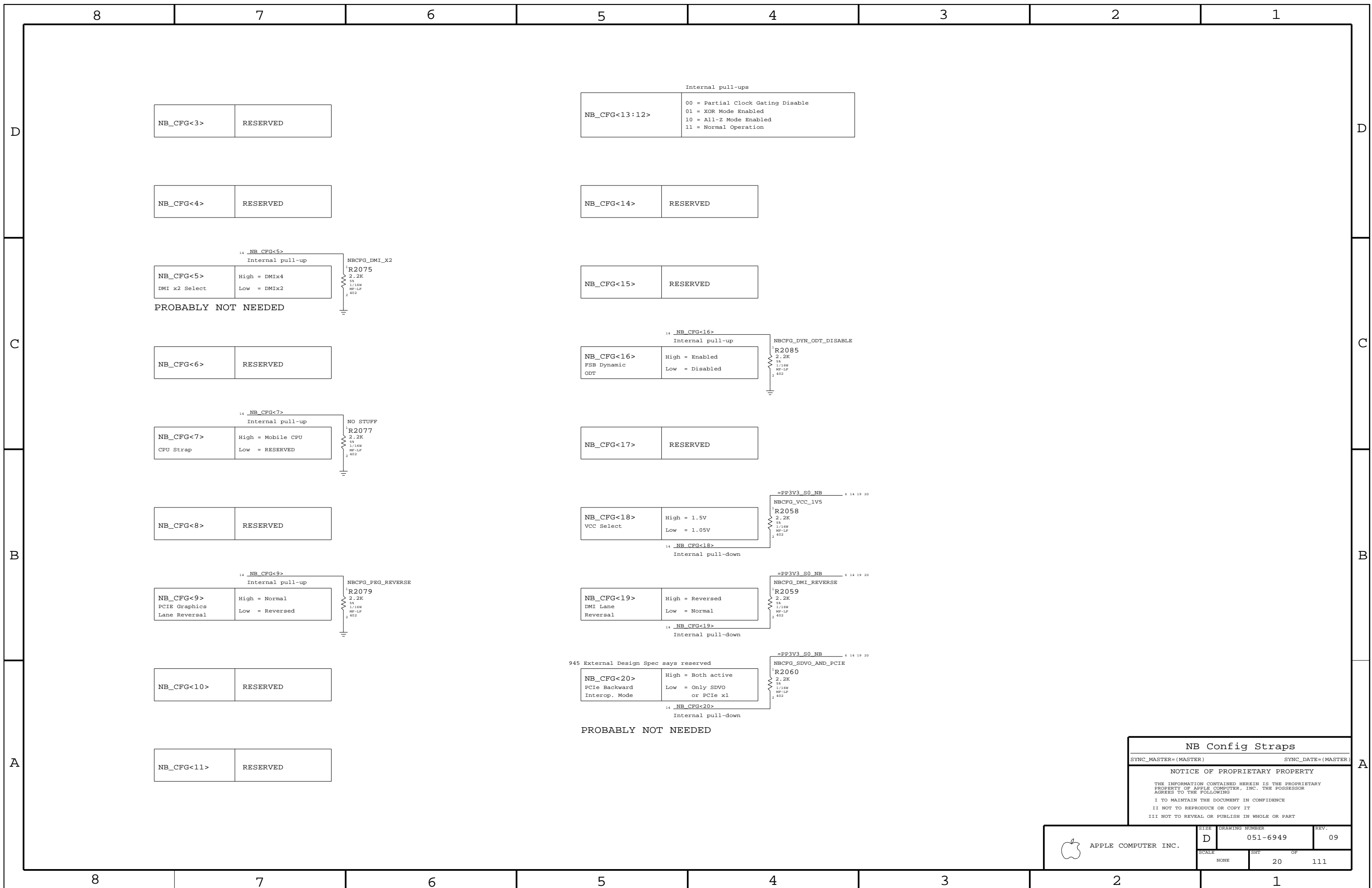
DISPLAY DISABLE



TVOUT DISABLE



NB (GM) Decoupling
 SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)
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NB_CFG<3>	RESERVED
-----------	----------

NB_CFG<13:12>	Internal pull-ups 00 = Partial Clock Gating Disable 01 = XOR Mode Enabled 10 = All-Z Mode Enabled 11 = Normal Operation
---------------	---

NB_CFG<4>	RESERVED
-----------	----------

NB_CFG<14>	RESERVED
------------	----------

14 NB_CFG<5> Internal pull-up	
NB_CFG<5>	High = DMIX4 DMI x2 Select Low = DMIX2

PROBABLY NOT NEEDED

NB_CFG<15>	RESERVED
------------	----------

NB_CFG<6>	RESERVED
-----------	----------

14 NB_CFG<16> Internal pull-up	
NB_CFG<16>	High = Enabled FSB Dynamic ODT Low = Disabled

14 NB_CFG<7> Internal pull-up	
NB_CFG<7>	High = Mobile CPU CPU Strap Low = RESERVED

NO STUFF

NB_CFG<17>	RESERVED
------------	----------

NB_CFG<8>	RESERVED
-----------	----------

=PP3V3_S0_NB 6 14 19 20 NBCFG_VCC_LV5	
NB_CFG<18>	High = 1.5V VCC Select Low = 1.05V
14 NB_CFG<18> Internal pull-down	

14 NB_CFG<9> Internal pull-up	
NB_CFG<9>	High = Normal PCIe Graphics Lane Reversal Low = Reversed

=PP3V3_S0_NB 6 14 19 20 NBCFG_DMI_REVERSE	
NB_CFG<19>	High = Reversed DMI Lane Reversal Low = Normal
14 NB_CFG<19> Internal pull-down	

NB_CFG<10>	RESERVED
------------	----------

945 External Design Spec says reserved =PP3V3_S0_NB 6 14 19 20 NBCFG_SDVO_AND_PCIE	
NB_CFG<20>	High = Both active PCIe Backward Interop. Mode Low = Only SDVO or PCIe x1
14 NB_CFG<20> Internal pull-down	

NB_CFG<11>	RESERVED
------------	----------

PROBABLY NOT NEEDED

NB Config Straps

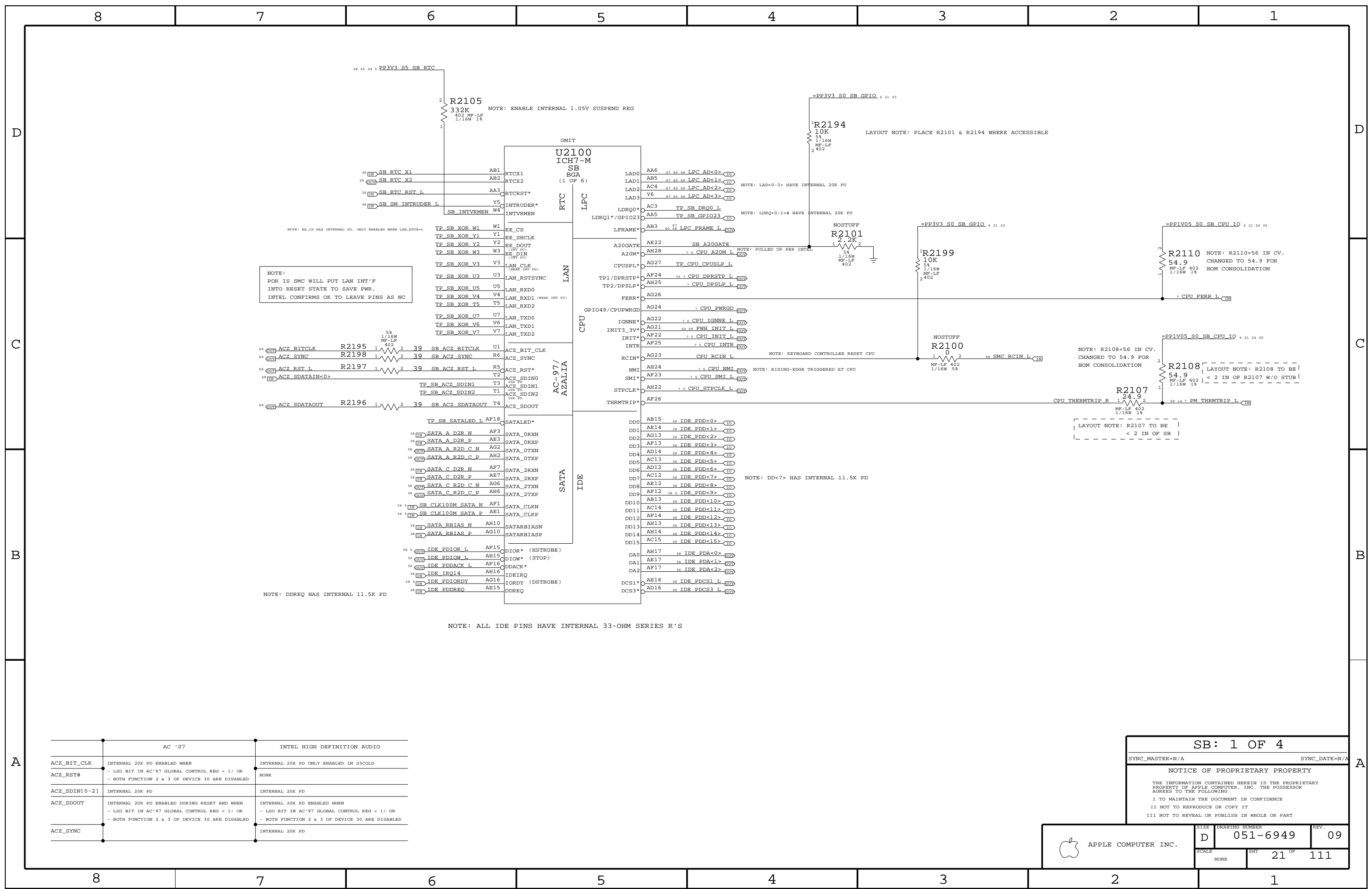
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SCALE	SHT	OF	
NONE	20	111	



NOTE:
 POR IS SMC WILL PUT LAN INT'F
 INTO RESET STATE TO SAVE PWR.
 INTEL CONFIRMS OK TO LEAVE PINS AS NC

NOTE: DDREQ HAS INTERNAL 11.5K PD

NOTE: ALL IDE PINS HAVE INTERNAL 33-OHM SERIES R'S

AC '07	INTEL HIGH DEFINITION AUDIO
ACZ_BIT_CLK	INTERNAL 20K PD ENABLED WHEN - LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR
ACZ_RST#	NONE
ACZ_SDIN[0-2]	INTERNAL 20K PD
ACZ_SDOUT	INTERNAL 20K PD ENABLED DURING RESET AND WHEN - LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR - BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED
ACZ_SYNC	INTERNAL 20K PD

SB: 1 OF 4

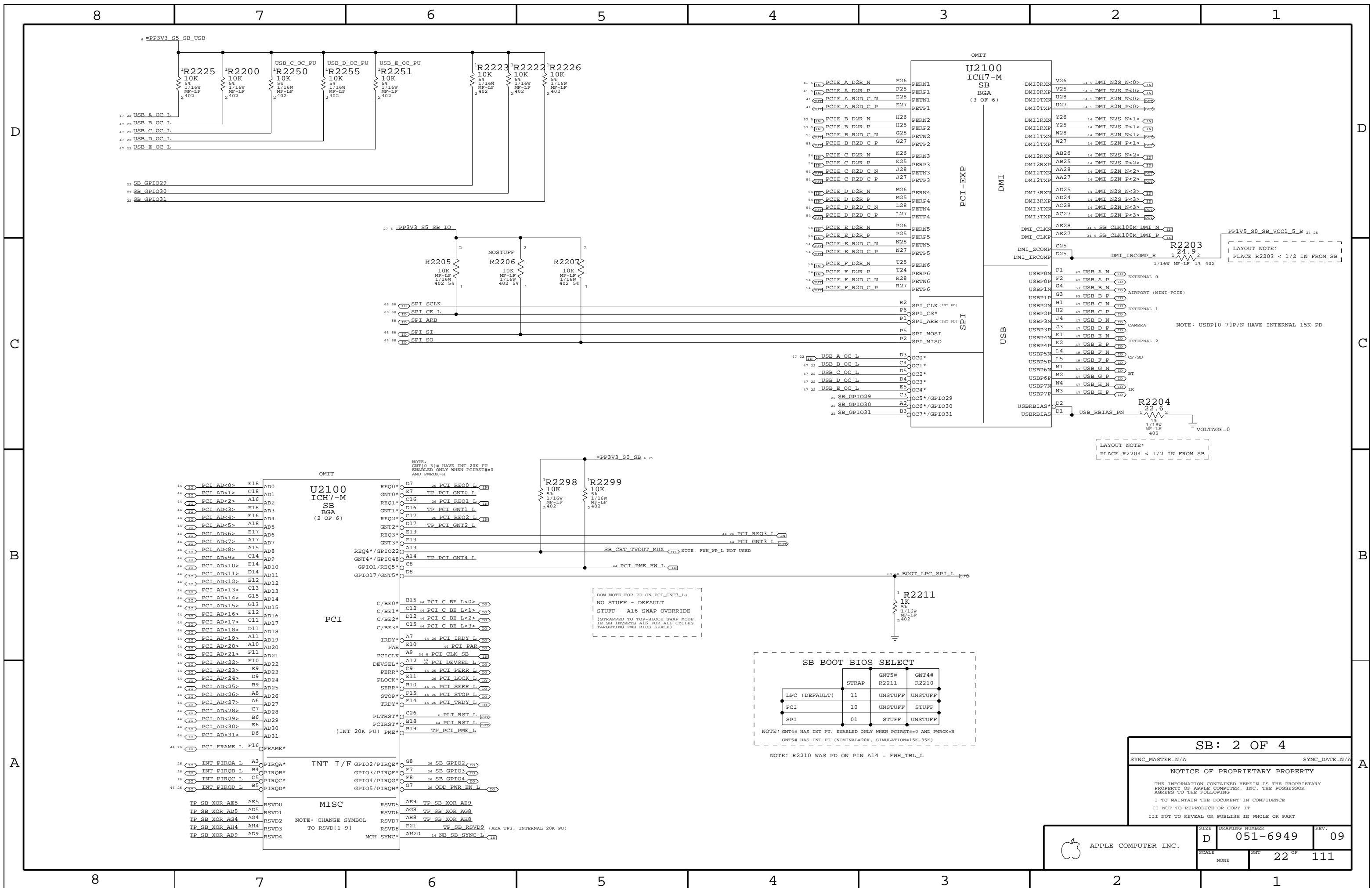
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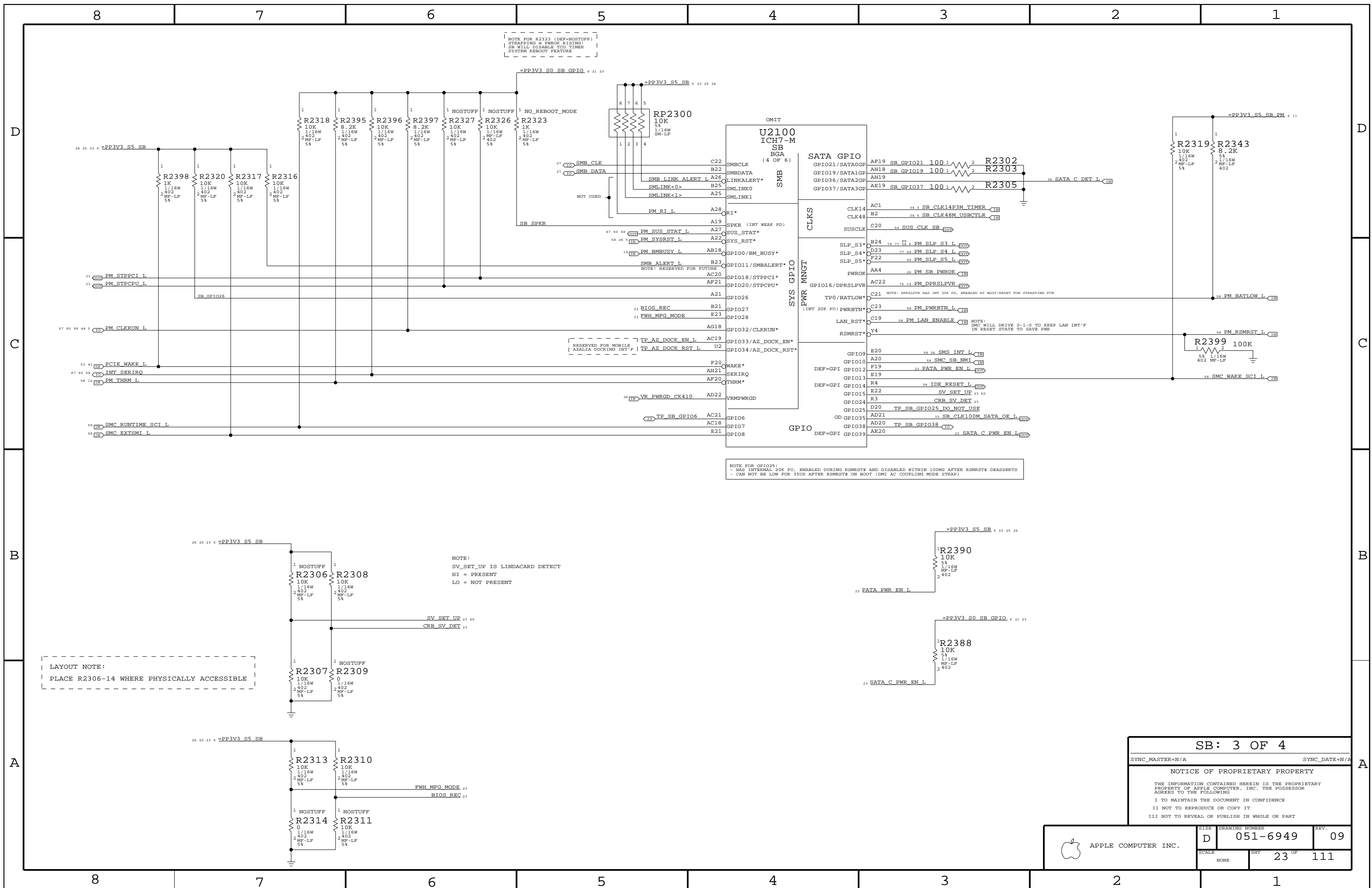
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	D	051-6949	09
SCALE	SHT	21 OF	111
NONE			





NOTE FOR R2323 (DEF-NOSTUFF)
STRAPPING # PWROK RISING:
SB WILL DISABLE TOO TIMER
SYSTEM REBOOT FEATURE

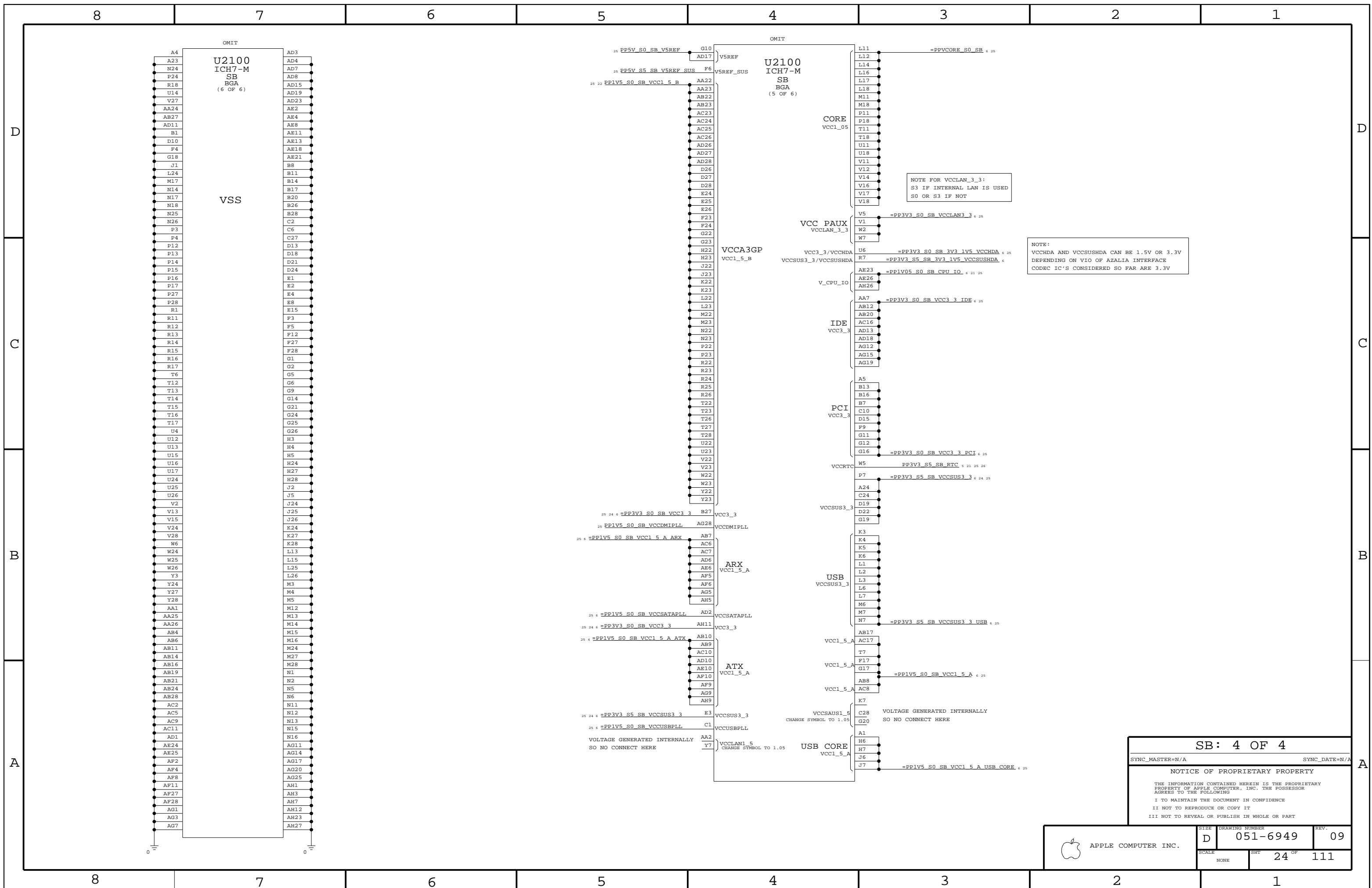
NOTE FOR GPIO25:
- HAS INTERNAL 20K PU, ENABLED DURING RSMRST# AND DISABLED WITHIN 100MS AFTER RSMRST# DEASSERTS
- CAN NOT BE LOW FOR 35US AFTER RSMRST# ON BOOT (DMI AC COUPLING MODE STRAP)

LAYOUT NOTE:
PLACE R2306-14 WHERE PHYSICALLY ACCESSIBLE

NOTE:
SV_SET_UP IS LINDACARD DETECT
HI = PRESENT
LO = NOT PRESENT

SB: 3 OF 4
SYNC_MASTER=N/A SYNC_DATE=N/A
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	D	051-6949	09
SCALE	NONE	SHT	23 OF 111



NOTE FOR VCCLAN_3_3:
S3 IF INTERNAL LAN IS USED
S0 OR S3 IF NOT

NOTE:
VCCCHA AND VCCSUSCHA CAN BE 1.5V OR 3.3V
DEPENDING ON VIO OF AZALIA INTERFACE
CODEC IC'S CONSIDERED SO FAR ARE 3.3V

SB: 4 OF 4

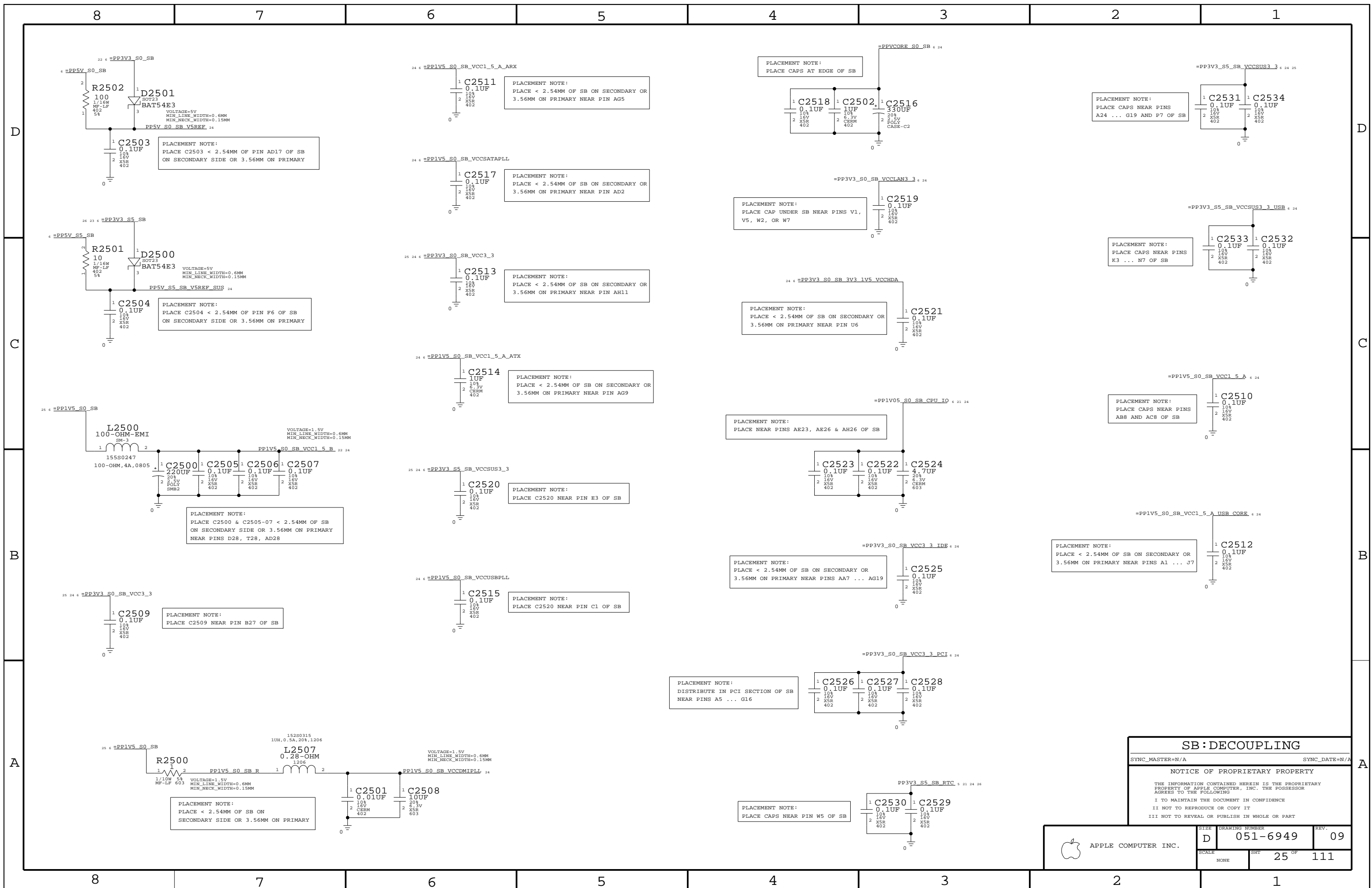
SYNC_MASTER=N/A SYNC_DATE=N/A

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	D	051-6949	09
SCALE	SHT	REV.	
NONE	24 OF 111		



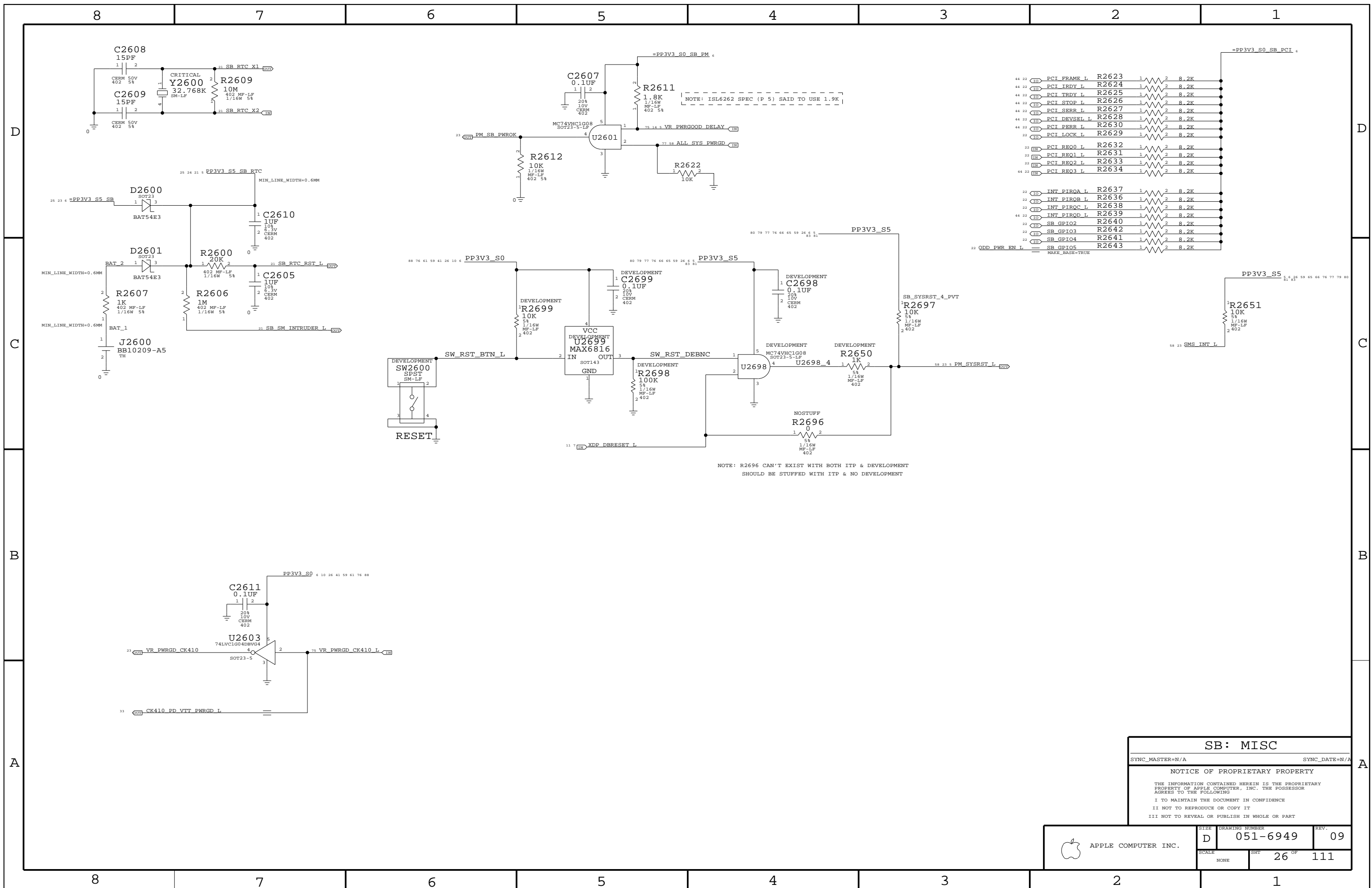
SB: DECOUPLING

SYNC_MASTER=N/A SYNC_DATE=N/A

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SCALE	SHT	25 OF	111
NONE			



SB: MISC

SYNC_MASTER=N/A SYNC_DATE=N/A

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	SCALE NONE	SHEET 26 OF 111	

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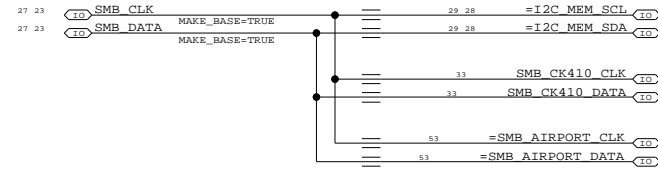
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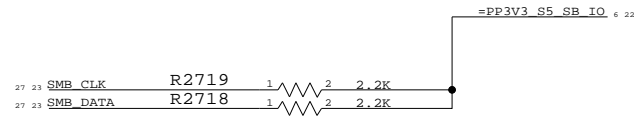
D

SB I2C BUSSES



C

C



B


B

A

A

SB: SMB HUB

SYNC_MASTER=N/A SYNC_DATE=N/A
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	SCALE NONE	SHEET 27 OF	TOTAL SHEETS 111

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Page Notes

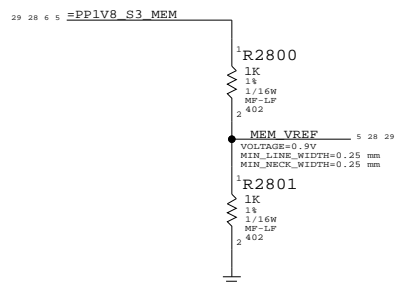
Power aliases required by this page:
 - =PPIV8_S3_MEM
 - =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:
 - =I2C_MEM_SCL
 - =I2C_MEM_SDA

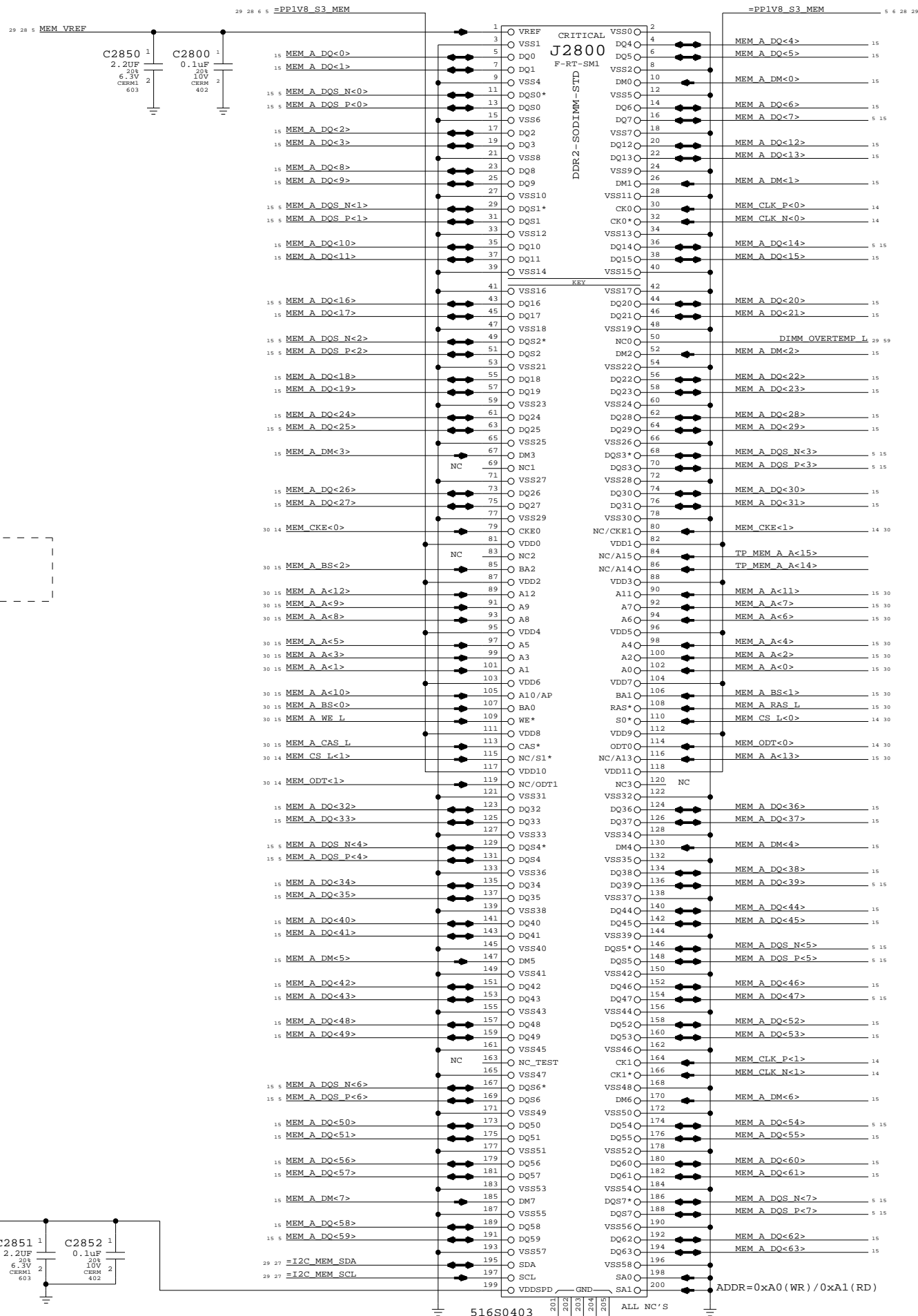
BOM options provided by this page:
 (NONE)

DDR2 VRef

One 0.1uF per connector

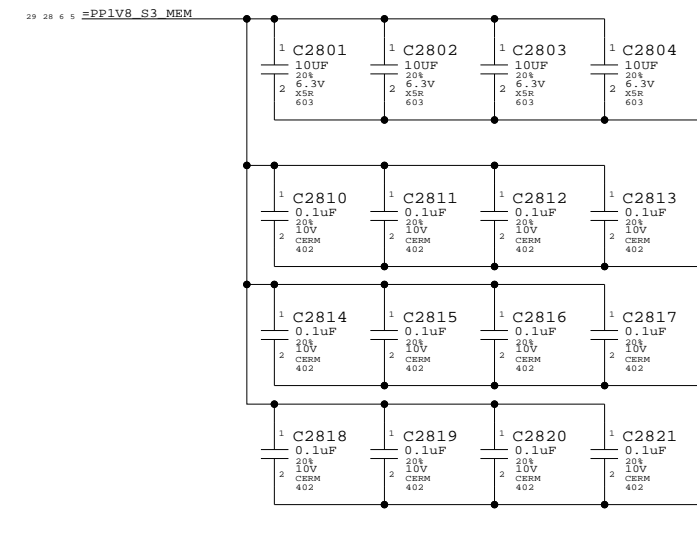


Yellow uses 10K divider and TLV2463 to drive MCH and DIMM connectors. (See Capell Valley pg 47)



DDR2 Bypass Caps

(For return current)



DDR2 SO-DIMM Connector A
 SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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	D	051-6949	09
SCALE	SHT	OF	
NONE	28	111	

Page Notes

Power aliases required by this page:
 - =PP1V8_S3_MEM
 - =PPSPD_S0_MEM (2.5V - 3.3V)

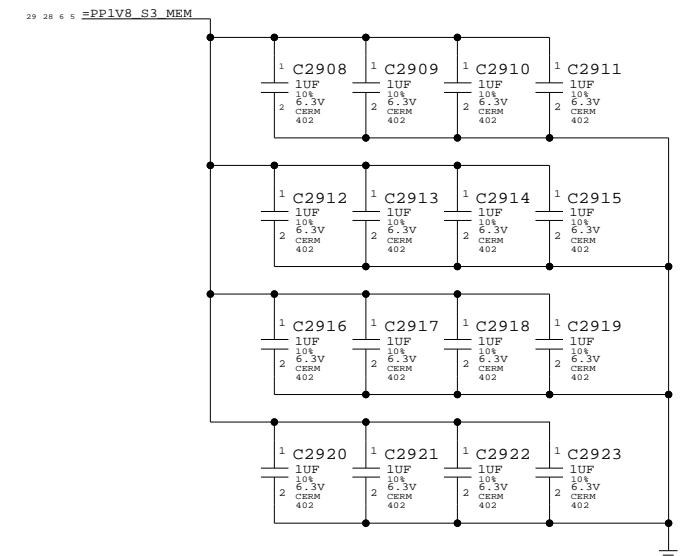
Signal aliases required by this page:
 - =I2C_MEM_SCL
 - =I2C_MEM_SDA

BOM options provided by this page:
 (NONE)

NOTE: This page does not supply VREF.
 The reference voltage must be provided by another page.



DDR2 Bypass Caps (For return current)



DDR2 SO-DIMM Connector B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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	SHT	OF	
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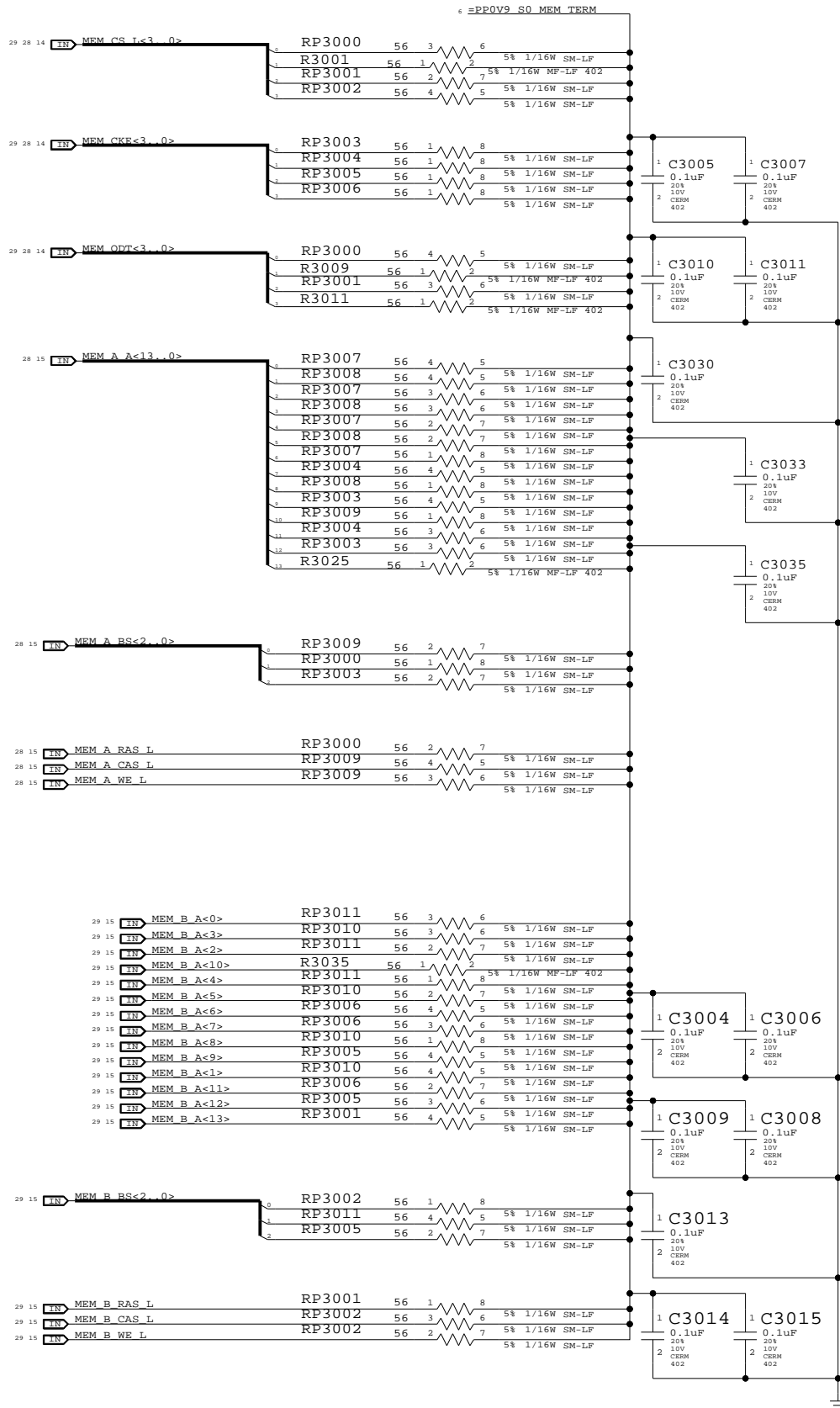
4

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1

One cap for each side of every RPAK, one cap for every two discrete resistors
BOMOPTION shown at the top of each group applies to every part below it



Memory Active Termination

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SCALE	SHT		OF
NONE	30		111

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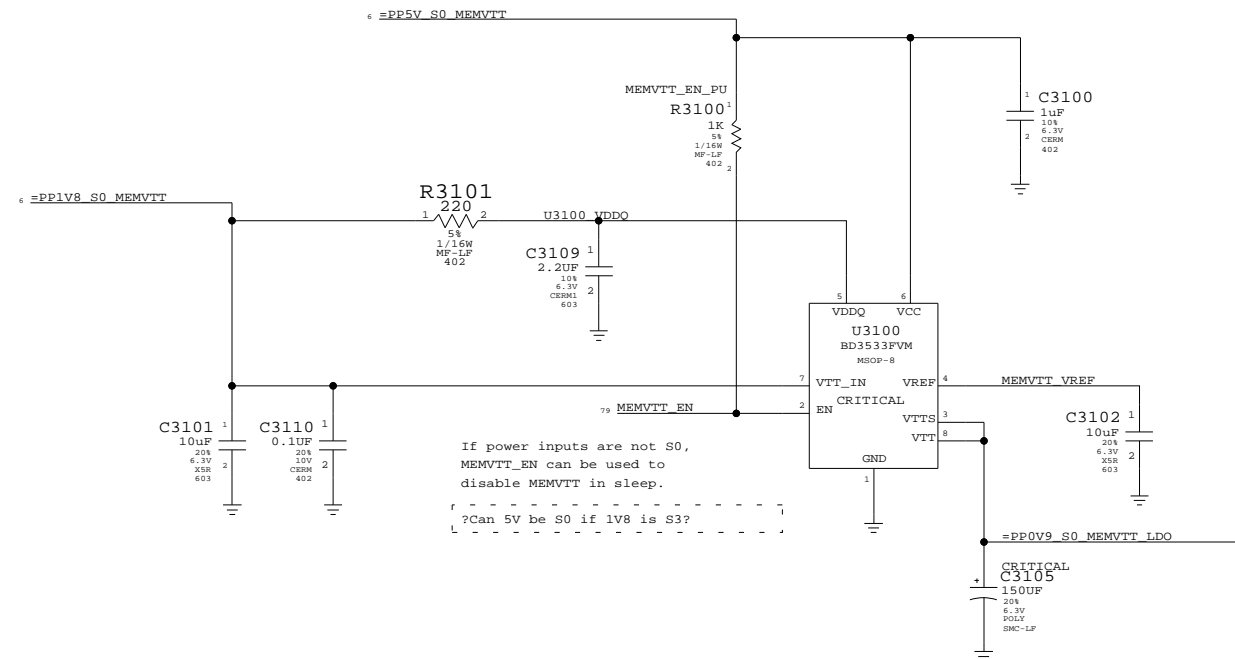
Page Notes

Power aliases required by this page:
 - =PP5V_S0_MEMVTT
 - =PP1V8_S0_MEMVTT
 - =PP0V9_S0_MEMVTT_LDO

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)

DDR2 Vtt Regulator



Memory Vtt Supply

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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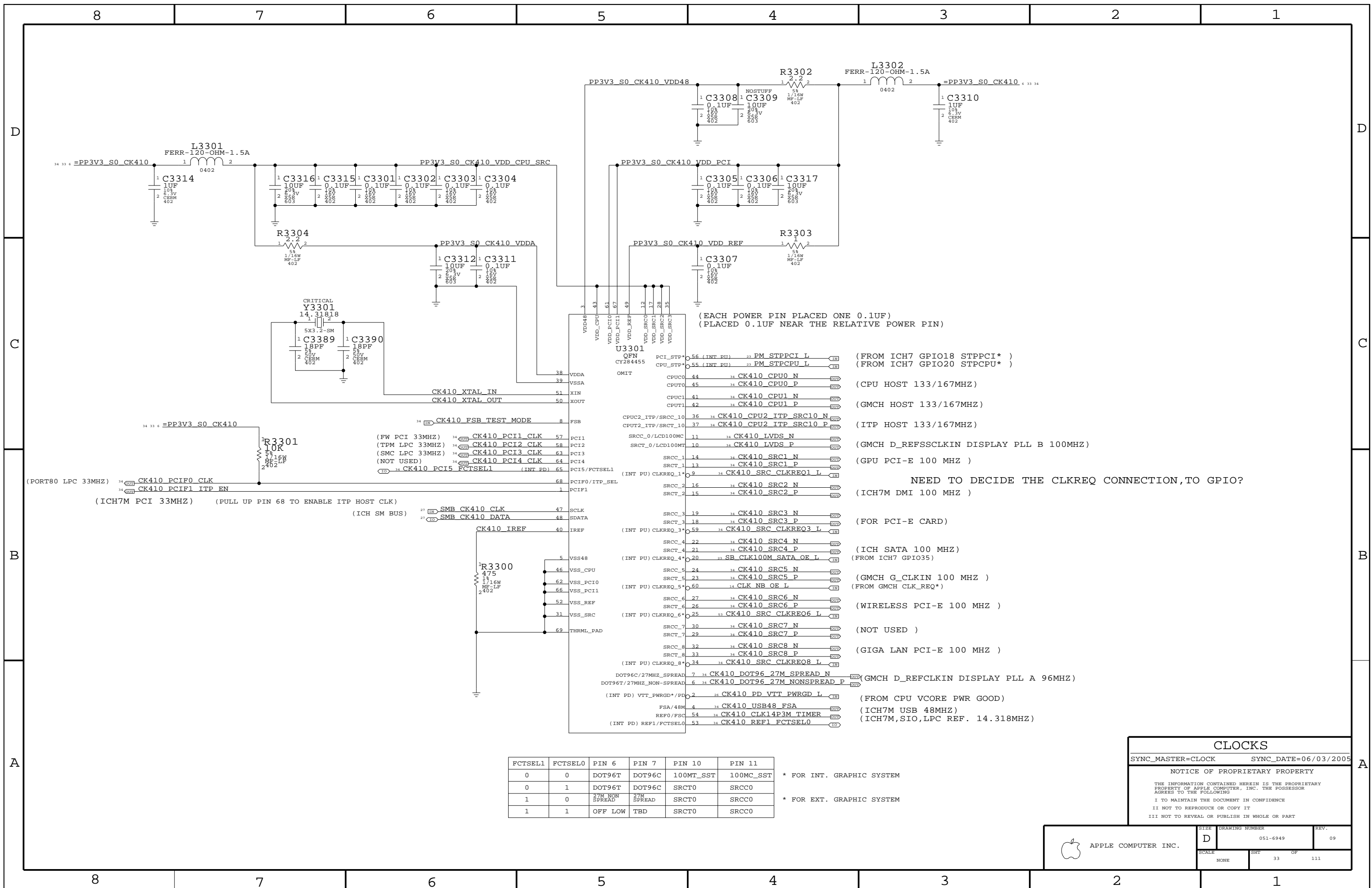
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	D	051-6949	09
SCALE		SHT	OF
NONE		31	111



- (EACH POWER PIN PLACED ONE 0.1UF)
(PLACED 0.1UF NEAR THE RELATIVE POWER PIN)
- 56 (INT PU) PM_STPPCI_L (FROM ICH7 GPIO18 STPPCI*)
 - 55 (INT PU) PM_STPCPU_L (FROM ICH7 GPIO20 STPCPU*)
 - 44 CK410_CPU0_N (CPU HOST 133/167MHZ)
 - 45 CK410_CPU0_P
 - 41 CK410_CPU1_N (GMCH HOST 133/167MHZ)
 - 42 CK410_CPU1_P
 - 36 CK410_CPU2_ITP_SRC10_N (ITP HOST 133/167MHZ)
 - 37 CK410_CPU2_ITP_SRC10_P
 - 11 CK410_LVDS_N (GMCH D_REFSSCLKIN DISPLAY PLL B 100MHZ)
 - 10 CK410_LVDS_P
 - 14 CK410_SRC1_N (GPU PCI-E 100 MHZ)
 - 13 CK410_SRC1_P
 - 9 CK410_SRC_CLKREQ1_L (NEED TO DECIDE THE CLKREQ CONNECTION, TO GPIO?)
 - 16 CK410_SRC2_N (ICH7M DMI 100 MHZ)
 - 15 CK410_SRC2_P
 - 19 CK410_SRC3_N (FOR PCI-E CARD)
 - 18 CK410_SRC3_P
 - 59 CK410_SRC_CLKREQ3_L
 - 22 CK410_SRC4_N (ICH SATA 100 MHZ)
 - 21 CK410_SRC4_P (FROM ICH7 GPIO35)
 - 20 SB_CLK100M_SATA_OE_L
 - 24 CK410_SRC5_N (GMCH G_CLKIN 100 MHZ)
 - 23 CK410_SRC5_P (FROM GMCH CLK_REQ*)
 - 60 CLK_NB_OE_L
 - 27 CK410_SRC6_N (WIRELESS PCI-E 100 MHZ)
 - 26 CK410_SRC6_P
 - 25 CK410_SRC_CLKREQ6_L
 - 30 CK410_SRC7_N (NOT USED)
 - 29 CK410_SRC7_P
 - 32 CK410_SRC8_N (GIGA LAN PCI-E 100 MHZ)
 - 33 CK410_SRC8_P
 - 34 CK410_SRC_CLKREQ8_L
 - 7 CK410_DOT96_27M_SPREAD_N (GMCH D_REFCLKIN DISPLAY PLL A 96MHZ)
 - 6 CK410_DOT96_27M_NONSPREAD_P
 - 2 CK410_PD_VTT_PWRGD_L (FROM CPU VCORE PWR GOOD)
 - 4 CK410_USB48_FSA (ICH7M USB 48MHZ)
 - 54 CK410_CLK14P3M_TIMER (ICH7M, SIO, LPC REF. 14.318MHZ)
 - 53 CK410_REF1_FCTSEL0

FCTSEL1	FCTSEL0	PIN 6	PIN 7	PIN 10	PIN 11
0	0	DOT96T	DOT96C	100MT_SST	100MC_SST
0	1	DOT96T	DOT96C	SRCT0	SRCC0
1	0	27M NON SPREAD	27M SPREAD	SRCT0	SRCC0
1	1	OFF LOW	TBD	SRCT0	SRCC0

* FOR INT. GRAPHIC SYSTEM
* FOR EXT. GRAPHIC SYSTEM

CLOCKS

SYNC_MASTER=CLOCK SYNC_DATE=06/03/2005

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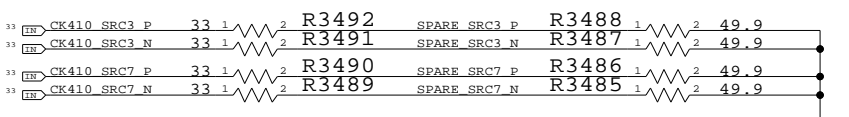
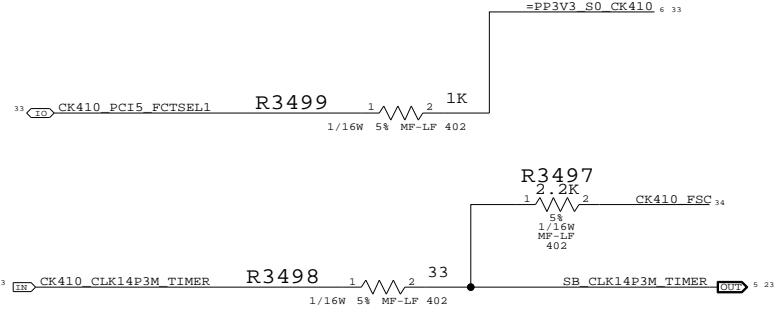
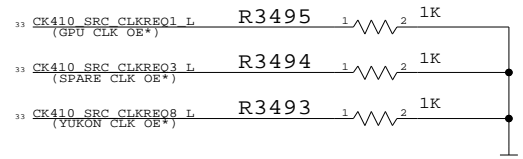
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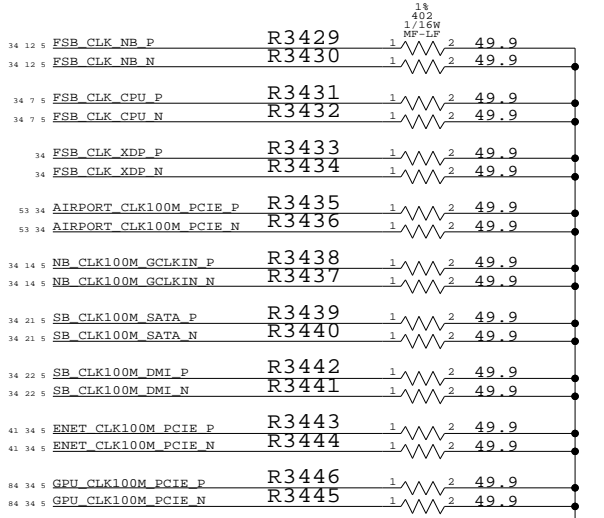
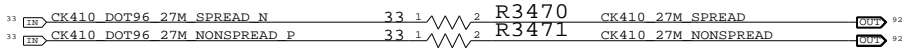
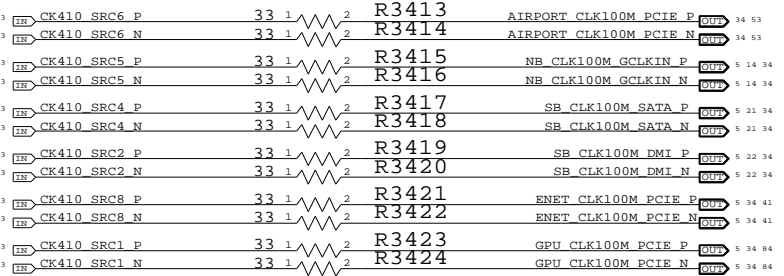
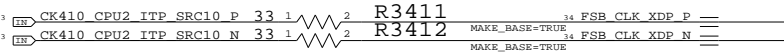
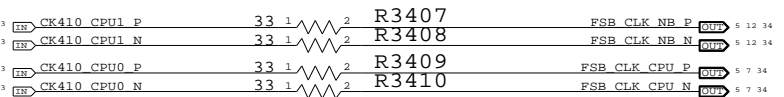
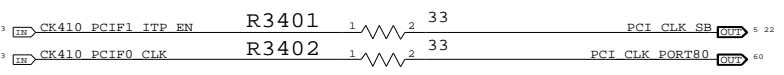
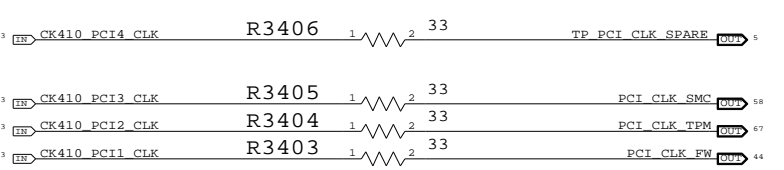
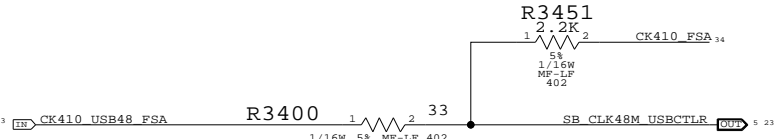
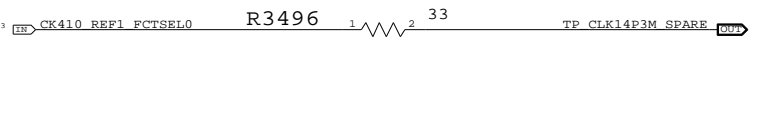
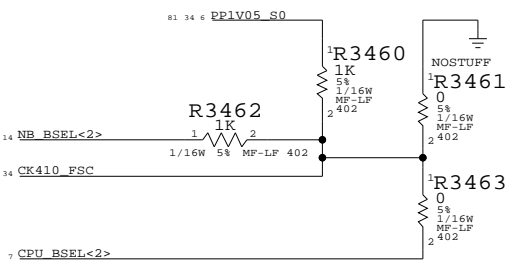
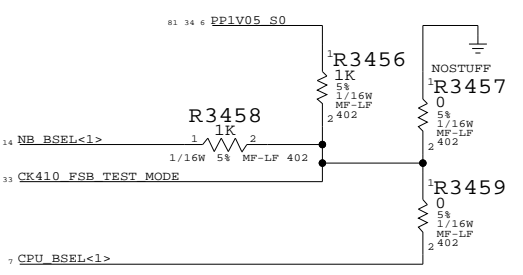
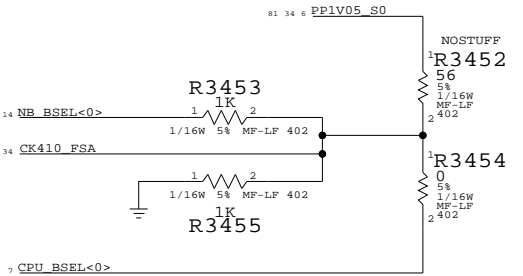
SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	33	111

NOTE: USE THESE PULL-DOWNS IF NOT CONNECTED TO GPIO'S



FSB FREQUENCY SELECT:

	STUFF	NO STUFF
CPU DRIVEN	R3453 R3454 R3455	R3456 R3457
533MHZ (133MHZ CPU CLK)	R3452 R3454 R3455	R3456 R3457
667MHZ (166MHZ CPU CLK)	R3452 R3454 R3455	R3456 R3457



CLOCKS: TERMINATIONS

SYNC_MASTER=N/A SYNC_DATE=N/A

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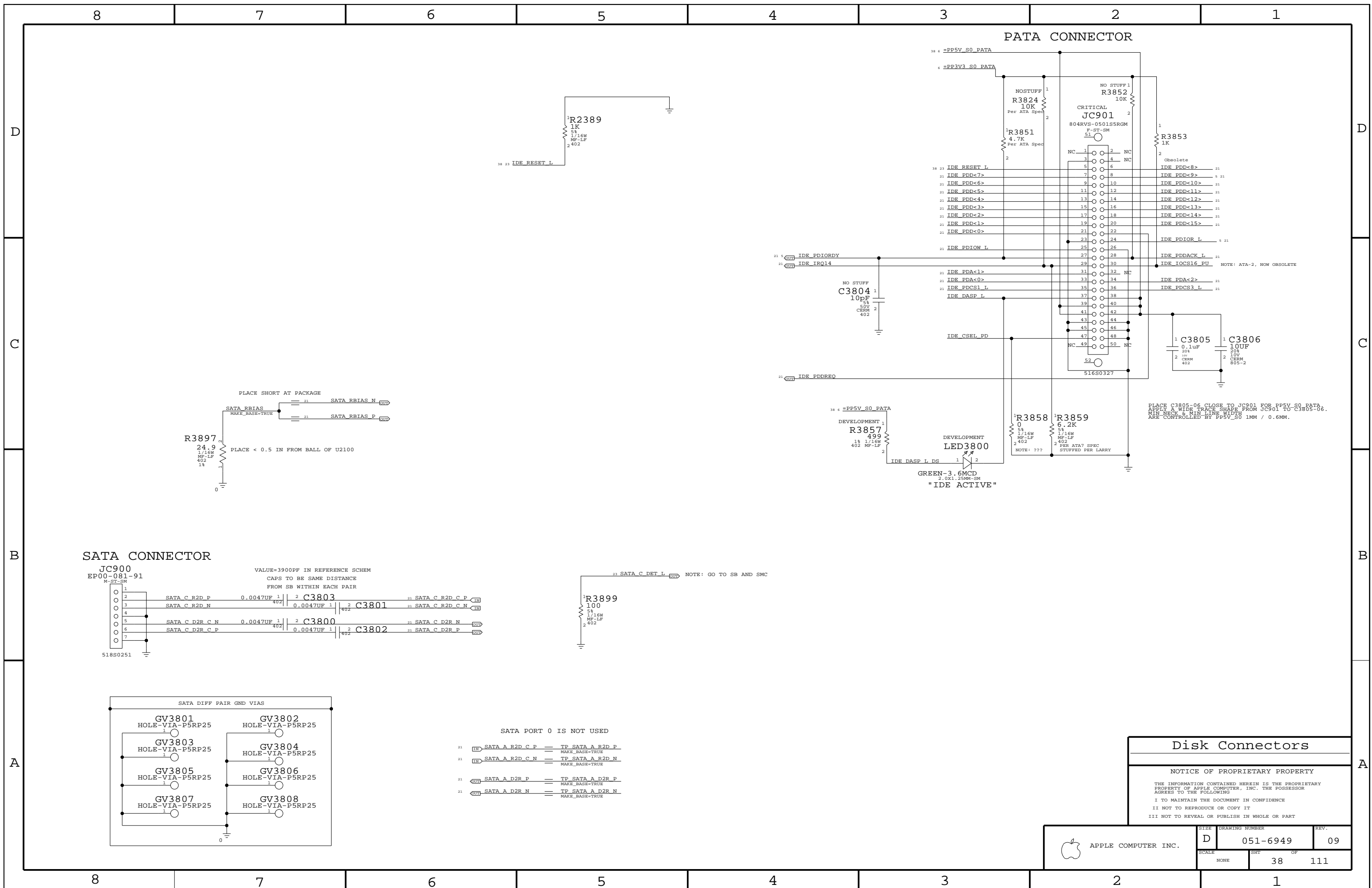
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APPLE COMPUTER INC.

SCALE: NONE SHEET: 34 OF 111

SIZE: D DRAWING NUMBER: 051-6949 REV: 09



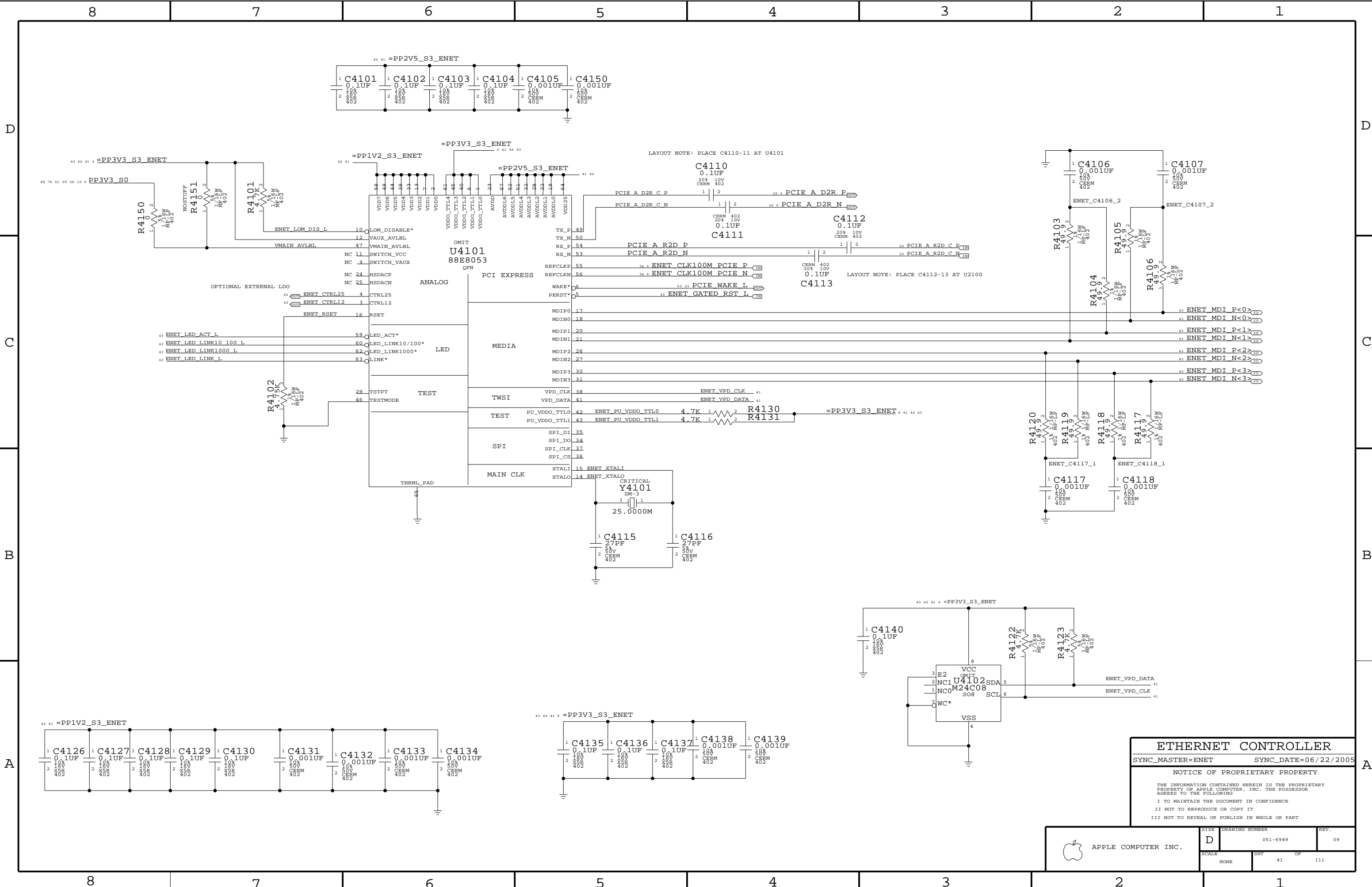
Disk Connectors

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	D	051-6949	09
SCALE	SHT OF		
NONE	38		111



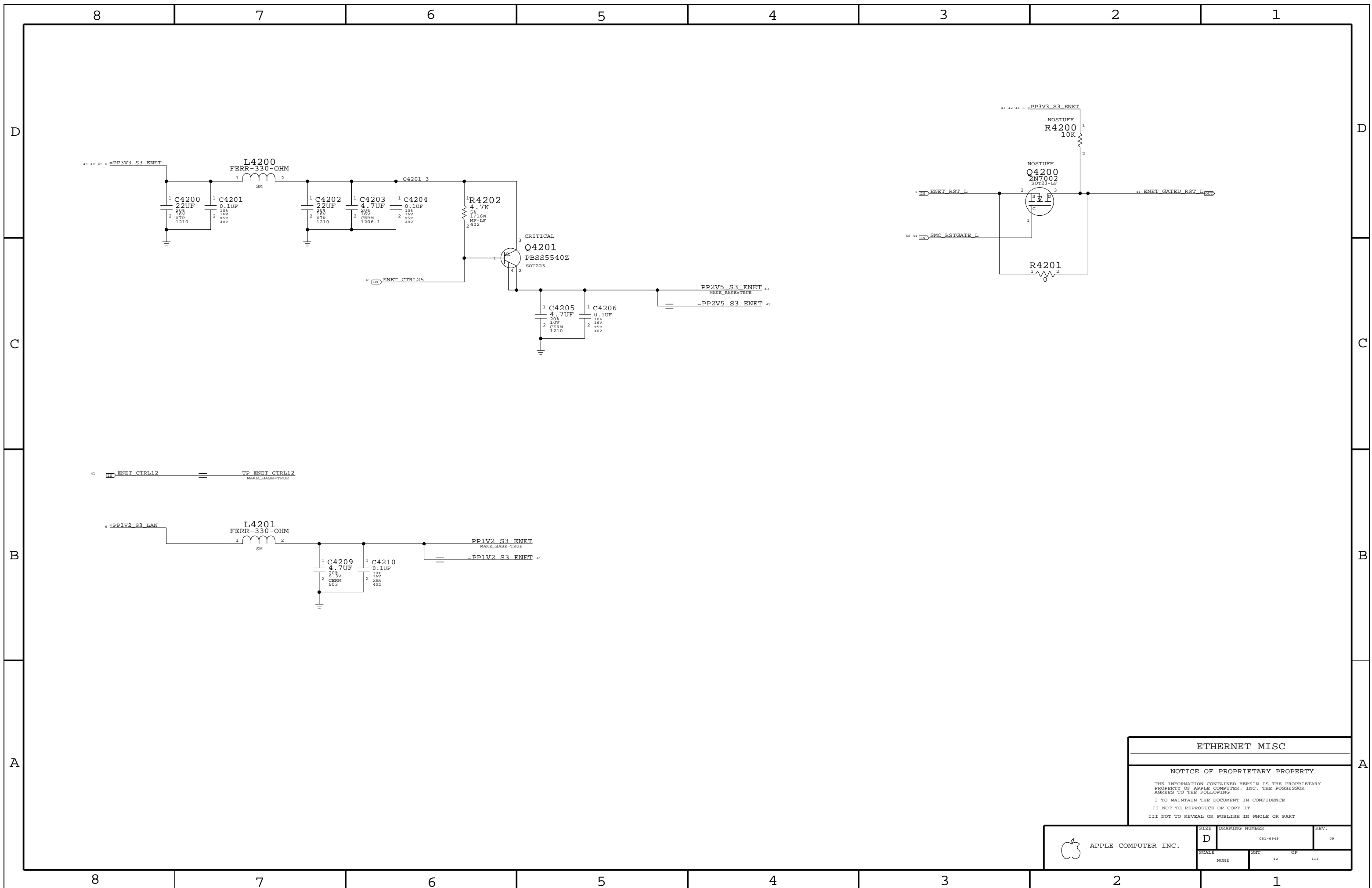
ETHERNET CONTROLLER

SYNC_MASTER=ENET SYNC_DATE=06/22/2005

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	SCALE NONE	SHEET 41	OF 111



ETHERNET MISC

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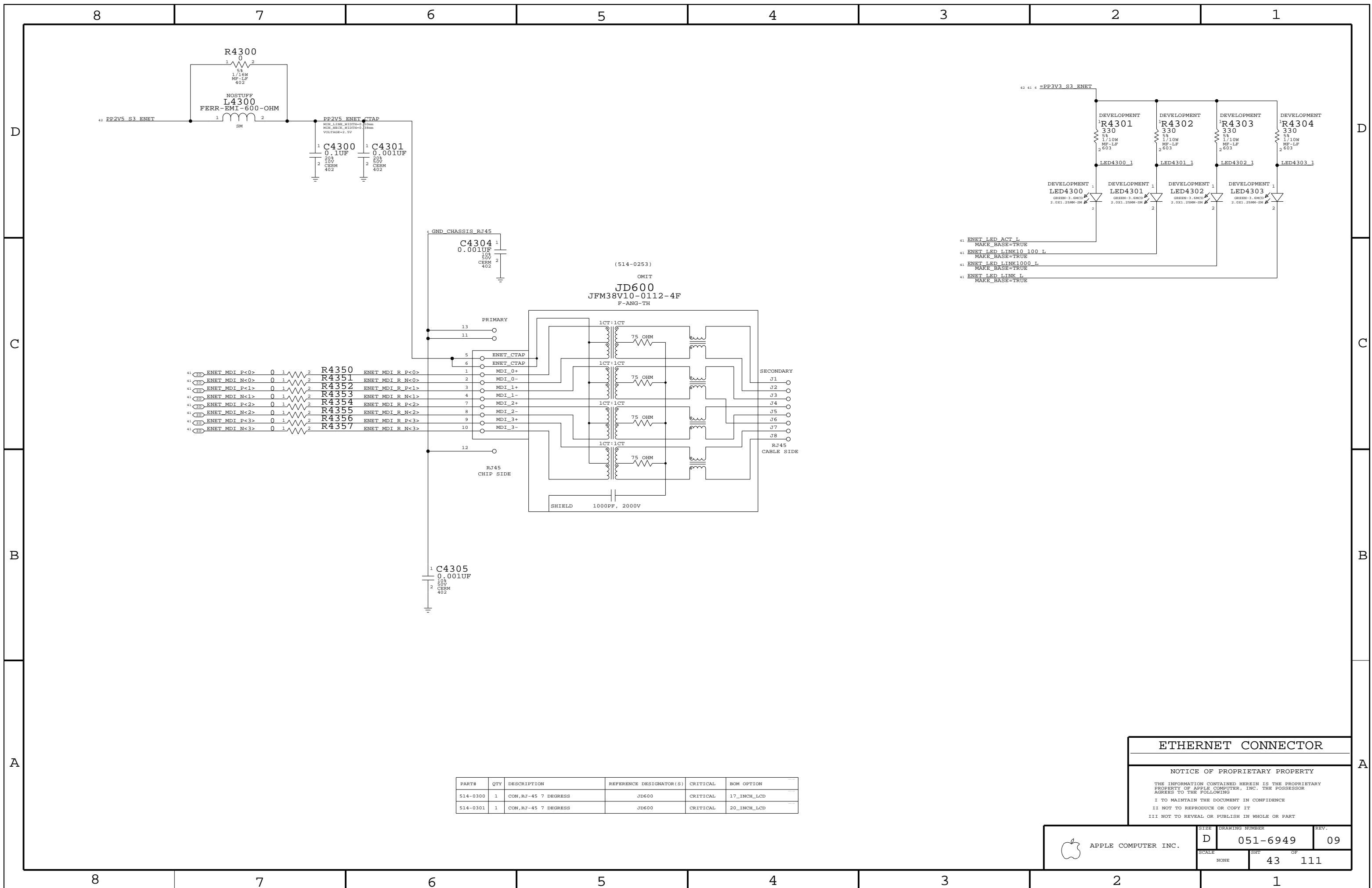
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6949	REV. 09
	SCALE NONE	SHEET 42	OF 111



ENET MDI P<0>	0	1	2	R4350	ENET MDI R P<0>
ENET MDI N<0>	0	1	2	R4351	ENET MDI R N<0>
ENET MDI P<1>	0	1	2	R4352	ENET MDI R P<1>
ENET MDI N<1>	0	1	2	R4353	ENET MDI R N<1>
ENET MDI P<2>	0	1	2	R4354	ENET MDI R P<2>
ENET MDI N<2>	0	1	2	R4355	ENET MDI R N<2>
ENET MDI P<3>	0	1	2	R4356	ENET MDI R P<3>
ENET MDI N<3>	0	1	2	R4357	ENET MDI R N<3>

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0300	1	CON,RJ-45 7 DEGRESS	JD600	CRITICAL	17_INCH_LCD
514-0301	1	CON,RJ-45 7 DEGRESS	JD600	CRITICAL	20_INCH_LCD

ETHERNET CONNECTOR

NOTICE OF PROPRIETARY PROPERTY

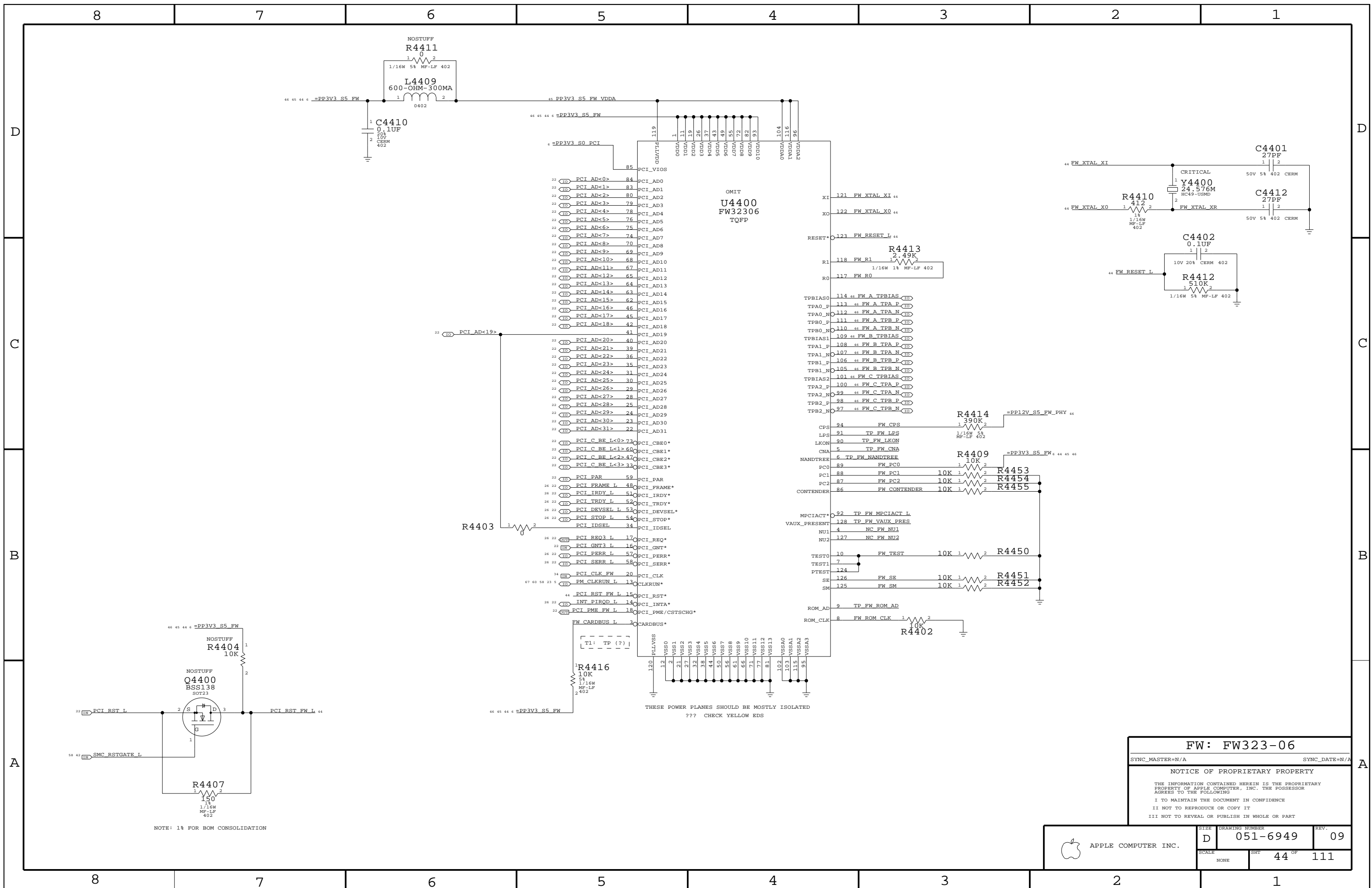
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	D	051-6949	09
SCALE	SHT		OF
NONE	43		111



THESE POWER PLANES SHOULD BE MOSTLY ISOLATED
 ??? CHECK YELLOW EDS

NOTE: 1% FOR BOM CONSOLIDATION

FW: FW323-06

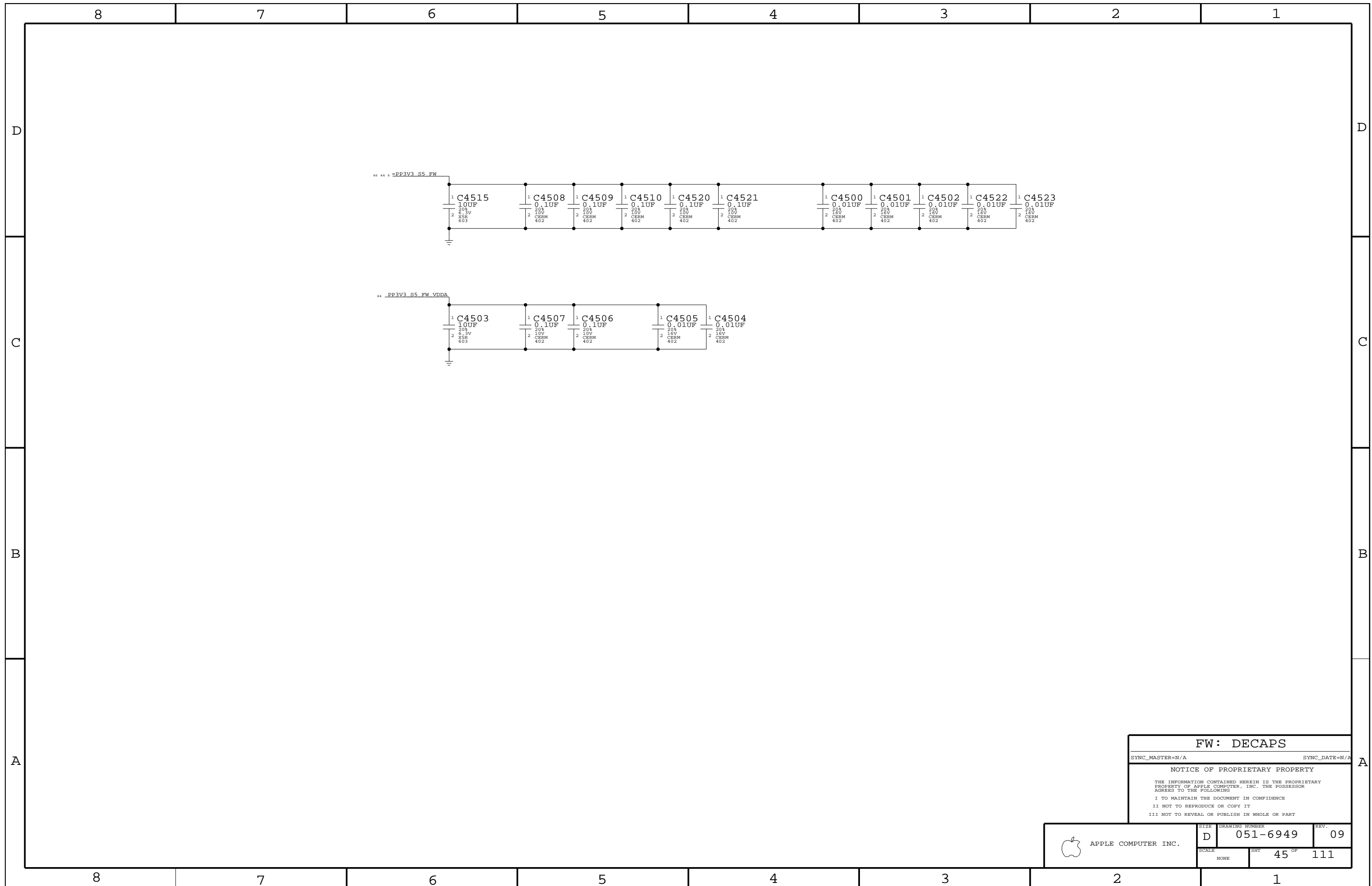
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	SCALE NONE	SHEET 44 OF 111	



FW: DECAPS

SYNC_MASTER=N/A SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

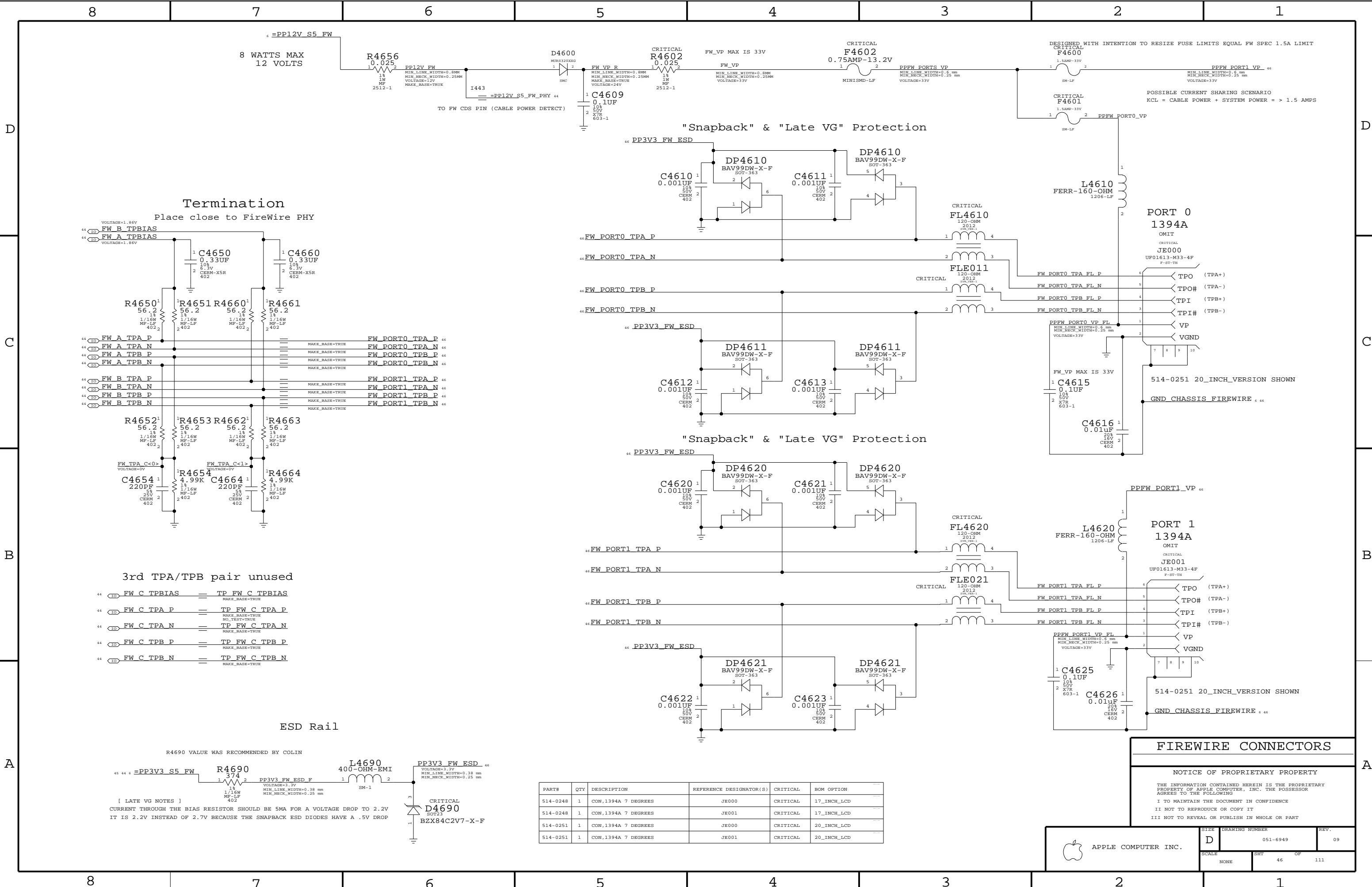
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	D	051-6949	09
SCALE		SHT	OF
NONE		45	111



Termination
Place close to FireWire PHY

"Snapback" & "Late VG" Protection

"Snapback" & "Late VG" Protection

3rd TPA/TPB pair unused

ESD Rail

R4690 VALUE WAS RECOMMENDED BY COLIN

[LATE VG NOTES]
CURRENT THROUGH THE BIAS RESISTOR SHOULD BE 5MA FOR A VOLTAGE DROP TO 2.2V
IT IS 2.2V INSTEAD OF 2.7V BECAUSE THE SNAPBACK ESD DIODES HAVE A .5V DROP

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0248	1	CON, 1394A 7 DEGREES	JE000	CRITICAL	17_INCH_LCD
514-0248	1	CON, 1394A 7 DEGREES	JE001	CRITICAL	17_INCH_LCD
514-0251	1	CON, 1394A 7 DEGREES	JE000	CRITICAL	20_INCH_LCD
514-0251	1	CON, 1394A 7 DEGREES	JE001	CRITICAL	20_INCH_LCD

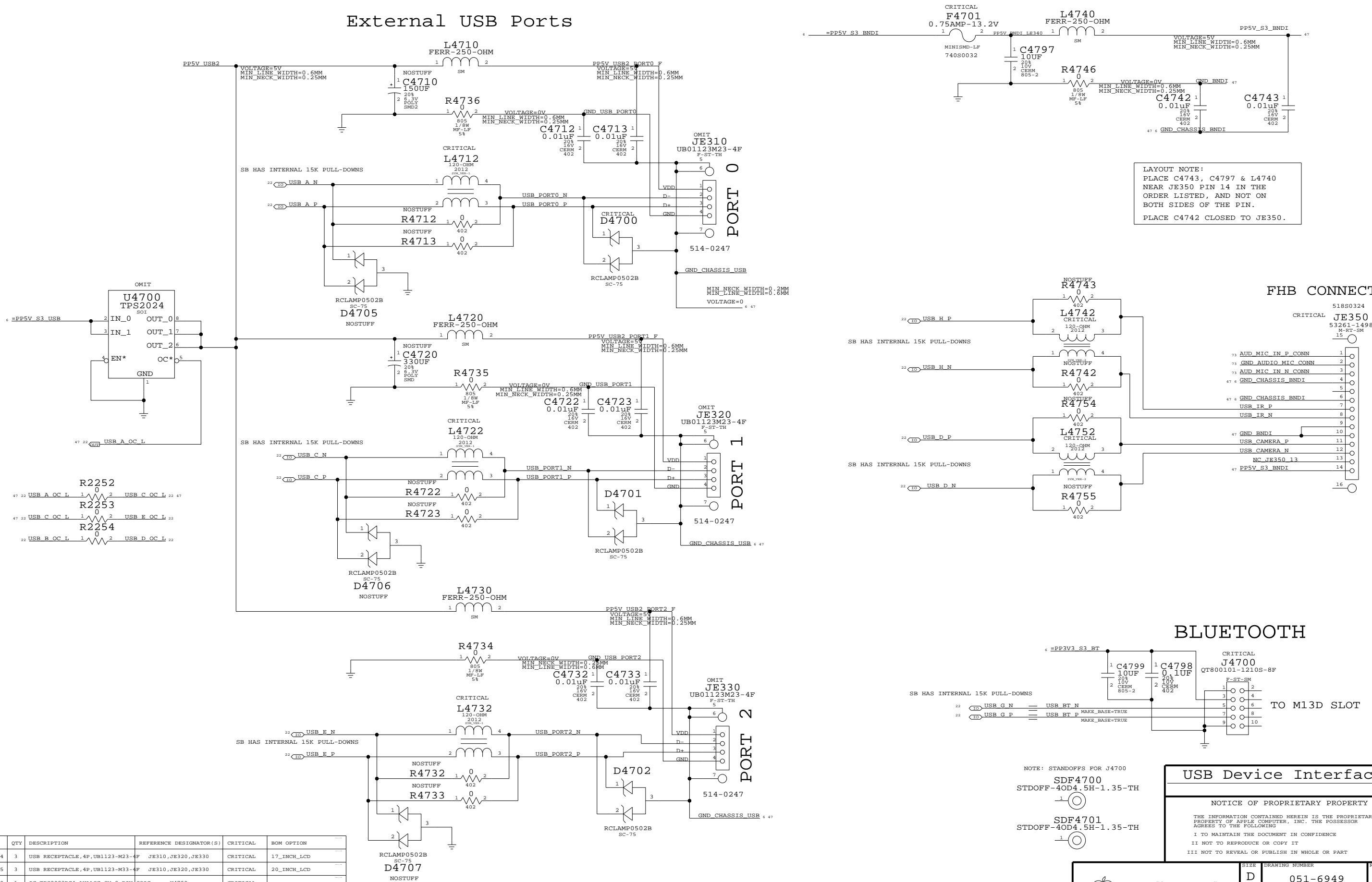
FIREWIRE CONNECTORS

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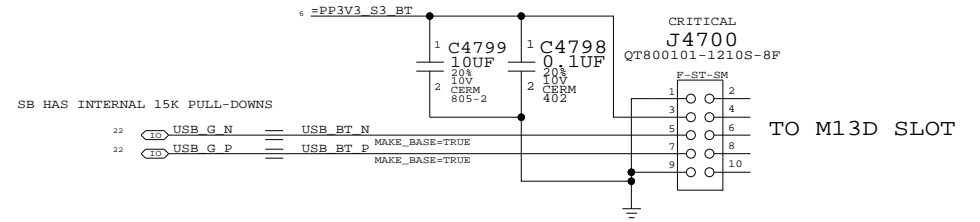
APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6949	REV. 09
	SCALE NONE	SHEET 46	OF 111

External USB Ports



LAYOUT NOTE:
 PLACE C4743, C4797 & L4740
 NEAR JE350 PIN 14 IN THE
 ORDER LISTED, AND NOT ON
 BOTH SIDES OF THE PIN.
 PLACE C4742 CLOSED TO JE350.

BLUETOOTH



NOTE: STANDOFFS FOR J4700
 SDF4700
 STDOFF-40D4.5H-1.35-TH
 SDF4701
 STDOFF-40D4.5H-1.35-TH

USB Device Interfaces

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PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0294	3	USB RECEPTACLE, 4P, UB1123-M23-4F	JE310, JE320, JE330	CRITICAL	17_INCH_LCD
514-0295	3	USB RECEPTACLE, 4P, UB1123-M33-4F	JE310, JE320, JE330	CRITICAL	20_INCH_LCD
35381370	1	IC, TPS2023DG4, ANALOG SW, 8 PIN	SOIC U4700	CRITICAL	

APPLE COMPUTER INC.	SIZE: D	DRAWING NUMBER: 051-6949	REV.: 09
	SCALE: NONE	SHEET: 47	OF: 111

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 APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6949	REV. 09
	SCALE NONE	SHEET 48 OF	TOTAL SHEETS 111

8

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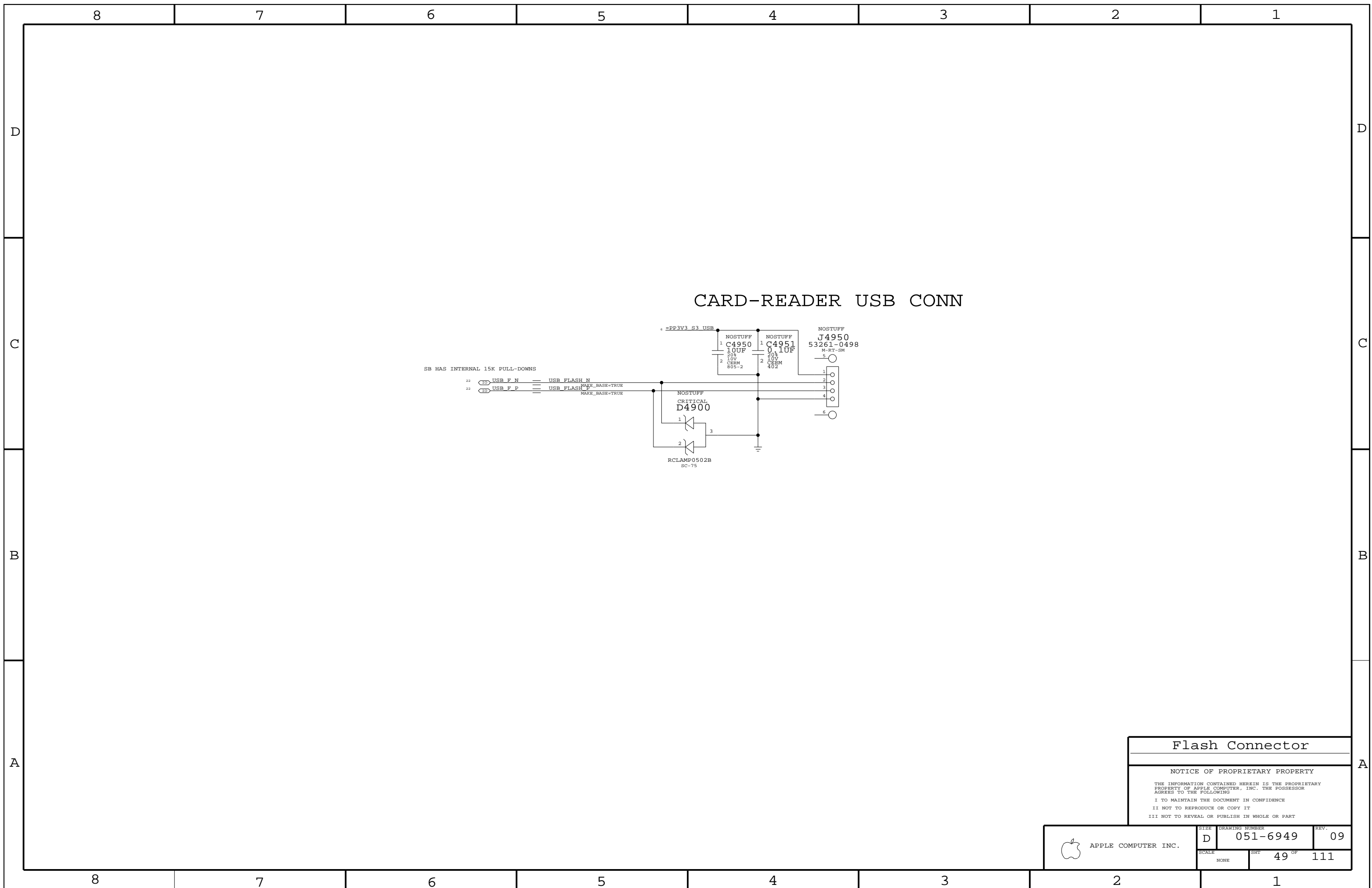
5

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Flash Connector

NOTICE OF PROPRIETARY PROPERTY

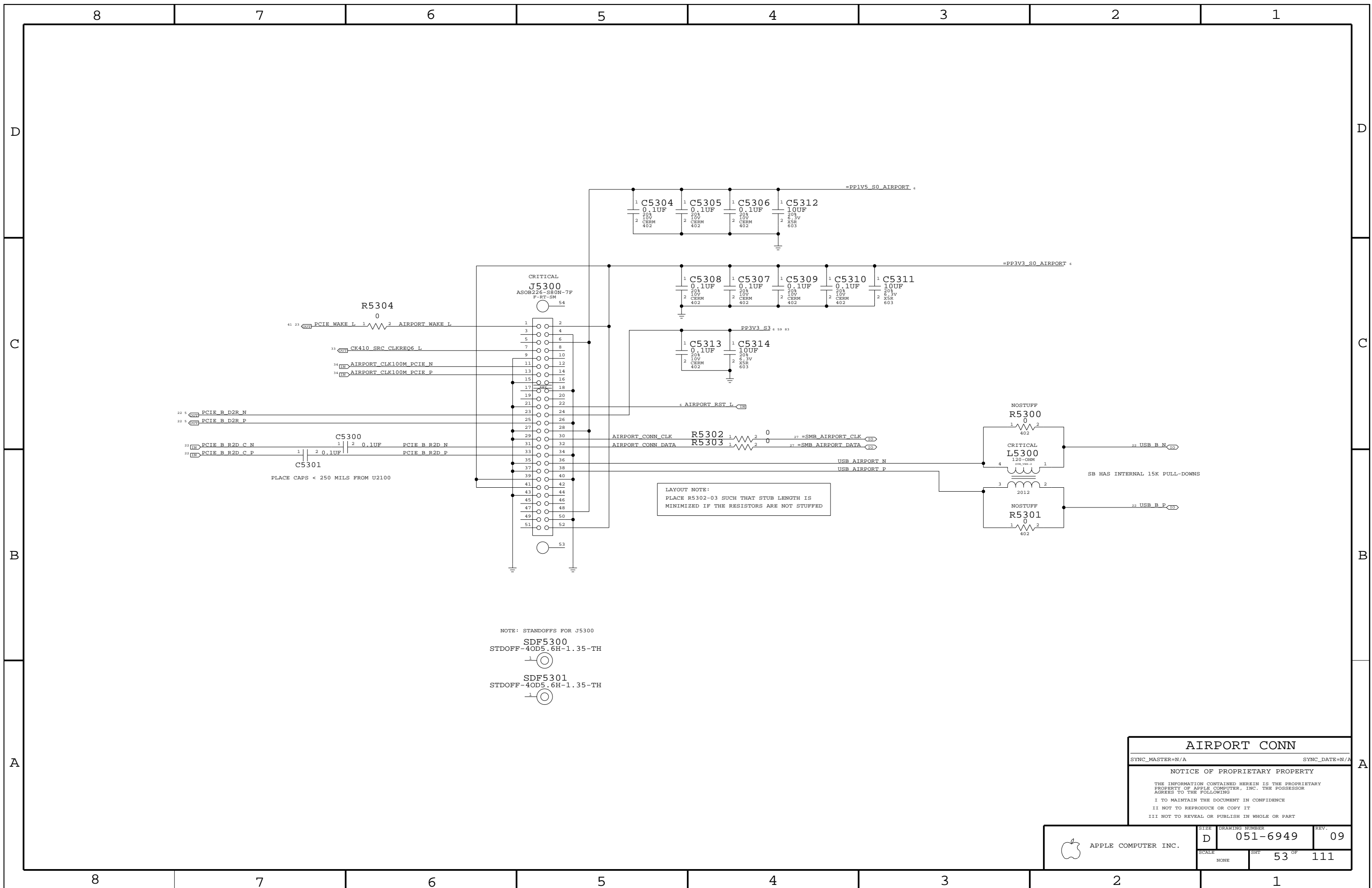
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6949	REV. 09
	SCALE NONE	SHIT 49 OF 111	



AIRPORT CONN

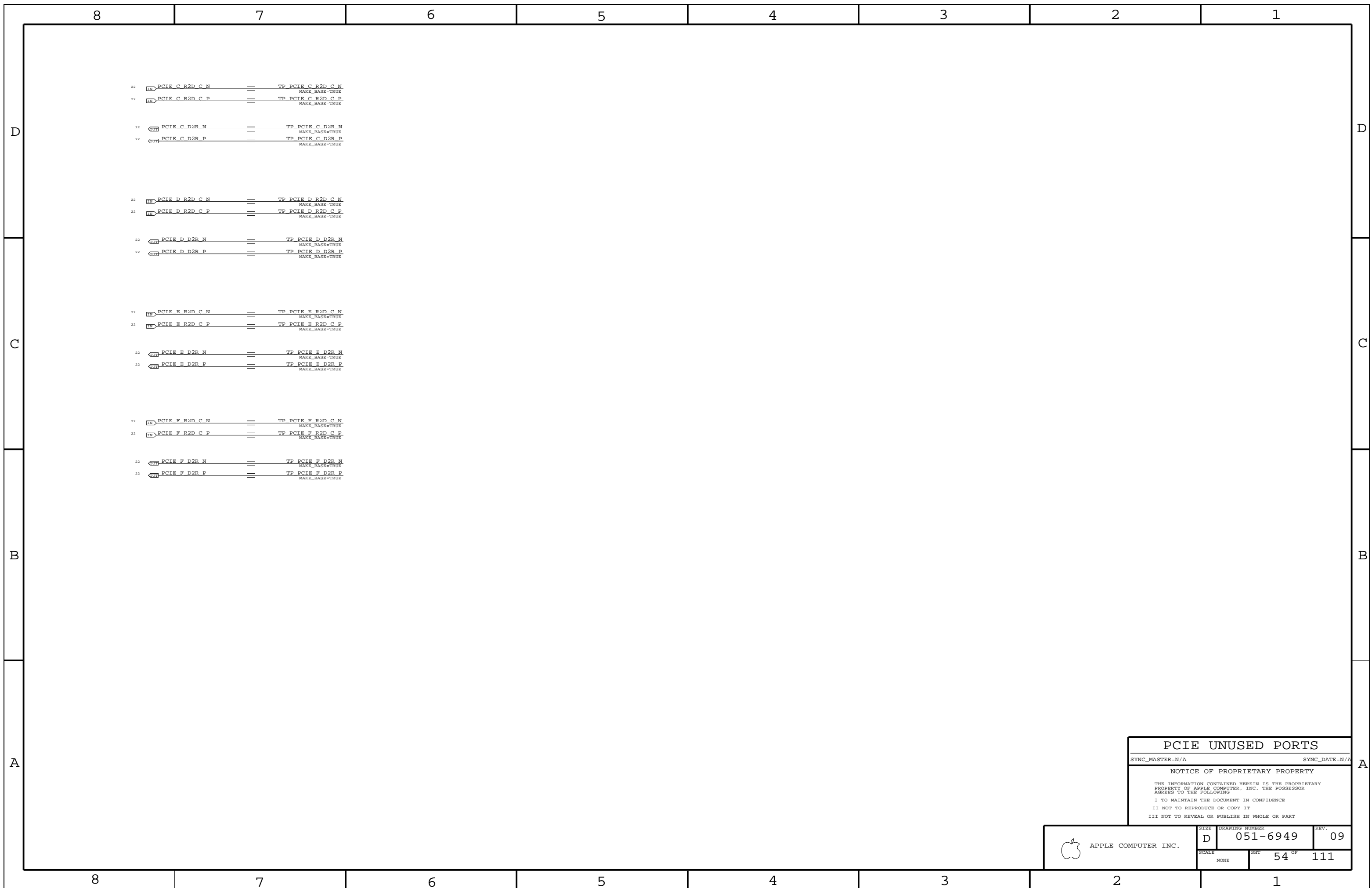
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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6949	REV. 09
	SCALE NONE	SHEET 53 OF 111	



22	IN	PCIE C R2D C N	==	TP PCIE C R2D C N	MAKE_BASE=TRUE
22	IN	PCIE C R2D C P	==	TP PCIE C R2D C P	MAKE_BASE=TRUE
22	OUT	PCIE C D2R N	==	TP PCIE C D2R N	MAKE_BASE=TRUE
22	OUT	PCIE C D2R P	==	TP PCIE C D2R P	MAKE_BASE=TRUE
22	IN	PCIE D R2D C N	==	TP PCIE D R2D C N	MAKE_BASE=TRUE
22	IN	PCIE D R2D C P	==	TP PCIE D R2D C P	MAKE_BASE=TRUE
22	OUT	PCIE D D2R N	==	TP PCIE D D2R N	MAKE_BASE=TRUE
22	OUT	PCIE D D2R P	==	TP PCIE D D2R P	MAKE_BASE=TRUE
22	IN	PCIE E R2D C N	==	TP PCIE E R2D C N	MAKE_BASE=TRUE
22	IN	PCIE E R2D C P	==	TP PCIE E R2D C P	MAKE_BASE=TRUE
22	OUT	PCIE E D2R N	==	TP PCIE E D2R N	MAKE_BASE=TRUE
22	OUT	PCIE E D2R P	==	TP PCIE E D2R P	MAKE_BASE=TRUE
22	IN	PCIE F R2D C N	==	TP PCIE F R2D C N	MAKE_BASE=TRUE
22	IN	PCIE F R2D C P	==	TP PCIE F R2D C P	MAKE_BASE=TRUE
22	OUT	PCIE F D2R N	==	TP PCIE F D2R N	MAKE_BASE=TRUE
22	OUT	PCIE F D2R P	==	TP PCIE F D2R P	MAKE_BASE=TRUE

PCIE UNUSED PORTS

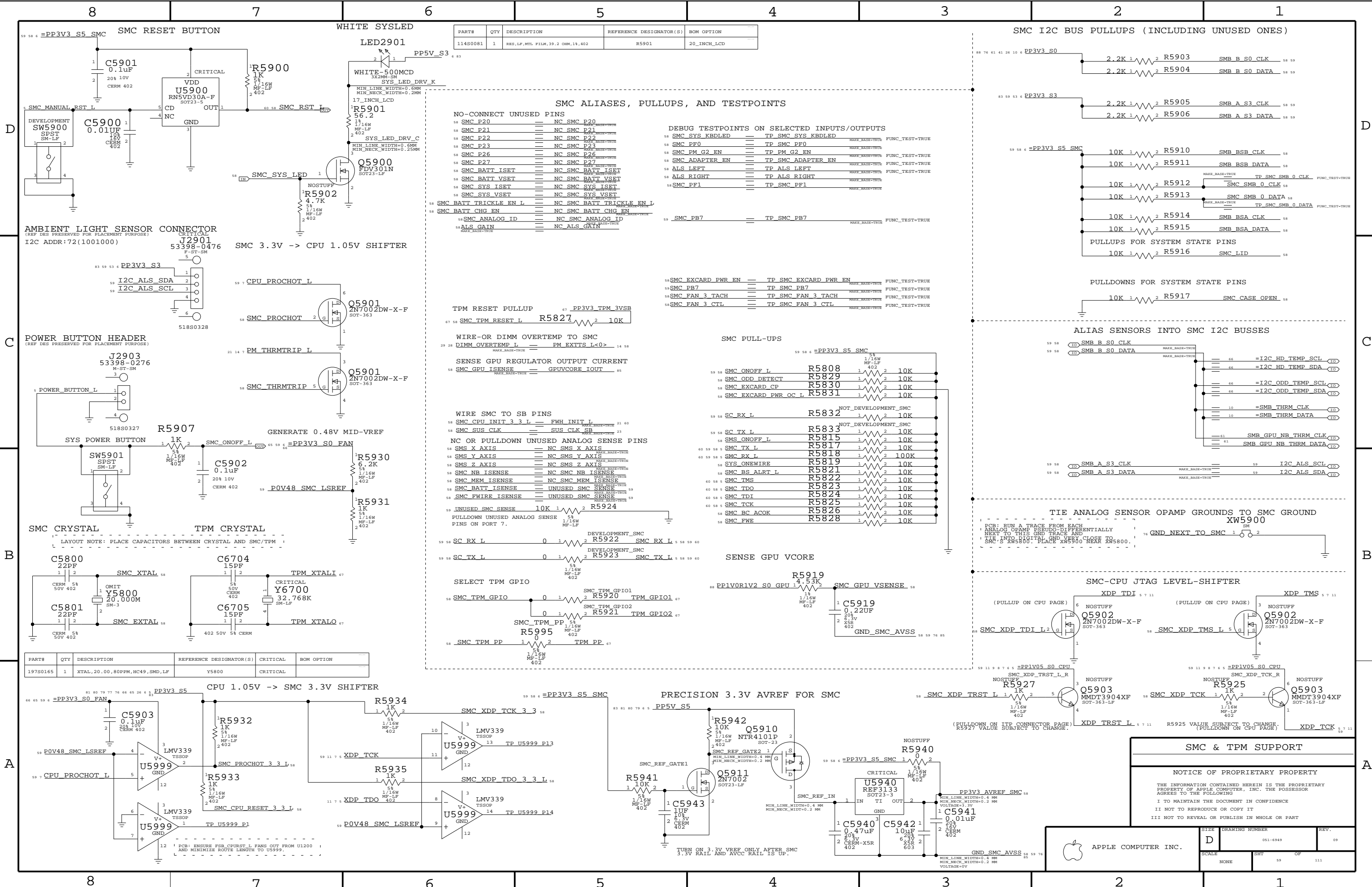
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	SHT	OF	
NONE	54	111	



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
11450081	1	RES,LP,WTL FILM,39.2 OHM,14,402	R5901	20_INCH_LCD

SMC ALIASES, PULLUPS, AND TESTPOINTS

NO-CONNECT UNUSED PINS	DEBUG TESTPOINTS ON SELECTED INPUTS/OUTPUTS
58 SMC P20 == NC SMC P20	58 SMC SYS_KBDLED == TP_SMC_SYS_KBDLED MAKE_BASE=TRUE FUNC_TEST=TRUE
58 SMC P21 == NC SMC P21	58 SMC PF0 == TP_SMC_PF0 MAKE_BASE=TRUE
58 SMC P22 == NC SMC P22	58 SMC PM_G2_EN == TP_PM_G2_EN MAKE_BASE=TRUE FUNC_TEST=TRUE
58 SMC P23 == NC SMC P23	58 SMC ADAPTER_EN == TP_SMC_ADAPTER_EN MAKE_BASE=TRUE FUNC_TEST=TRUE
58 SMC P26 == NC SMC P26	58 ALS_LEFT == TP_ALS_LEFT MAKE_BASE=TRUE FUNC_TEST=TRUE
58 SMC P27 == NC SMC P27	58 ALS_RIGHT == TP_ALS_RIGHT MAKE_BASE=TRUE FUNC_TEST=TRUE
58 SMC_BATT_ISET == NC SMC_BATT_ISET	58 SMC_PF1 == TP_SMC_PF1 MAKE_BASE=TRUE
58 SMC_BATT_VSET == NC SMC_BATT_VSET	
58 SMC_SYS_ISET == NC SMC_SYS_ISET	
58 SMC_SYS_VSET == NC SMC_SYS_VSET	
58 SMC_BATT_TRICKLE_EN_L == NC SMC_BATT_TRICKLE_EN_L	59 SMC_PB7 == TP_SMC_PB7 MAKE_BASE=TRUE FUNC_TEST=TRUE
58 SMC_BATT_CHG_EN == NC SMC_BATT_CHG_EN	
58 SMC_ANALOG_ID == NC SMC_ANALOG_ID	
58 ALS_GAIN == NC ALS_GAIN	

SMC PULL-UPS

58 SMC_ONOFF_L	R5808	10K
58 SMC_ODD_DETECT	R5829	10K
58 SMC_EXCARD_CP	R5830	10K
58 SMC_EXCARD_PWR_OC_L	R5831	10K
58 SC_RX_L	R5832	10K
58 SC_TX_L	R5833	10K
58 SMS_ONOFF_L	R5815	10K
58 SMC_TX_L	R5817	10K
58 SMC_RX_L	R5818	100K
58 SYS_ONEWIRE	R5819	10K
58 SMC_BS_ALERT_L	R5821	10K
58 SMC_TMS	R5822	10K
58 SMC_TDO	R5823	10K
58 SMC_TDI	R5824	10K
58 SMC_TCK	R5825	10K
58 SMC_BC_ACOK	R5826	10K
58 SMC_FWE	R5828	10K

WIRE SMC TO SB PINS

58 SMC_CPU_INIT_3_3_L	FWH_INIT_L	21
58 SMC_SUS_CLK	SUS_CLK_SB	23
58 SMS_X_AXIS	NC_SMS_X_AXIS	
58 SMS_Y_AXIS	NC_SMS_Y_AXIS	
58 SMS_Z_AXIS	NC_SMS_Z_AXIS	
58 SMC_NB_ISENSE	NC_SMC_NB_ISENSE	
58 SMC_MEM_ISENSE	NC_SMC_MEM_ISENSE	
58 SMC_BATT_ISENSE	UNUSED_SMC_ISENSE	59
58 SMC_FWIRE_ISENSE	UNUSED_SMC_ISENSE	59

WIRE SMC TO SB PINS

58 SMC_CPU_INIT_3_3_L	FWH_INIT_L	21
58 SMC_SUS_CLK	SUS_CLK_SB	23
58 SMC_TPM_GPI0	SMC_TPM_GPI01	67
58 SMC_TPM_GPI1	SMC_TPM_GPI02	67
58 SMC_TPM_PP	SMC_TPM_PP	67

SELECT TPM GPIO

58 SMC_TPM_PP	R5995	10K
58 SMC_TPM_PP	R5924	10K

PRECISION 3.3V AVREF FOR SMC

58 SMC_REF_GATE1	R5941	10K
58 SMC_REF_GATE2	R5942	10K
58 SMC_REF_IN	R5943	10K

SMC CPU 1.05V -> SMC 3.3V SHIFTER

58 SMC_XDP_TCK_3_3	R5934	1K
58 SMC_XDP_TDO_3_3_L	R5935	1K

SMC & TPM SUPPORT

58 SMC_XDP_TRST_L_R	R5927	1K
58 SMC_XDP_TCK_R	R5925	1K
58 XDP_TCK	R5940	10K

SMC & TPM SUPPORT

58 SMC_XDP_TCK_R	R5925	1K
58 XDP_TCK	R5940	10K

SMC & TPM SUPPORT

58 SMC_XDP_TCK_R	R5925	1K
58 XDP_TCK	R5940	10K

SMC & TPM SUPPORT

58 SMC_XDP_TCK_R	R5925	1K
58 XDP_TCK	R5940	10K

SMC I2C BUS PULLUPS (INCLUDING UNUSED ONES)

88 76 61 41 26 10 6	PP3V3_S0	2.2K	R5903	SMB_B_S0_CLK	58 59
		2.2K	R5904	SMB_B_S0_DATA	58 59
83 59 53 6	PP3V3_S3	2.2K	R5905	SMB_A_S3_CLK	58 59
		2.2K	R5906	SMB_A_S3_DATA	58 59

PULLUPS FOR SYSTEM STATE PINS

59 58 6	PP3V3_S5_SMC	10K	R5910	SMB_BSB_CLK	58
		10K	R5911	SMB_BSB_DATA	58
		10K	R5912	TP_SMC_SMB_0_CLK	58
		10K	R5913	SMC_SMB_0_CLK	58
		10K	R5914	SMB_BSA_CLK	58
		10K	R5915	SMB_BSA_DATA	58
		10K	R5916	SMC_LID	58

PULLDOWNS FOR SYSTEM STATE PINS

		10K	R5917	SMC_CASE_OPEN	58
--	--	-----	-------	---------------	----

ALIAS SENSORS INTO SMC I2C BUSESSE

59 58	SMB_B_S0_CLK	MAKE_BASE=TRUE		58
59 58	SMB_B_S0_DATA	MAKE_BASE=TRUE		58
			=I2C_HD_TEMP_SCL	58
			=I2C_HD_TEMP_SDA	58
			=I2C_ODD_TEMP_SCL	58
			=I2C_ODD_TEMP_SDA	58
			=SMB_THRM_CLK	58
			=SMB_THRM_DATA	58
			SMB_GPU_NB_THRM_CLK	58
			SMB_GPU_NB_THRM_DATA	58
59 58	SMB_A_S3_CLK	MAKE_BASE=TRUE		59
59 58	SMB_A_S3_DATA	MAKE_BASE=TRUE		59
			I2C_ALS_SCL	58
			I2C_ALS_SDA	58

TIE ANALOG SENSOR OPAMP GROUNDS TO SMC GROUND

			XW5900	
			GND_NEXT_TO_SMC	

SMC-CPU JTAG LEVEL-SHIFTER

58	SMC_XDP_TDI_L	2		58
58	SMC_XDP_TMS_L	5		58

SMC-CPU JTAG LEVEL-SHIFTER

58	SMC_XDP_TRST_L_R	1		58
58	SMC_XDP_TCK_R	2		58

SMC & TPM SUPPORT

58	SMC_XDP_TCK_R	R5925	1K	58
58	XDP_TCK	R5940	10K	58

SMC & TPM SUPPORT

58	SMC_XDP_TCK_R	R5925	1K	58
58	XDP_TCK	R5940	10K	58

SMC & TPM SUPPORT

58	SMC_XDP_TCK_R	R5925	1K	58
58	XDP_TCK	R5940	10K	58

SMC & TPM SUPPORT

58	SMC_XDP_TCK_R	R5925	1K	58
58	XDP_TCK	R5940	10K	58

SMC & TPM SUPPORT

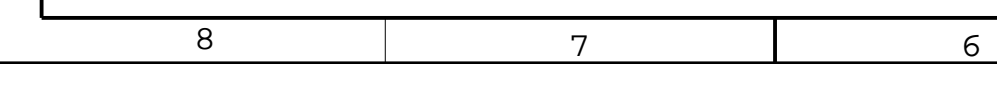
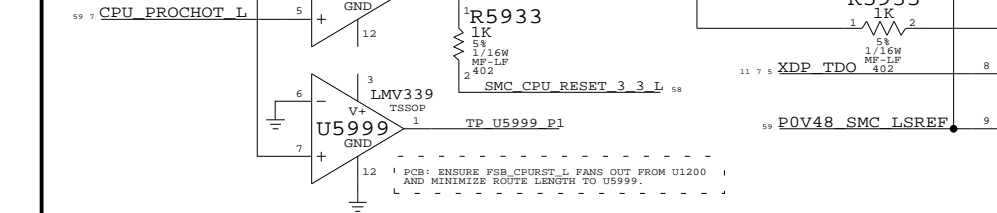
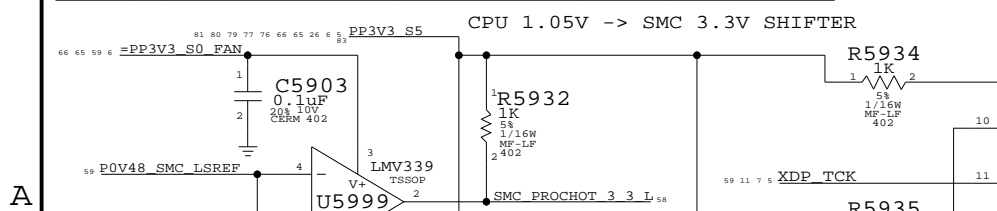
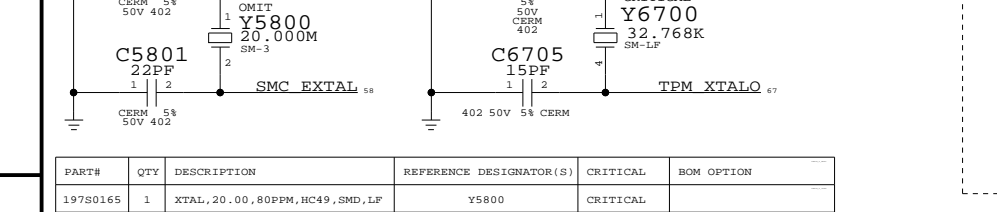
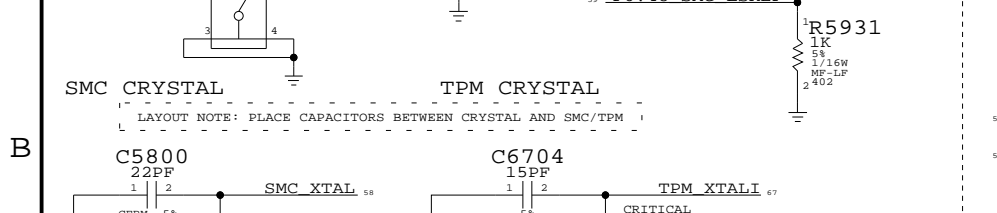
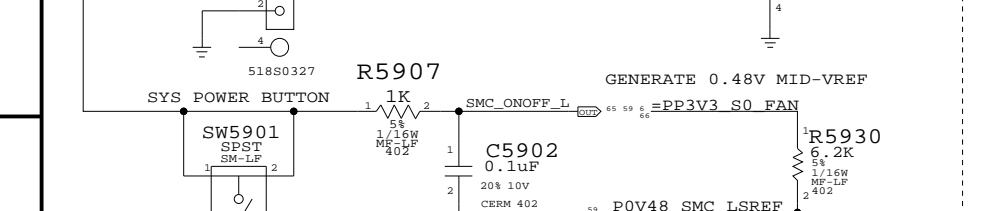
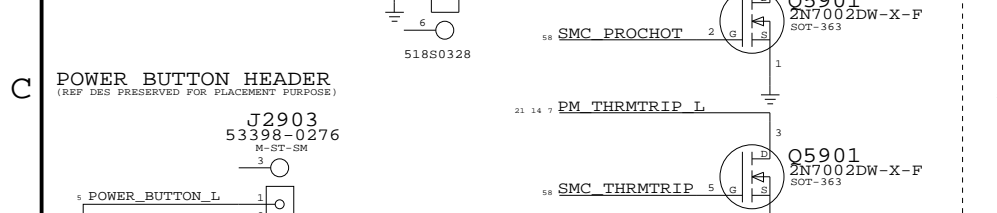
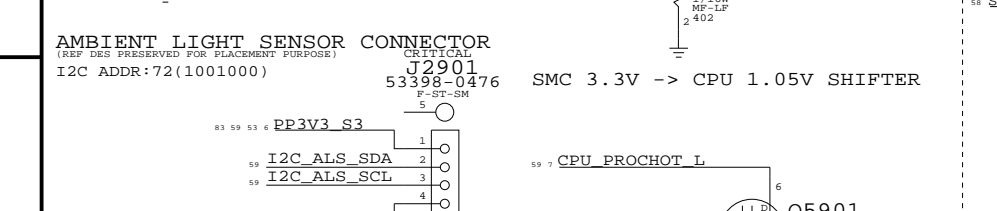
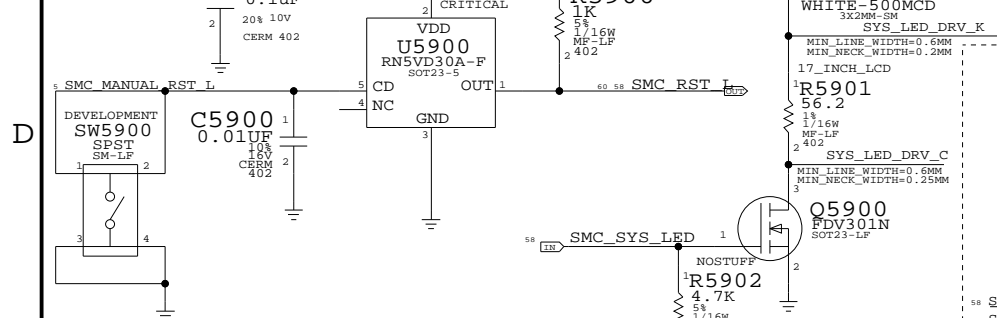
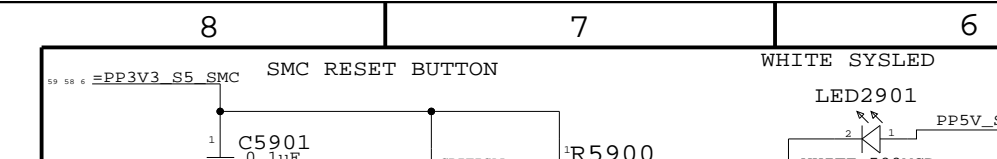
58	SMC_XDP_TCK_R	R5925	1K	58
58	XDP_TCK	R5940	10K	58

SMC & TPM SUPPORT

58	SMC_XDP_TCK_R	R5925	1K	58
58	XDP_TCK	R5940	10K	58

SMC & TPM SUPPORT

58	SMC_XDP_TCK_R	R5925	1K	58
58	XDP_TCK	R5940	10K	58



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
197S0165	1	XTAL,20.00,80PPM,HC49,SMD,LF	Y5800	CRITICAL	

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
58		SMC P20	NC SMC P20		
58		SMC P21	NC SMC P21		
58		SMC P22	NC SMC P22		
58		SMC P23	NC SMC P23		
58		SMC P26	NC SMC P26		
58		SMC P27	NC SMC P27		
58		SMC_BATT_ISET	NC SMC_BATT_ISET		
58		SMC_BATT_VSET	NC SMC_BATT_VSET		
58		SMC_SYS_ISET	NC SMC_SYS_ISET		
58		SMC_SYS_VSET	NC SMC_SYS_VSET		
58		SMC_BATT_TRICKLE_EN_L	NC SMC_BATT_TRICKLE_EN_L		
58		SMC_BATT_CHG_EN	NC SMC_BATT_CHG_EN		
58		SMC_ANALOG_ID	NC SMC_ANALOG_ID		
58		ALS_GAIN	NC ALS_GAIN		

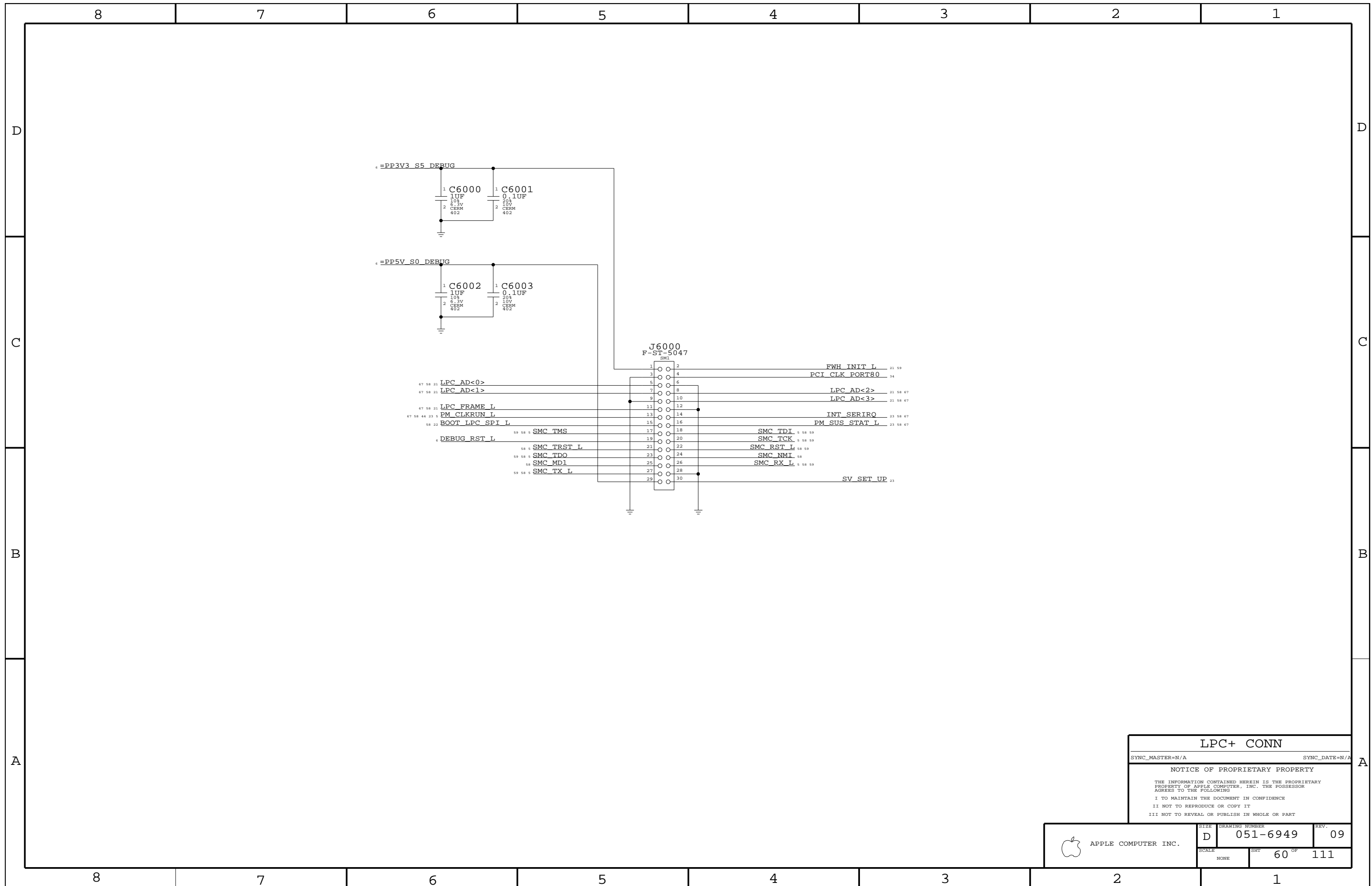
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
58		SMC ONOFF L	R5808		
58		SMC ODD_DETECT	R5829		
58		SMC EXCARD_CP	R5830		
58		SMC EXCARD_PWR_OC_L	R5831		
58		SC_RX_L	R5832		
58		SC_TX_L	R5833		
58		SMS_ONOFF_L	R5815		
58		SMC_TX_L	R5817		
58		SMC_RX_L	R5818		
58		SYS_ONEWIRE	R5819		
58		SMC_BS_ALERT_L	R5821		
58		SMC_TMS	R5822		
58		SMC_TDO	R5823		
58		SMC_TDI	R5824		
58		SMC_TCK	R5825		
58		SMC_BC_ACOK	R5826		
58		SMC_FWE	R5828		

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
58		SMC CPU INIT 3_3_L	FWH_INIT_L		21
58		SMC SUS_CLK	SUS_CLK_SB		23
58		SMS X_AXIS	NC_SMS_X_AXIS		
58		SMS Y_AXIS	NC_SMS_Y_AXIS		
58		SMS Z_AXIS	NC_SMS_Z_AXIS		
58		SMC NB ISENSE	NC_SMC_NB_ISENSE		
58		SMC MEM ISENSE	NC_SMC_MEM_ISENSE		
58		SMC BATT ISENSE	UNUSED_SMC_ISENSE		59
58		SMC FWIRE ISENSE	UNUSED_SMC_ISENSE		59
58		UNUSED SMC SENSE	10K		R5924
58		PULLDOWN UNUSED ANALOG SENSE	1/16W MF-LF		59
58		PINS ON PORT 7.			

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
58		SC_RX_L	R5922		58 59 60
58		SC_TX_L	R5923		58 59 60
58		SMC TPM GPIO	SMC_TPM_GPI01		67
58		SMC TPM GPIO	SMC_TPM_GPI02		67
58		SMC TPM PP	SMC_TPM_PP		67

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
58		SMC CPU INIT 3_3_L	FWH_INIT_L		21
58		SMC SUS_CLK	SUS_CLK_SB		23
58		SMS X_AXIS	NC_SMS_X_AXIS		
58		SMS Y_AXIS	NC_SMS_Y_AXIS		
58		SMS Z_AXIS	NC_SMS_Z_AXIS		
58		SMC NB ISENSE	NC_SMC_NB_ISENSE		
58		SMC MEM ISENSE	NC_SMC_MEM_ISENSE		
58		SMC BATT ISENSE	UNUSED_SMC_ISENSE		59
58		SMC FWIRE ISENSE	UNUSED_SMC_ISENSE		59
58		UNUSED SMC SENSE	10K		R5924
58		PULLDOWN UNUSED ANALOG SENSE	1/16W MF-LF		59
58		PINS ON PORT 7.			

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
58		SC_RX_L	R5922		58 59 60
58		SC_TX_L	R592		



LPC+ CONN

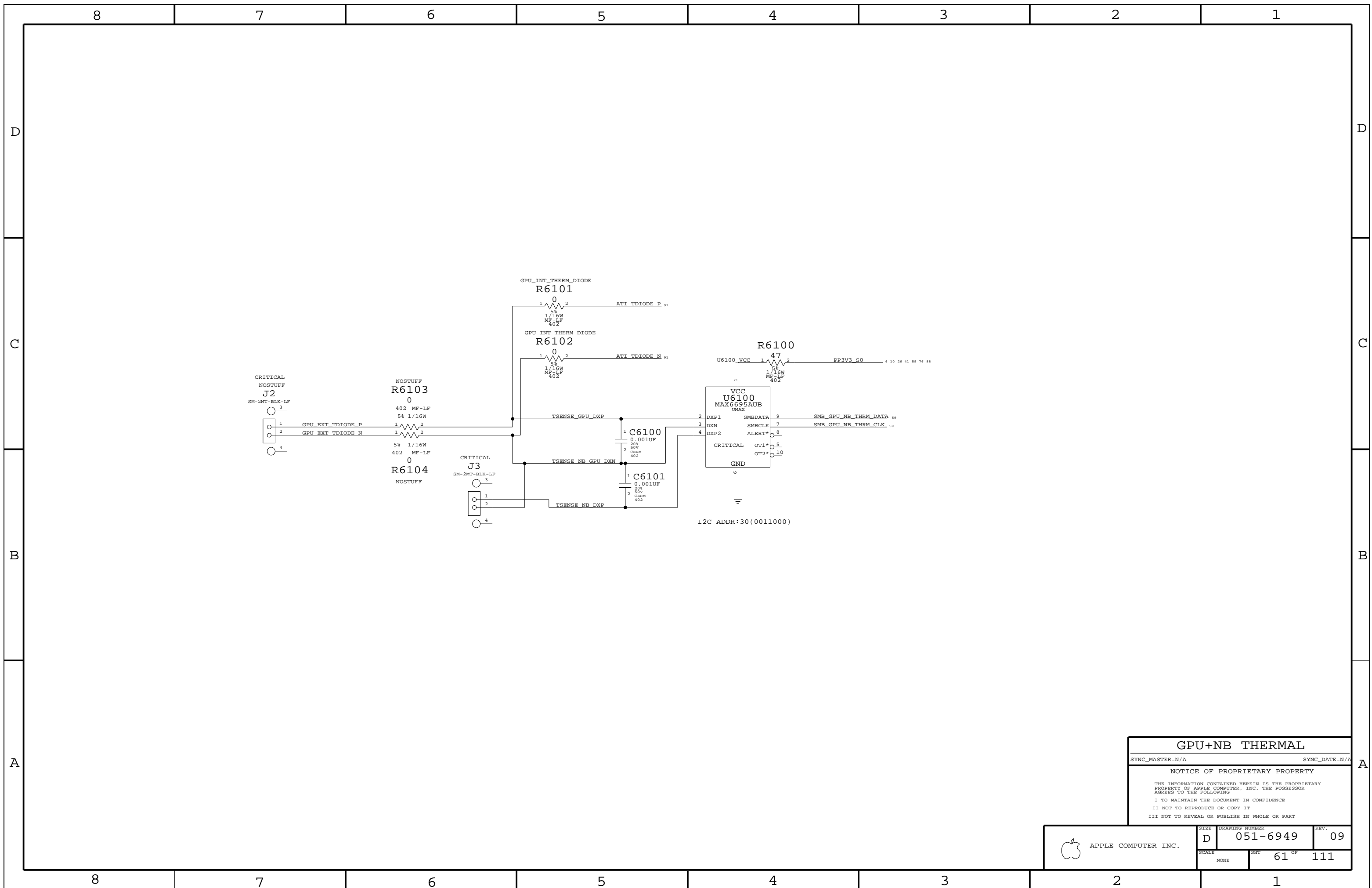
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	D	051-6949	09
SCALE		SHT	OF
NONE		60	111



GPU+NB THERMAL

SYNC_MASTER=N/A SYNC_DATE=N/A

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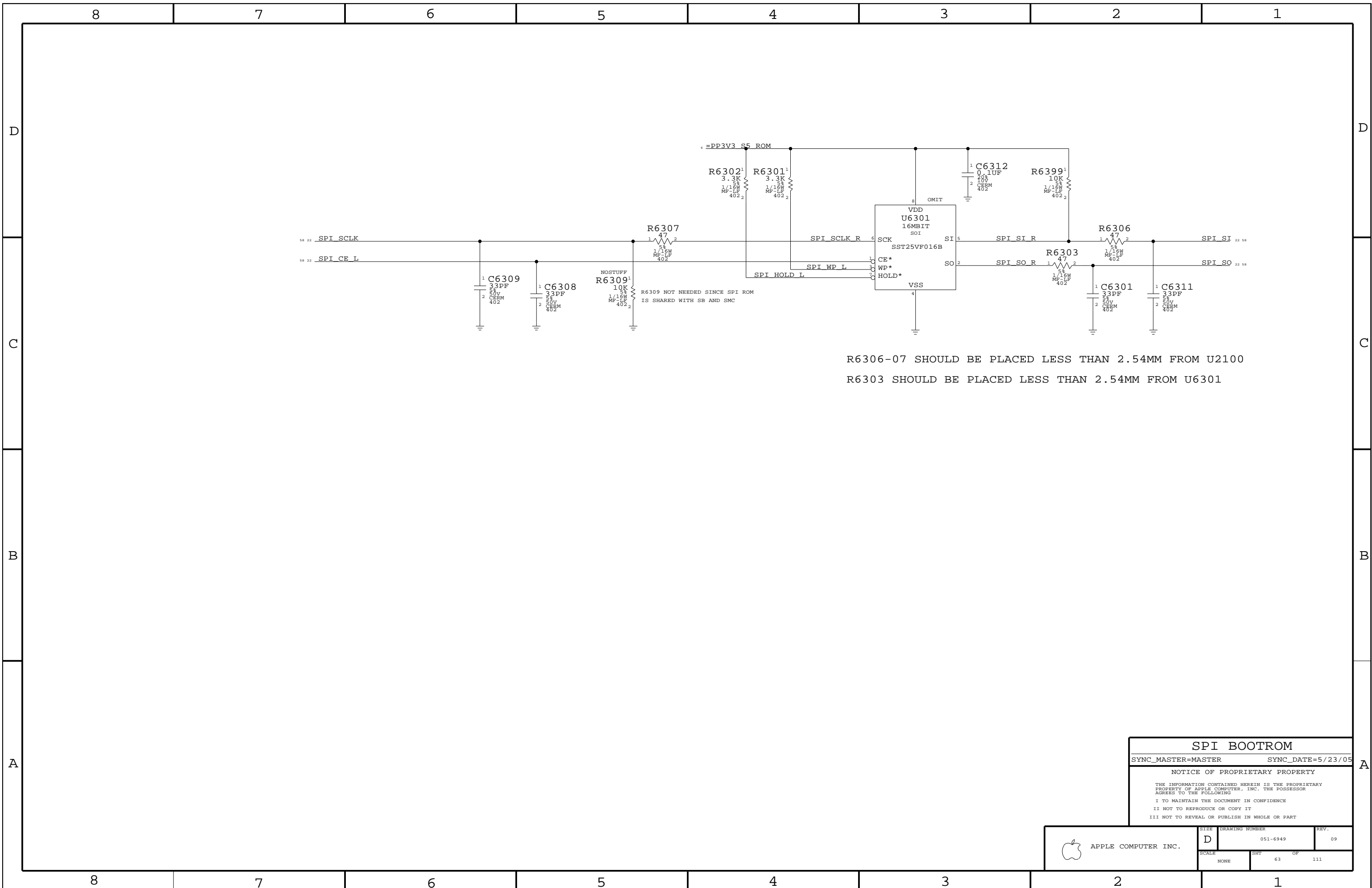
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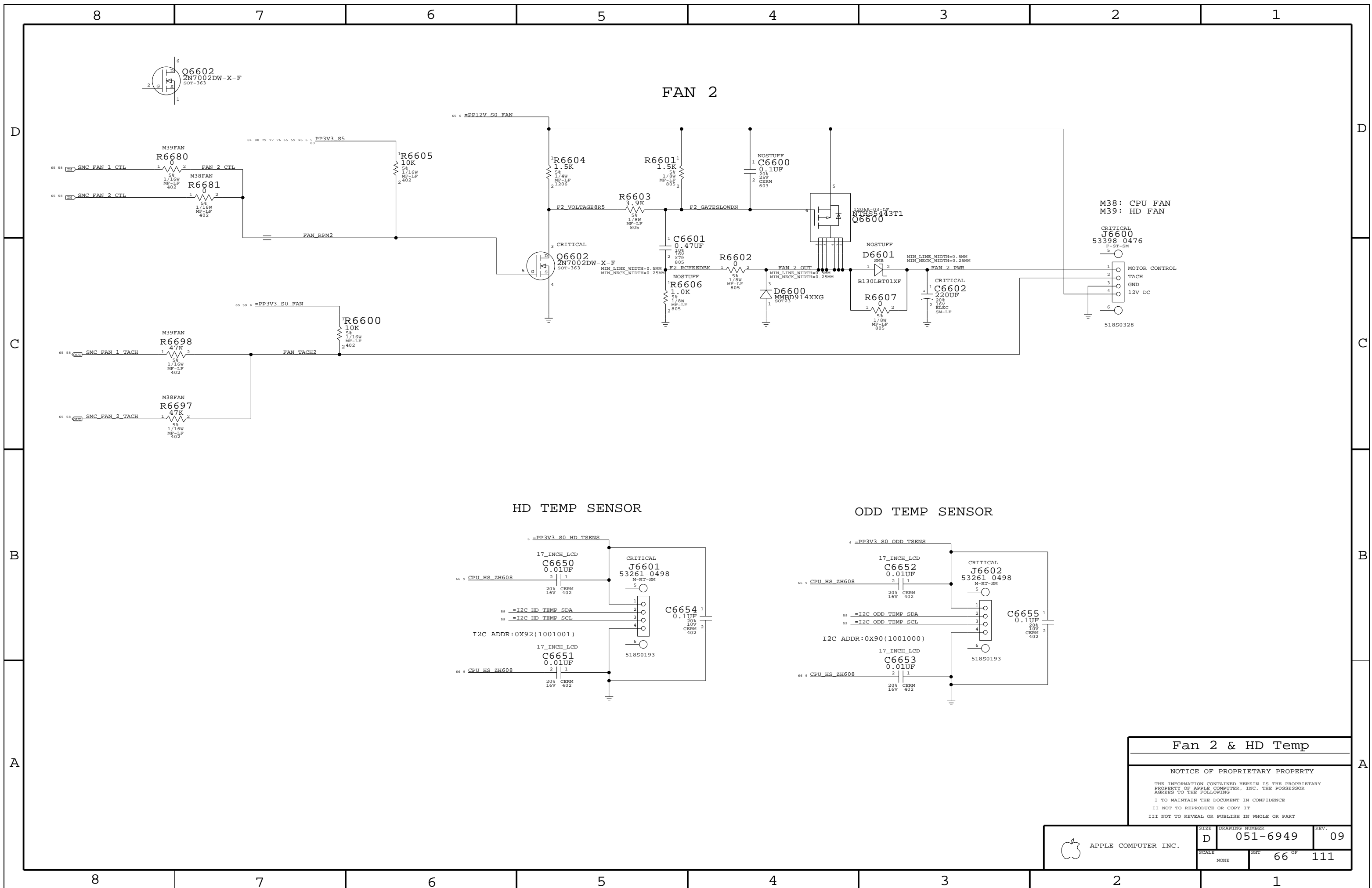
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE		SHT	OF
NONE		61	111



R6306-07 SHOULD BE PLACED LESS THAN 2.54MM FROM U2100
 R6303 SHOULD BE PLACED LESS THAN 2.54MM FROM U6301

SPI BOOTROM
 SYNC_MASTER=MASTER SYNC_DATE=5/23/05
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	D	051-6949	09
SCALE	SHT	OF	
NONE	63	111	



Fan 2 & HD Temp

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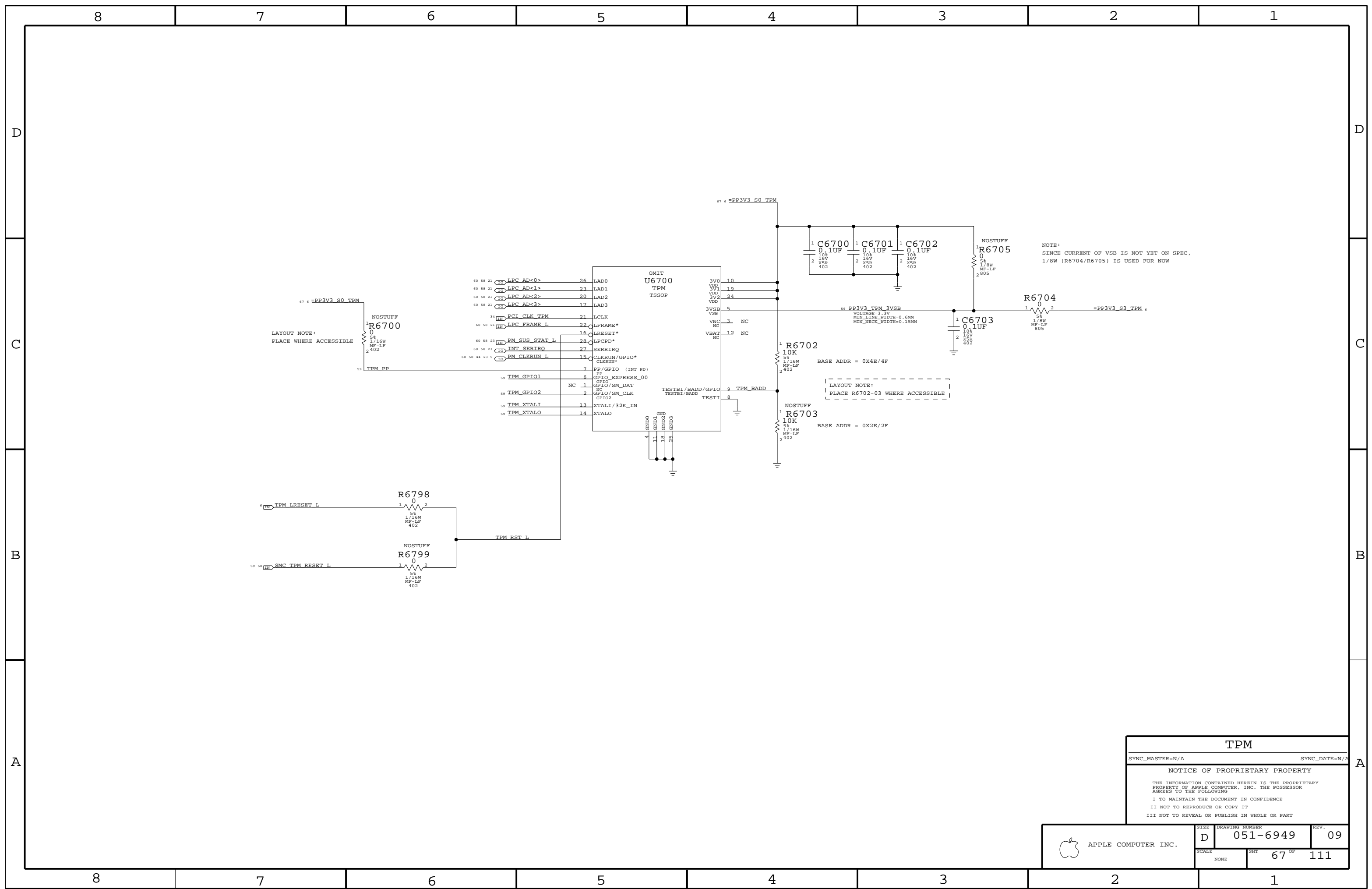
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APPLE COMPUTER INC.	SIZE: D	DRAWING NUMBER: 051-6949	REV.: 09
	SCALE: NONE	SHEET: 66 OF 111	



NOTE:
SINCE CURRENT OF VSB IS NOT YET ON SPEC,
1/8W (R6704/R6705) IS USED FOR NOW

LAYOUT NOTE:
PLACE R6702-03 WHERE ACCESSIBLE

TPM

SYNC_MASTER=N/A SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

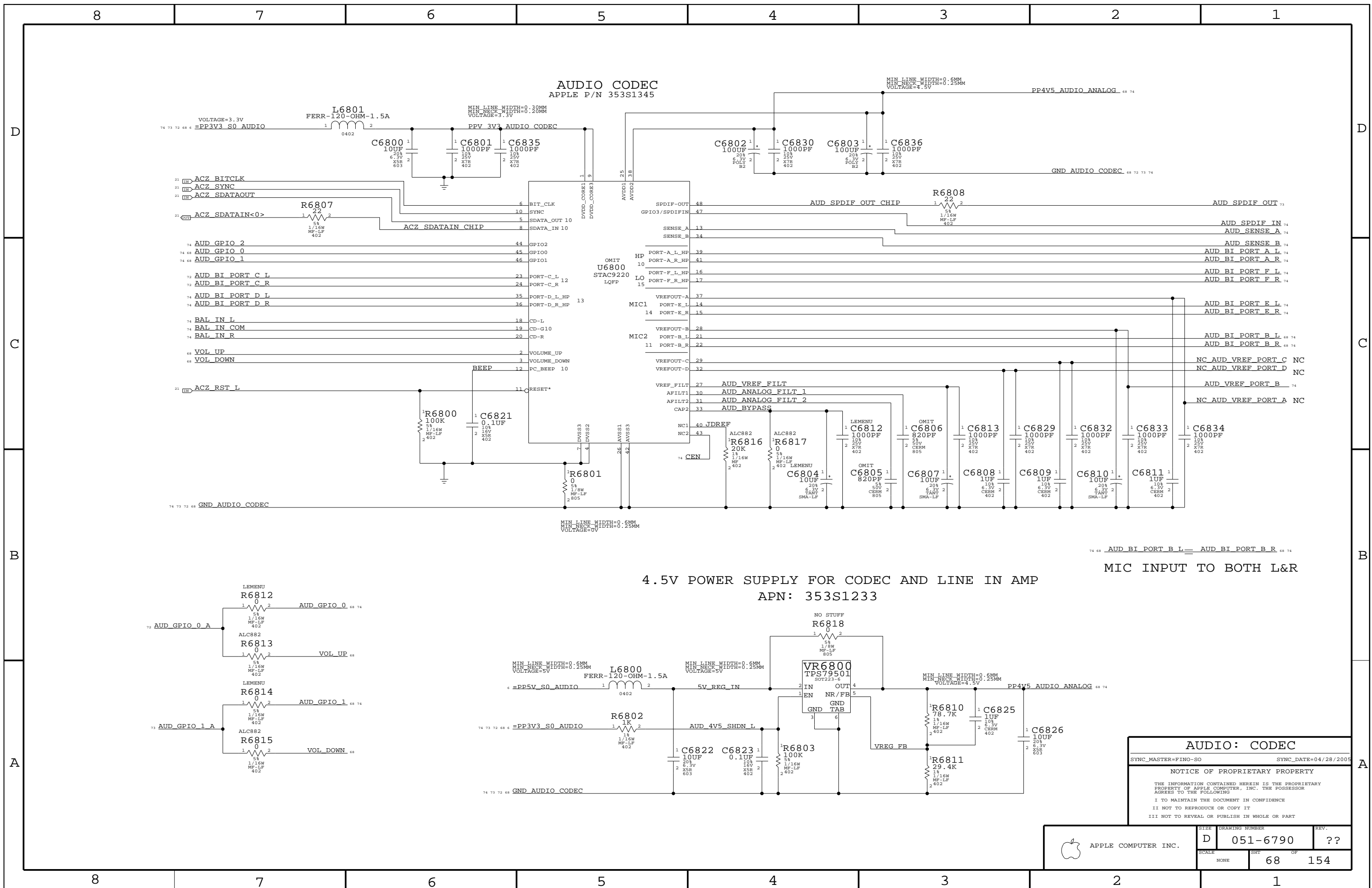
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	SHT	67 OF	111
NONE			



AUDIO: CODEC

SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005

NOTICE OF PROPRIETARY PROPERTY

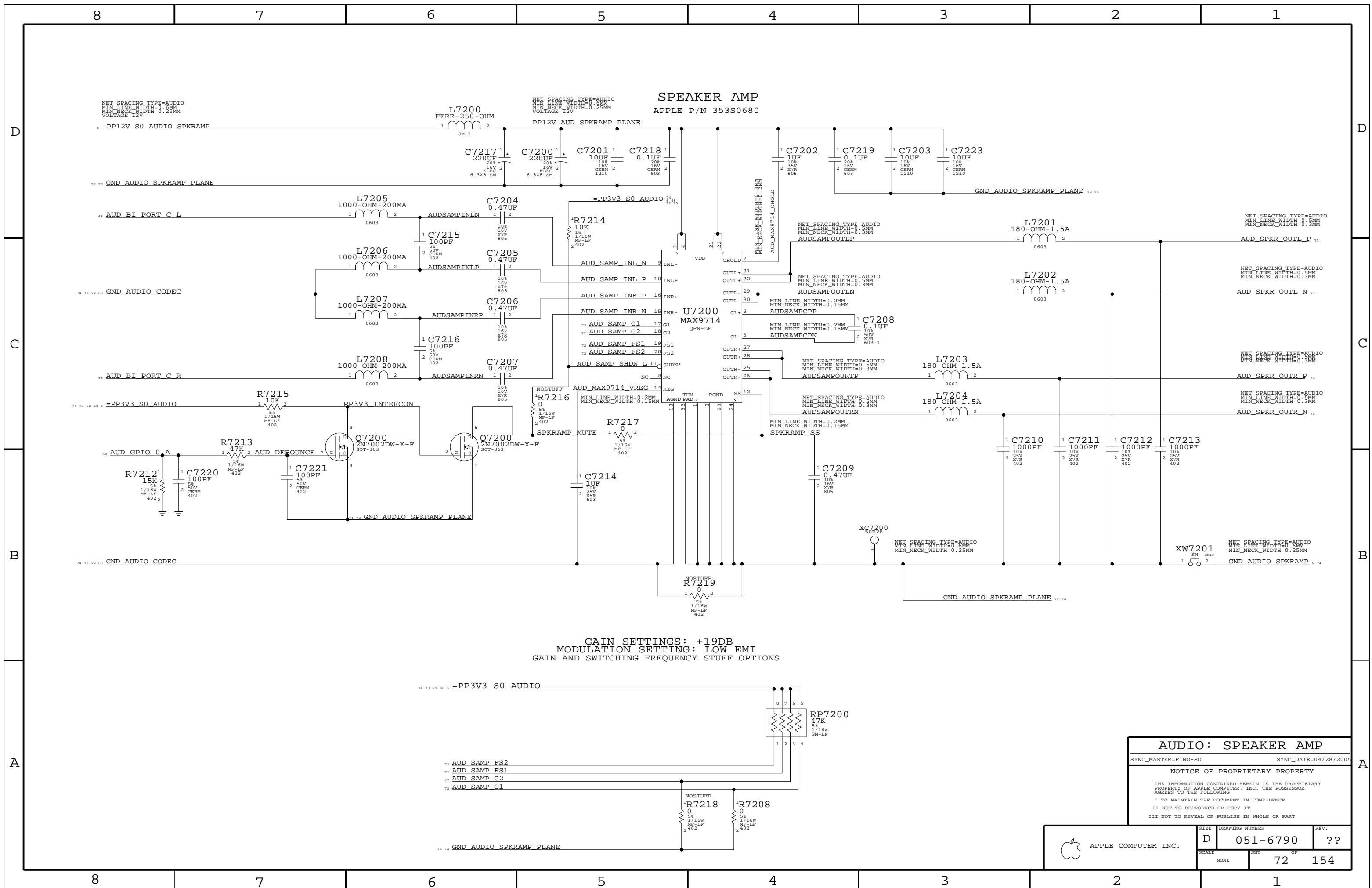
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APPLE COMPUTER INC.	SIZE: D	DRAWING NUMBER: 051-6790	REV.: ??
	SCALE: NONE	SHEET: 68	OF: 154



SPEAKER AMP
APPLE P/N 353S0680

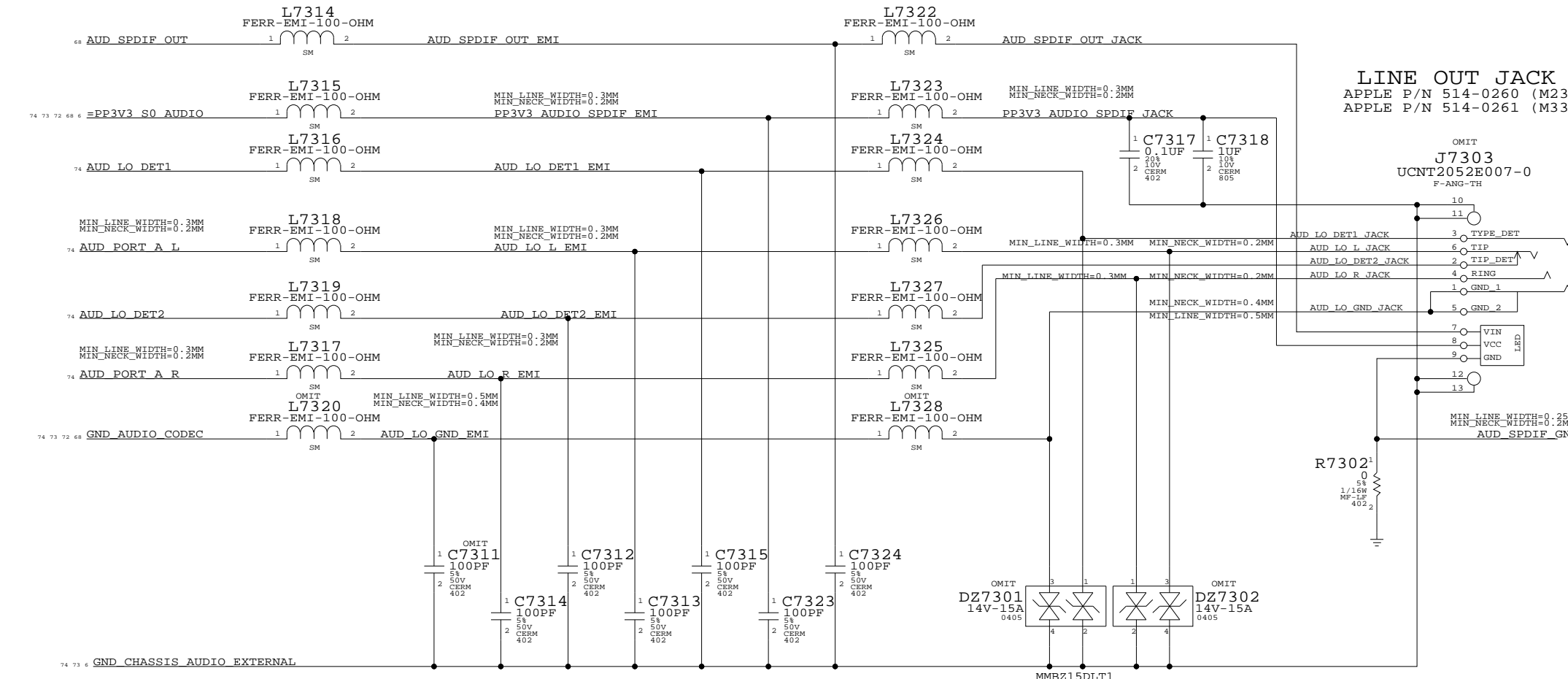
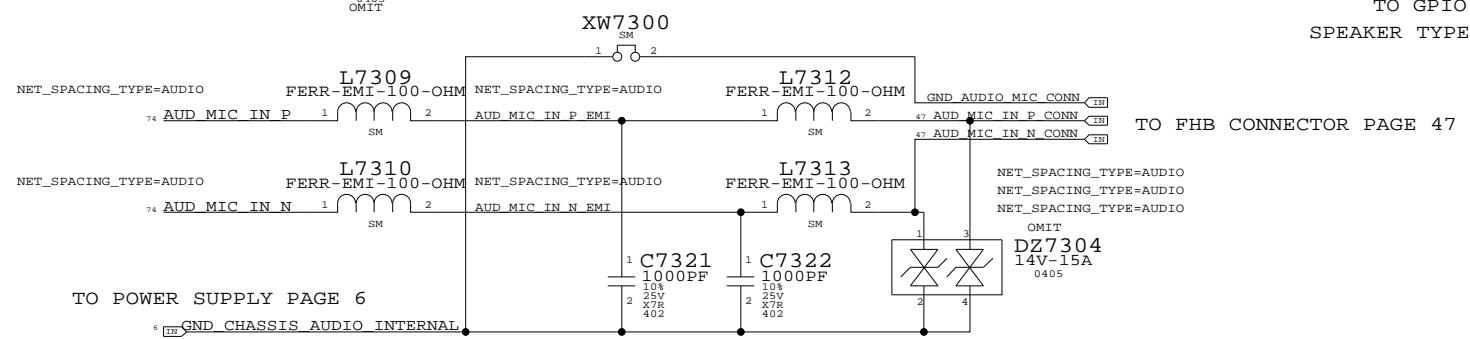
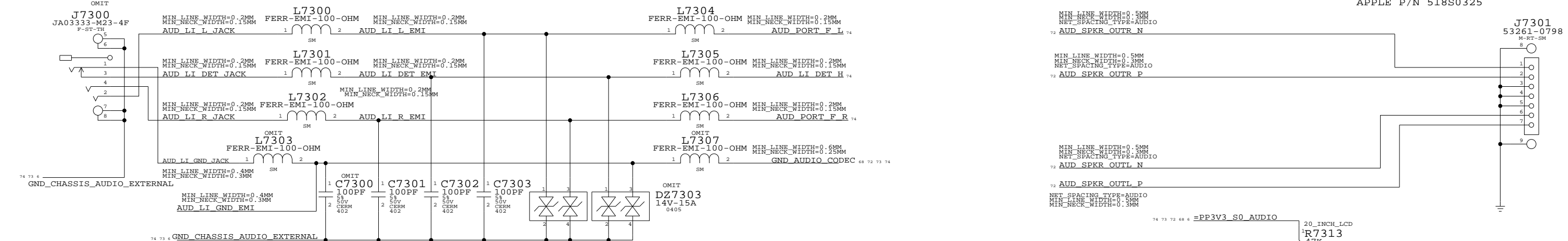
GAIN SETTINGS: +19DB
MODULATION SETTING: LOW EMI
GAIN AND SWITCHING FREQUENCY STUFF OPTIONS

AUDIO: SPEAKER AMP
SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6790	??
SCALE	NONE	SHT	OF
		72	154

LINE IN JACK
APPLE P/N 514-0246 (M23) APPLE P/N 514-0249 (M33)

SPEAKER CABLE CONNECTOR
APPLE P/N 518S0325



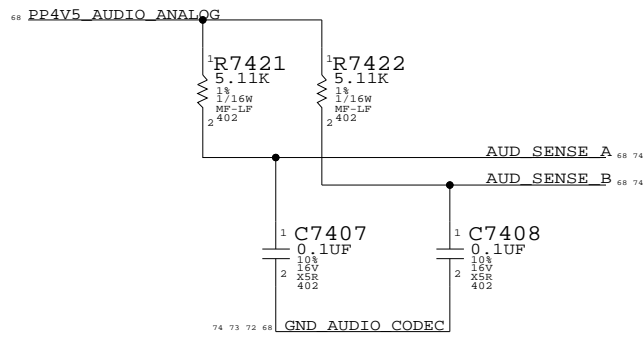
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0246	1	LINE IN CONNECTOR, 5.5 DEG	J7300	CRITICAL	17_INCH_LCD
514-0260	1	COMBO OUT CONN, 5.5 DEG	J7303	CRITICAL	17_INCH_LCD
514-0249	1	LINE IN CONNECTOR, 4.5 DEG	J7300	CRITICAL	20_INCH_LCD
514-0261	1	COMBO OUT CONN, 4.5 DEG	J7303	CRITICAL	20_INCH_LCD
353S1345	1	SIGMATEL STAC9220	U6800	CRITICAL	LEMENU
353S1268	1	REALTEK ALC882	U6800	CRITICAL	ALC882
131S8223	2	CAPACITOR, 820PF	C6805, C6806	CRITICAL	LEMENU
131S0534	2	CAPACITOR, 1UF	C6805, C6806	CRITICAL	ALC882
377S0043	5	TRANSIENT VOLTAGE DIODE	DZ7300, DZ7301, DZ7302, DZ7303, DZ7304	CRITICAL	
113S0022	4	RESISTOR, 0 OHM, 0603	L7303, L7307, L7320, L7328		
116S0004	2	RESISTOR, 0 OHM, 0402	C7300, C7311		



AUDIO: CONNECTORS
SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005
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APPLE COMPUTER INC. DRAWING NUMBER: 051-6790 REV. ??
SCALE: NONE SHEET: 73 OF 154

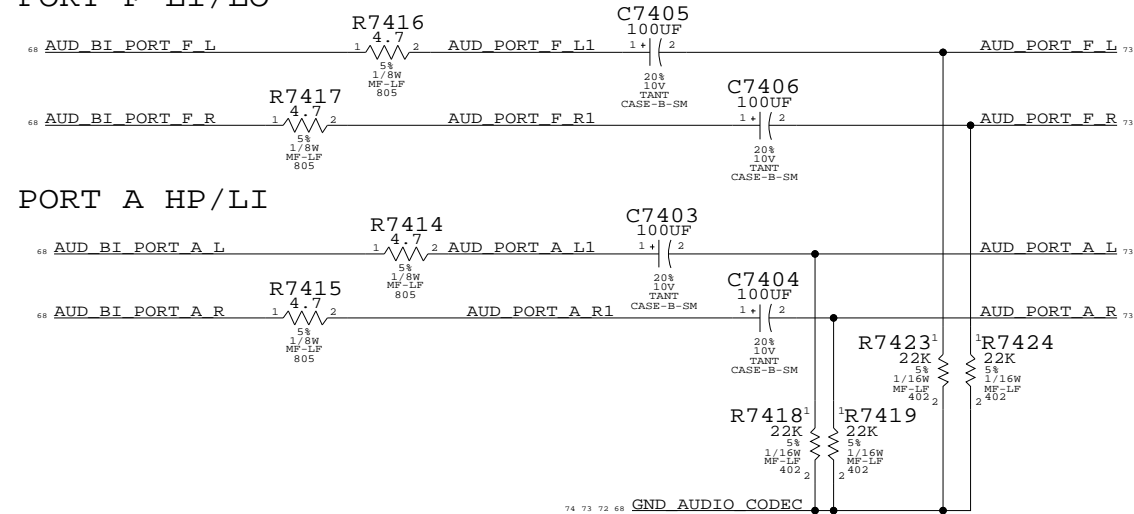
JACK SENSE PULL UPS (PLACE NEXT TO CODEC)



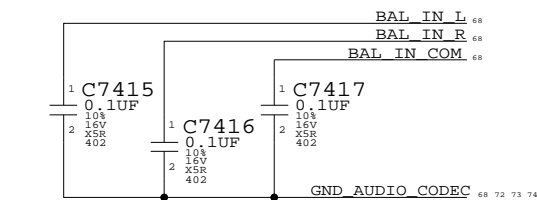
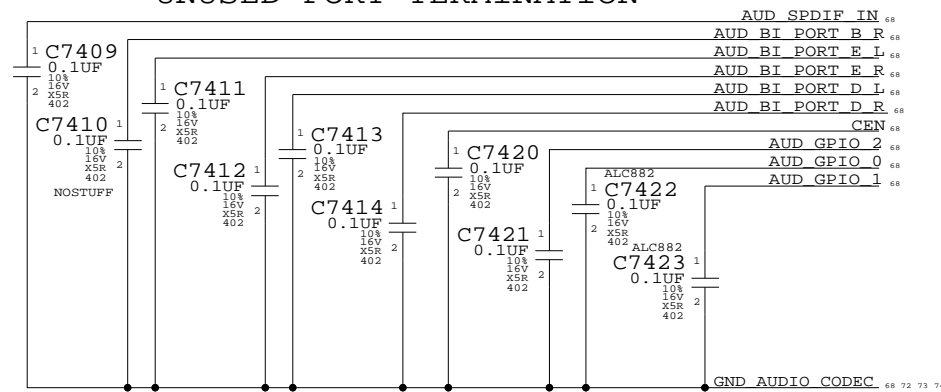
USED PORTS
 PORT A HP/LI
 PORT B MIC IN
 PORT C BI SPEAKERS
 PORT F LI/LO

UNUSED PORTS
 PORT E
 PORT D

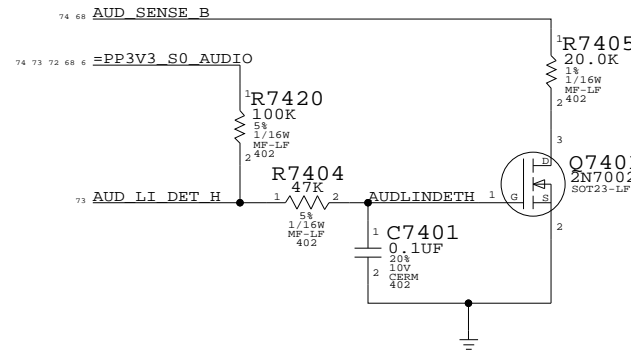
PORT F LI/LO



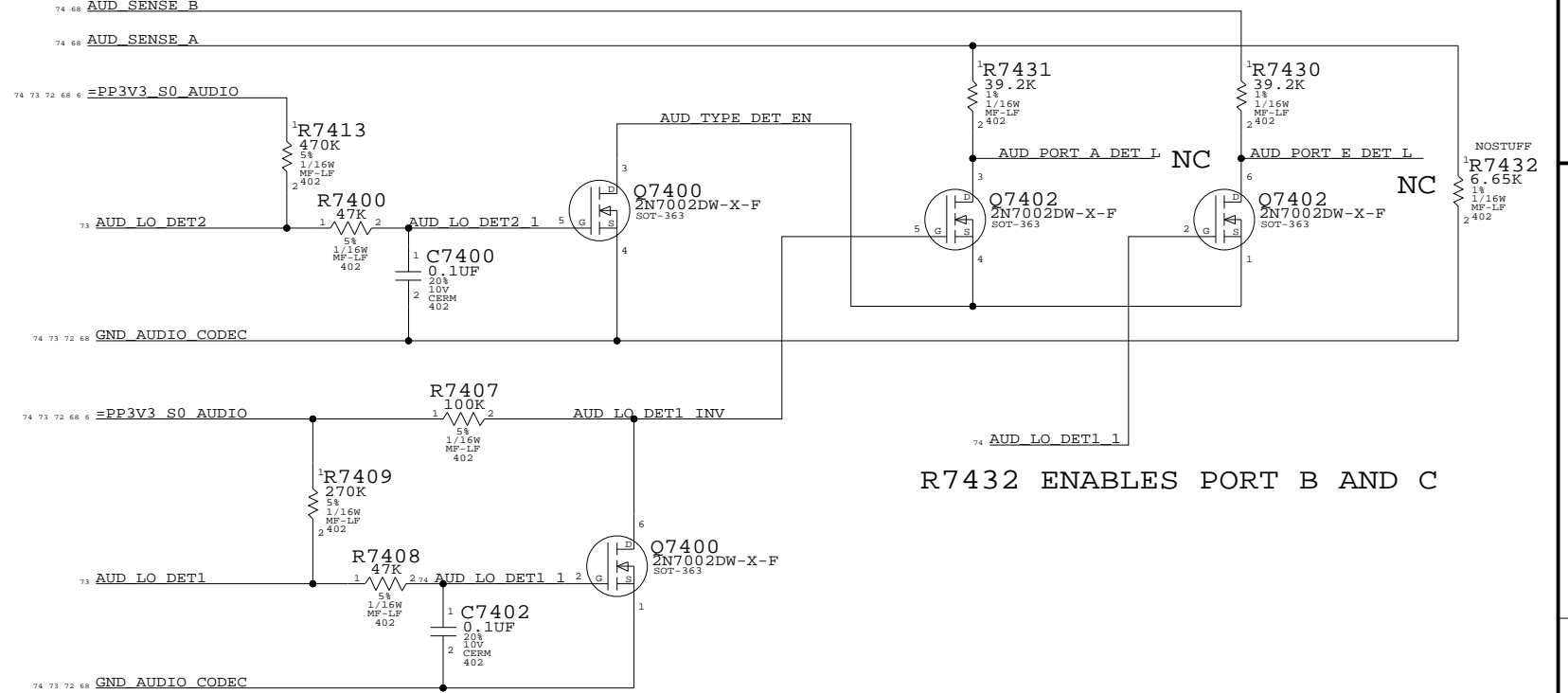
UNUSED PORT TERMINATION



PORT F (LI/LO) PLUG DETECT

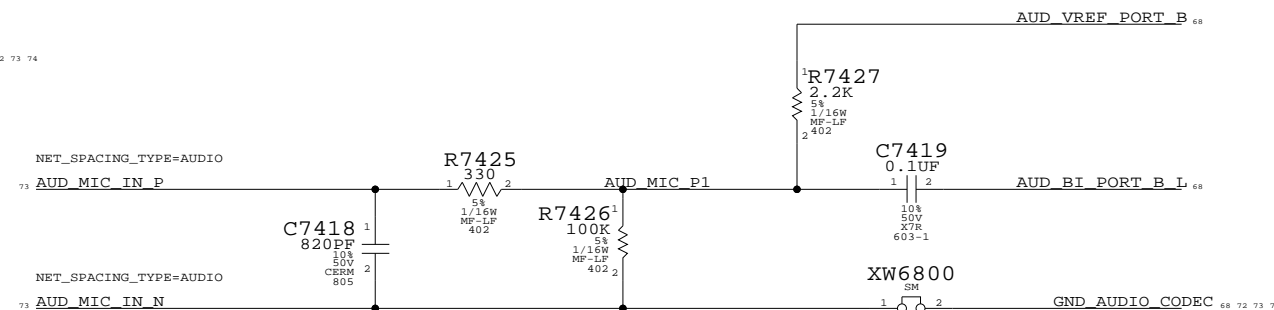


PORT A/H (HP/LI/DIG_OUT) PLUG DETECT (E TELLS H TO COME ON)

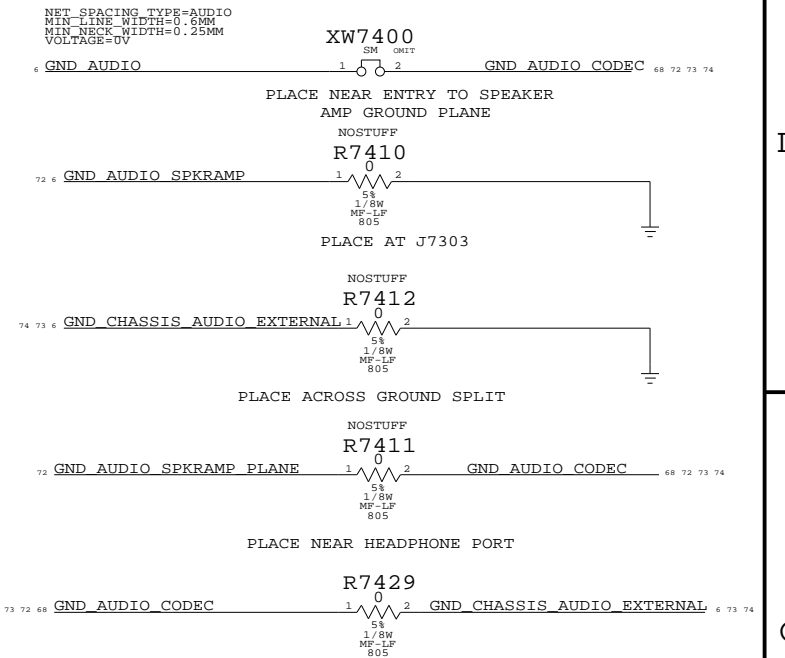


R7432 ENABLES PORT B AND C

MICROPHONE IMPEDANCE MATCHING CIRCUIT



AUDIO GROUND RETURNS



AUDIO: POWER SUPPLIES

SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005

NOTICE OF PROPRIETARY PROPERTY

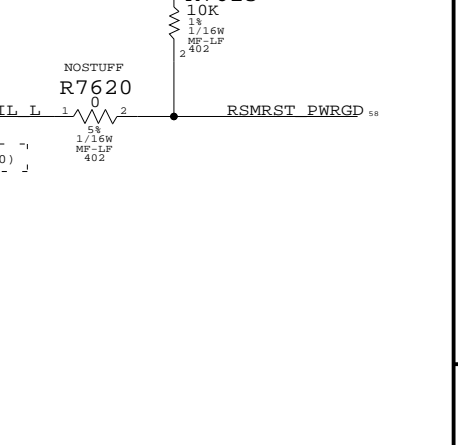
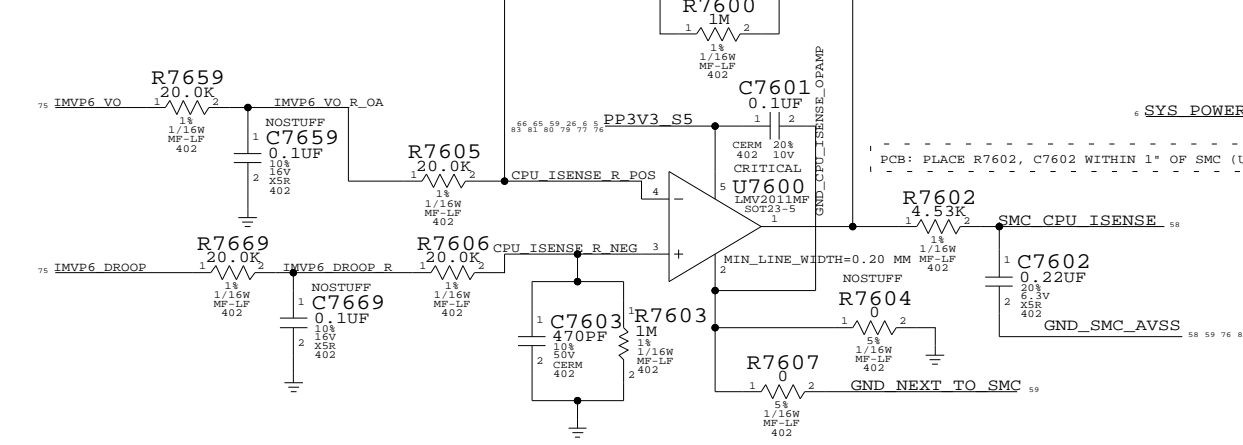
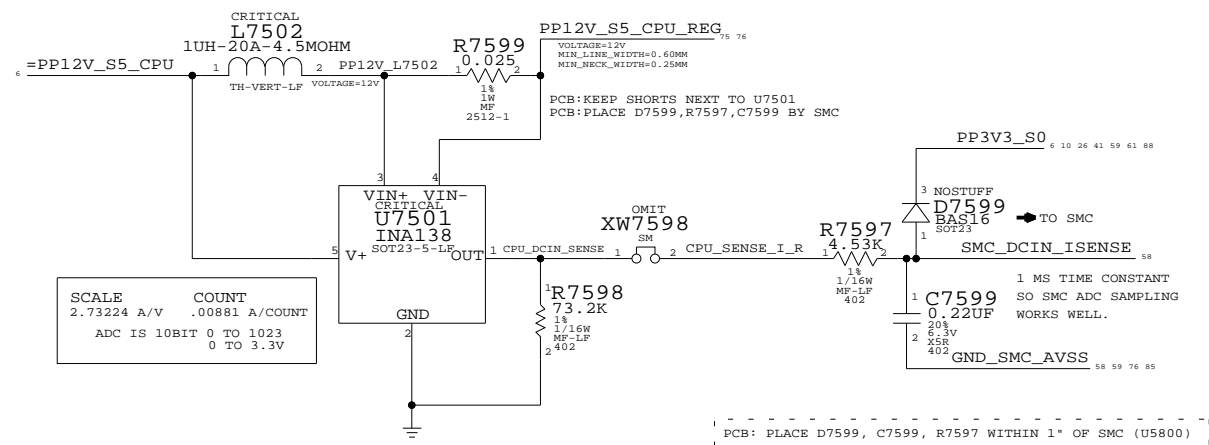
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6790	??
SCALE	NONE	SHT	OF
		74	154

PROCESSOR VCORE CURRENT SENSE
(USING 12V INPUT CURRENT TO DERIVE CPU CURRENT)

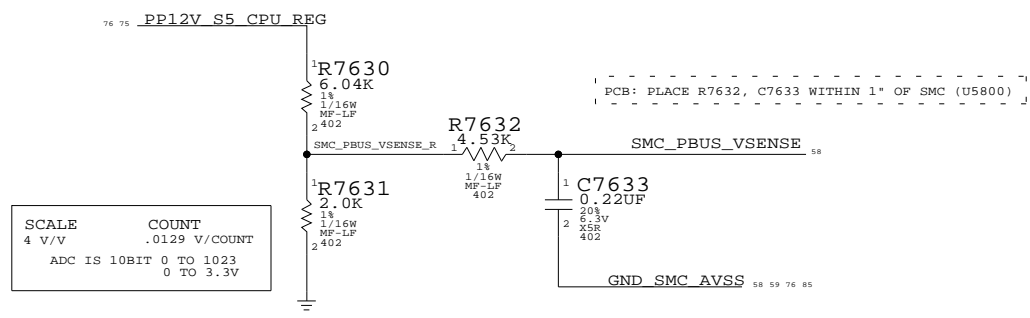
PROCESSOR VCORE CURRENT SENSE
(MEASURING DC/DC INDUCTOR DCR TO DERIVE CPU CURRENT)

SMC PWRGD PULLUP

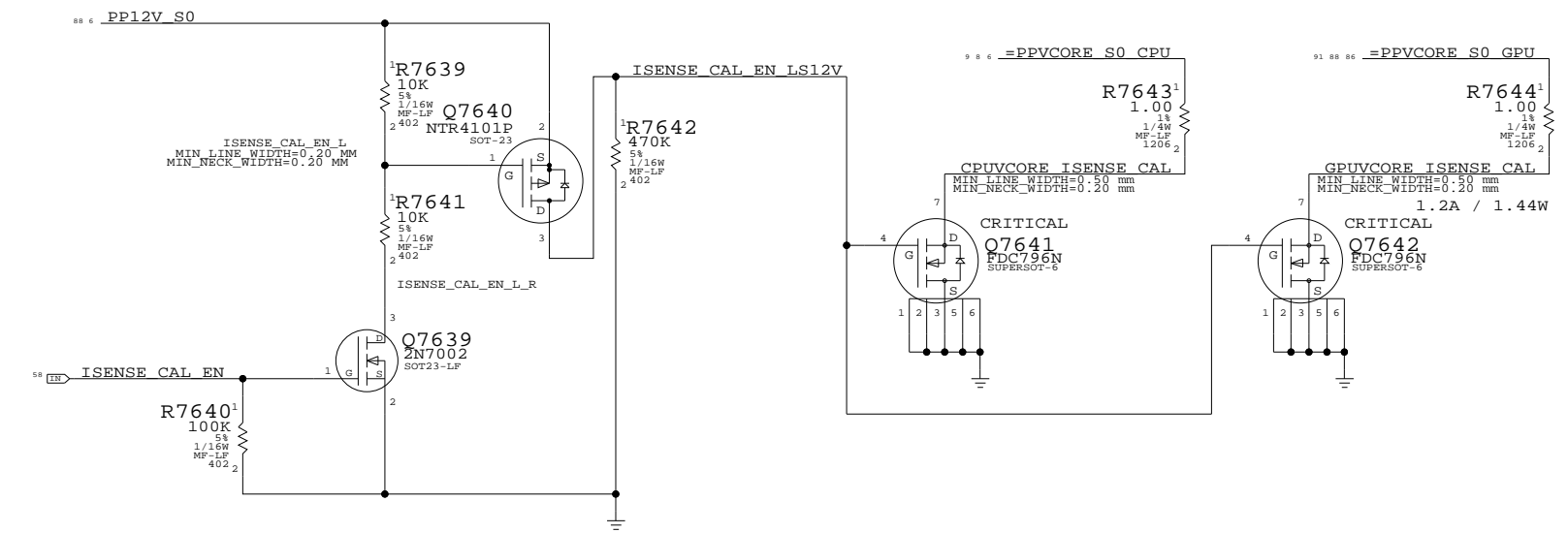


PROCESSOR DCIN VOLTAGE SENSE
(SCALING 12V INPUT VOLTAGE TO SMC)

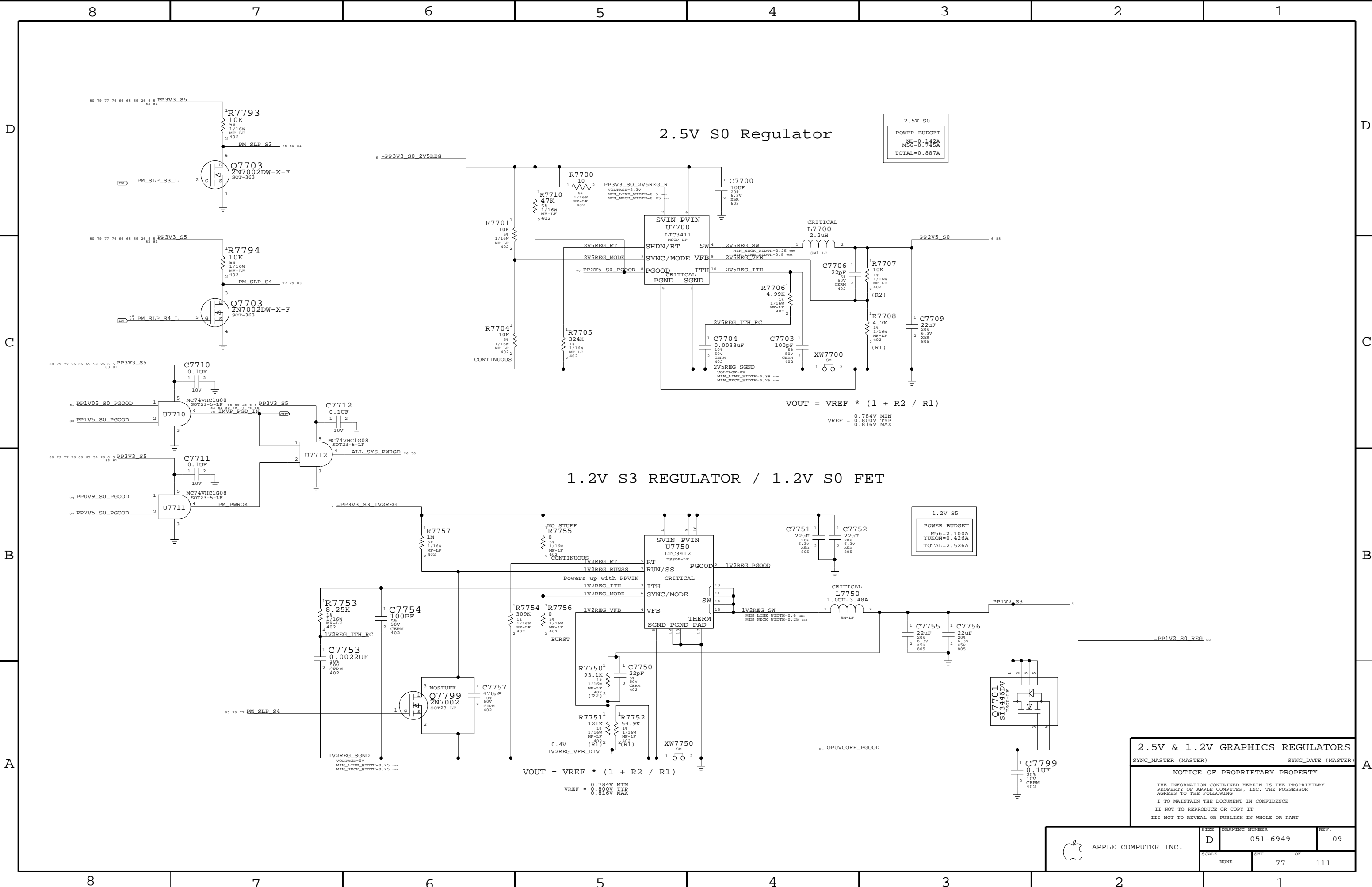
PROCESSOR VCORE SENSE



Current Sense Calibration Circuit
Switches in fixed load on power supplies to calibrate current sense circuits



CPU SENSE CIRCUITRIES
SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)
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2.5V S0 Regulator

2.5V S0
POWER BUDGET
MS6=0.142A
YUKON=0.745A
TOTAL=0.887A

$V_{OUT} = V_{REF} * (1 + R2 / R1)$
 $V_{REF} = 0.784V \text{ MIN}$
 $V_{REF} = 0.800V \text{ TYP}$
 $V_{REF} = 0.816V \text{ MAX}$

1.2V S3 REGULATOR / 1.2V S0 FET

1.2V S5
POWER BUDGET
MS6=2.100A
YUKON=0.426A
TOTAL=2.526A

$V_{OUT} = V_{REF} * (1 + R2 / R1)$
 $V_{REF} = 0.784V \text{ MIN}$
 $V_{REF} = 0.800V \text{ TYP}$
 $V_{REF} = 0.816V \text{ MAX}$

2.5V & 1.2V GRAPHICS REGULATORS

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

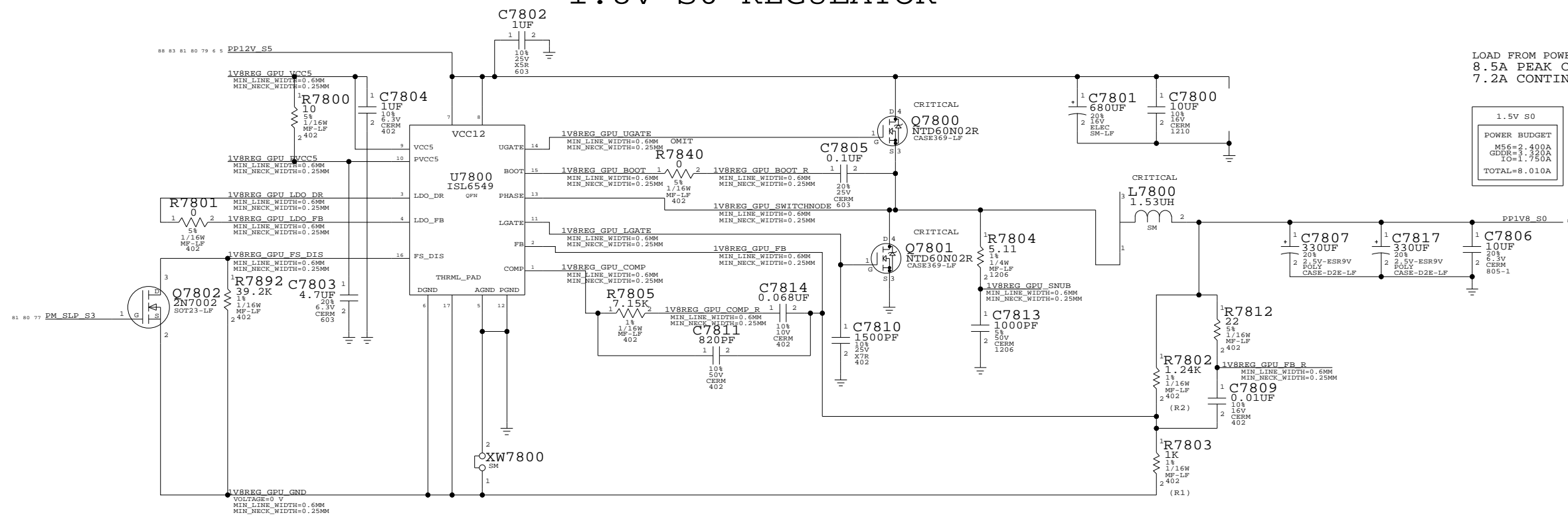
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	D	051-6949	09
SCALE	SHT	OF	
NONE	77	111	

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
11480514	1	5.11 OHM 0402 1% 1/16W LF	R7840		

1.8V S0 REGULATOR



LOAD FROM POWER BUDGET
8.5A PEAK CURRENT DRAW
7.2A CONTINUOUS CURRENT DRAW

1.5V S0	
POWER BUDGET	
M56=	2.400A
GDDR=	3.200A
IO=	1.750A
TOTAL=8.010A	

$$V_{OUT} = V_{REF} * (1 + R2/R1)$$

VREF = 0.784V MIN
0.800V TYP
0.816V MAX

1.8V GDDR REGULATOR

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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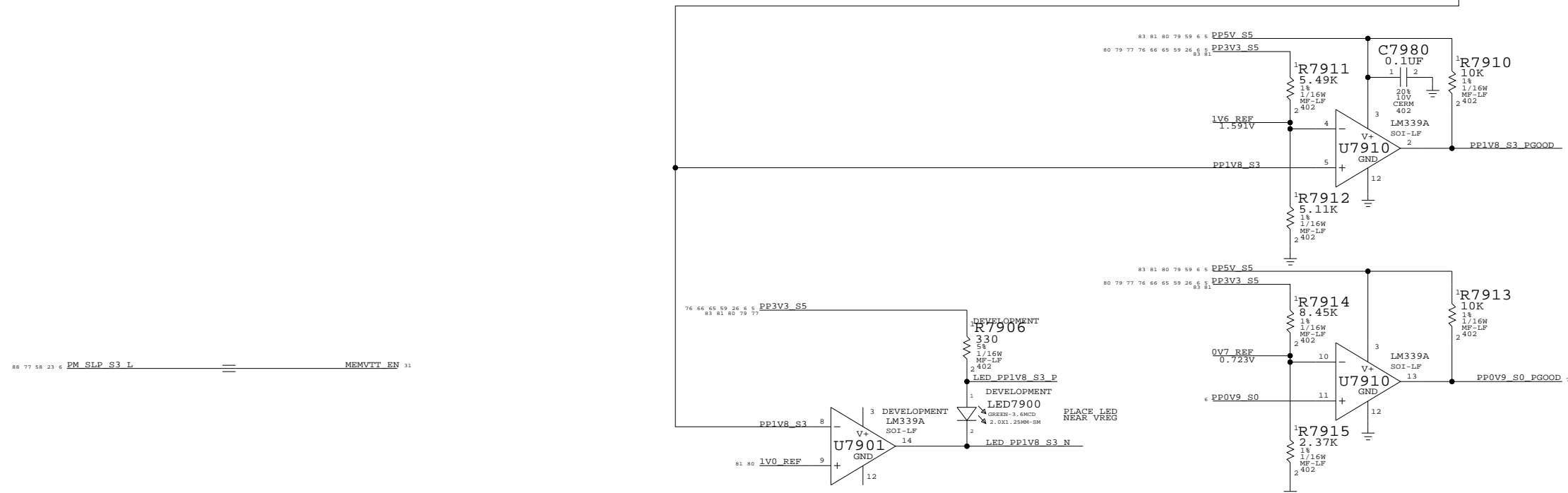
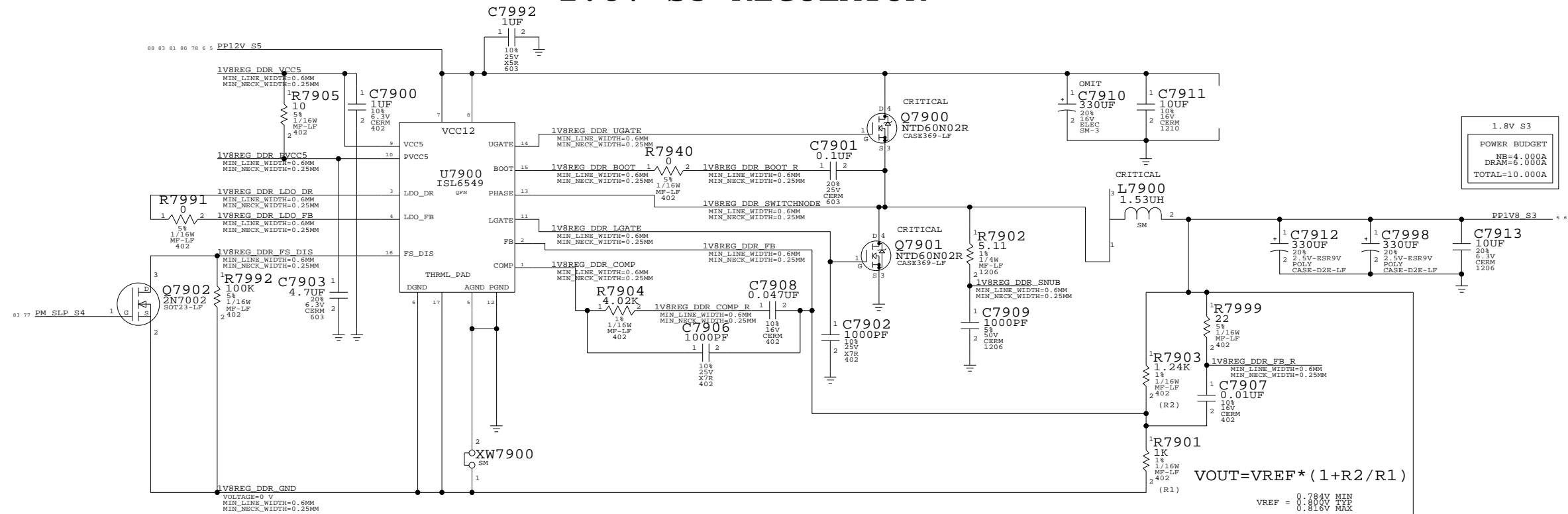
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	SHT	78 OF 111	
NONE			

TRUE

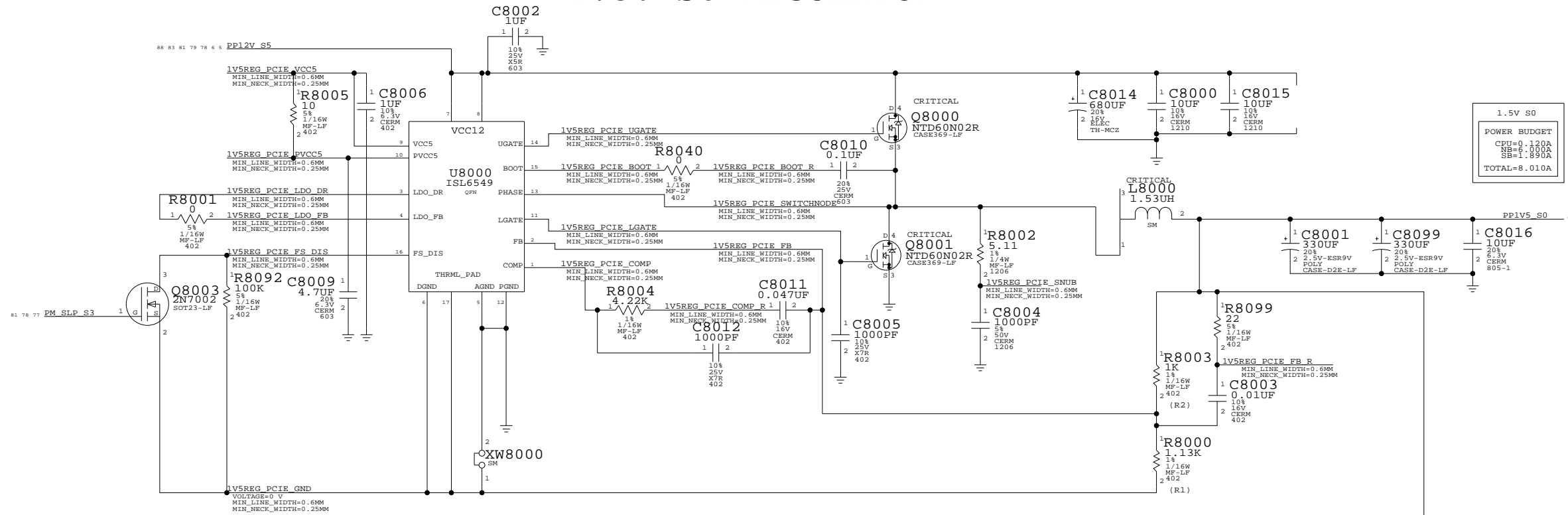
1.8V S3 REGULATOR



1.8V Vreg
 SYNC_MASTER=M23-PC SYNC_DATE=04/12/2005
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	SHT	79 OF	111
NONE			

1.5V S0 REGULATOR

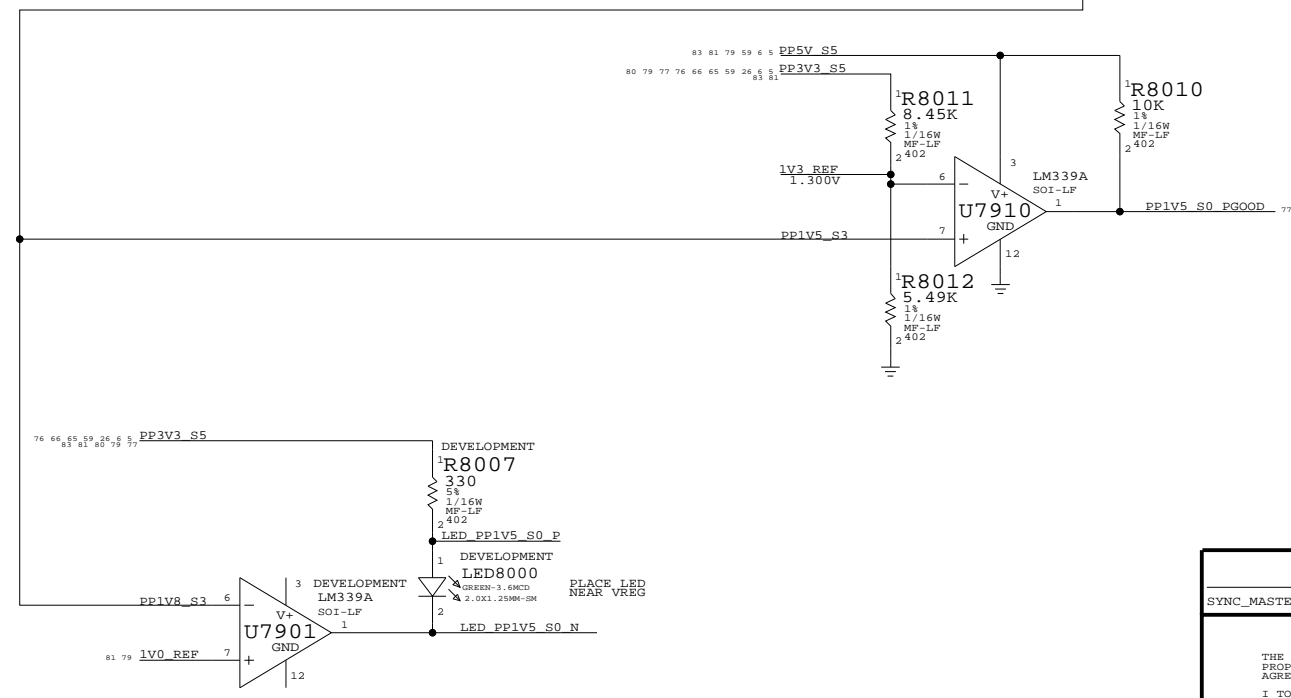


$$V_{OUT} = V_{REF} * (1 + R2/R1)$$

$$V_{REF} = 0.784V \text{ MIN}$$

$$V_{REF} = 0.800V \text{ TYP}$$

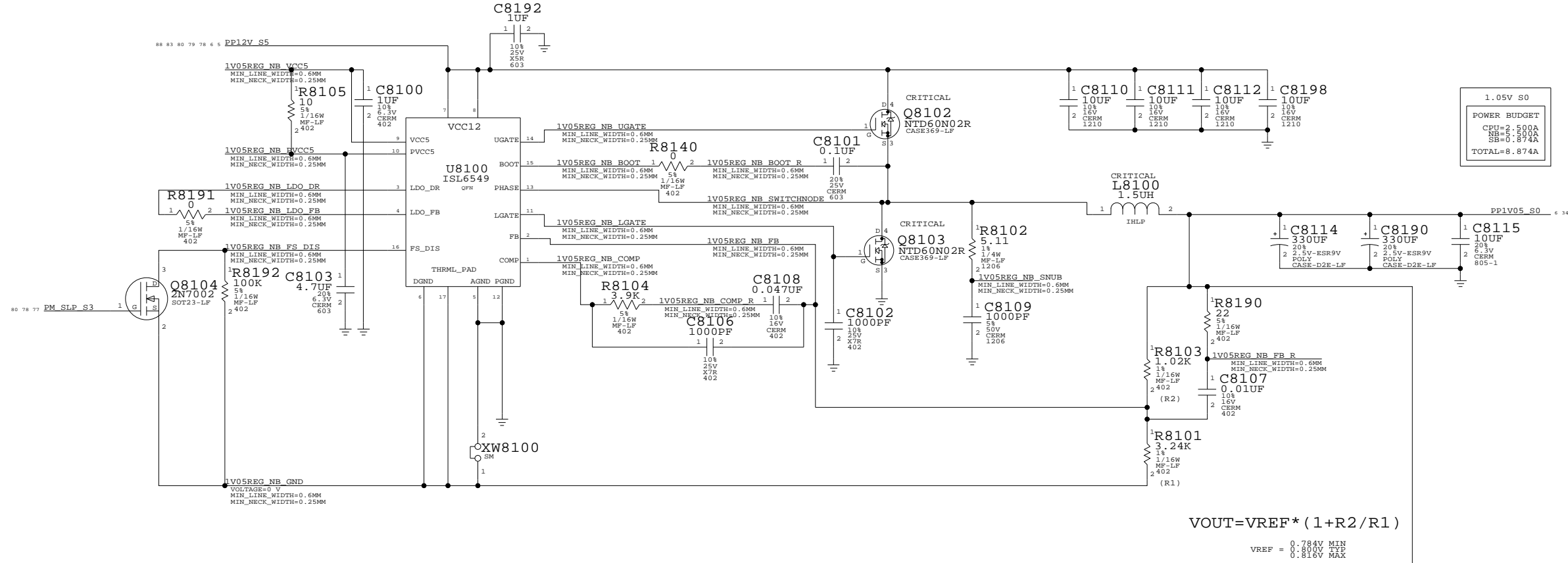
$$V_{REF} = 0.816V \text{ MAX}$$



1.5V Vreg
 SYNC_MASTER=FINO-PC SYNC_DATE=05/18/2005
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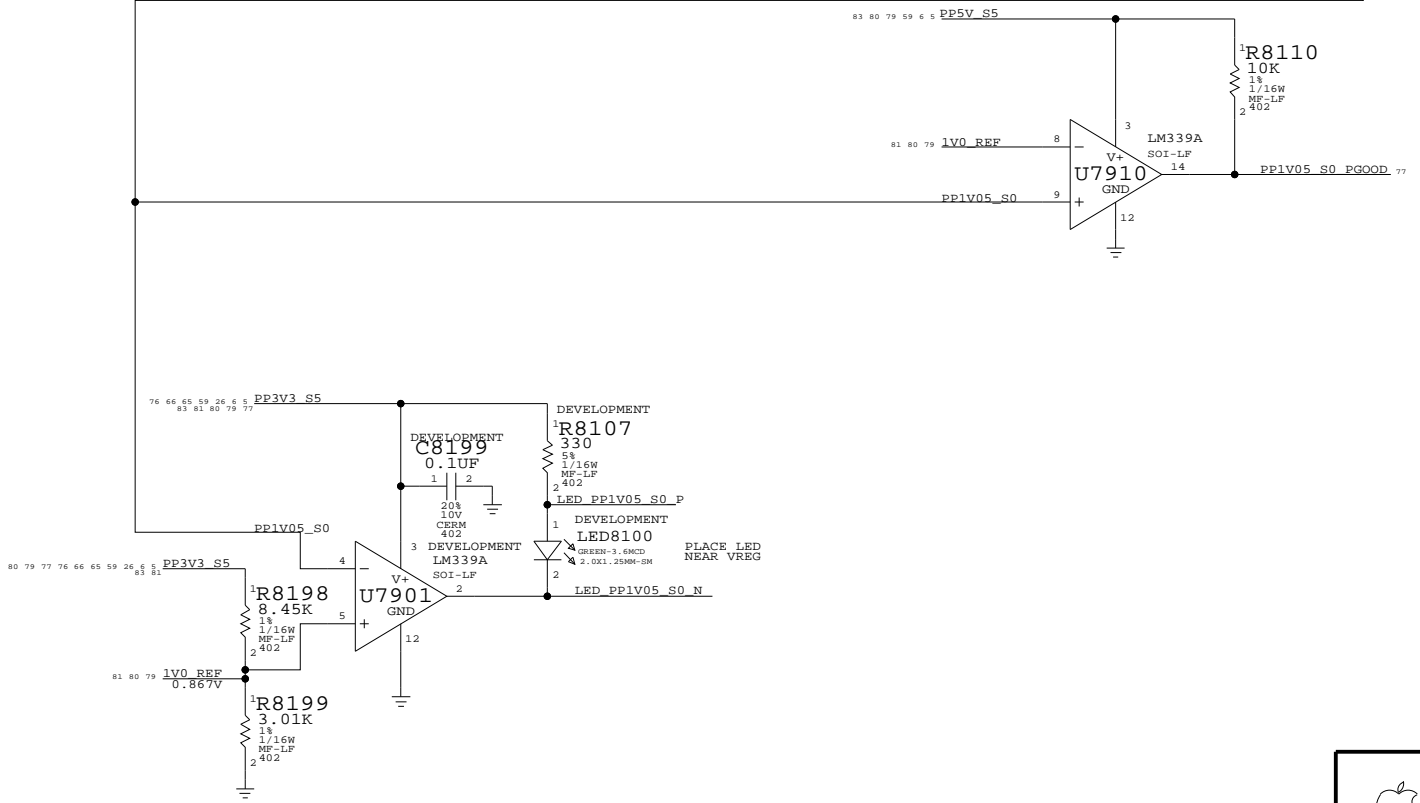
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	SHT	80 OF	111
NONE			

1.05V S0 REGULATOR



$$V_{OUT} = V_{REF} * (1 + R2/R1)$$

VREF = 0.784V MIN
0.800V TYP
0.816V MAX



1.05V VREG

SYNC_MASTER=M38-RT SYNC_DATE=05/18/2005

NOTICE OF PROPRIETARY PROPERTY

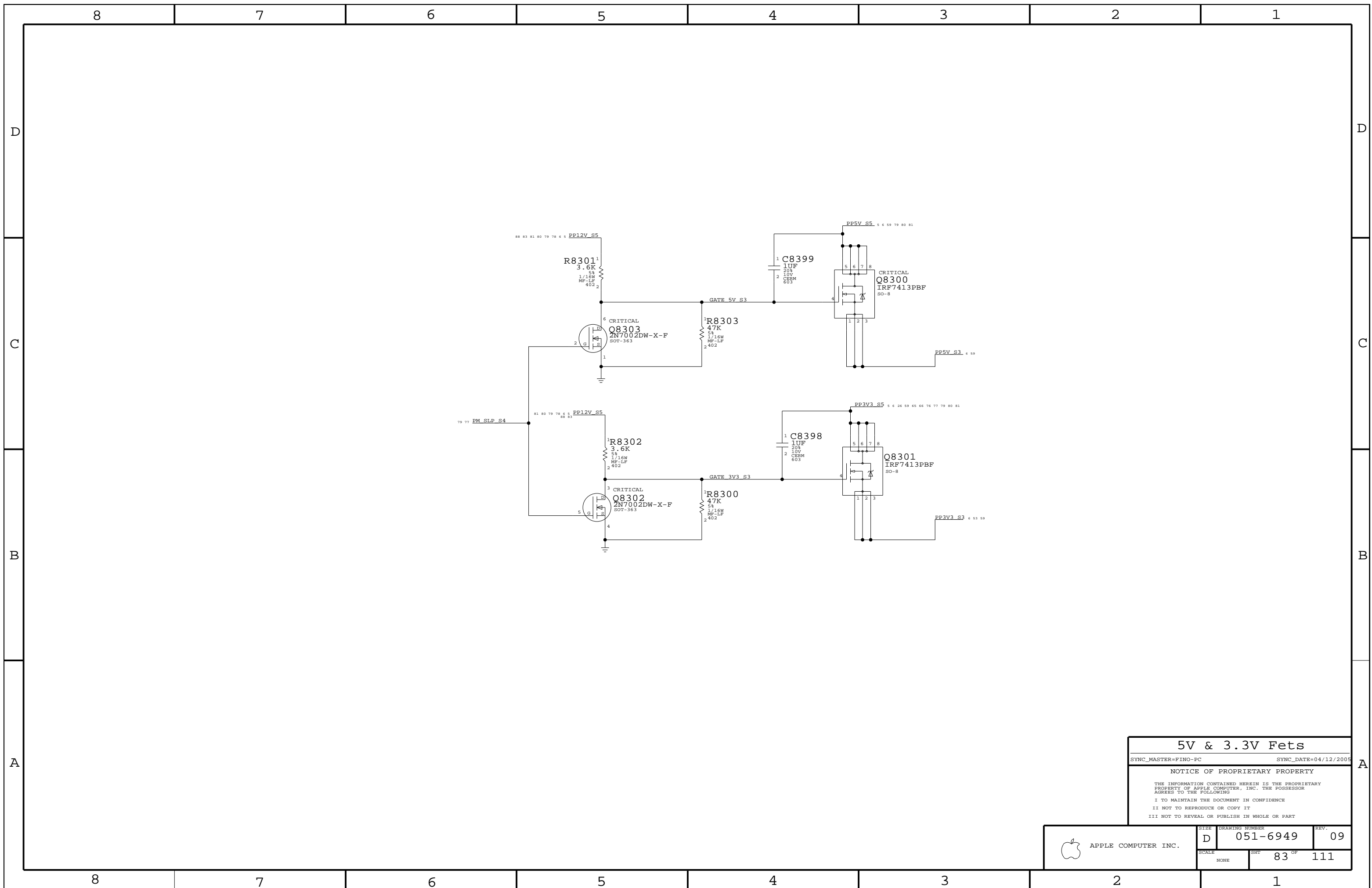
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	D	051-6949	09
SCALE	SHT	81 OF	111
NONE			



5V & 3.3V Fets

SYNC_MASTER=FINO-PC SYNC_DATE=04/12/2005


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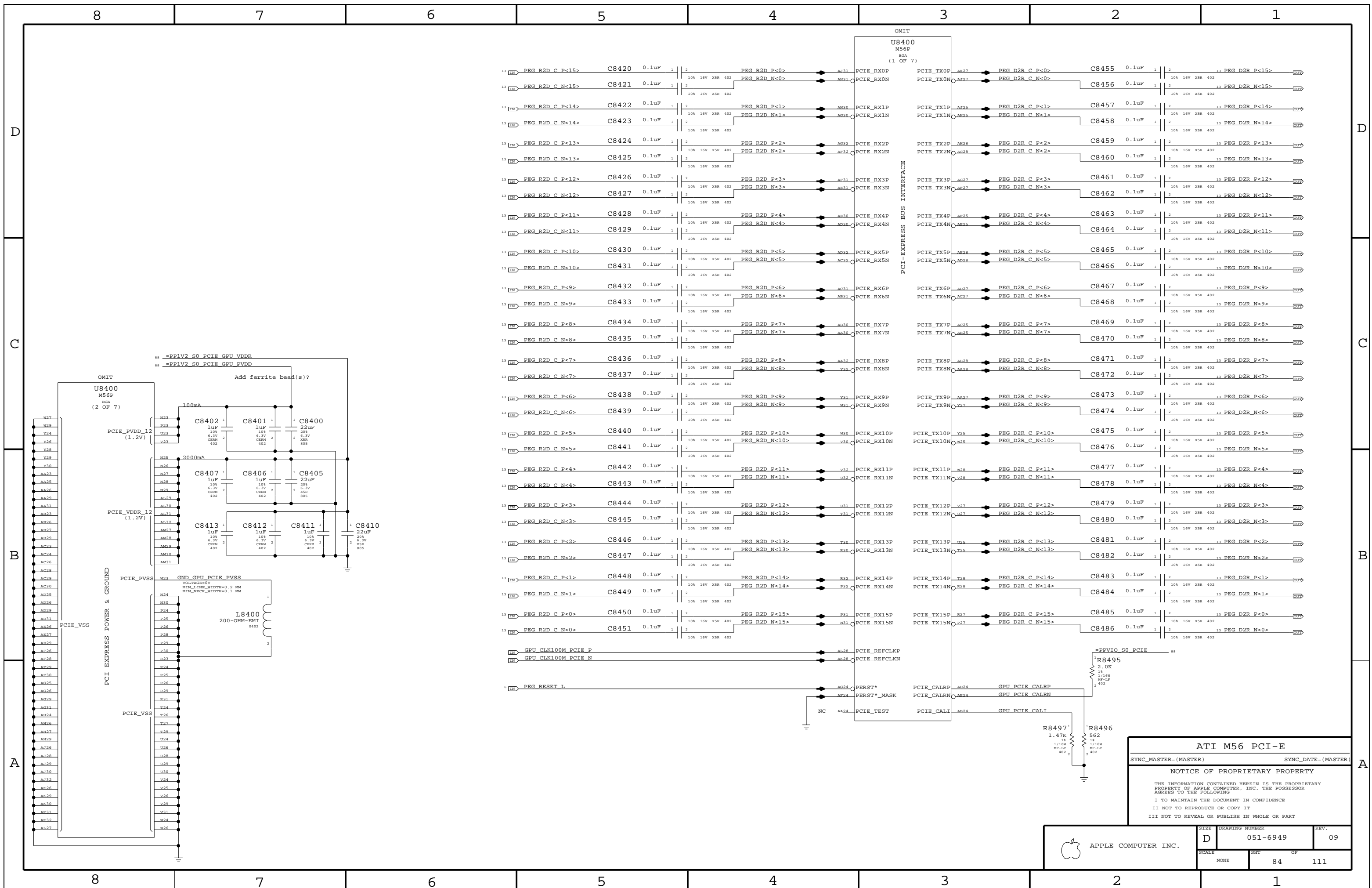
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	D	051-6949	09
SCALE	SHT	OF	
NONE	83	111	



Signal Name	Capacitor	Value	GPU Pin	System Pin	GPU Pin	System Pin
PEG R2D C P<15>	C8420	0.1uF	1	2	PCIE_RX0P	AE27
PEG R2D C N<15>	C8421	0.1uF	1	2	PCIE_RX0N	AE27
PEG R2D C P<14>	C8422	0.1uF	1	2	PCIE_RX1P	AE25
PEG R2D C N<14>	C8423	0.1uF	1	2	PCIE_RX1N	AE25
PEG R2D C P<13>	C8424	0.1uF	1	2	PCIE_RX2P	AE28
PEG R2D C N<13>	C8425	0.1uF	1	2	PCIE_RX2N	AE28
PEG R2D C P<12>	C8426	0.1uF	1	2	PCIE_RX3P	AE27
PEG R2D C N<12>	C8427	0.1uF	1	2	PCIE_RX3N	AE27
PEG R2D C P<11>	C8428	0.1uF	1	2	PCIE_RX4P	AE25
PEG R2D C N<11>	C8429	0.1uF	1	2	PCIE_RX4N	AE25
PEG R2D C P<10>	C8430	0.1uF	1	2	PCIE_RX5P	AE28
PEG R2D C N<10>	C8431	0.1uF	1	2	PCIE_RX5N	AE28
PEG R2D C P<9>	C8432	0.1uF	1	2	PCIE_RX6P	AE27
PEG R2D C N<9>	C8433	0.1uF	1	2	PCIE_RX6N	AE27
PEG R2D C P<8>	C8434	0.1uF	1	2	PCIE_RX7P	AE25
PEG R2D C N<8>	C8435	0.1uF	1	2	PCIE_RX7N	AE25
PEG R2D C P<7>	C8436	0.1uF	1	2	PCIE_RX8P	AE28
PEG R2D C N<7>	C8437	0.1uF	1	2	PCIE_RX8N	AE28
PEG R2D C P<6>	C8438	0.1uF	1	2	PCIE_RX9P	AE27
PEG R2D C N<6>	C8439	0.1uF	1	2	PCIE_RX9N	AE27
PEG R2D C P<5>	C8440	0.1uF	1	2	PCIE_RX10P	AE25
PEG R2D C N<5>	C8441	0.1uF	1	2	PCIE_RX10N	AE25
PEG R2D C P<4>	C8442	0.1uF	1	2	PCIE_RX11P	AE28
PEG R2D C N<4>	C8443	0.1uF	1	2	PCIE_RX11N	AE28
PEG R2D C P<3>	C8444	0.1uF	1	2	PCIE_RX12P	AE27
PEG R2D C N<3>	C8445	0.1uF	1	2	PCIE_RX12N	AE27
PEG R2D C P<2>	C8446	0.1uF	1	2	PCIE_RX13P	AE25
PEG R2D C N<2>	C8447	0.1uF	1	2	PCIE_RX13N	AE25
PEG R2D C P<1>	C8448	0.1uF	1	2	PCIE_RX14P	AE28
PEG R2D C N<1>	C8449	0.1uF	1	2	PCIE_RX14N	AE28
PEG R2D C P<0>	C8450	0.1uF	1	2	PCIE_RX15P	AE27
PEG R2D C N<0>	C8451	0.1uF	1	2	PCIE_RX15N	AE27
GPU_CLK100M_PCIE_P					PCIE_REFCLKP	AE28
GPU_CLK100M_PCIE_N					PCIE_REFCLKN	AE28
PEG RESET L					PERST*	AE24
					PERST*_MASK	AE24
					PCIE_TEST	AA24
					PCIE_CALRP	AD24
					PCIE_CALRN	AE24
					PCIE_CALI	AB24
					GPU_PCIE_CALRP	
					GPU_PCIE_CALRN	
					GPU_PCIE_CALI	

ATI M56 PCI-E

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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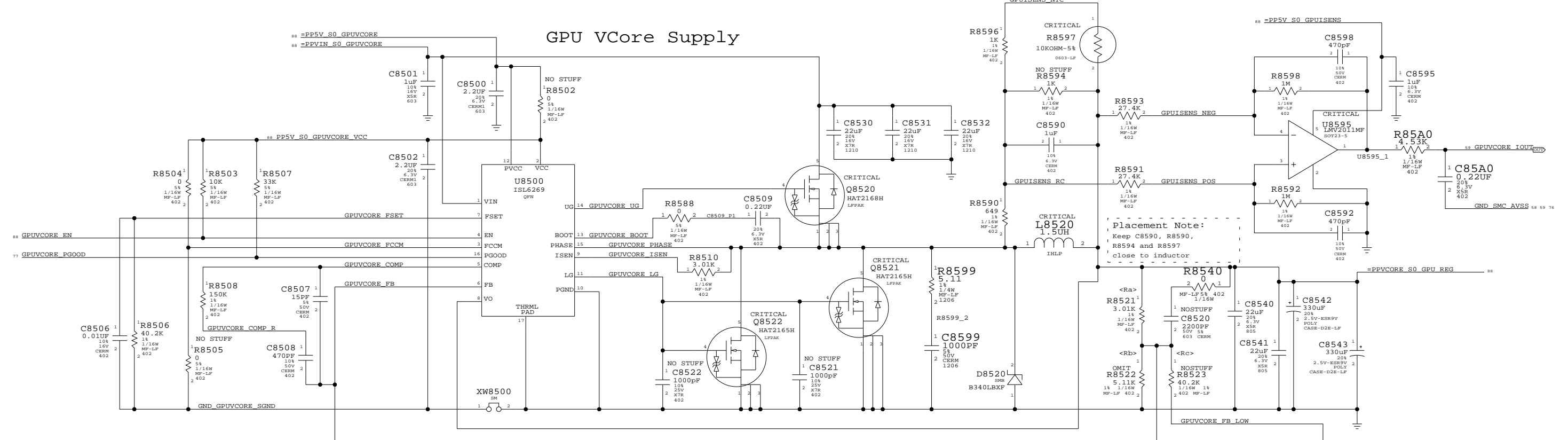
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SCALE	SHT	OF	
NONE	84	111	

GPU VCore Current Sense



Placement Note:
 Keep C8590, R8590,
 R8594 and R8597
 close to inductor

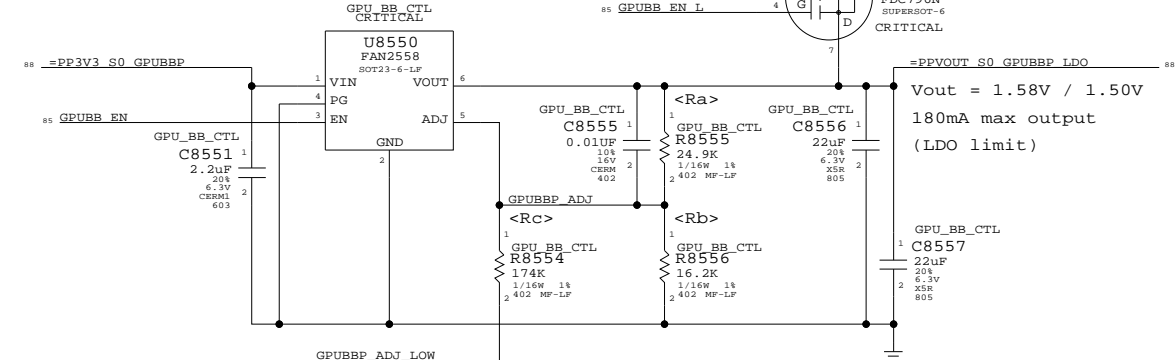
$$V_{out}(low) = 0.6V * (1 + R_a/R_b)$$

$$V_{out}(high) = 0.6V * (1 + R_a/Req)$$

$$Req = R_b || R_c$$

Back-Bias Positive Supply

Back-bias positive supply provides VDDC + 0.5V when active. When inactive, provides VDDC to BBP pins.
 NOTE: BBP tracks VDDC based on GPU voltage GPIO.



$$V_{out} = 1.58V / 1.50V$$

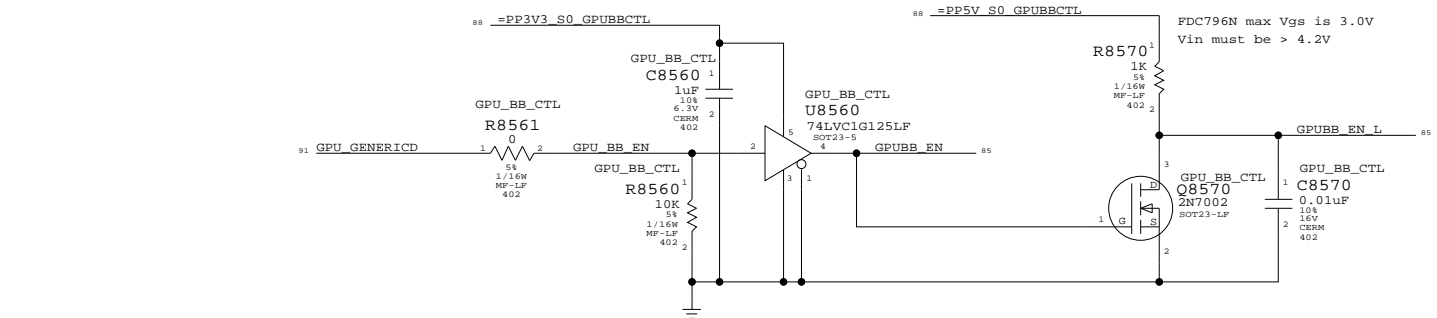
180mA max output (LDO limit)

$$V_{out}(low) = 0.59V * (1 + R_a/R_b)$$

$$V_{out}(high) = 0.59V * (1 + R_a/Req)$$

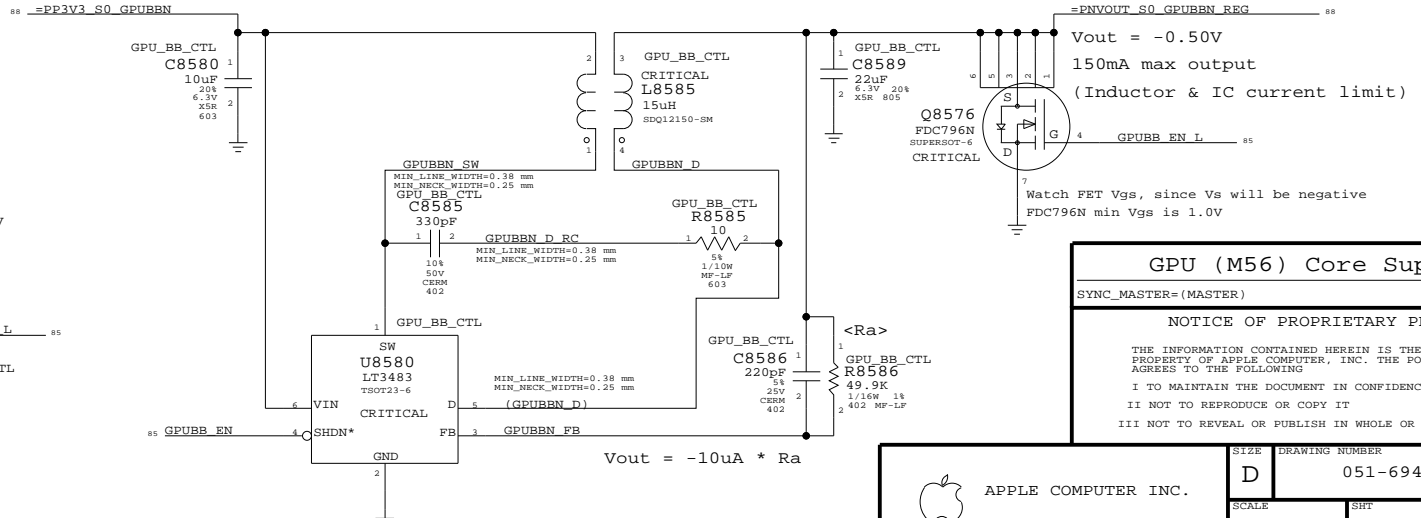
$$Req = R_b || R_c$$

Pull-up voltage must be high enough to satisfy BBP FET Vgs (where Vs = 1.2V)



Back-Bias Negative Supply

Back-bias negative supply provides VSS - 0.5V when active. When inactive, provides VSS to BBN pins.



$$V_{out} = -0.50V$$

150mA max output (Inductor & IC current limit)

$$V_{out} = -10\mu A * R_a$$

GPU (M56) Core Supplies

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	D	051-6949	09
SCALE	SHT	OF	
NONE	85	111	

Page Notes

Power aliases required by this page:
 - =PP1V5_GPU_VDD15
 - =PP1VR1V3_GPU_VCORE

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)

8 7 6 5 4 3 2 1

D

D

C

C

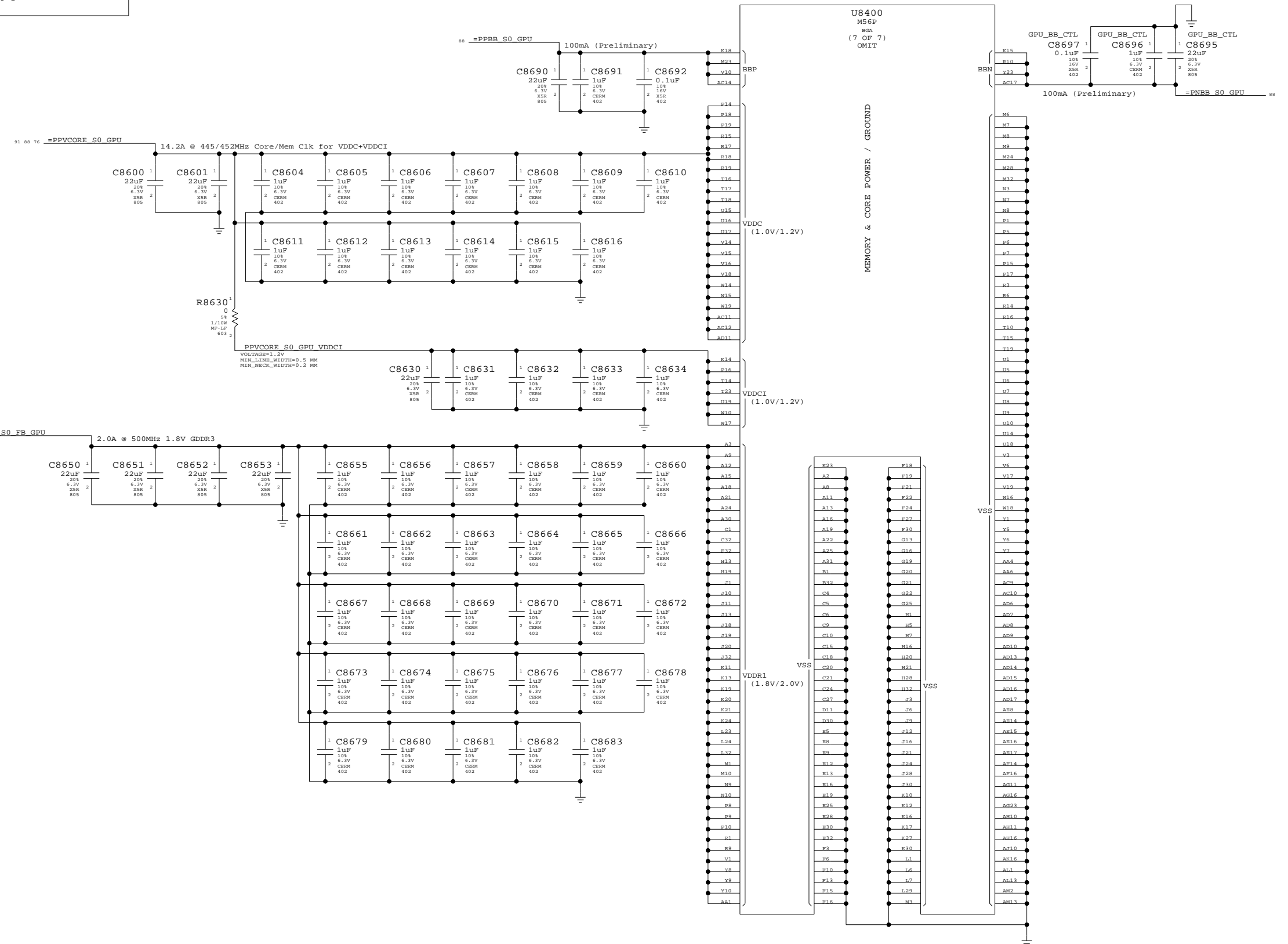
B

B

A

A

8 7 6 5 4 3 2 1



ATI M56 Core Power

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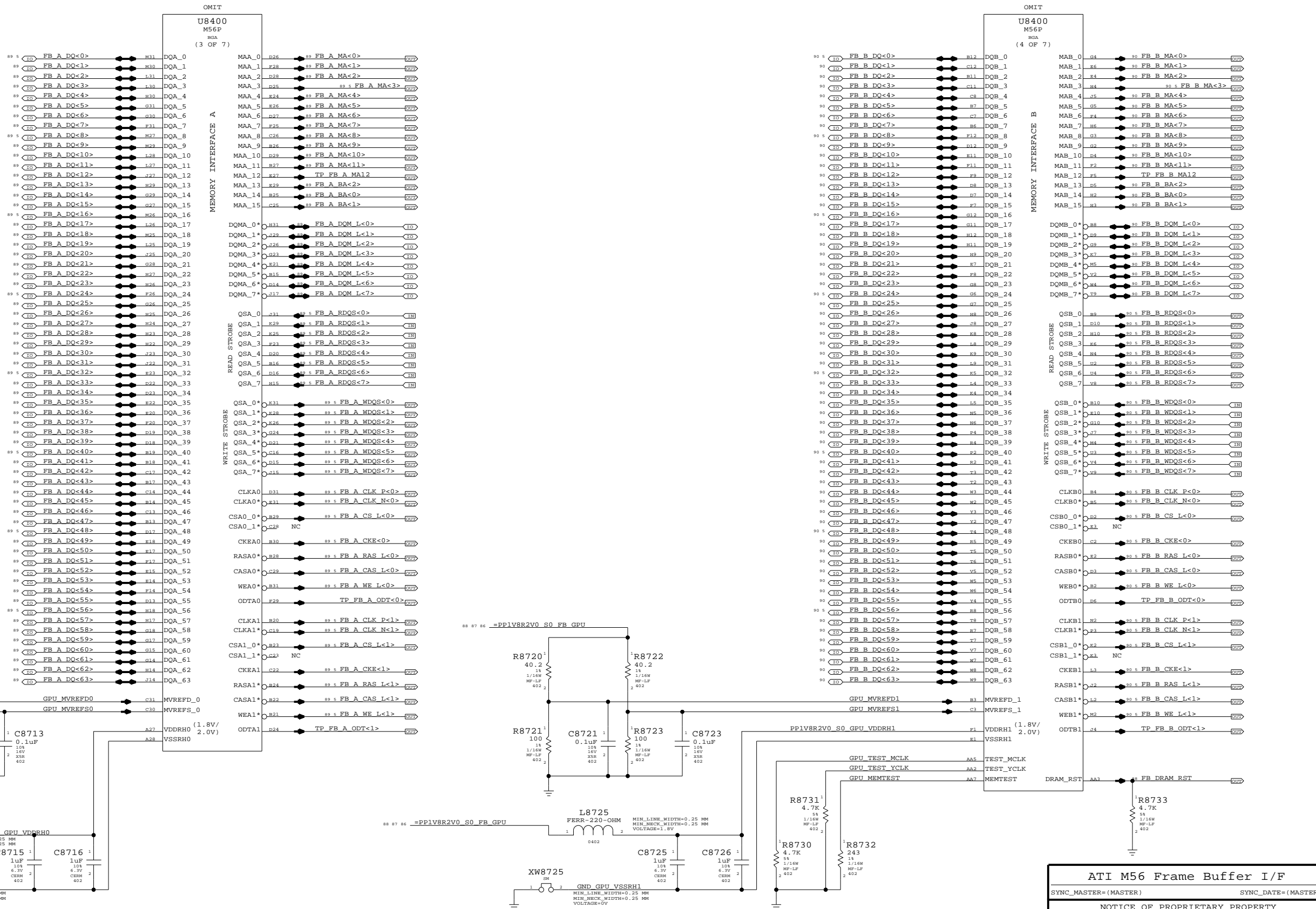
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	NONE	SHT	OF
		86	111

Page Notes

Power aliases required by this page:
- =PP1V8R2V0_S0_FB_GPU
Signal aliases required by this page:
(NONE)
BOM options provided by this page:
(NONE)



ATI M56 Frame Buffer I/F

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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Table with columns for DRAWING NUMBER (D 051-6949), REV. (09), SCALE (NONE), SHEET (87), and TOTAL SHEETS (111). Includes the Apple Computer Inc. logo.

8

7

6

5

4

3

2

1

"S0" GPU RAILS

ONLY ON IN RUN

59 EP1V0R1V2_S0_GPU
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=1.2V

85 PP5V_S0_GPUVCORE_VCC
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=5V

PP1V2_GPU_IO_S0
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=1.2V

PPBB_S0_GPU
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.5MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=1.2V

PNBB_S0_GPU
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.5MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=0V

76 61 59 41 26 10 6 PP3V3_S0
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=1.2V

77 6 PP2V5_S0
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=1.2V

PP1V8R2V0_S0_FB_GPU
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=1.8V

83 81 80 79 78 6 5 PP12V_S5
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=1.2V

76 6 PP12V_S0
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=1.2V

97 94 76 6 PP5V_S0
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=5V

85 GPUVCORE_EN
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=5V

87 FB_DRAM_RST
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=5V

M56 GPIOs

94 91 GPU_GPIO_0
 GPIO 0 = TRANSMITTER POWER SAVINGS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_1
 GPIO 1 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_2
 GPIO 2 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_3
 GPIO 3 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_4
 GPIO 4 = DEBUG SIGNALS OUT

91 GPU_GPIO_5
 GPIO 5 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_6
 GPIO 6 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

TP_GPU_GPIO_7
 MAKE_BASE=TRUE
 GPIO 7 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_8
 GPIO 8 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

NC_GPU_GPIO_10
 MAKE_BASE=TRUE
 GPIO 10 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_9
 GPIO 9 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_13
 GPIO 13 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_12
 GPIO 12 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_11
 GPIO 11 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

GPIO 9,13,12,11 = ROM ID CFG
 INTERNAL PULL DOWN
 0010 = 256 M APERATURE SIZE

91 GPU_GPIO_24
 GPIO 24 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_27
 GPIO 27 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_28
 GPIO 28 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

91 GPU_GPIO_29
 GPIO 29 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

85 GPU_VCORE_LOW
 MAKE_BASE=TRUE
 MIN_LINE_WIDTH=0.6MM
 MIN_NECK_WIDTH=0.125MM
 VOLTAGE=5V
 GPIO 15 = SWITCH CORE VOLTAGE HIGH TO LOW
 EXTERNAL PULL DOWN RECOMMENDED

=PP3V3_S0_GPU_VDDR3 88 91

TP_GPU_GPIO_14
 MAKE_BASE=TRUE
 GPIO 14 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

TP_GPU_GPIO_17
 MAKE_BASE=TRUE
 GPIO 17 = TRANSMITTER DE-EMPHASIS ENABLE
 INTERNAL PULL DOWN, ATI RECOMMENDS HIGH

TP_GPU_VGA_R
 MAKE_BASE=TRUE
 GPU_VGA_R

TP_GPU_VGA_G
 MAKE_BASE=TRUE
 GPU_VGA_G

TP_GPU_VGA_B
 MAKE_BASE=TRUE
 GPU_VGA_B

TP_GPU_VGA_HSYNC
 MAKE_BASE=TRUE
 GPU_VGA_HSYNC

TP_GPU_VGA_VSYNC
 MAKE_BASE=TRUE
 GPU_VGA_VSYNC

TP_GPU_TV_Y
 MAKE_BASE=TRUE
 GPU_TV_Y

TP_GPU_TV_COMP
 MAKE_BASE=TRUE
 GPU_TV_COMP

TP_GPU_TV_C
 MAKE_BASE=TRUE
 GPU_TV_C

TP_GPU_DDC_B_CLK
 MAKE_BASE=TRUE
 GPU_DDC_B_CLK

TP_GPU_DDC_B_DATA
 MAKE_BASE=TRUE
 GPU_DDC_B_DATA

GPU MISC

8

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C

B

A

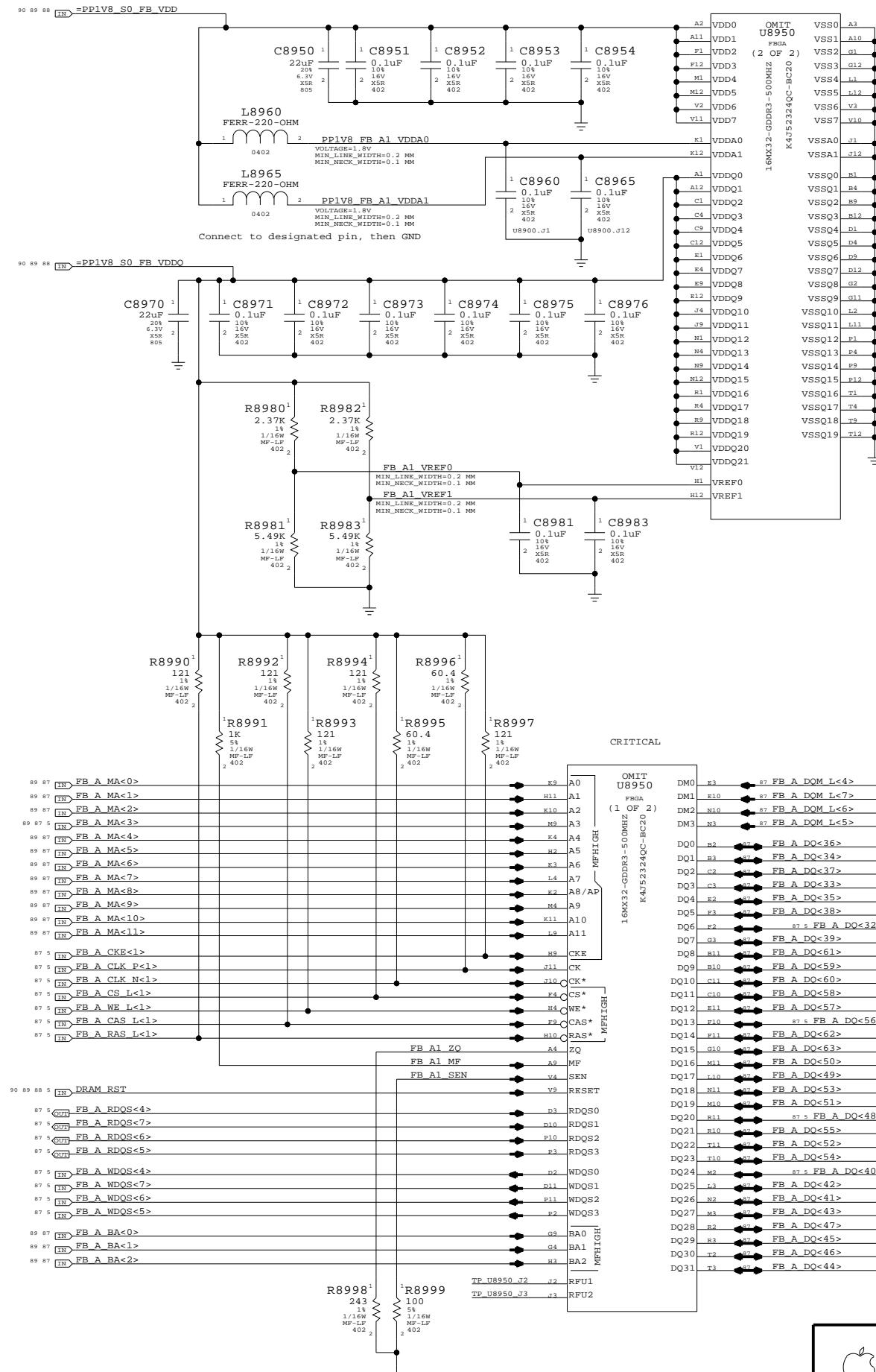
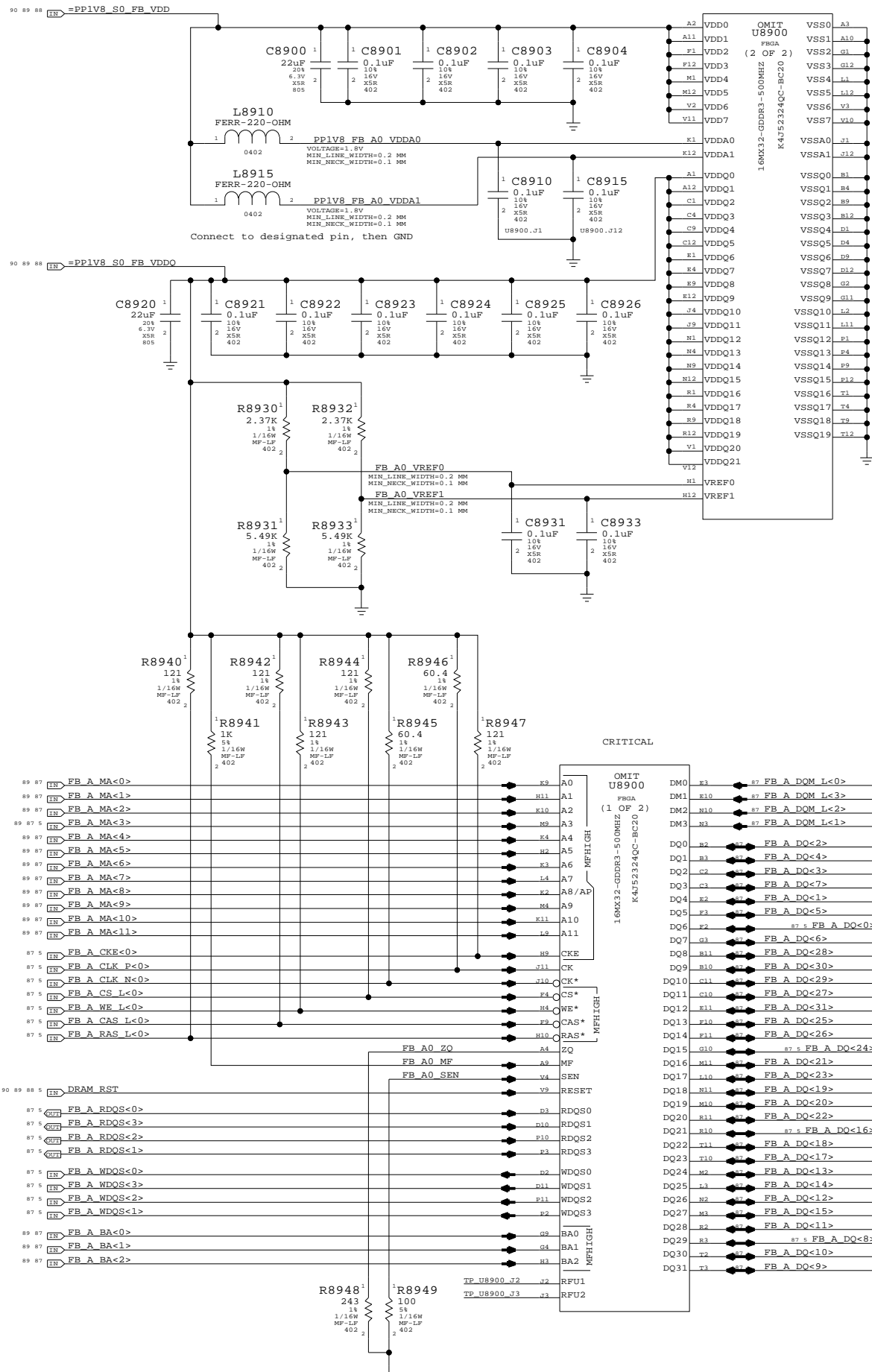
Power aliases required by this page:
 - =PPIV8_S0_FB_VDD
 - =PPIV8_S0_FB_VDDQ

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)

CRITICAL

CRITICAL



GDDR3 Frame Buffer A

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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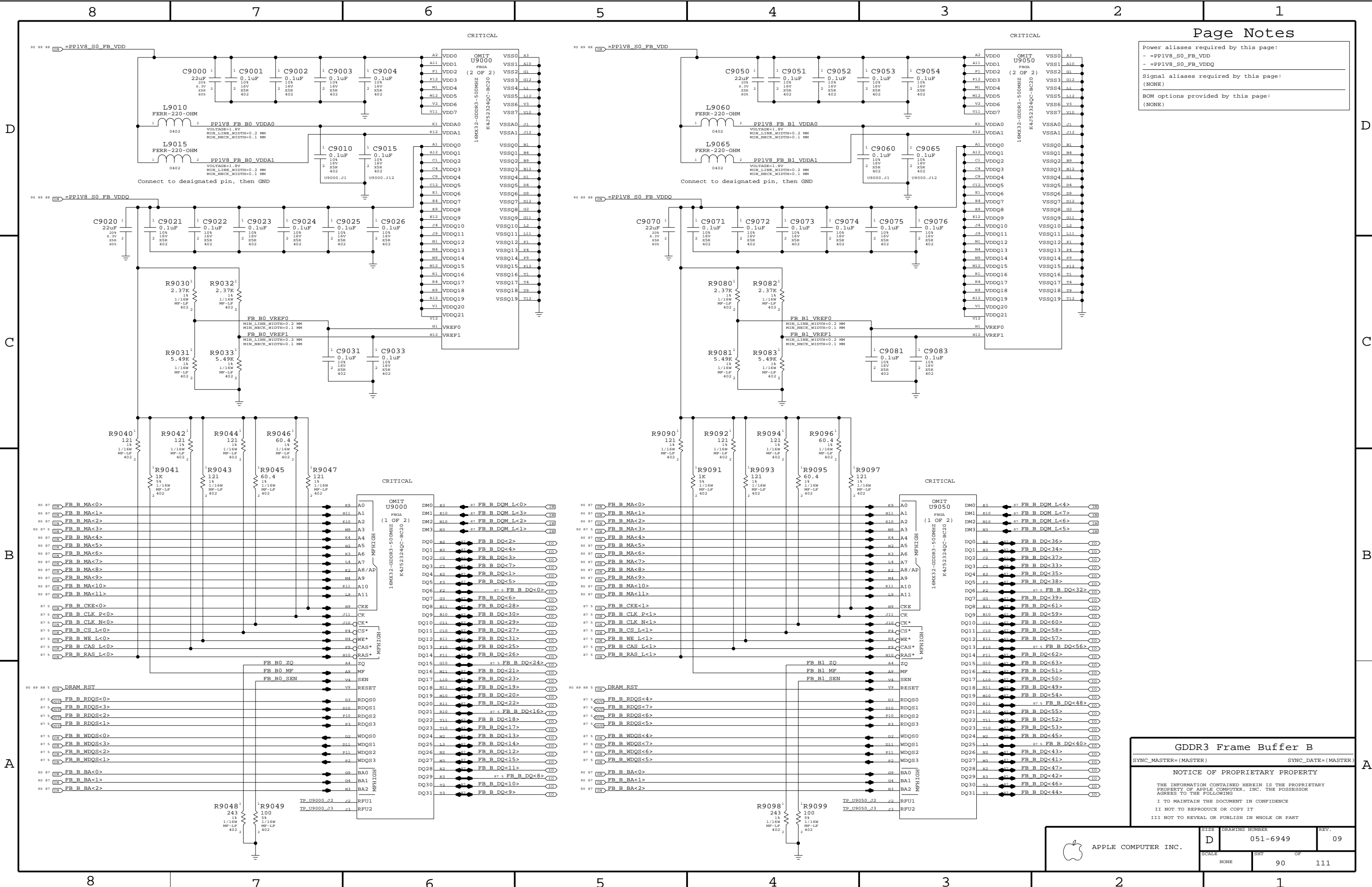
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 - =PPIV8_S0_FB_VDD
 - =PPIV8_S0_FB_VDDQ

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)



GDDR3 Frame Buffer B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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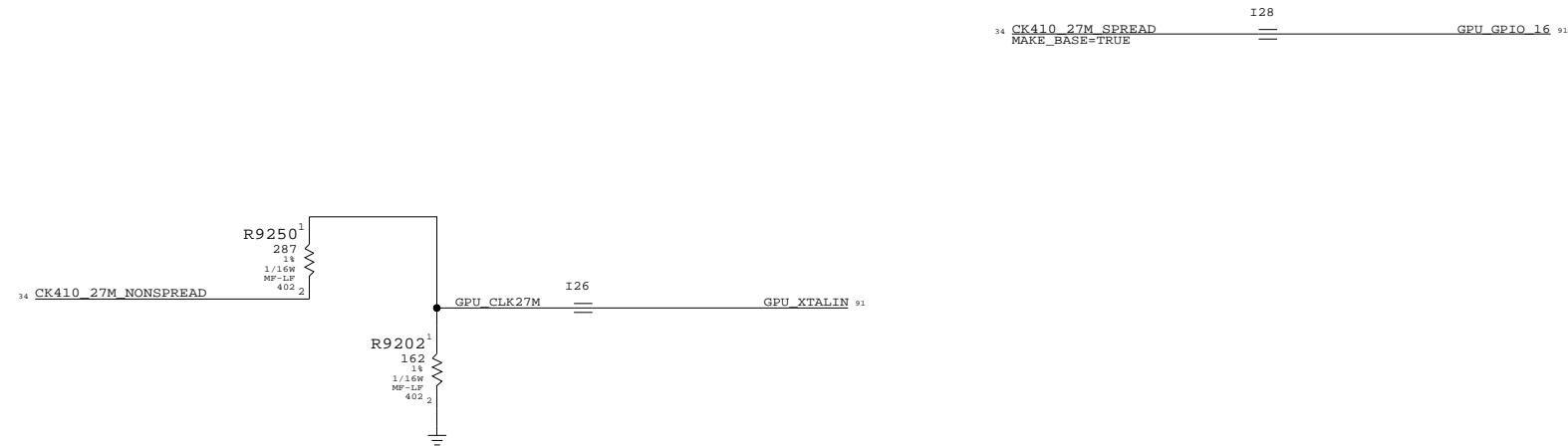
- =PP3V3_GPU_CLOCKS - =PP3V3_GPU_PWRSEQ
- =PPVIN_GPU_LVDDR_LDO - =PP2V5_GPU_PWRSEQ
- =PP2V5_GPU_LVDDR_LDO - =PP1V8_GPU_PWRSEQ
- =PP1V5_GPU_PWRSEQ

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

- GPU_SS - GPU_LVDDR_2V8



GPU CLOCKS

SYNC_MASTER=BOZEMAN SYNC_DATE=05/21/2005

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NONE	92	111	

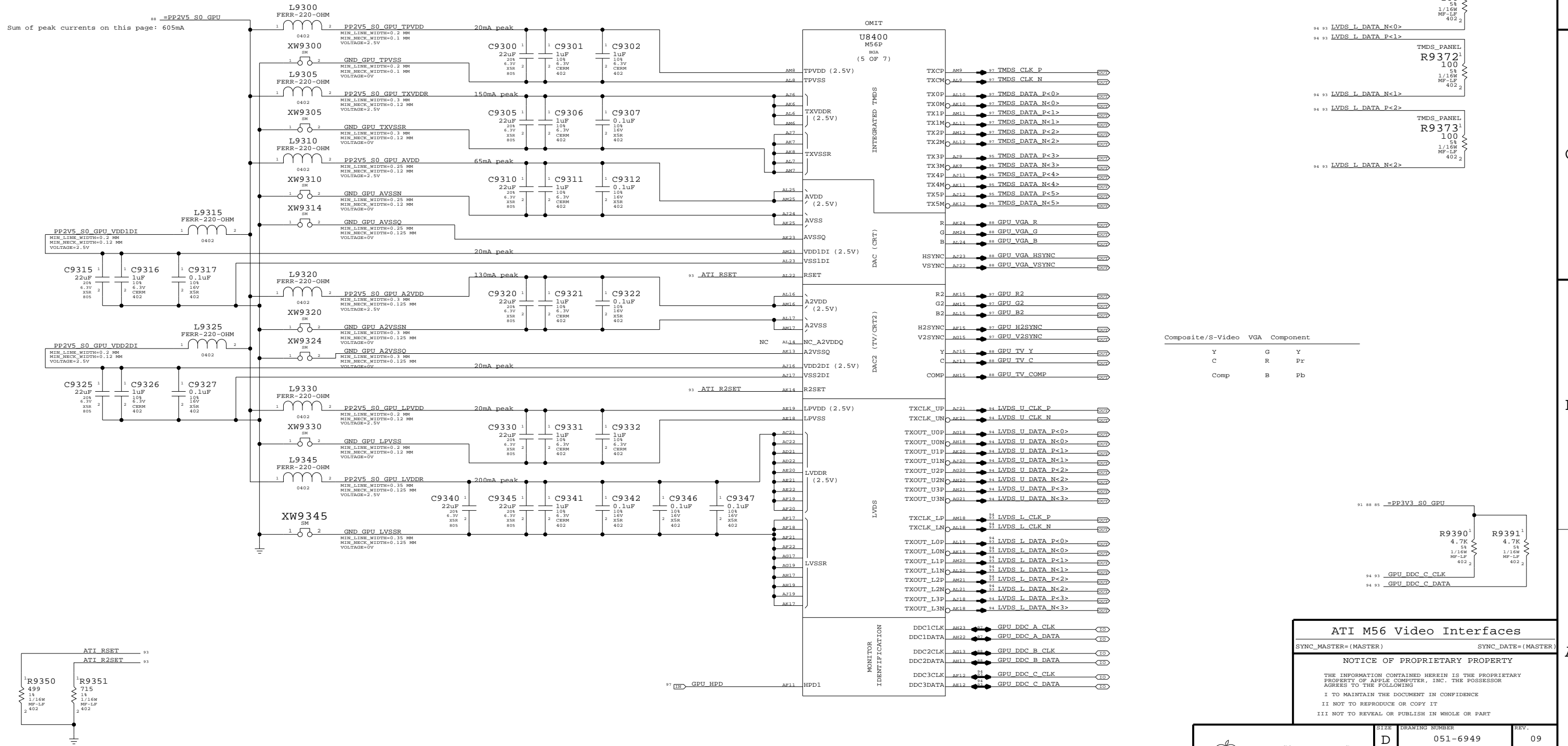
Page Notes

Power aliases required by this page:
 - =PP2V5_S0_GPU
 - =PP1V8R2V5_S0_GPU_LVDDR

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)

TERMINATION FOR TMDS USAGE OF LVDS PINS
 PLACE CLOSE TO GPU (U8400)



Sum of peak currents on this page: 605mA

Composite/S-Video	VGA	Component
Y	G	Y
C	R	Pr
Comp	B	Pb

ATI M56 Video Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

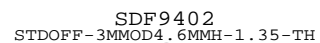
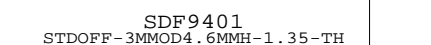
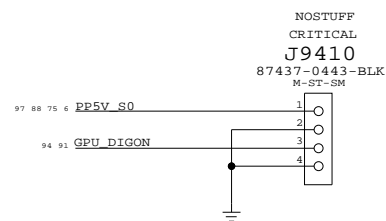
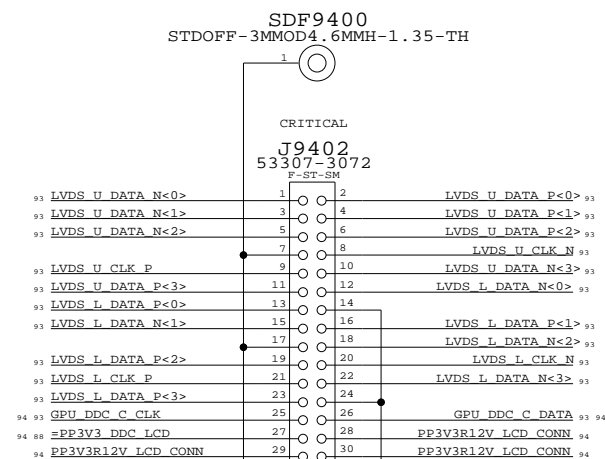
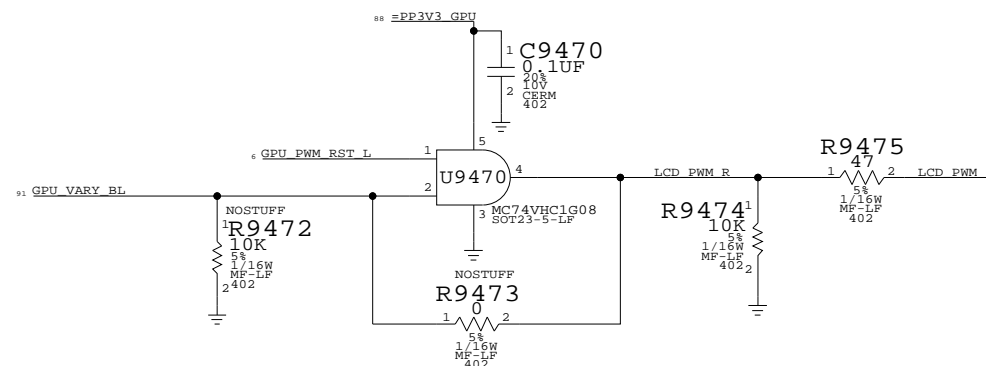
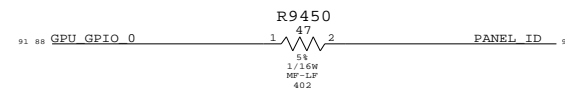
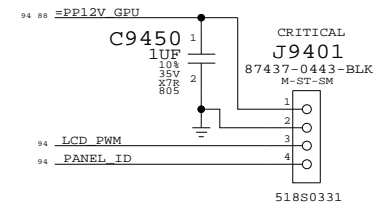
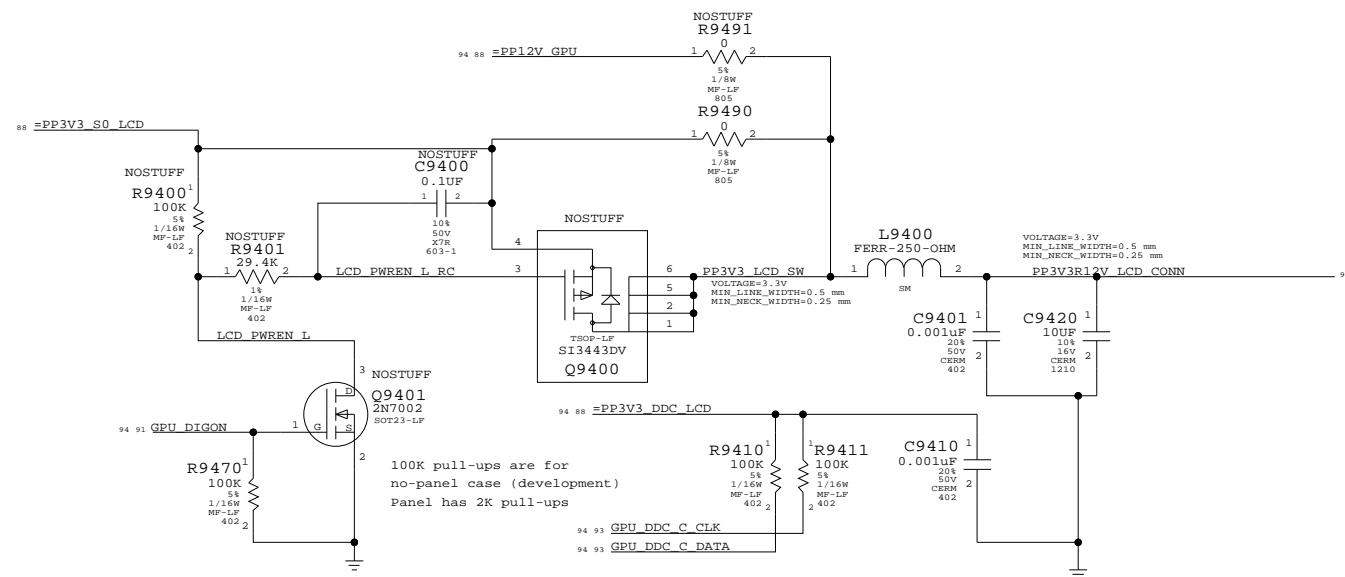
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APPLE COMPUTER INC.	SCALE	SHT	OF	REV.
	NONE	93	111	09

LCD (LVDS) INTERFACE

INVERTER INTERFACE



NOTE: 3RD STANDOFF FOR LVDS->TMDS CONVERTER BOARD

Internal Display Conns
 SYNC_MASTER=BOZEMAN SYNC_DATE=04/27/2005
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	SHT	OF	
NONE	94	111	

8

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M56 TPS

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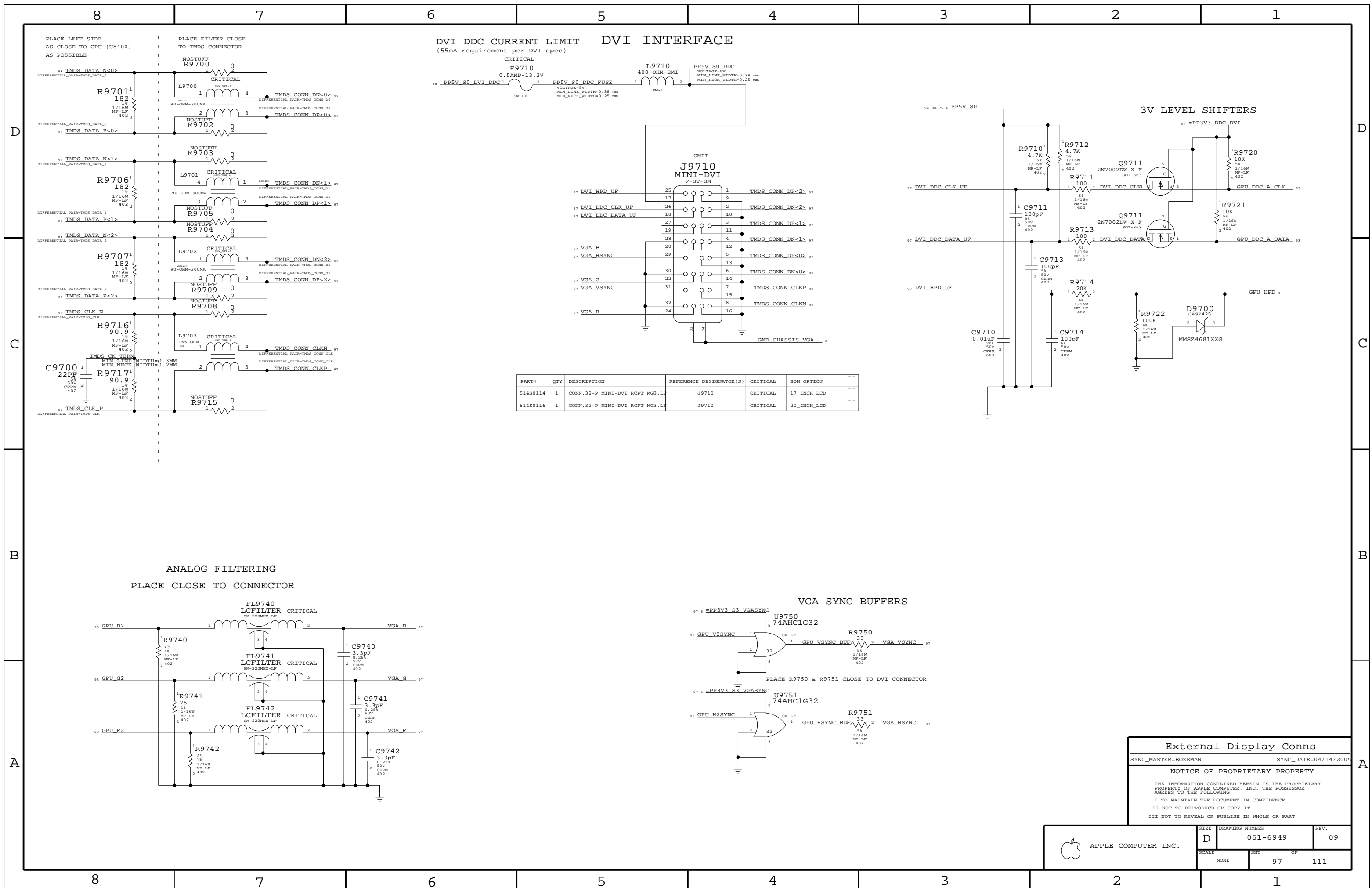
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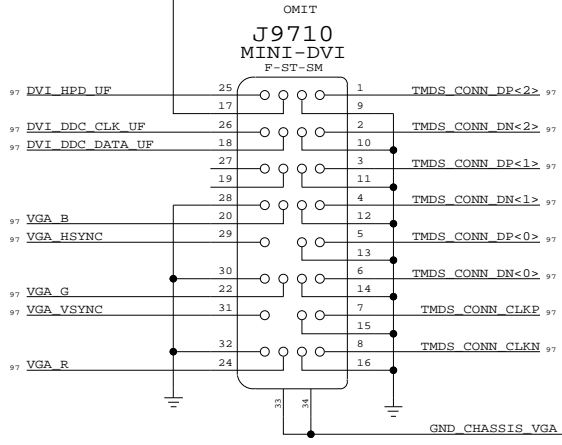
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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT 95 OF 111	
NONE		



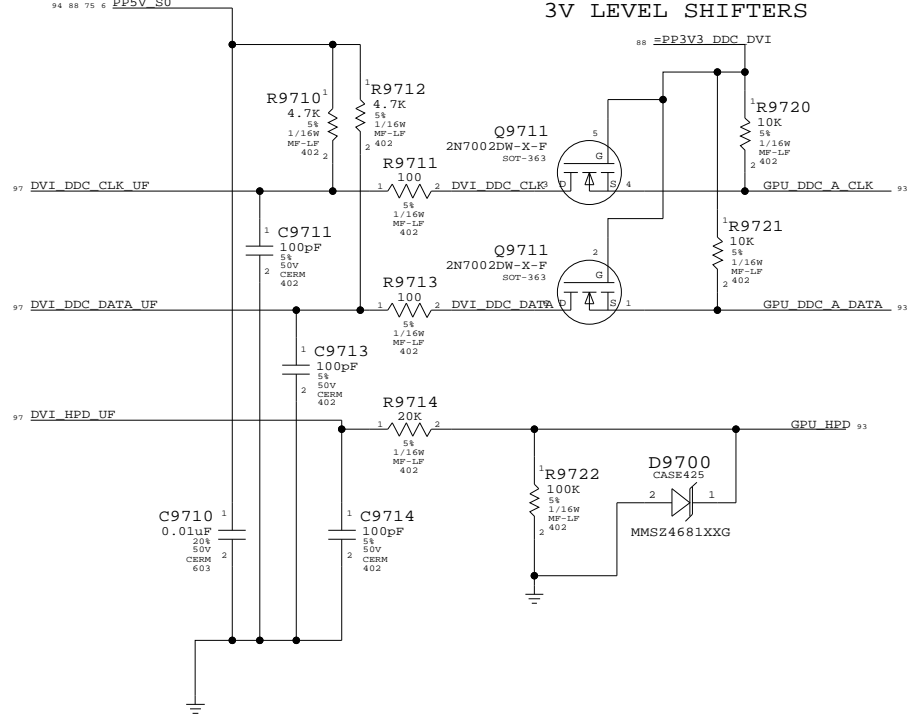
DVI DDC CURRENT LIMIT DVI INTERFACE

(55mA requirement per DVI spec)
 CRITICAL
 F9710 0.5AMP-13.2V
 L9710 400-OHM-EMI
 PP5V_S0 DDC VOLTAGE=5V
 MIN_LINE_WIDTH=0.38 mm
 MIN_NECK_WIDTH=0.25 mm

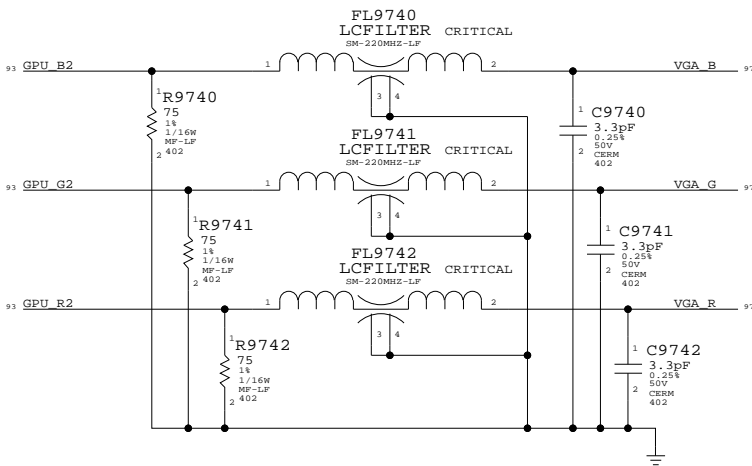


PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
51480114	1	CONN, 32-P MINI-DVI RCPT MG3,LF	J9710	CRITICAL	17_INCH_LCD
51480116	1	CONN, 32-P MINI-DVI RCPT MG3,LF	J9710	CRITICAL	20_INCH_LCD

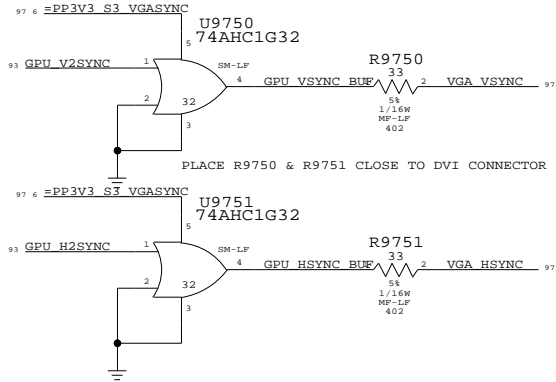
3V LEVEL SHIFTERS



ANALOG FILTERING
 PLACE CLOSE TO CONNECTOR



VGA SYNC BUFFERS



External Display Conns
 SYNC_MASTER=BOZEMAN SYNC_DATE=04/14/2005
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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	97	111

	8	7	6	5	4	3	2	1				
	AUD_GPIO1_A	AUD_GPIO1_A - @m38_1ib.M38	68A8 73C2	CK410_SRC6_P	CK410_SRC6_P - @m38_1ib.M38	3384 3486	DM1_S2N_P<0>	DM1_S2N_P<0> - @m38_1ib.M38	5C7 14B4 22D2	FB_A_DQ<30>	FB_A_DQ<30> - @m38_1ib.M38	87C7 8986
	AUD_GPIO2	AUD_GPIO2 - @m38_1ib.M38	68C7 74A6	CK410_SRC7_N	CK410_SRC7_N - @m38_1ib.M38	3384 34D3	DM1_S2N_P<1>	DM1_S2N_P<1> - @m38_1ib.M38	1484 22D2	FB_A_DQ<31>	FB_A_DQ<31> - @m38_1ib.M38	87C7 89A6
	AUD_LI_DET_EMI	AUD_LI_DET_EMI - @m38_1ib.M38	73D7	CK410_SRC7_P	CK410_SRC7_P - @m38_1ib.M38	3384 34D3	DM1_S2N_P<2>	DM1_S2N_P<2> - @m38_1ib.M38	1484 22D2	FB_A_DQ<32>	FB_A_DQ<32> - @m38_1ib.M38	5A6 5D6 87C7 89B2
	AUD_LI_DET_H	AUD_LI_DET_H - @m38_1ib.M38	73D4 74C5	CK410_SRC8_N	CK410_SRC8_N - @m38_1ib.M38	33A4 34A6	DM1_S2N_P<3>	DM1_S2N_P<3> - @m38_1ib.M38	1484 22D2	FB_A_DQ<33>	FB_A_DQ<33> - @m38_1ib.M38	87C7 89B3
	AUD_LI_DET_JACK	AUD_LI_DET_JACK - @m38_1ib.M38	73D8	CK410_SRC8_P	CK410_SRC8_P - @m38_1ib.M38	33A4 34A6	DVI_DDC_CLK	DVI_DDC_CLK - @m38_1ib.M38	97D2	FB_A_DQ<34>	FB_A_DQ<34> - @m38_1ib.M38	87C7 89B3
	AUD_LI_GND_EMI	AUD_LI_GND_EMI - @m38_1ib.M38	73C7	CK410_SRC_CLKREQ1_L	CK410_SRC_CLKREQ1_L - @m38_1ib.M38	3384 34D8	DVI_DDC_CLK_UF	DVI_DDC_CLK_UF - @m38_1ib.M38	97D3 97D5	FB_A_DQ<35>	FB_A_DQ<35> - @m38_1ib.M38	87C7 89B3
	AUD_LI_GND_JACK	AUD_LI_GND_JACK - @m38_1ib.M38	73D8	CK410_SRC_CLKREQ3_L	CK410_SRC_CLKREQ3_L - @m38_1ib.M38	3384 34D8	DVI_DDC_DATA	DVI_DDC_DATA - @m38_1ib.M38	97C2	FB_A_DQ<36>	FB_A_DQ<36> - @m38_1ib.M38	87C7 89B3
	AUD_LI_L_EMI	AUD_LI_L_EMI - @m38_1ib.M38	73D7	CK410_SRC_CLKREQ6_L	CK410_SRC_CLKREQ6_L - @m38_1ib.M38	3384 53C6	DVI_DDC_DATA_UF	DVI_DDC_DATA_UF - @m38_1ib.M38	97C3 97D5	FB_A_DQ<37>	FB_A_DQ<37> - @m38_1ib.M38	87C7 89B3
	AUD_LI_L_JACK	AUD_LI_L_JACK - @m38_1ib.M38	73D8	CK410_SRC_CLKREQ8_L	CK410_SRC_CLKREQ8_L - @m38_1ib.M38	3384 34D8	DVI_HPD_UF	DVI_HPD_UF - @m38_1ib.M38	97C3 97D5	FB_A_DQ<38>	FB_A_DQ<38> - @m38_1ib.M38	87C7 89B3
	AUD_LI_R_EMI	AUD_LI_R_EMI - @m38_1ib.M38	73D7	CK410_USB48_FSA	CK410_USB48_FSA - @m38_1ib.M38	33A4 34C6	ENET_C4106_2	ENET_C4106_2 - @m38_1ib.M38	41D2	FB_A_DQ<39>	FB_A_DQ<39> - @m38_1ib.M38	87C7 89B3
	AUD_LI_R_JACK	AUD_LI_R_JACK - @m38_1ib.M38	73D8	CK410_XTAL_IN	CK410_XTAL_IN - @m38_1ib.M38	33C6	ENET_C4107_2	ENET_C4107_2 - @m38_1ib.M38	41D2	FB_A_DQ<40>	FB_A_DQ<40> - @m38_1ib.M38	5A6 5D6 87C7 89A2
	AUD_LO_DET1	AUD_LO_DET1 - @m38_1ib.M38	7388 74A5	CK410_XTAL_OUT	CK410_XTAL_OUT - @m38_1ib.M38	33C6	ENET_C4117_1	ENET_C4117_1 - @m38_1ib.M38	41B2	FB_A_DQ<41>	FB_A_DQ<41> - @m38_1ib.M38	87C7 89A3
	AUD_LO_DET1_1	AUD_LO_DET1_1 - @m38_1ib.M38	74A4 74B2	CLK_NB_OE_L	CLK_NB_OE_L - @m38_1ib.M38	1486 33B4	ENET_C4118_1	ENET_C4118_1 - @m38_1ib.M38	41B2	FB_A_DQ<42>	FB_A_DQ<42> - @m38_1ib.M38	87C7 89A3
	AUD_LO_DET1_EMI	AUD_LO_DET1_EMI - @m38_1ib.M38	73B7	CPUVCORE_ISENSE_CAL	CPUVCORE_ISENSE_CAL - @m38_1ib.M38	74B5	ENET_CLK100M_PCIE_N	ENET_CLK100M_PCIE_N - @m38_1ib.M38	5D5 34A4 34B2 41C5	FB_A_DQ<43>	FB_A_DQ<43> - @m38_1ib.M38	87C7 89A3
	AUD_LO_DET1_INV	AUD_LO_DET1_INV - @m38_1ib.M38	74B3	CPU_A20M_L	CPU_A20M_L - @m38_1ib.M38	5C8 7C7 21C4	ENET_CLK100M_PCIE_P	ENET_CLK100M_PCIE_P - @m38_1ib.M38	5D5 34A4 34B2 41C5	FB_A_DQ<44>	FB_A_DQ<44> - @m38_1ib.M38	87B7 89A3
	AUD_LO_DET1_JACK	AUD_LO_DET1_JACK - @m38_1ib.M38	73B4	CPU_BSEL<0>	CPU_BSEL<0> - @m38_1ib.M38	7B4 3488	ENET_CTRL12	ENET_CTRL12 - @m38_1ib.M38	41C7 42B8	FB_A_DQ<45>	FB_A_DQ<45> - @m38_1ib.M38	87B7 89A3
	AUD_LO_DET2	AUD_LO_DET2 - @m38_1ib.M38	73B8 74B5	CPU_BSEL<1>	CPU_BSEL<1> - @m38_1ib.M38	7B4 3488	TP_ENET_CTRL12	TP_ENET_CTRL12 - @m38_1ib.M38	42B7	FB_A_DQ<46>	FB_A_DQ<46> - @m38_1ib.M38	87B7 89A3
	AUD_LO_DET2_EMI	AUD_LO_DET2_EMI - @m38_1ib.M38	73B7	CPU_COMP<0>	CPU_COMP<0> - @m38_1ib.M38	7B4 3488	ENET_CTRL25	ENET_CTRL25 - @m38_1ib.M38	41C7 42C6	FB_A_DQ<47>	FB_A_DQ<47> - @m38_1ib.M38	87B7 89A3
	AUD_LO_DET2_JACK	AUD_LO_DET2_JACK - @m38_1ib.M38	73B4	CPU_COMP<1>	CPU_COMP<1> - @m38_1ib.M38	7B2	ENET_GATED_RST_L	ENET_GATED_RST_L - @m38_1ib.M38	41C5 42D2	FB_A_DQ<48>	FB_A_DQ<48> - @m38_1ib.M38	5A6 5D6 87B7 89A2
	AUD_LO_GND_EMI	AUD_LO_GND_EMI - @m38_1ib.M38	73A7	CPU_COMP<2>	CPU_COMP<2> - @m38_1ib.M38	7B2	ENET_LED_ACT_L	ENET_LED_ACT_L - @m38_1ib.M38	41C8 43C3	FB_A_DQ<49>	FB_A_DQ<49> - @m38_1ib.M38	87B7 89A3
	AUD_LO_GND_JACK	AUD_LO_GND_JACK - @m38_1ib.M38	73B4	CPU_COMP<3>	CPU_COMP<3> - @m38_1ib.M38	7B2	ENET_LED_LINK10_100_L	ENET_LED_LINK10_100_L - @m38_1ib.M38	41C8 43C3	FB_A_DQ<50>	FB_A_DQ<50> - @m38_1ib.M38	87B7 89A3
	AUD_LO_L_EMI	AUD_LO_L_EMI - @m38_1ib.M38	73B7	CPU_DCIN_SENSE	CPU_DCIN_SENSE - @m38_1ib.M38	76D7	ENET_LED_LINK1000_L	ENET_LED_LINK1000_L - @m38_1ib.M38	41C8 43C3	FB_A_DQ<51>	FB_A_DQ<51> - @m38_1ib.M38	87B7 89A3
	AUD_LO_L_JACK	AUD_LO_L_JACK - @m38_1ib.M38	73B4	CPU_DPRSTP_L	CPU_DPRSTP_L - @m38_1ib.M38	7B3 21C4 75C6	ENET_LED_LINK_L	ENET_LED_LINK_L - @m38_1ib.M38	41C8 43C3	FB_A_DQ<52>	FB_A_DQ<52> - @m38_1ib.M38	87B7 89A3
	AUD_LO_R_EMI	AUD_LO_R_EMI - @m38_1ib.M38	73A7	CPU_DPSLP_L	CPU_DPSLP_L - @m38_1ib.M38	7B3 21C4	ENET_LOM_DIS_L	ENET_LOM_DIS_L - @m38_1ib.M38	41C7	FB_A_DQ<53>	FB_A_DQ<53> - @m38_1ib.M38	87B7 89A3
	AUD_LO_R_JACK	AUD_LO_R_JACK - @m38_1ib.M38	73B4	CPU_FERR_L	CPU_FERR_L - @m38_1ib.M38	7C7 21C4	ENET_MDI_N<0>	ENET_MDI_N<0> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<54>	FB_A_DQ<54> - @m38_1ib.M38	87B7 89A3
	AUD_MAK9714_CHOLD	AUD_MAK9714_CHOLD - @m38_1ib.M38	72C4	CPU_GTLREF	CPU_GTLREF - @m38_1ib.M38	5D4 7B4	ENET_MDI_N<1>	ENET_MDI_N<1> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<55>	FB_A_DQ<55> - @m38_1ib.M38	87B7 89A3
	AUD_MAK9714_VREG	AUD_MAK9714_VREG - @m38_1ib.M38	72C5	CPU_HS_ZHE67	CPU_HS_ZHE67 - @m38_1ib.M38	904	ENET_MDI_N<2>	ENET_MDI_N<2> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<56>	FB_A_DQ<56> - @m38_1ib.M38	5A6 5D6 87B7 89A2
	AUD_MIC_IN_N	AUD_MIC_IN_N - @m38_1ib.M38	73C6 74A6	CPU_HS_ZHE68	CPU_HS_ZHE68 - @m38_1ib.M38	9D3 66A4 66A6 66B4 66B6	ENET_MDI_N<3>	ENET_MDI_N<3> - @m38_1ib.M38	41C2 43B7	FB_A_DQ<57>	FB_A_DQ<57> - @m38_1ib.M38	87B7 89B3
	AUD_MIC_IN_N_CONN	AUD_MIC_IN_N_CONN - @m38_1ib.M38	47C2 73C4	CPU_HS_ZHE69	CPU_HS_ZHE69 - @m38_1ib.M38	9D3	ENET_MDI_P<0>	ENET_MDI_P<0> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<58>	FB_A_DQ<58> - @m38_1ib.M38	87B7 89B3
	AUD_MIC_IN_N_EMI	AUD_MIC_IN_N_EMI - @m38_1ib.M38	73C5	CPU_HS_ZHE10	CPU_HS_ZHE10 - @m38_1ib.M38	9D2	ENET_MDI_P<1>	ENET_MDI_P<1> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<59>	FB_A_DQ<59> - @m38_1ib.M38	87B7 89B3
	AUD_MIC_IN_P	AUD_MIC_IN_P - @m38_1ib.M38	73C6 74A6	CPU_IGNNE_L	CPU_IGNNE_L - @m38_1ib.M38	5C8 7C7 21C4	ENET_MDI_P<2>	ENET_MDI_P<2> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<60>	FB_A_DQ<60> - @m38_1ib.M38	87B7 89B3
	AUD_MIC_IN_P_CONN	AUD_MIC_IN_P_CONN - @m38_1ib.M38	47C2 73C4	CPU_INIT_L	CPU_INIT_L - @m38_1ib.M38	5C8 7D6 21C4	ENET_MDI_P<3>	ENET_MDI_P<3> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<61>	FB_A_DQ<61> - @m38_1ib.M38	87B7 89B3
	AUD_MIC_IN_P_EMI	AUD_MIC_IN_P_EMI - @m38_1ib.M38	73C5	CPU_INTR	CPU_INTR - @m38_1ib.M38	5C8 7C7 21C4	ENET_MDI_R<0>	ENET_MDI_R<0> - @m38_1ib.M38	43C6	FB_A_DQ<62>	FB_A_DQ<62> - @m38_1ib.M38	87B7 89A3
	AUD_MIC_P1	AUD_MIC_P1 - @m38_1ib.M38	74A4	CPU_ISENSE_OUT_R	CPU_ISENSE_OUT_R - @m38_1ib.M38	76D3	ENET_MDI_R<1>	ENET_MDI_R<1> - @m38_1ib.M38	43C6	FB_A_DQ<63>	FB_A_DQ<63> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_A_DET_L	AUD_PORT_A_DET_L - @m38_1ib.M38	74B2	CPU_ISENSE_R_NEG	CPU_ISENSE_R_NEG - @m38_1ib.M38	76D4	ENET_MDI_R<2>	ENET_MDI_R<2> - @m38_1ib.M38	43C6	FB_A_DQM_L<1>	FB_A_DQM_L<1> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_A_L	AUD_PORT_A_L - @m38_1ib.M38	74B5 74B5	CPU_ISENSE_R_POS	CPU_ISENSE_R_POS - @m38_1ib.M38	76D4	ENET_MDI_R<3>	ENET_MDI_R<3> - @m38_1ib.M38	43C6	FB_A_DQM_L<2>	FB_A_DQM_L<2> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_A_L1	AUD_PORT_A_L1 - @m38_1ib.M38	74B7	CPU_MMI	CPU_MMI - @m38_1ib.M38	5C8 7C7 21C4	ENET_MDI_R_P<0>	ENET_MDI_R_P<0> - @m38_1ib.M38	43C6	FB_A_DQM_L<3>	FB_A_DQM_L<3> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_A_R	AUD_PORT_A_R - @m38_1ib.M38	74B5 74B5	CPU_PROCHOT_L	CPU_PROCHOT_L - @m38_1ib.M38	7C6 59A8 59C7	ENET_MDI_R_P<1>	ENET_MDI_R_P<1> - @m38_1ib.M38	43C6	FB_A_DQM_L<4>	FB_A_DQM_L<4> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_A_R1	AUD_PORT_A_R1 - @m38_1ib.M38	74B7	CPU_PSI_L	CPU_PSI_L - @m38_1ib.M38	7A3 21C4	ENET_MDI_R_P<2>	ENET_MDI_R_P<2> - @m38_1ib.M38	43C6	FB_A_DQM_L<5>	FB_A_DQM_L<5> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_E_DET_L	AUD_PORT_E_DET_L - @m38_1ib.M38	74B1	CPU_PWRGD	CPU_PWRGD - @m38_1ib.M38	7B3 21C4	ENET_MDI_R_P<3>	ENET_MDI_R_P<3> - @m38_1ib.M38	43C6	FB_A_DQM_L<6>	FB_A_DQM_L<6> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_F_L	AUD_PORT_F_L - @m38_1ib.M38	73D4 74C5	CPU_RCIN_L	CPU_RCIN_L - @m38_1ib.M38	21C4	ENET_PU_VDDO_TTL0	ENET_PU_VDDO_TTL0 - @m38_1ib.M38	41C5	FB_A_DQM_L<7>	FB_A_DQM_L<7> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_F_L1	AUD_PORT_F_L1 - @m38_1ib.M38	74C7	CPU_SENSE_I_R	CPU_SENSE_I_R - @m38_1ib.M38	76D7	ENET_PU_VDDO_TTL1	ENET_PU_VDDO_TTL1 - @m38_1ib.M38	41C5	FB_A_MA<0>	FB_A_MA<0> - @m38_1ib.M38	87D5 89B5 89B8
	AUD_PORT_F_R	AUD_PORT_F_R - @m38_1ib.M38	73D4 74C5	CPU_SMI_L	CPU_SMI_L - @m38_1ib.M38	5C8 7C7 21C4	ENET_RST	ENET_RST - @m38_1ib.M38	41C7	FB_A_MA<1>	FB_A_MA<1> - @m38_1ib.M38	87D5 89B5 89B8
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	AUD_SAMP_FS1	AUD_SAMP_FS1 - @m38_1ib.M38	72A6 72C5	CPU_TEST1	CPU_TEST1 - @m38_1ib.M38	7B4	ENET_VPD_CLK	ENET_VPD_CLK - @m38_1ib.M38	41A2 41C4	FB_A_MA<3>	FB_A_MA<3> - @m38_1ib.M38	5A6 5D6 87D5 89B5
	AUD_SAMP_FS2	AUD_SAMP_FS2 - @m38_1ib.M38	72A6 72C5	CPU_TEST2	CPU_TEST2 - @m38_1ib.M38	7B4	ENET_VPD_DATA	ENET_VPD_DATA - @m38_1ib.M38	41A2 41C4	FB_A_RA<0>	FB_A_RA<0> - @m38_1ib.M38	5B6 87B5 89A8
	AUD_SAMP_G1	AUD_SAMP_G1 - @m38_1ib.M38	72A6 72C5	CPU_THERMD_EXT_N	CPU_THERMD_EXT_N - @m38_1ib.M38	10B6	ENET_XTAL1	ENET_XTAL1 - @m38_1ib.M38	41B5	FB_A_RA<1>	FB_A_RA<1> - @m38_1ib.M38	5D6 87C5 89A5
	AUD_SAMP_G2	AUD_SAMP_G2 - @m38_1ib.M38	72A6 72C5	CPU_THERMD_EXT_P	CPU_THERMD_EXT_P - @m38_1ib.M38	10B6	ENET_XTALO	ENET_XTALO - @m38_1ib.M38	41B5	FB_A_RDQS<0>	FB_A_RDQS<0> - @m38_1ib.M38	5D6 87C5 89A8
	AUD_SAMP_INL_N	AUD_SAMP_INL_N - @m38_1ib.M38	72C5	CPU_THERMD_N	CPU_THERMD_N - @m38_1ib.M38	7C6 10C6	F0_GATESLOWDN	F0_GATESLOWDN - @m38_1ib.M38	6D5	FB_A_RDQS<1>	FB_A_RDQS<1> - @m38_1ib.M38	5D6 87C5 89A8
	AUD_SAMP_INL_P	AUD_SAMP_INL_P - @m38_1ib.M38	72C5	CPU_THERMD_P	CPU_THERMD_P - @m38_1ib.M38	7C6 10C6	F0_RCFFEDBK	F0_RCFFEDBK - @m38_1ib.M38	6D5	FB_A_RDQS<2>	FB_A_RDQS<2> - @m38_1ib.M38	5D6 87C5 89A8
	AUD_SAMP_INR_N	AUD_SAMP_INR_N - @m38_1ib.M38	72C5	CPU_THERMTRIP_R	CPU_THERMTRIP_R - @m38_1ib.M38	21C2	F0_VOLTAGEERR5	F0_VOLTAGEERR5 - @m38_1ib.M38	6D6	FB_A_RDQS<3>	FB_A_RDQS<3> - @m38_1ib.M38	5D6 87C5 89A8
	AUD_SAMP_INR_P	AUD_SAMP_INR_P - @m38_1ib.M38	72C5	CPU_VCCSENSE_N	CPU_VCCSENSE_N - @m38_1ib.M38	8B6 75A4	F1_GATESLOWDN	F1_GATESLOWDN - @m38_1ib.M38	6D5	FB_A_RDQS<4>	FB_A_RDQS<4> - @m38_1ib.M38	5D6 87C5 89A5
	AUD_SAMP_SHDN_L	AUD_SAMP_SHDN_L - @m38_1ib.M38	72C5	CPU_VCCSENSE_P	CPU_VCCSENSE_P - @m38_1ib.M38	8B6 75A4	F1_RCFFEDBK	F1_RCFFEDBK - @m38_1ib.M38	6D5	FB_A_RDQS<5>	FB_A_RDQS<5> - @m38_1ib.M38	5D6 87C5 89A5
	AUD_SENSE_A	AUD_SENSE_A - @m38_1ib.M38	68C1 74C5 74D7	CPU_VID<0>	CPU_VID<0> - @m38_1ib.M38	8B7 75C7	F1_VOLTAGEERR5	F1_VOLTAGEERR5 - @m38_1ib.M38	6D6	FB_A_RDQS<6>	FB_A_RDQS<6> - @m38_1ib.M38	5D6 87C5 89A5
	AUD_SENSE_B	AUD_SENSE_B - @m38_1ib.M38	68C1 74C5 74D5 74D7	CPU_VID<1>	CPU_VID<1> - @m38_1ib.M38	8B7 75C7	F2_GATESLOWDN	F2_GATESLOWDN - @m38_1ib.M38	6D6	FB_A_RA<L<0>	FB_A_RA<L<0> - @m38_1ib.M38	5B6 87B5 89A8
	AUD_SPDIF_CMD	AUD_SPDIF_CMD - @m38_1ib.M38	73A3	CPU_VID<2>	CPU_VID<2> - @m38_1ib.M38	8B7 75C7	F2_RCFFEDBK	F2_RCFFEDBK - @m38_1ib.M38	6D5	FB_A_RA<L<1>	FB_A_RA<L<1> - @m38_1ib.M38	5D6 87C5 89A5
	AUD_SPDIF_IN	AUD_SPDIF_IN - @m38_1ib.M38	68C1 74B6	CPU_VID<3>	CPU_VID<3> - @m38_1ib.M38	8B7 75C7	F2_VOLTAGEERR5	F2_VOLTAGEERR5 - @m38_1ib.M38	6D5	FB_A_RDQS<7>	FB_A_RDQS<7> - @m38_1ib.M38	5D6 87C5 89A8
	A											

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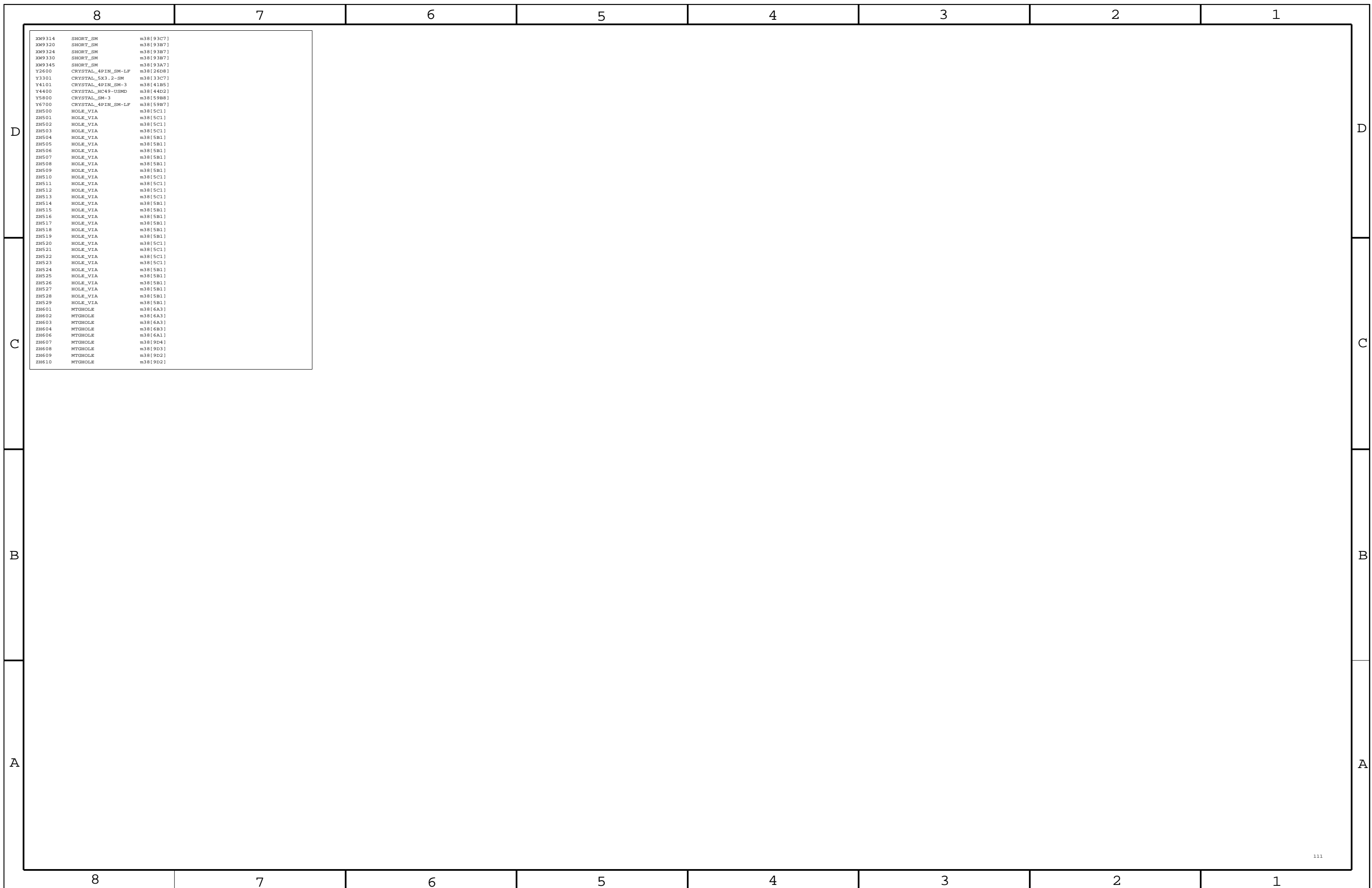
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	SMC_XTAL	SMC_XTAL - @m38_lib.M38	58C3 59B8	TP_PCI_GNT2_L	TP_PCI_GNT2_L - @m38_lib.M38	22B6		
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	SMLINK<1>	SMLINK<1> - @m38_lib.M38	23D5	TP_PCI_PME_L	TP_PCI_PME_L - @m38_lib.M38	22A6		
	SMS_INT_L	SMS_INT_L - @m38_lib.M38	23C3 26C2 58B5	TP_SB_ACZ_SDIN1	TP_SB_ACZ_SDIN1 - @m38_lib.M38	21C6		
	SMS_ONOFF_L	SMS_ONOFF_L - @m38_lib.M38	58A5 59B4	TP_SB_ACZ_SDIN2	TP_SB_ACZ_SDIN2 - @m38_lib.M38	21C6		
	SMS_X_AXIS	SMS_X_AXIS - @m38_lib.M38	58B7 59B6	TP_SB_DRQ0_L	TP_SB_DRQ0_L - @m38_lib.M38	21D4		
	SMS_Y_AXIS	NC_SMS_X_AXIS - @m38_lib.M38	59B5	TP_SB_DRQ106	TP_SB_DRQ106 - @m38_lib.M38	23C5		
	SMS_Z_AXIS	SMS_Y_AXIS - @m38_lib.M38	58B7 59B6	TP_SB_GPI023	TP_SB_GPI023 - @m38_lib.M38	21D5		
	SPARE_SRC3_N	NC_SMS_Y_AXIS - @m38_lib.M38	59B5	TP_SB_GPI025_DO_NOT_USR	TP_SB_GPI025_DO_NOT_USR - @m38_lib.M38	23C3		
	SPARE_SRC3_P	SMS_Z_AXIS - @m38_lib.M38	58A7 59B6	TP_SB_GPI038	TP_SB_GPI038 - @m38_lib.M38	23C3		
	SPARE_SRC7_N	NC_SMS_Z_AXIS - @m38_lib.M38	59B5	TP_SB_RSVD9	TP_SB_RSVD9 - @m38_lib.M38	22A6		
	SPARE_SRC7_P	SPARE_SRC3_N - @m38_lib.M38	34D2	TP_SB_SATALED_L	TP_SB_SATALED_L - @m38_lib.M38	21C6		
	SPI_ARB	SPARE_SRC3_P - @m38_lib.M38	34D2	TP_SB_XOR_AD5	TP_SB_XOR_AD5 - @m38_lib.M38	22A7		
	SPI_CLK_L	SPARE_SRC7_N - @m38_lib.M38	34D2	TP_SB_XOR_AD9	TP_SB_XOR_AD9 - @m38_lib.M38	22A7		
	SPI_HOLD_L	SPARE_SRC7_P - @m38_lib.M38	34D2	TP_SB_XOR_AE5	TP_SB_XOR_AE5 - @m38_lib.M38	22A7		
	SPI_SCLK	SPI_ARB - @m38_lib.M38	22C6 58D5 63C7	TP_SB_XOR_AE9	TP_SB_XOR_AE9 - @m38_lib.M38	22A6		
	SPI_SCLK_R	SPI_CLK_L - @m38_lib.M38	63C4	TP_SB_XOR_AG4	TP_SB_XOR_AG4 - @m38_lib.M38	22A7		
	SPI_SI	SPI_SCLK_R - @m38_lib.M38	63C4	TP_SB_XOR_AG8	TP_SB_XOR_AG8 - @m38_lib.M38	22A6		
	SPI_SI_R	SPI_SI - @m38_lib.M38	22C6 58D5 63C1	TP_SB_XOR_AH4	TP_SB_XOR_AH4 - @m38_lib.M38	22A7		
	SPI_SO	SPI_SI_R - @m38_lib.M38	63C3	TP_SB_XOR_AH8	TP_SB_XOR_AH8 - @m38_lib.M38	22A6		
	SPI_SO_R	SPI_SO - @m38_lib.M38	22C6 58D5 63C1	TP_SB_XOR_T5	TP_SB_XOR_T5 - @m38_lib.M38	21C6		
	SPI_WP_L	SPI_SO_R - @m38_lib.M38	63C3	TP_SB_XOR_U3	TP_SB_XOR_U3 - @m38_lib.M38	21C6		
	SPKRAMP_MUTE	SPI_WP_L - @m38_lib.M38	63C4	TP_SB_XOR_U5	TP_SB_XOR_U5 - @m38_lib.M38	21C6		
	SPKRAMP_SS	SPI_WP_L - @m38_lib.M38	63C4	TP_SB_XOR_U7	TP_SB_XOR_U7 - @m38_lib.M38	21C6		
	SUS_CLK_SB	SPKRAMP_MUTE - @m38_lib.M38	72B5	TP_SB_XOR_V3	TP_SB_XOR_V3 - @m38_lib.M38	21C6		
	SV_SET_UP	SPKRAMP_SS - @m38_lib.M38	72B4	TP_SB_XOR_V4	TP_SB_XOR_V4 - @m38_lib.M38	21C6		
	SW_RST_BTN_L	SUS_CLK_SB - @m38_lib.M38	23C3 59B5	TP_SB_XOR_V6	TP_SB_XOR_V6 - @m38_lib.M38	21C6		
	SW_RST_DEBNC	SMC_SUS_CLK - @m38_lib.M38	58C5 59B6	TP_SB_XOR_V7	TP_SB_XOR_V7 - @m38_lib.M38	21C6		
	SYS_LED_DRV_C	SV_SET_UP - @m38_lib.M38	23B6 23C3 60B3	TP_SB_XOR_W1	TP_SB_XOR_W1 - @m38_lib.M38	21C6		
	SYS_LED_DRV_K	SW_RST_BTN_L - @m38_lib.M38	5D1 26C6	TP_SB_XOR_W3	TP_SB_XOR_W3 - @m38_lib.M38	21C6		
	SYS_ONWIRE	SW_RST_DEBNC - @m38_lib.M38	26C4	TP_SB_XOR_Y1	TP_SB_XOR_Y1 - @m38_lib.M38	21C6		
	SYS_POWERFAIL_L	SYS_LED_DRV_C - @m38_lib.M38	59D6	TP_SB_XOR_Y2	TP_SB_XOR_Y2 - @m38_lib.M38	21C6		
	SYN_PWRUP_L	SYS_LED_DRV_K - @m38_lib.M38	59D6	TP_U5999_P1	TP_U5999_P1 - @m38_lib.M38	59A7		
	THERM_DX_N	SYS_ONWIRE - @m38_lib.M38	58B7 59B4	TP_U5999_P13	TP_U5999_P13 - @m38_lib.M38	59A5		
	THERM_DX_P	SYS_POWERFAIL_L - @m38_lib.M38	6D8 76D2	TP_U8400_A014	TP_U8400_A014 - @m38_lib.M38	91A5		
	THERM_ALERT_L	SYN_PWRUP_L - @m38_lib.M38	6C7	TP_U8900_J2	TP_U8900_J2 - @m38_lib.M38	89A7		
	THERM_THM	THERM_DX_N - @m38_lib.M38	10B5 10C5	TP_U8900_J3	TP_U8900_J3 - @m38_lib.M38	89A7		
	TMD5_CHK_TERM	THERM_DX_P - @m38_lib.M38	10B5 10C5	TP_U8950_J2	TP_U8950_J2 - @m38_lib.M38	89A4		
	TMD5_CLK_N	THERM_ALERT_L - @m38_lib.M38	10D3	TP_U8950_J3	TP_U8950_J3 - @m38_lib.M38	89A4		
	TMD5_CLK_P	THERM_THM - @m38_lib.M38	10C4	TP_U9000_J2	TP_U9000_J2 - @m38_lib.M38	90A7		
	TMD5_CONN_CLKN	TMD5_CHK_TERM - @m38_lib.M38	97C8	TP_U9000_J3	TP_U9000_J3 - @m38_lib.M38	90A7		
	TMD5_CONN_CLKP	TMD5_CLK_N - @m38_lib.M38	93C3 97C8	TP_U9050_J2	TP_U9050_J2 - @m38_lib.M38	90A4		
	TMD5_CONN_DM<0>	TMD5_CLK_P - @m38_lib.M38	93C3 97C8	TP_U9050_J3	TP_U9050_J3 - @m38_lib.M38	90A4		
	TMD5_CONN_DM<1>	TMD5_CONN_CLKN - @m38_lib.M38	97C4 97C7	TSNSNR_GPU_DXP	TSNSNR_GPU_DXP - @m38_lib.M38	61B5		
	TMD5_CONN_DM<2>	TMD5_CONN_CLKP - @m38_lib.M38	97C4 97D7	TSNSNR_NB_DXP	TSNSNR_NB_DXP - @m38_lib.M38	61B5		
	TMD5_CONN_DP<0>	TMD5_CONN_DM<0> - @m38_lib.M38	97C4 97D7	TV_DACA_OUT	TV_DACA_OUT - @m38_lib.M38	13C5 19B1		
	TMD5_CONN_DP<1>	TMD5_CONN_DM<1> - @m38_lib.M38	97C4 97D7	TV_DACC_OUT	TV_DACC_OUT - @m38_lib.M38	13C5 19B1		
	TMD5_CONN_DP<2>	TMD5_CONN_DM<2> - @m38_lib.M38	97C4 97D7	TV_IRTNA	TV_IRTNA - @m38_lib.M38	13C5 19B1		
	TMD5_CONN_DP<3>	TMD5_CONN_DP<0> - @m38_lib.M38	97C7 97D4	TV_IRTNB	TV_IRTNB - @m38_lib.M38	13C5 19B1		
	TMD5_CONN_DP<4>	TMD5_CONN_DP<1> - @m38_lib.M38	97C7 97D4	TV_IRTNC	TV_IRTNC - @m38_lib.M38	13C5 19B1		
	TMD5_DATA_N<0>	TMD5_CONN_DP<2> - @m38_lib.M38	93C3 97D8	TV_IREF	TV_IREF - @m38_lib.M38	13C5 19B1		
	TMD5_DATA_N<1>	TMD5_CONN_DP<3> - @m38_lib.M38	93C3 97D8	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_N<2>	TMD5_CONN_DP<4> - @m38_lib.M38	93C3 97D8	PP3V3_S0_NB_VCCA_TVDDACC	PP3V3_S0_NB_VCCA_TVDDACC - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_N<3>	TMD5_DATA_N<0> - @m38_lib.M38	93C3 97C8	PP3V3_S0_NB_VCCA_TVDDACB	PP3V3_S0_NB_VCCA_TVDDACB - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_N<4>	TMD5_DATA_N<1> - @m38_lib.M38	93C3 97C8	PP3V3_S0_NB_VCCA_TVDDACD	PP3V3_S0_NB_VCCA_TVDDACD - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_N<5>	TMD5_DATA_N<2> - @m38_lib.M38	93C3 97C8	PP3V3_S0_NB_VCCA_TVDDACE	PP3V3_S0_NB_VCCA_TVDDACE - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_P<0>	TMD5_DATA_N<3> - @m38_lib.M38	93C3 95D6	PP3V3_S0_NB_VCCA_TVDDACF	PP3V3_S0_NB_VCCA_TVDDACF - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_P<1>	TMD5_DATA_N<4> - @m38_lib.M38	93C3 95D6	PP3V3_S0_NB_VCCA_TVDDACG	PP3V3_S0_NB_VCCA_TVDDACG - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_P<2>	TMD5_DATA_N<5> - @m38_lib.M38	93C3 95D6	PP3V3_S0_NB_VCCA_TVDDACH	PP3V3_S0_NB_VCCA_TVDDACH - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_P<3>	TMD5_DATA_P<0> - @m38_lib.M38	93C3 97D8	PP3V3_S0_NB_VCCA_TVDDACI	PP3V3_S0_NB_VCCA_TVDDACI - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_P<4>	TMD5_DATA_P<1> - @m38_lib.M38	93C3 97C8	PP3V3_S0_NB_VCCA_TVDDACJ	PP3V3_S0_NB_VCCA_TVDDACJ - @m38_lib.M38	17C6 19B1		
	TMD5_DATA_P<5>	TMD5_DATA_P<2> - @m38_lib.M38	93C3 97C8	PP3V3_S0_NB_VCCA_TVDDACK	PP3V3_S0_NB_VCCA_TVDDACK - @m38_lib.M38	17C6 19B1		
	TPM_BADD	TMD5_DATA_P<3> - @m38_lib.M38	93C3 95D6	PP3V3_S0_NB_VCCA_TVDDACL	PP3V3_S0_NB_VCCA_TVDDACL - @m38_lib.M38	17C6 19B1		
	TPM_GPI01	TMD5_DATA_P<4> - @m38_lib.M38	93C3 95D6	PP3V3_S0_NB_VCCA_TVDDACM	PP3V3_S0_NB_VCCA_TVDDACM - @m38_lib.M38	17C6 19B1		
	TPM_GPI02	TMD5_DATA_P<5> - @m38_lib.M38	93C3 95D6	PP3V3_S0_NB_VCCA_TVDDACN	PP3V3_S0_NB_VCCA_TVDDACN - @m38_lib.M38	17C6 19B1		
	TPM_LRESET_L	TPM_BADD - @m38_lib.M38	67C4	PP3V3_S0_NB_VCCA_TVDDACO	PP3V3_S0_NB_VCCA_TVDDACO - @m38_lib.M38	17C6 19B1		
	TPM_PP	TPM_GPI01 - @m38_lib.M38	59B5 67C6	PP3V3_S0_NB_VCCA_TVDDACP	PP3V3_S0_NB_VCCA_TVDDACP - @m38_lib.M38	17C6 19B1		
	TPM_RST_L	TPM_GPI02 - @m38_lib.M38	59B5 67C6	PP3V3_S0_NB_VCCA_TVDDACQ	PP3V3_S0_NB_VCCA_TVDDACQ - @m38_lib.M38	17C6 19B1		
	TPM_XTALI	TPM_LRESET_L - @m38_lib.M38	6B7 67B7	PP3V3_S0_NB_VCCA_TVDDACR	PP3V3_S0_NB_VCCA_TVDDACR - @m38_lib.M38	17C6 19B1		
	TPM_XTALO	TPM_PP - @m38_lib.M38	59A5 67C5	PP3V3_S0_NB_VCCA_TVDDACS	PP3V3_S0_NB_VCCA_TVDDACS - @m38_lib.M38	17C6 19B1		
	TP_ATI_ROMCS_L	TPM_RST_L - @m38_lib.M38	67B6	PP3V3_S0_NB_VCCA_TVDDACT	PP3V3_S0_NB_VCCA_TVDDACT - @m38_lib.M38	17C6 19B1		
	TP_AZ_DOCK_EN_L	TPM_XTALI - @m38_lib.M38	59B7 67C6	PP3V3_S0_NB_VCCA_TVDDACU	PP3V3_S0_NB_VCCA_TVDDACU - @m38_lib.M38	17C6 19B1		
	TP_AZ_DOCK_RST_L	TPM_XTALO - @m38_lib.M38	59B7 67C6	PP3V3_S0_NB_VCCA_TVDDACV	PP3V3_S0_NB_VCCA_TVDDACV - @m38_lib.M38	17C6 19B1		
	TP_CLK14P3M_SPARE	TP_ATI_ROMCS_L - @m38_lib.M38	91A3	PP3V3_S0_NB_VCCA_TVDDACW	PP3V3_S0_NB_VCCA_TVDDACW - @m38_lib.M38	17C6 19B1		
	TP_CPU_A32_L	TP_AZ_DOCK_EN_L - @m38_lib.M38	23C5	PP3V3_S0_NB_VCCA_TVDDACX	PP3V3_S0_NB_VCCA_TVDDACX - @m38_lib.M38	17C6 19B1		
	TP_CPU_A33_L	TP_AZ_DOCK_RST_L - @m38_lib.M38	23C5	PP3V3_S0_NB_VCCA_TVDDACY	PP3V3_S0_NB_VCCA_TVDDACY - @m38_lib.M38	17C6 19B1		
	TP_CPU_A34_L	TP_CLK14P3M_SPARE - @m38_lib.M38	34C4	PP3V3_S0_NB_VCCA_TVDDACZ	PP3V3_S0_NB_VCCA_TVDDACZ - @m38_lib.M38	17C6 19B1		
	TP_CPU_A35_L	TP_CPU_A32_L - @m38_lib.M38	7C7	PP3V3_S0_NB_VCCA_TVDDACA	PP3V3_S0_NB_VCCA_TVDDACA - @m38_lib.M38	17C6 19B1		
	TP_CPU_A36_L	TP_CPU_A33_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACB	PP3V3_S0_NB_VCCA_TVDDACB - @m38_lib.M38	17C6 19B1		
	TP_CPU_A37_L	TP_CPU_A34_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACC	PP3V3_S0_NB_VCCA_TVDDACC - @m38_lib.M38	17C6 19B1		
	TP_CPU_A38_L	TP_CPU_A35_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACD	PP3V3_S0_NB_VCCA_TVDDACD - @m38_lib.M38	17C6 19B1		
	TP_CPU_A39_L	TP_CPU_A36_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACE	PP3V3_S0_NB_VCCA_TVDDACE - @m38_lib.M38	17C6 19B1		
	TP_CPU_APM0_L	TP_CPU_A37_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACF	PP3V3_S0_NB_VCCA_TVDDACF - @m38_lib.M38	17C6 19B1		
	TP_CPU_APM1_L	TP_CPU_A38_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACG	PP3V3_S0_NB_VCCA_TVDDACG - @m38_lib.M38	17C6 19B1		
	TP_CPU_CFU5LP_L	TP_CPU_A39_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACH	PP3V3_S0_NB_VCCA_TVDDACH - @m38_lib.M38	17C6 19B1		
	TP_CPU_EXTBREF	TP_CPU_APM0_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACI	PP3V3_S0_NB_VCCA_TVDDACI - @m38_lib.M38	17C6 19B1		
	TP_CPU_HFPLL	TP_CPU_APM1_L - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACJ	PP3V3_S0_NB_VCCA_TVDDACJ - @m38_lib.M38	17C6 19B1		
	TP_CPU_SPARE0	TP_CPU_CFU5LP_L - @m38_lib.M38	21C4	PP3V3_S0_NB_VCCA_TVDDACK	PP3V3_S0_NB_VCCA_TVDDACK - @m38_lib.M38	17C6 19B1		
	TP_CPU_SPARE1	TP_CPU_EXTBREF - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACL	PP3V3_S0_NB_VCCA_TVDDACL - @m38_lib.M38	17C6 19B1		
	TP_CPU_SPARE2	TP_CPU_HFPLL - @m38_lib.M38	7B7	PP3V3_S0_NB_VCCA_TVDDACM	PP3V3_S0_NB_VCCA_TVDDACM - @m38_lib.M38	17C6 19B1		
	TP_CPU_SPARE3	TP_CPU_SPARE0 - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACN	PP3V3_S0_NB_VCCA_TVDDACN - @m38_lib.M38	17C6 19B1		
	TP_CPU_SPARE4	TP_CPU_SPARE1 - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACO	PP3V3_S0_NB_VCCA_TVDDACO - @m38_lib.M38	17C6 19B1		
	TP_CPU_SPARE5	TP_CPU_SPARE2 - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACP	PP3V3_S0_NB_VCCA_TVDDACP - @m38_lib.M38	17C6 19B1		
	TP_CPU_SPARE6	TP_CPU_SPARE3 - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACQ	PP3V3_S0_NB_VCCA_TVDDACQ - @m38_lib.M38	17C6 19B1		
	TP_CPU_SPARE7	TP_CPU_SPARE4 - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACR	PP3V3_S0_NB_VCCA_TVDDACR - @m38_lib.M38	17C6 19B1		
	TP_FB_A_MA12	TP_CPU_SPARE5 - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACS	PP3V3_S0_NB_VCCA_TVDDACS - @m38_lib.M38	17C6 19B1		
	TP_FB_A_ODT<0>	TP_CPU_SPARE6 - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACT	PP3V3_S0_NB_VCCA_TVDDACT - @m38_lib.M38	17C6 19B1		
	TP_FB_A_ODT<1>	TP_CPU_SPARE7 - @m38_lib.M38	7B6	PP3V3_S0_NB_VCCA_TVDDACU	PP3V3_S0_NB_VCCA_TVDDACU - @m38_lib.M38	17C6 19B1		
	TP_FB_B_MA12	TP_FB_A_MA12 - @m38_lib.M38	87D5	PP3V3_S0_NB_VCCA_TVDDACV	PP3V3_S0_NB_VCCA_TVDDACV - @m38_lib.M38	17C6 19B1		
	TP_FB_B_ODT<0>	TP_FB_A_ODT<0> - @m38_lib.M38	87B5	PP3V3_S0_NB_VCCA_TVDDACW	PP3V3_S0_NB_VCCA_TVDDACW - @m38_lib.M38	17C6 19B1		
	TP_FB_B_ODT<1>	TP_FB_A_ODT<1> - @m38_lib.M38	87B5	PP3V3_S0_NB_VCCA_TVDDACX	PP3V3_S0_NB_VCCA_TVDDACX - @m38_lib.M38	17C6 19B1		
	TP_FW_CMA	TP_FB_B_MA12 - @m38_lib.M38	87D1	PP3V3_S0_NB_VCCA_TVDDACY	PP3V3			

	8	7	6	5	4	3	2	1
D	Title: Cref Part Report Design: m38 Date: Dec 8 15:02:55 2005		C2514 CAP_402 m38[25C6]	C4113 CAP_402 m38[41C4]	C6308 CAP_402 m38[63C5]			
	C85A0 CAP_402 m38[85D1]	C2515 CAP_402 m38[25B6]	C4115 CAP_402 m38[41B5]	C6309 CAP_402 m38[63C6]				
	C600 CAP_402 m38[6C7]	C2516 CAP_P_CASE-C2 m38[25D3]	C4116 CAP_402 m38[41B5]	C6311 CAP_402 m38[63C2]				
	C601 CAP_402 m38[6A3]	C2517 CAP_402 m38[25D6]	C4117 CAP_402 m38[41B2]	C6312 CAP_402 m38[63D3]				
	C602 CAP_402 m38[6A3]	C2518 CAP_402 m38[25D4]	C4118 CAP_402 m38[41B2]	C6500 CAP_603 m38[65D5]				
	C603 CAP_402 m38[6A3]	C2519 CAP_402 m38[25D3]	C4126 CAP_402 m38[41A8]	C6501 CAP_805 m38[65D5]				
	C604 CAP_402 m38[6A4]	C2520 CAP_402 m38[25B6]	C4127 CAP_402 m38[41A8]	C6502 CAP_603 m38[65B4]				
	C610 CAP_402 m38[6C7]	C2521 CAP_402 m38[25C3]	C4128 CAP_402 m38[41A8]	C6503 CAP_805 m38[65B5]				
	C699 CAP_P_CASE-C1 m38[6D7]	C2522 CAP_402 m38[25B3]	C4129 CAP_402 m38[41A8]	C6504 CAP_P_6_3X11-TH-LF m38[65C4]				
	C0800 CAP_402 m38[8B5]	C2523 CAP_402 m38[25B4]	C4130 CAP_402 m38[41A7]	C6505 CAP_P_6_3X11-TH-LF m38[65B3]				
	C0801 CAP_603 m38[8B5]	C2524 CAP_603 m38[25B3]	C4131 CAP_402 m38[41A7]	C6600 CAP_603 m38[66D4]				
	C900 CAP_805 m38[9B6]	C2525 CAP_402 m38[25B3]	C4132 CAP_402 m38[41A7]	C6601 CAP_805 m38[66C5]				
	C901 CAP_805 m38[9B6]	C2526 CAP_402 m38[25A4]	C4133 CAP_402 m38[41A6]	C6602 CAP_P_SM-LF m38[66C3]				
	C902 CAP_805 m38[9A6]	C2527 CAP_402 m38[25A3]	C4134 CAP_402 m38[41A6]	C6650 CAP_402 m38[66B5]				
	C903 CAP_805 m38[9A6]	C2528 CAP_402 m38[25A3]	C4135 CAP_402 m38[41A5]	C6651 CAP_402 m38[66A5]				
	C904 CAP_805 m38[9A6]	C2529 CAP_402 m38[25A3]	C4136 CAP_402 m38[41A5]	C6652 CAP_402 m38[66B3]				
	C905 CAP_805 m38[9A6]	C2530 CAP_402 m38[25A3]	C4137 CAP_402 m38[41A5]	C6653 CAP_402 m38[66A3]				
	C906 CAP_805 m38[9A6]	C2531 CAP_402 m38[25D1]	C4138 CAP_402 m38[41A4]	C6654 CAP_402 m38[66B2]				
	C907 CAP_805 m38[9B5]	C2532 CAP_402 m38[25C1]	C4139 CAP_402 m38[41A4]	C6655 CAP_402 m38[66B2]				
	C908 CAP_805 m38[9B7]	C2533 CAP_402 m38[25C1]	C4140 CAP_402 m38[41B3]	C6700 CAP_402 m38[67C4]				
	C909 CAP_805 m38[9B5]	C2534 CAP_402 m38[25D1]	C4150 CAP_402 m38[41D5]	C6701 CAP_402 m38[67C4]				
	C910 CAP_805 m38[9B7]	C2605 CAP_402 m38[26C7]	C4200 CAP_1210 m38[42D8]	C6702 CAP_402 m38[67C3]				
	C911 CAP_805 m38[9B7]	C2607 CAP_402 m38[26D5]	C4201 CAP_402 m38[42D7]	C6703 CAP_402 m38[67C3]				
	C912 CAP_805 m38[9A7]	C2608 CAP_402 m38[26D8]	C4202 CAP_1210 m38[42D7]	C6704 CAP_402 m38[67C3]				
	C913 CAP_805 m38[9A7]	C2609 CAP_402 m38[26D8]	C4203 CAP_1206-1 m38[42D6]	C6705 CAP_402 m38[67C4]				
C914 CAP_805 m38[9A7]	C2610 CAP_402 m38[26C7]	C4204 CAP_402 m38[42D6]	C6800 CAP_603 m38[68D6]					
C915 CAP_805 m38[9A7]	C2611 CAP_402 m38[26B7]	C4205 CAP_1210 m38[42C5]	C6801 CAP_402 m38[68D6]					
C916 CAP_805 m38[9A7]	C2698 CAP_402 m38[26C4]	C4206 CAP_402 m38[42C5]	C6802 CAP_P_B2 m38[68B2]					
C917 CAP_805 m38[9A7]	C2699 CAP_402 m38[26C5]	C4209 CAP_603 m38[42B7]	C6803 CAP_P_B2 m38[68D3]					
C918 CAP_805 m38[9A7]	C2800 CAP_402 m38[28D6]	C4210 CAP_402 m38[42B6]	C6804 CAP_P_SMA-LF m38[68B4]					
C919 CAP_805 m38[9A7]	C2801 CAP_603 m38[28B2]	C4300 CAP_402 m38[43D7]	C6805 CAP_805 m38[68B3]					
C920 CAP_805 m38[9A5]	C2802 CAP_603 m38[28B2]	C4301 CAP_402 m38[43D6]	C6806 CAP_805 m38[68B3]					
C921 CAP_805 m38[9A7]	C2803 CAP_603 m38[28B1]	C4304 CAP_402 m38[43C6]	C6807 CAP_P_SMA-LF m38[68B3]					
C922 CAP_805 m38[9A7]	C2804 CAP_603 m38[28B1]	C4305 CAP_402 m38[43B6]	C6808 CAP_402 m38[68B3]					
C923 CAP_805 m38[9B7]	C2810 CAP_402 m38[28B2]	C4401 CAP_402 m38[44D1]	C6809 CAP_402 m38[68B2]					
C924 CAP_805 m38[9A7]	C2811 CAP_402 m38[28B2]	C4402 CAP_402 m38[44C1]	C6810 CAP_P_SMA-LF m38[68B2]					
C925 CAP_805 m38[9A7]	C2812 CAP_402 m38[28B1]	C4410 CAP_402 m38[44D6]	C6811 CAP_402 m38[68B1]					
C926 CAP_402 m38[9B7]	C2813 CAP_402 m38[28B1]	C4412 CAP_402 m38[44D1]	C6812 CAP_402 m38[68B4]					
C928 CAP_805 m38[9B6]	C2814 CAP_402 m38[28B2]	C4500 CAP_402 m38[45D4]	C6813 CAP_402 m38[68B3]					
C929 CAP_805 m38[9B5]	C2815 CAP_402 m38[28B2]	C4501 CAP_402 m38[45D3]	C6821 CAP_402 m38[68C6]					
C930 CAP_805 m38[9A6]	C2816 CAP_402 m38[28B1]	C4502 CAP_402 m38[45D3]	C6822 CAP_603 m38[68A5]					
C931 CAP_805 m38[9A5]	C2817 CAP_402 m38[28B1]	C4503 CAP_603 m38[45C6]	C6823 CAP_402 m38[68A4]					
C932 CAP_805 m38[9B7]	C2818 CAP_402 m38[28B2]	C4504 CAP_402 m38[45C4]	C6825 CAP_402 m38[68A3]					
C934 CAP_402 m38[9B7]	C2819 CAP_402 m38[28B2]	C4505 CAP_402 m38[45C5]	C6826 CAP_603 m38[68A3]					
C935 CAP_402 m38[9B7]	C2820 CAP_402 m38[28B1]	C4506 CAP_402 m38[45C5]	C6829 CAP_402 m38[68B3]					
C936 CAP_402 m38[9B7]	C2821 CAP_402 m38[28B1]	C4507 CAP_402 m38[45C5]	C6830 CAP_402 m38[68D4]					
C937 CAP_402 m38[9B6]	C2850 CAP_603 m38[28D6]	C4508 CAP_402 m38[45D5]	C6832 CAP_402 m38[68B1]					
C938 CAP_402 m38[9B6]	C2851 CAP_603 m38[28A6]	C4509 CAP_402 m38[45D5]	C6833 CAP_402 m38[68B2]					
C939 CAP_805 m38[9A5]	C2852 CAP_402 m38[28A6]	C4510 CAP_402 m38[45D5]	C6834 CAP_402 m38[68B2]					
C940 CAP_P_CASE-C1 m38[9C7]	C2900 CAP_402 m38[29D6]	C4515 CAP_603 m38[45D6]	C6835 CAP_402 m38[68D6]					
C941 CAP_P_3P_D2T m38[9A7]	C2908 CAP_402 m38[29B2]	C4520 CAP_402 m38[45D5]	C6836 CAP_402 m38[68D3]					
C942 CAP_P_3P_D2T m38[9A7]	C2909 CAP_402 m38[29B2]	C4521 CAP_402 m38[45D4]	C7200 CAP_P_6_3X8-SM m38[72D5]					
C943 CAP_P_3P_D2T m38[9A7]	C2910 CAP_402 m38[29B1]	C4522 CAP_402 m38[45D3]	C7201 CAP_1210 m38[72D5]					
C944 CAP_P_3P_D2T m38[9A7]	C2911 CAP_402 m38[29B1]	C4523 CAP_402 m38[45D3]	C7202 CAP_805 m38[72D4]					
C945 CAP_P_3P_D2T m38[9A6]	C2912 CAP_402 m38[29B2]	C4609 CAP_603-1 m38[46D5]	C7203 CAP_1210 m38[72D3]					
C946 CAP_P_3P_D2T m38[9A6]	C2913 CAP_402 m38[29B2]	C4610 CAP_402 m38[46D4]	C7204 CAP_805 m38[72D6]					
C950 CAP_402 m38[9D4]	C2914 CAP_402 m38[29B1]	C4611 CAP_402 m38[46D4]	C7205 CAP_805 m38[72C6]					
C951 CAP_402 m38[9D3]	C2915 CAP_402 m38[29B1]	C4612 CAP_402 m38[46C4]	C7206 CAP_805 m38[72C6]					
C952 CAP_402 m38[9D3]	C2916 CAP_402 m38[29B2]	C4613 CAP_402 m38[46C4]	C7207 CAP_805 m38[72C6]					
C953 CAP_402 m38[9D2]	C2917 CAP_402 m38[29B2]	C4615 CAP_603-1 m38[46C2]	C7208 CAP_603-1 m38[72C4]					
C1000 CAP_402 m38[10C6]	C2918 CAP_402 m38[29B1]	C4616 CAP_402 m38[46B2]	C7209 CAP_805 m38[72B4]					
C1001 CAP_402 m38[10D4]	C2919 CAP_402 m38[29B1]	C4620 CAP_402 m38[46B4]	C7210 CAP_402 m38[72B3]					
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C1211 CAP_402 m38[12C3]	C2921 CAP_402 m38[29B2]	C4622 CAP_402 m38[46A4]	C7212 CAP_402 m38[72B2]					
C1226 CAP_402 m38[12B6]	C2922 CAP_402 m38[29B1]	C4623 CAP_402 m38[46A4]	C7213 CAP_402 m38[72B2]					
C1236 CAP_402 m38[12A6]	C2923 CAP_402 m38[29B1]	C4625 CAP_603-1 m38[46A2]	C7214 CAP_603 m38[72D5]					
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C1611 CAP_402 m38[16B4]	C2951 CAP_603 m38[29A7]	C4650 CAP_402 m38[46C7]	C7216 CAP_402 m38[72C6]					
C1612 CAP_402 m38[16B4]	C2952 CAP_402 m38[29A6]	C4654 CAP_402 m38[46B8]	C7217 CAP_P_6_3X8-SM m38[72D6]					
C1613 CAP_402 m38[16B8]	C3004 CAP_402 m38[30B4]	C4660 CAP_402 m38[46C7]	C7218 CAP_603 m38[72D5]					
C1614 CAP_402 m38[16B8]	C3005 CAP_402 m38[30D4]	C4664 CAP_402 m38[46B7]	C7219 CAP_603 m38[72D4]					
C1615 CAP_402 m38[16B6]	C3006 CAP_402 m38[30B3]	C4710 CAP_P_SMD2 m38[47D6]	C7220 CAP_402 m38[72B7]					
C1620 CAP_603 m38[16B5]	C3007 CAP_402 m38[30D3]	C4712 CAP_402 m38[47D5]	C7221 CAP_402 m38[72B7]					
C1621 CAP_603 m38[16B5]	C3008 CAP_402 m38[30A3]	C4713 CAP_402 m38[47D5]	C7223 CAP_1210 m38[72D3]					
C1711 CAP_402 m38[17A3]	C3009 CAP_402 m38[30A4]	C4720 CAP_P_SMD m38[47C6]	C7300 CAP_402 m38[73D7]					
C1712 CAP_402 m38[17A3]	C3010 CAP_402 m38[30D4]	C4722 CAP_402 m38[47C5]	C7301 CAP_402 m38[73D6]					
C1713 CAP_402 m38[17B3]	C3011 CAP_402 m38[30D3]	C4723 CAP_402 m38[47C5]	C7302 CAP_402 m38[73D6]					
C1900 CAP_P_CASE-C1 m38[19B5]	C3013 CAP_402 m38[30A4]	C4732 CAP_402 m38[47A5]	C7303 CAP_402 m38[73D6]					
C1901 CAP_P_CASE-C1 m38[19B5]	C3014 CAP_402 m38[30A4]	C4733 CAP_402 m38[47A5]	C7311 CAP_402 m38[73A7]					
C1902 CAP_603 m38[19B5]	C3015 CAP_402 m38[30A3]	C4742 CAP_402 m38[47D2]	C7312 CAP_402 m38[73A6]					
C1903 CAP_603 m38[19B4]	C3030 CAP_402 m38[30C4]	C4743 CAP_402 m38[47D1]	C7313 CAP_402 m38[73A6]					
C1904 CAP_402 m38[19B4]	C3033 CAP_402 m38[30C3]	C4797 CAP_805-2 m38[47D3]	C7314 CAP_403 m38[73A7]					
C1905 CAP_402 m38[19B4]	C3035 CAP_402 m38[30C3]	C4798 CAP_402 m38[47A2]	C7315 CAP_402 m38[73A6]					
C1906 CAP_402 m38[19B3]	C3100 CAP_402 m38[31C4]	C4799 CAP_805-2 m38[47A2]	C7317 CAP_402 m38[73B4]					
C1907 CAP_402 m38[19B3]	C3101 CAP_603 m38[31B6]	C4950 CAP_805-2 m38[49C4]	C7318 CAP_805 m38[73B4]					
C1914 CAP_603 m38[19A8]	C3102 CAP_402 m38[31B4]	C4951 CAP_402 m38[49C4]	C7321 CAP_402 m38[73C5]					
C1915 CAP_402 m38[19A7]	C3105 CAP_P_SMC-LF m38[31B4]	C5300 CAP_402 m38[53B7]	C7322 CAP_402 m38[73C5]					
C1916 CAP_402 m38[19A8]	C3109 CAP_603 m38[31C5]	C5301 CAP_402 m38[53B7]	C7323 CAP_402 m38[73A6]					
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C1920 CAP_402 m38[19A2]	C3301 CAP_402 m38[33D6]	C5305 CAP_402 m38[53D5]	C7400 CAP_402 m38[74B4]					
C1921 CAP_402 m38[19A2]	C3302 CAP_402 m38[33D6]	C5306 CAP_402 m38[53D4]	C7401 CAP_402 m38[74C4]					
C1934 CAP_805 m38[19C7]	C3303 CAP_402 m38[33D6]	C5307 CAP_402 m38[53C4]	C7402 CAP_402 m38[74A4]					
C1935 CAP_402 m38[19C7]	C3304 CAP_402 m38[33D6]	C5308 CAP_402 m38[53C5]	C7403 CAP_P_CASE-B-SM m38[74B7]					
C1936 CAP_805 m38[19C7]	C3305 CAP_402 m38[33D4]	C5309 CAP_402 m38[53C4]	C7404 CAP_P_CASE-B-SM m38[74B6]					
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C1965 CAP_603 m38[19B8]	C3307 CAP_402 m38[33C4]	C5311 CAP_603 m38[53C3]	C7406 CAP_P_CASE-B-SM m38[74C6]					
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C1967 CAP_402 m38[19B7]	C3309 CAP_603 m38[33D4]	C5313 CAP_402 m38[53C5]	C7408 CAP_402 m38[74D7]					
C1968 CAP_P_CASE-C1 m38[19B7]	C3310 CAP_402 m38[33D3]	C5314 CAP_603 m38[53C4]	C7409 CAP_402 m38[74B8]					
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C1971 CAP_603 m38[19A4]	C3312 CAP_603 m38[33C6]	C5801 CAP_402 m38[58B8]	C7411 CAP_402 m38[74B8]					
C1972 CAP_603 m38[19A4]	C3314 CAP_402 m38[33D8]	C5802 CAP_805 m38[58D3]	C7412 CAP_402 m38[74A8]					
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C1981 CAP_603 m38[19B6]	C3317 CAP_603 m38[33D4]	C5806 CAP_402 m38[58D1]	C7415 CAP_402 m38[74A8]					
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C2504 CAP_402 m38[25C8]	C3803 CAP_402 m38[38B7]	C5903 CAP_402 m38[59A8]	C7421 CAP_402 m38[74A7]					
C2505 CAP_402 m38[25B7]	C3804 CAP_402 m38[38C3]	C5919 CAP_402 m38[59B4]	C7422 CAP_402 m38[74A7]					
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C2507 CAP_402 m38[25B7]	C3806 CAP_805-2 m38[38C1]	C5941 CAP_402 m38[59A3]	C7500 CAP_402 m38[75C4]					
C2508 CAP_603 m38[25A6]	C4101 CAP_402 m38[41D7]	C5942 CAP_603 m38[59A3]	C7501 CAP_P					

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D	C7511 CAP_603 m38[7582]	C7512 CAP_603 m38[75C2]	C7513 CAP_402 m38[75B7]	C7514 CAP_402 m38[7588]	C7515 CAP_603 m38[75C4]	C7516 CAP_402 m38[75B4]	C7517 CAP_P_SM-3 m38[75D2]	C7518 CAP_P_SM-3 m38[75D2]	C7521 CAP_402 m38[75A6]	C7526 CAP_603 m38[75D6]	C7527 CAP_603 m38[75C5]	C7528 CAP_402 m38[75B5]	C7529 CAP_402 m38[75B5]	C7530 CAP_402 m38[75D6]	C7531 CAP_402 m38[75B5]	C7532 CAP_402 m38[75B6]	C7533 CAP_402 m38[75B6]	C7534 CAP_402 m38[75B5]	C7535 CAP_603 m38[75D5]	C7550 CAP_603 m38[75D1]	C7551 CAP_603 m38[75D1]	C7590 CAP_402 m38[75C3]	C7592 CAP_402 m38[75B3]	C7596 CAP_402 m38[75D6]	C7597 CAP_1210 m38[75D1]	C7598 CAP_1210 m38[75D1]	C7599 CAP_402 m38[75D6]	C7600 CAP_402 m38[76D3]	C7601 CAP_402 m38[76D3]	C7602 CAP_402 m38[76D2]	C7603 CAP_402 m38[76C4]	C7612 CAP_402 m38[76B2]	C7633 CAP_402 m38[76C7]	C7659 CAP_402 m38[76D4]	C7669 CAP_402 m38[76D4]	C7700 CAP_603 m38[77B4]	C7703 CAP_402 m38[77C4]	C7704 CAP_402 m38[77C4]	C7706 CAP_402 m38[77C4]	C7709 CAP_805 m38[77C3]	C7710 CAP_402 m38[77C7]	C7711 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CAP_402 m38[84D2]	C8460 CAP_402 m38[84D2]	C8461 CAP_402 m38[84D2]	C8462 CAP_402 m38[84D2]	C8463 CAP_402 m38[84D2]	C8464 CAP_402 m38[84C2]	C8465 CAP_402 m38[84C2]	C8466 CAP_402 m38[84C2]	C8467 CAP_402 m38[84C2]	C8468 CAP_402 m38[84C2]	C8469 CAP_402 m38[84C2]	C8470 CAP_402 m38[84C2]	C8471 CAP_402 m38[84C2]	C8472 CAP_402 m38[84C2]	C8473 CAP_402 m38[84C2]	C8474 CAP_402 m38[84C2]	C8475 CAP_402 m38[84B2]	C8476 CAP_402 m38[84B2]	C8477 CAP_402 m38[84B2]	C8478 CAP_402 m38[84B2]	C8479 CAP_402 m38[84B2]	C8480 CAP_402 m38[84B2]	C8481 CAP_402 m38[84B2]	C8482 CAP_402 m38[84B2]	C8483 CAP_402 m38[84B2]	C8484 CAP_402 m38[84B2]	C8485 CAP_402 m38[84B2]	C8486 CAP_402 m38[84B2]	C8500 CAP_603 m38[85D6]	C8501 CAP_603 m38[85D6]	C8502 CAP_603 m38[85D6]	C8506 CAP_402 m38[85C8]	C8507 CAP_402 m38[85C7]	C8508 CAP_402 m38[85C7]	C8509 CAP_402 m38[85C5]	C8520 CAP_603 m38[85C3]	C8521 CAP_402 m38[85C4]	C8522 CAP_402 m38[85C5]	C8523 CAP_402 m38[85B2]	C8530 CAP_1210 m38[85D4]	C8531 CAP_1210 m38[85D4]	C8532 CAP_1210 m38[85D4]	C8540 CAP_805 m38[85C2]	C8541 CAP_805 m38[85C2]	C8542 CAP_P_CASE-D2E-LF m38[85C2]	C8543 CAP_P_CASE-D2E-LF m38[85C2]	C8551 CAP_603 m38[85B8]	C8555 CAP_402 m38[85B7]	C8556 CAP_805 m38[85B6]	C8557 CAP_805 m38[85B6]	C8560 CAP_402 m38[85A6]	C8570 CAP_402 m38[85A5]	C8580 CAP_603 m38[85A4]	C8585 CAP_402 m38[85A4]	C8586 CAP_402 m38[85A3]	C8589 CAP_805 m38[85A3]	C8590 CAP_402 m38[85D3]	C8592 CAP_402 m38[85C2]	C8595 CAP_402 m38[85D1]	C8598 CAP_402 m38[85D2]	C8599 CAP_1206 m38[85C4]	C8600 CAP_805 m38[86C7]	C8601 CAP_805 m38[86C7]	C8604 CAP_402 m38[86C7]	C8605 CAP_402 m38[86C6]	C8606 CAP_402 m38[86C6]	C8607 CAP_402 m38[86C6]	C8608 CAP_402 m38[86C5]	C8609 CAP_402 m38[86C5]	C8610 CAP_402 m38[86C5]	C8611 CAP_402 m38[86C7]	C8612 CAP_402 m38[86C6]	C8613 CAP_402 m38[86C6]	C8614 CAP_402 m38[86C6]	C8615 CAP_402 m38[86C5]	C8616 CAP_402 m38[86C5]	C8630 CAP_805 m38[86C6]	C8631 CAP_402 m38[86C6]	C8632 CAP_402 m38[86C5]	C8633 CAP_402 m38[86C5]	C8634 CAP_402 m38[86C5]	C8650 CAP_805 m38[86B7]	C8651 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m38[87A6]	C8721 CAP_402 m38[87B4]	C8723 CAP_402 m38[87B4]	C8725 CAP_402 m38[87A4]	C8726 CAP_402 m38[87A3]	C8900 CAP_805 m38[89D7]	C8901 CAP_402 m38[89D7]	C8902 CAP_402 m38[89D7]	C8903 CAP_402 m38[89D7]	C8904 CAP_402 m38[89D6]	C8910 CAP_402 m38[89D7]	C8915 CAP_402 m38[89D6]	C8920 CAP_805 m38[89C8]	C8921 CAP_402 m38[89C8]	C8922 CAP_402 m38[89C7]	C8923 CAP_402 m38[89C7]	C8924 CAP_402 m38[89C7]	C8925 CAP_402 m38[89C7]	C8926 CAP_402 m38[89C6]	C8931 CAP_402 m38[89C7]	C8933 CAP_402 m38[89C6]	C8950 CAP_805 m38[89D4]	C8951 CAP_402 m38[89D4]	C8952 CAP_402 m38[89D4]	C8953 CAP_402 m38[89D3]	C8954 CAP_402 m38[89D3]	C8960 CAP_402 m38[89D3]	C8965 CAP_402 m38[89D3]	C8970 CAP_805 m38[89C5]	C8971 CAP_402 m38[89C4]	C8972 CAP_402 m38[89C4]	C8973 CAP_402 m38[89C4]	C8974 CAP_402 m38[89C4]	C8975 CAP_402 m38[89C3]	C8976 CAP_402 m38[89C3]	C8981 CAP_402 m38[89C3]	C8983 CAP_402 m38[89C3]	C9000 CAP_805 m38[90D7]	C9001 CAP_402 m38[90D7]	C9002 CAP_402 m38[90D7]	C9003 CAP_402 m38[90D7]	C9004 CAP_402 m38[90D6]	C9010 CAP_402 m38[90D7]	C9015 CAP_402 m38[90D6]	C9020 CAP_805 m38[90C8]	C9021 CAP_402 m38[90C8]	C9022 CAP_402 m38[90C7]	C9023 CAP_402 m38[90C7]	C9024 CAP_402 m38[90C7]	C9025 CAP_402 m38[90C7]	C9026 CAP_402 m38[90C6]	C9031 CAP_402 m38[90C7]	C9033 CAP_402 m38[90C6]	C9050 CAP_805 m38[90D4]	C9051 CAP_402 m38[90D4]	C9052 CAP_402 m38[90D4]	C9053 CAP_402 m38[90D3]	C9054 CAP_402 m38[90D3]	C9060 CAP_402 m38[90D3]	C9065 CAP_402 m38[90D3]	C9070 CAP_805 m38[90C5]	C9071 CAP_402 m38[90C4]	C9072 CAP_402 m38[90C4]	C9073 CAP_402 m38[90C4]	C9074 CAP_402 m38[90C4]	C9075 CAP_402 m38[90C3]	C9076 CAP_402 m38[90C3]	C9081 CAP_402 m38[90C3]	C9083 CAP_402 m38[90C3]	C9100 CAP_805 m38[91C5]	C9101 CAP_402 m38[91C5]	C9102 CAP_402 m38[91C5]	C9103 CAP_402 m38[91C5]	C9110 CAP_805 m38[91C5]	C9111 CAP_402 m38[91C5]	C9112 CAP_402 m38[91C5]	C9115 CAP_805 m38[91B5]	C9116 CAP_402 m38[91B5]	C9117 CAP_402 m38[91B5]	C9120 CAP_805 m38[91B5]	C9121 CAP_402 m38[91B5]	C9122 CAP_402 m38[91B5]	C9125 CAP_805 m38[91B5]	C9126 CAP_402 m38[91B5]	C9127 CAP_402 m38[91B5]	C9130 CAP_805 m38[91A6]	C9131 CAP_402 m38[91A6]	C9132 CAP_402 m38[91A5]	C9135 CAP_805 m38[91A6]	C9136 CAP_402 m38[91A6]	C9137 CAP_402 m38[91A5]	C9140 CAP_805 m38[91A6]	C9141 CAP_402 m38[91A6]	C9142 CAP_402 m38[91A5]	C9191 CAP_402 m38[91D2]	C9300 CAP_805 m38[93C6]	C9301 CAP_402 m38[93C6]	C9302 CAP_402 m38[93C5]	C9305 CAP_805 m38[93C6]	C9306 CAP_402 m38[93C6]	C9307 CAP_402 m38[93C5]	C9310 CAP_805 m38[93C6]	C9311 CAP_402 m38[93C6]	C9312 CAP_402 m38[93C5]	C9315 CAP_805 m38[93B8]	C9316 CAP_402 m38[93B8]	C9317 CAP_402 m38[93B7]	C9320 CAP_805 m38[93B6]	C9321 CAP_402 m38[93B6]	C9322 CAP_402 m38[93B5]	C9325 CAP_805 m38[93B8]	C9326 CAP_402 m38[93B8]	C9327 CAP_402 m38[93B7]	C9330 CAP_805 m38[93B6]	C9331 CAP_402 m38[93B6]	C9332 CAP_402 m38[93B5]	C9340 CAP_805 m38[93A6]	C9341 CAP_402 m38[93A6]	C9342 CAP_402 m38[93A5]	C9345 CAP_805 m38[93A6]	C9346 CAP_402 m38[93A5]	C9347 CAP_402 m38[93A5]	C9400 CAP_603-1 m38[94C7]	C9401 CAP_402 m38[94C6]	C9410 CAP_402 m38[94C6]	C9420 CAP_1210 m38[94C5]	C9450 CAP_805 m38[94C2]	C9470 CAP_402 m38[94B2]	C9700 CAP_402 m38[97C8]	C9710 CAP_603 m38[97C3]	C9711 CAP_402 m38[97D3]	C9713 CAP_402 m38[97C2]	C9714 CAP_402 m38[97C2]	C9740 CAP_402 m38[97A7]	C9741 CAP_402 m38[97A7]	C9742 CAP_402 m38[97A6]	D2500 DIODE_SCHOT_SOT23 m38[25C8]	D2501 DIODE_SCHOT_SOT23 m38[25D8]	D2600 DIODE_SCHOT_SOT23 m38[26D8]	D2601 DIODE_SCHOT_SOT23 m38[26C8]	D4600 DIODE_SMC m38[46D5]	D4690 ZENER_SOT23 m38[46A6]	D4700 DIODE_SCHOT_3P_A_SC- m38[47C5]	75	D4701 DIODE_SCHOT_3P_A_SC- m38[47B5]	75	D4702 DIODE_SCHOT_3P_A_SC- m38[47A5]	75	D4705 DIODE_SCHOT_3P_A_SC- m38[47C6]	75	D4706 DIODE_SCHOT_3P_A_SC- m38[47B6]	75	D4707 DIODE_SCHOT_3P_A_SC- m38[47A6]	75	D4900 DIODE_SCHOT_3P_A_SC- m38[49B4]	75	D6500 DIODE_SOT23 m38[65C4]	D6501 DIODE_SOT23 m38[65B4]	D6502 DIODE_SCHOT_SMB m38[65C4]	D6503 DIODE_SCHOT_SMB m38[65B4]	D6600 DIODE_SOT23 m38[66C4]	D6601 DIODE_SCHOT_SMB m38[66C3]	D7500 DIODE_SCHOT_SMB m38[75C3]	D7501 DIODE_SCHOT_SMB m38[75B2]	D7599 DIODE_SOT23 m38[76D6]	D8520 DIODE_SCHOT_SMB m38[85C3]	D9700 ZENER_CASE425 m38[97C1]	DP4610 DIODE_DUAL_6P_SOT-36 m38[46D4 46D3]	3	DP4611 DIODE_DUAL_6P_SOT-36 m38[46C4 46C3]	3	DP4620 DIODE_DUAL_6P_SOT-36 m38[46B4 46B3]	3	DP4621 DIODE_DUAL_6P_SOT-36 m38[46A4 46A3]	3	DZ7300 SUPPR_TRANSIENT_4P1_ m38[73C6]	0405	DZ7301 SUPPR_TRANSIENT_4P1_ m38[73A5]	0405	DZ7302 SUPPR_TRANSIENT_4P1_ m38[73A4]	0405	DZ7303 SUPPR_TRANSIENT_4P1_ m38[73C5]	0405	DZ7304 SUPPR_TRANSIENT_4P1_ m38[73C4]	0405	F4600 FUSE_SM-LF m38[46D2]	F4601 FUSE_SM-LF m38[46D2]	F4602 FUSE_MINISMD-LF m38[46D3]	F4701 FUSE_MINISMD-LF m38[47D3]	F9710 FUSE_SM-LF m38[97D5]	FL4610 FILTER_4P_2012 m38[46C3]	FL4620 FILTER_4P_2012 m38[46B3]	FL9740 FILTER_LC_SM-220MHZ- m38[97B7]	LF	FL9741 FILTER_LC_SM-220MHZ- m38[97A7]	LF	FL9742 FILTER_LC_SM-220MHZ- m38[97A7]	LF	FLE011 FILTER_4P_2012 m38[46C3]	FLE021 FILTER_4P_2012 m38[46B3]	GV3801 HOLE_VIA m38[38A8]	GV3802 HOLE_VIA m38[38A7]	GV3803 HOLE_VIA m38[38A8]	GV3804 HOLE_VIA m38[38A7]	GV3805 HOLE_VIA m38[38A8]	GV3806 HOLE_VIA m38[38A7]	GV3807 HOLE_VIA m38[38A8]	GV3808 HOLE_VIA m38[38A7]	J2 CON_2RTSM_125_SM-2MT m38[61B7]	-BLK-LF	J3 CON_2RTSM_125_SM-2MT m38[61B6]	-BLK-LF
A	8	7	6	5	4	3	2	1																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																								

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	J600	CON_M12RT_D_THA_RT-M38[6D7]	L7325	IND_SM	m38[73A5]	PP623	PROBEPOINT_SM	m38[5C8]
	J0700	CPU_YONAH_SKT_BGA	L7326	IND_SM	m38[73B5]	PP624	PROBEPOINT_SM	m38[5C8]
	J0700	CPU_YONAH_SKT_BGA	L7327	IND_SM	m38[73B5]	PP625	PROBEPOINT_SM	m38[5C8]
	J1000	CON_2RTSM_125_SM-2MT	L7328	IND_SM	m38[73A5]	PP626	PROBEPOINT_SM	m38[5C8]
	J1101	CON_F28RT_S2MT_SM_F-	L7500	IND_SM	m38[75D1]	PP627	PROBEPOINT_SM	m38[5C8]
	J2600	BATTERY_2P_TH	L7501	IND_SM	m38[75B2]	PP628	PROBEPOINT_SM	m38[5C8]
	J2800	CON_F200RT_DDR2DIMM	L7502	IND_TH-VERT-LF	m38[76D8]	PP629	PROBEPOINT_SM	m38[5C8]
	J2900	CON_F200RT_DDR2DIMM	L7700	IND_SM1-LF	m38[77C4]	PP630	PROBEPOINT_SM	m38[5C8]
	J2900	CON_F200RT_DDR2DIMM	L7750	IND_SM-LF	m38[77A4]	PP631	PROBEPOINT_SM	m38[5D6]
	J2901	CON_F45T_S2MT_SM_F-S	L7800	IND_3P_SM	m38[78B3]	PP632	PROBEPOINT_SM	m38[5D6]
	J2903	CON_M2ST_S2MT_SM_M-S	L7900	IND_3P_SM	m38[79C3]	PP633	PROBEPOINT_SM	m38[5D6]
	J4700	CON_F10ST_D_SMA_F-ST	L8000	IND_3P_SM	m38[80C3]	PP634	PROBEPOINT_SM	m38[5D6]
	J4950	CON_M4RT_S2MT_SM_M-R	L8100	IND_IHLP	m38[81C3]	PP635	PROBEPOINT_SM	m38[5D6]
	J5300	CON_F52RT_D2MT_SM_F-	L8400	IND_0402	m38[84B7]	PP636	PROBEPOINT_SM	m38[5D6]
	J6000	CON_F30STSM_5047_S1	L8520	IND_IHLP	m38[85C3]	PP637	PROBEPOINT_SM	m38[5D6]
	J6500	CON_M4RT_S2MT_SM_M-R	L8585	IND_4P_2COIL_SQ1215	m38[85A3]	PP638	PROBEPOINT_SM	m38[5D6]
	J6501	CON_M5RT_S2MT_SM_M-A	L8715	IND_0402	m38[87A7]	PP639	PROBEPOINT_SM	m38[5D6]
	J6600	CON_F45T_S2MT_SM_F-S	L8725	IND_0402	m38[87A4]	PP640	PROBEPOINT_SM	m38[5D6]
	J6601	CON_M4RT_S2MT_SM_M-R	L8910	IND_0402	m38[89D7]	PP641	PROBEPOINT_SM	m38[5D6]
	J6602	CON_M4RT_S2MT_SM_M-R	L8915	IND_0402	m38[89D7]	PP642	PROBEPOINT_SM	m38[5D6]
	J7300	CON_F45T_S4MT_TH1_F-	L8960	IND_0402	m38[89D4]	PP643	PROBEPOINT_SM	m38[5D6]
	J7301	CON_M7RT_S2MT_SM_M-R	L8965	IND_0402	m38[89D4]	PP644	PROBEPOINT_SM	m38[5D6]
	J7303	CON_F9ANG_S4MT_TH1_F	L9010	IND_0402	m38[90D7]	PP645	PROBEPOINT_SM	m38[5D6]
	J9401	CON_M4ST_S_SM_M-ST-S	L9015	IND_0402	m38[90D7]	PP646	PROBEPOINT_SM	m38[5D6]
	J9402	CON_F30ST_D_SM_F-ST-	L9060	IND_0402	m38[90D4]	PP647	PROBEPOINT_SM	m38[5D6]
	J9410	CON_M4ST_S_SM_M-ST-S	L9065	IND_0402	m38[90D4]	PP648	PROBEPOINT_SM	m38[5D6]
	J9710	CON_DV1_F32ST_Q2MT_S	L9120	IND_0402	m38[91B6]	PP649	PROBEPOINT_SM	m38[5D6]
	J9900	CON_M7ST_SATA_SM_M-S	L9125	IND_0402	m38[91B6]	PP650	PROBEPOINT_SM	m38[5D6]
	J9901	CON_F50ST_D2MT_SM_F-	L9130	IND_0402	m38[91B7]	PP651	PROBEPOINT_SM	m38[5D6]
	JD600	CON_RJ45_10ANG_S3MT_	L9135	IND_0402	m38[91A7]	PP652	PROBEPOINT_SM	m38[5C6]
	JE000	CON_F6ST_S4MT_TH1_F-	L9140	IND_0402	m38[91A7]	PP653	PROBEPOINT_SM	m38[5C6]
	JE001	CON_F6ST_S4MT_TH1_F-	L9140	IND_0402	m38[91A7]	PP654	PROBEPOINT_SM	m38[5C6]
	JE310	CON_F45T_USB_S3MT_TH	L9300	IND_0402	m38[93C7]	PP655	PROBEPOINT_SM	m38[5C6]
	JE320	CON_F45T_USB_S3MT_TH	L9305	IND_0402	m38[93C7]	PP656	PROBEPOINT_SM	m38[5C6]
	JE330	CON_F45T_USB_S3MT_TH	L9310	IND_0402	m38[93C7]	PP657	PROBEPOINT_SM	m38[5C6]
	JE350	CON_M14RT_S2MT_SM_M-	L9315	IND_0402	m38[93C7]	PP658	PROBEPOINT_SM	m38[5C6]
	L1934	IND_0603	L9320	IND_0402	m38[93B7]	PP659	PROBEPOINT_SM	m38[5C6]
	L1936	IND_0603	L9325	IND_0402	m38[93B7]	PP660	PROBEPOINT_SM	m38[5C6]
	L1970	IND_1210	L9330	IND_0402	m38[93B7]	PP661	PROBEPOINT_SM	m38[5C6]
	L1975	IND_0805	L9345	IND_0402	m38[93B7]	PP662	PROBEPOINT_SM	m38[5C6]
	L2500	IND_SM-3	L9400	IND_SM	m38[94C6]	PP663	PROBEPOINT_SM	m38[5C6]
	L2507	IND_1206	L9700	FILTER_4P_2012H	m38[97D7]	PP664	PROBEPOINT_SM	m38[5C6]
	L3301	IND_0402	L9701	FILTER_4P_2012H	m38[97D7]	PP665	PROBEPOINT_SM	m38[5C6]
	L3302	IND_0402	L9702	FILTER_4P_2012H	m38[97C7]	PP666	PROBEPOINT_SM	m38[5C6]
	L4200	IND_SM	L9703	FILTER_4P_SM	m38[97C7]	PP667	PROBEPOINT_SM	m38[5C6]
	L4300	IND_SM	L9710	IND_SM-1	m38[97D5]	PP668	PROBEPOINT_SM	m38[5C6]
	L4409	IND_0402	LED600	LED_2_0X1.25MM-SM	m38[6A7]	PP669	PROBEPOINT_SM	m38[5C6]
	L4610	IND_1206-LF	LED601	LED_2_0X1.25MM-SM	m38[6A8]	PP670	PROBEPOINT_SM	m38[5C6]
	L4620	IND_1206-LF	LED602	LED_2_0X1.25MM-SM	m38[6A7]	PP671	PROBEPOINT_SM	m38[5B6]
	L4690	IND_SM-1	LED2901	LED_3X2MM-SM	m38[59D6]	PP672	PROBEPOINT_SM	m38[5B6]
	L4710	IND_SM	LED3800	LED_2_0X1.25MM-SM	m38[38B3]	PP673	PROBEPOINT_SM	m38[5B6]
	L4712	FILTER_4P_2012	LED4300	LED_2_0X1.25MM-SM	m38[43D2]	PP674	PROBEPOINT_SM	m38[5B6]
	L4720	IND_SM	LED4301	LED_2_0X1.25MM-SM	m38[43D2]	PP675	PROBEPOINT_SM	m38[5B6]
	L4722	FILTER_4P_2012	LED4302	LED_2_0X1.25MM-SM	m38[43D1]	PP676	PROBEPOINT_SM	m38[5B6]
	L4730	IND_SM	LED4303	LED_2_0X1.25MM-SM	m38[43D1]	PP677	PROBEPOINT_SM	m38[5B6]
	L4732	FILTER_4P_2012	LED7900	LED_2_0X1.25MM-SM	m38[79A4]	PP678	PROBEPOINT_SM	m38[5B6]
	L4740	IND_SM	LED8000	LED_2_0X1.25MM-SM	m38[80A4]	PP679	PROBEPOINT_SM	m38[5B6]
	L4742	FILTER_4P_2012	LED8100	LED_2_0X1.25MM-SM	m38[81A4]	PP680	PROBEPOINT_SM	m38[5B6]
	L4752	FILTER_4P_2012	PP5E1	PROBEPOINT_SM	m38[5B8]	PP681	PROBEPOINT_SM	m38[5B6]
	L5300	FILTER_4P_2012	PP5E2	PROBEPOINT_SM	m38[5B8]	PP682	PROBEPOINT_SM	m38[5B6]
	L6800	IND_0402	PP6A0	PROBEPOINT_SM	m38[5A6]	PP683	PROBEPOINT_SM	m38[5B6]
	L6801	IND_0402	PP6A1	PROBEPOINT_SM	m38[5A6]	PP684	PROBEPOINT_SM	m38[5B6]
	L7200	IND_SM-1	PP6A2	PROBEPOINT_SM	m38[5A6]	PP685	PROBEPOINT_SM	m38[5B6]
	L7201	IND_0603	PP6A3	PROBEPOINT_SM	m38[5A6]	PP686	PROBEPOINT_SM	m38[5B6]
	L7202	IND_0603	PP6A4	PROBEPOINT_SM	m38[5A6]	PP687	PROBEPOINT_SM	m38[5B6]
	L7203	IND_0603	PP6A5	PROBEPOINT_SM	m38[5A6]	PP688	PROBEPOINT_SM	m38[5B6]
	L7204	IND_0603	PP6A6	PROBEPOINT_SM	m38[5A6]	PP689	PROBEPOINT_SM	m38[5B6]
	L7205	IND_0603	PP6A7	PROBEPOINT_SM	m38[5A6]	PP690	PROBEPOINT_SM	m38[5B6]
	L7206	IND_0603	PP6A8	PROBEPOINT_SM	m38[5A6]	PP691	PROBEPOINT_SM	m38[5B6]
	L7207	IND_0603	PP6A9	PROBEPOINT_SM	m38[5A6]	PP692	PROBEPOINT_SM	m38[5B6]
	L7208	IND_0603	PP6B0	PROBEPOINT_SM	m38[5A6]	PP693	PROBEPOINT_SM	m38[5B6]
	L7300	IND_SM	PP6B1	PROBEPOINT_SM	m38[5A6]	PP694	PROBEPOINT_SM	m38[5B6]
	L7301	IND_SM	PP6B2	PROBEPOINT_SM	m38[5A6]	PP695	PROBEPOINT_SM	m38[5B6]
	L7302	IND_SM	PP6B3	PROBEPOINT_SM	m38[5A6]	PP696	PROBEPOINT_SM	m38[5B6]
	L7303	IND_SM	PP6B4	PROBEPOINT_SM	m38[5A6]	PP697	PROBEPOINT_SM	m38[5B6]
	L7304	IND_SM	PP6B5	PROBEPOINT_SM	m38[5A6]	PP698	PROBEPOINT_SM	m38[5B6]
	L7305	IND_SM	PP6B6	PROBEPOINT_SM	m38[5A6]	PP699	PROBEPOINT_SM	m38[5B6]
	L7306	IND_SM	PP6B7	PROBEPOINT_SM	m38[5A6]	PP700	TP_SM-TP50-TOP	m38[5D3]
	L7307	IND_SM	PP6B8	PROBEPOINT_SM	m38[5A6]	PP701	TP_SM-TP50-TOP	m38[5D3]
	L7309	IND_SM	PP6B9	PROBEPOINT_SM	m38[5A6]	PP702	TP_SM-TP50-TOP	m38[5D3]
	L7310	IND_SM	PP6C0	PROBEPOINT_SM	m38[5A6]	PP1200	TP_SM-TP50-TOP	m38[5D3]
	L7312	IND_SM	PP6C1	PROBEPOINT_SM	m38[5A6]	PP1201	TP_SM-TP50-TOP	m38[5D3]
	L7322	IND_SM	PP6C2	PROBEPOINT_SM	m38[5A6]	PP1202	TP_SM-TP50-TOP	m38[5D3]
	L7323	IND_SM	PP6C3	PROBEPOINT_SM	m38[5A6]	PP2800	TP_SM-TP50-TOP	m38[5C3]
	L7324	IND_SM	PP6C4	PROBEPOINT_SM	m38[5A6]	PP2801	TP_SM-TP50-TOP	m38[5C3]
	L7325	IND_SM	PP6C5	PROBEPOINT_SM	m38[5A6]	PP2802	TP_SM-TP50-TOP	m38[5C3]
	L7326	IND_SM	PP6C6	PROBEPOINT_SM	m38[5A6]	PP4100	PROBEPOINT_SM	m38[5D4]
	L7327	IND_SM	PP6C7	PROBEPOINT_SM	m38[5A6]	PP4101	PROBEPOINT_SM	m38[5D4]
	L7328	IND_SM	PP6C8	PROBEPOINT_SM	m38[5A6]	PP8400	PROBEPOINT_SM	m38[5C5]
	L7329	IND_SM	PP6D0	PROBEPOINT_SM	m38[5C8]	PP8401	PROBEPOINT_SM	m38[5C5]
	L7330	IND_SM	PP6D1	PROBEPOINT_SM	m38[5C8]	PP8700	PROBEPOINT_SM	m38[5D5]
	L7331	IND_SM	PP6D2	PROBEPOINT_SM	m38[5C8]	PP8701	PROBEPOINT_SM	m38[5D5]
	L7332	IND_SM	PP6D3	PROBEPOINT_SM	m38[5C8]	PP8702	PROBEPOINT_SM	m38[5D5]
	L7333	IND_SM	PP6D4	PROBEPOINT_SM	m38[5C8]	PP8703	PROBEPOINT_SM	m38[5D5]
	L7334	IND_SM	PP6D5	PROBEPOINT_SM	m38[5C8]	PP8704	PROBEPOINT_SM	m38[5D5]
	L7335	IND_SM	PP6D6	PROBEPOINT_SM	m38[5C8]	PP8705	PROBEPOINT_SM	m38[5D5]
	L7336	IND_SM	PP6D7	PROBEPOINT_SM	m38[5C8]	PP8706	PROBEPOINT_SM	m38[5D5]
	L7337	IND_SM	PP6D8	PROBEPOINT_SM	m38[5C8]	PP8707	PROBEPOINT_SM	m38[5D5]
	L7338	IND_SM	PP6D9	PROBEPOINT_SM	m38[5C8]	PP8708	PROBEPOINT_SM	m38[5D5]
	L7339	IND_SM	PP6E0	PROBEPOINT_SM	m38[5B8]	PP8709	PROBEPOINT_SM	m38[5D5]
	L7340	IND_SM	PP6E1	PROBEPOINT_SM	m38[5B8]	PP8710	PROBEPOINT_SM	m38[5D5]
	L7341	IND_SM	PP6E2	PROBEPOINT_SM	m38[5B8]	PP8711	PROBEPOINT_SM	m38[5D5]
	L7342	IND_SM	PP6E3	PROBEPOINT_SM	m38[5B8]	PP8712	PROBEPOINT_SM	m38[5D5]
	L7343	IND_SM	PP6E4	PROBEPOINT_SM	m38[5B8]	PP8713	PROBEPOINT_SM	m38[5D5]
	L7344	IND_SM	PP6E5	PROBEPOINT_SM	m38[5B8]	PP8714	PROBEPOINT_SM	m38[5D5]
	L7345	IND_SM	PP6E6	PROBEPOINT_SM	m38[5B8]	PP8715	PROBEPOINT_SM	m38[5D5]
	L7346	IND_SM	PP6E7	PROBEPOINT_SM	m38[5B8]	PP8716	PROBEPOINT_SM	m38[5D5]
	L7347	IND_SM	PP6E8	PROBEPOINT_SM	m38[5B8]	PP8717	PROBEPOINT_SM	m38[5D5]
	L7348	IND_SM	PP6E9	PROBEPOINT_SM	m38[5B8]	PP8718	PROBEPOINT_SM	m38[5D5]
	L7349	IND_SM	PP6F0	PROBEPOINT_SM	m38[5D8]	PP8719	PROBEPOINT_SM	m38[5D5]
	L7350	IND_SM	PP6F1	PROBEPOINT_SM	m38[5D8]	PP8720	PROBEPOINT_SM	m38[5D5]
	L7351	IND_SM	PP6F2	PROBEPOINT_SM	m38[5D8]	PP8721	PROBEPOINT_SM	m38[5D5]
	L7352	IND_SM	PP6F3	PROBEPOINT_SM	m38[5D8]	PP8722	PROBEPOINT_SM	m38[5D5]
	L7353	IND_SM	PP6F4	PROBEPOINT_SM	m38[5D8]	PP8723	PROBEPOINT_SM	m38[5D5]
	L7354	IND_SM	PP6F5	PROBEPOINT_SM	m38[5D8]	PP8724	PROBEPOINT_SM	m38[5D5]
	L7355	IND_SM	PP6F6	PROBEPOINT_SM	m38[5D8]	PP8725	PROBEPOINT_SM	m38[5D5]
	L7356	IND_SM	PP6F7	PROBEPOINT_SM	m38[5D8]	PP8726	PROBEPOINT_SM	m38[5D5]
	L7357	IND_SM	PP6F8	PROBEPOINT_SM	m38[5D8]	PP8727	PROBEPOINT_SM	m38[5D5]
	L7358	IND_SM	PP6F9	PROBEPOINT_SM	m38[5D8]	PP8728	PROBEPOINT_SM	m38[5D5]
	L7359	IND_SM	PP6G0	PROBEPOINT_SM	m38[5D8]	PP8729	PROBEPOINT_SM	m38[5D5]
	L7360	IND_SM	PP6G1	PROBEPOINT_SM	m38[5D8]	PP8730	PROBEPOINT_SM	m38[5D5]
	L7361	IND_SM	PP6G2	PROBEPOINT_SM	m38[5D8]	PP8731	PROBEPOINT_SM	m38[5D5]
	L7362	IND_SM	PP6G3	PROBEPOINT_SM	m38[5D8]	PP		

	8	7	6	5	4	3	2	1
	Q8103 TRA_NTD60N02R_CASE36 m38[81C4] 9-LF		R2299 RES_402 m38[22B5]	R3446 RES_402 m38[34B1]	R5808 RES_402 m38[59C3]			
	Q8104 TRA_2N7002_SOT23-LF m38[81C7]		R2302 RES_402 m38[23D3]	R3451 RES_402 m38[34C4]	R5809 RES_402 m38[58C2]			
	Q8300 TRA_IRF7413_SO-8 m38[83C4]		R2303 RES_402 m38[23D3]	R3452 RES_402 m38[34B7]	R5815 RES_402 m38[59B3]			
	Q8301 TRA_IRF7413_SO-8 m38[83B4]		R2305 RES_402 m38[23D3]	R3453 RES_402 m38[34B8]	R5817 RES_402 m38[59B3]			
	Q8302 TRA_2N7002DW_SOT-363 m38[83B5]		R2306 RES_402 m38[23B7]	R3454 RES_402 m38[34B7]	R5818 RES_402 m38[59B3]			
	Q8303 TRA_2N7002DW_SOT-363 m38[83C5]		R2307 RES_402 m38[23A7]	R3455 RES_402 m38[34B8]	R5819 RES_402 m38[59B3]			
	Q8520 TRA_HAT2165H_LFPAK m38[85D4]		R2308 RES_402 m38[23B7]	R3456 RES_402 m38[34B7]	R5821 RES_402 m38[59B3]			
	Q8521 TRA_HAT2165H_LFPAK m38[85C4]		R2309 RES_402 m38[23A7]	R3457 RES_402 m38[34B7]	R5822 RES_402 m38[59B3]			
	Q8522 TRA_HAT2165H_LFPAK m38[85C5]		R2310 RES_402 m38[23A7]	R3458 RES_402 m38[34B8]	R5823 RES_402 m38[59B3]			
	Q8523 TRA_2N7002DW_SOT-363 m38[85B3 85B2]		R2311 RES_402 m38[23A7]	R3459 RES_402 m38[34A7]	R5824 RES_402 m38[59B3]			
	Q8554 TRA_2N7002_SOT23-LF m38[85A8]		R2313 RES_402 m38[23A7]	R3460 RES_402 m38[34A7]	R5825 RES_402 m38[59B3]			
	Q8570 TRA_2N7002_SOT23-LF m38[85A5]		R2314 RES_402 m38[23A7]	R3461 RES_402 m38[34A7]	R5826 RES_402 m38[59B3]			
	Q8575 TRA_FDC796N_SUPERSOT m38[85B6]		R2316 RES_402 m38[23D7]	R3462 RES_402 m38[34A8]	R5827 RES_402 m38[59C5]			
	Q8576 TRA_FDC796N_SUPERSOT m38[85A2]		R2317 RES_402 m38[23D7]	R3463 RES_402 m38[34A7]	R5828 RES_402 m38[59B3]			
	Q9400 TRA_ST13443DV_TSOB-LF m38[94C7]		R2318 RES_402 m38[23D7]	R3470 RES_402 m38[34A5]	R5829 RES_402 m38[59C3]			
	Q9401 TRA_2N7002_SOT23-LF m38[94C7]		R2319 RES_402 m38[23D2]	R3471 RES_402 m38[34A5]	R5830 RES_402 m38[59C3]			
	Q9711 TRA_2N7002DW_SOT-363 m38[97D2 97C2]		R2320 RES_402 m38[23D7]	R3485 RES_402 m38[34D1]	R5831 RES_402 m38[59C3]			
	R75A0 RES_402 m38[75C7]		R2323 RES_402 m38[23D5]	R3486 RES_402 m38[34D1]	R5832 RES_402 m38[59C3]			
	R85A0 RES_402 m38[85D1]		R2326 RES_402 m38[23D6]	R3487 RES_402 m38[34D1]	R5833 RES_402 m38[59B3]			
	R600 RES_603 m38[6A7]		R2327 RES_402 m38[23D6]	R3488 RES_402 m38[34D1]	R5898 RES_402 m38[58C2]			
	R601 RES_402 m38[6D8]		R2343 RES_402 m38[23D1]	R3489 RES_402 m38[34D2]	R5899 RES_402 m38[58D3]			
	R602 RES_603 m38[6A8]		R2388 RES_402 m38[23A3]	R3490 RES_402 m38[34D2]	R5900 RES_402 m38[59D7]			
	R603 RES_402 m38[6B1]		R2389 RES_402 m38[38D5]	R3491 RES_402 m38[34D2]	R5901 RES_402 m38[59D6]			
	R605 RES_603 m38[6A7]		R2390 RES_402 m38[23B3]	R3492 RES_402 m38[34D2]	R5902 RES_402 m38[59D7]			
	R611 RES_402 m38[6B7]		R2395 RES_402 m38[23D7]	R3493 RES_402 m38[34D7]	R5903 RES_402 m38[59D2]			
	R612 RES_402 m38[6B7]		R2396 RES_402 m38[23D6]	R3494 RES_402 m38[34D7]	R5904 RES_402 m38[59D2]			
	R614 RES_402 m38[6B7]		R2397 RES_402 m38[23D6]	R3495 RES_402 m38[34D7]	R5905 RES_402 m38[59D2]			
	R615 RES_402 m38[6B7]		R2398 RES_402 m38[23D8]	R3496 RES_402 m38[34C5]	R5906 RES_402 m38[59D7]			
	R616 RES_402 m38[6A7]		R2399 RES_402 m38[23C1]	R3497 RES_402 m38[34D4]	R5907 RES_402 m38[59D7]			
	R617 RES_402 m38[6A7]		R2500 RES_603 m38[25A8]	R3498 RES_402 m38[34D5]	R5910 RES_402 m38[59D2]			
	R618 RES_402 m38[6C7]		R2501 RES_402 m38[25C8]	R3499 RES_402 m38[34D5]	R5911 RES_402 m38[59D2]			
	R619 RES_402 m38[6B7]		R2502 RES_402 m38[25D8]	R3824 RES_402 m38[38D2]	R5912 RES_402 m38[59D2]			
	R7072 RES_402 m38[7D6]		R2503 RES_402 m38[26C7]	R3851 RES_402 m38[38D3]	R5913 RES_402 m38[59D2]			
	R7073 RES_402 m38[7C5]		R2504 RES_402 m38[26C7]	R3852 RES_402 m38[38D2]	R5914 RES_402 m38[59C2]			
	R7074 RES_402 m38[7C5]		R2609 RES_402 m38[26D7]	R3853 RES_402 m38[38D2]	R5915 RES_402 m38[59C2]			
	R7075 RES_402 m38[7B4]		R2611 RES_402 m38[26D5]	R3857 RES_402 m38[38B3]	R5916 RES_402 m38[59C2]			
	R7076 RES_402 m38[7B5]		R2612 RES_402 m38[26D5]	R3858 RES_402 m38[38B2]	R5917 RES_402 m38[59C2]			
	R7077 RES_402 m38[7A4]		R2622 RES_402 m38[26D4]	R3859 RES_402 m38[38B2]	R5919 RES_402 m38[59B4]			
	R7078 RES_402 m38[7A3]		R2623 RES_402 m38[26D2]	R3897 RES_402 m38[38B7]	R5920 RES_402 m38[59B5]			
	R7079 RES_402 m38[7B1]		R2624 RES_402 m38[26D2]	R3899 RES_402 m38[38B5]	R5921 RES_402 m38[59B5]			
	R7080 RES_402 m38[7B1]		R2625 RES_402 m38[26D2]	R4101 RES_402 m38[41D7]	R5922 RES_402 m38[59B5]			
	R7081 RES_402 m38[7B1]		R2626 RES_402 m38[26D2]	R4102 RES_402 m38[41C7]	R5923 RES_402 m38[59B5]			
	R7082 RES_402 m38[7B1]		R2627 RES_402 m38[26D2]	R4103 RES_402 m38[41C2]	R5924 RES_402 m38[59B5]			
	R7083 RES_402 m38[7B1]		R2628 RES_402 m38[26D2]	R4104 RES_402 m38[41C2]	R5925 RES_402 m38[59A1]			
	R7084 RES_402 m38[7B1]		R2629 RES_402 m38[26D2]	R4105 RES_402 m38[41C2]	R5927 RES_402 m38[59A3]			
	R7085 RES_402 m38[7B1]		R2630 RES_402 m38[26D2]	R4106 RES_402 m38[41C2]	R5930 RES_402 m38[59B6]			
	R7086 RES_402 m38[7B7]		R2631 RES_402 m38[26D2]	R4117 RES_402 m38[41B2]	R5931 RES_402 m38[59B6]			
	R7087 RES_402 m38[7A7]		R2632 RES_402 m38[26D2]	R4118 RES_402 m38[41B2]	R5932 RES_402 m38[59A7]			
	R7088 RES_402 m38[7A4]		R2633 RES_402 m38[26D2]	R4119 RES_402 m38[41B2]	R5933 RES_402 m38[59A7]			
	R7089 RES_402 m38[7A4]		R2634 RES_402 m38[26D2]	R4120 RES_402 m38[41B2]	R5934 RES_402 m38[59A6]			
	R7090 RES_402 m38[8B7]		R2635 RES_402 m38[26D2]	R4122 RES_402 m38[41A3]	R5935 RES_402 m38[59A6]			
	R7091 RES_402 m38[8A7]		R2636 RES_402 m38[26D2]	R4123 RES_402 m38[41A2]	R5940 RES_402 m38[59A3]			
	R7092 RES_402 m38[10D3]		R2637 RES_402 m38[26D2]	R4130 RES_402 m38[41C4]	R5941 RES_402 m38[59A5]			
	R7093 RES_402 m38[10D3]		R2638 RES_402 m38[26D2]	R4131 RES_402 m38[41C4]	R5942 RES_402 m38[59A4]			
	R7094 RES_402 m38[10C6]		R2639 RES_402 m38[26D2]	R4150 RES_402 m38[41C8]	R5995 RES_402 m38[59A5]			
	R7095 RES_402 m38[10D3]		R2640 RES_402 m38[26C2]	R4151 RES_402 m38[41D7]	R6100 RES_402 m38[61C4]			
	R7096 RES_402 m38[10C6]		R2641 RES_402 m38[26C2]	R4200 RES_402 m38[42D2]	R6101 RES_402 m38[61C5]			
	R7097 RES_402 m38[10B6]		R2642 RES_402 m38[26C2]	R4201 RES_402 m38[42C2]	R6102 RES_402 m38[61C5]			
	R7098 RES_402 m38[10B6]		R2643 RES_402 m38[26C2]	R4202 RES_402 m38[42D6]	R6103 RES_402 m38[61C6]			
	R7099 RES_402 m38[11B5]		R2644 RES_402 m38[26C4]	R4300 RES_402 m38[43D7]	R6104 RES_402 m38[61B6]			
	R7100 RES_402 m38[11C5]		R2651 RES_402 m38[26C1]	R4301 RES_603 m38[43D2]	R6301 RES_402 m38[63D4]			
	R7101 RES_402 m38[11C5]		R2652 RES_402 m38[26A4]	R4302 RES_603 m38[43D2]	R6302 RES_402 m38[63D4]			
	R7102 RES_402 m38[11B4]		R2653 RES_402 m38[26C3]	R4303 RES_603 m38[43D1]	R6303 RES_402 m38[63C2]			
	R7103 RES_402 m38[11C5]		R2654 RES_402 m38[26C5]	R4304 RES_603 m38[43D1]	R6306 RES_402 m38[63C2]			
	R7104 RES_402 m38[11A3]		R2655 RES_402 m38[26C5]	R4350 RES_402 m38[43C7]	R6307 RES_402 m38[63C5]			
	R7105 RES_402 m38[12C3]		R2718 RES_402 m38[27B7]	R4351 RES_402 m38[43C7]	R6308 RES_402 m38[63C5]			
	R7106 RES_402 m38[12C3]		R2719 RES_402 m38[27B7]	R4352 RES_402 m38[43C7]	R6309 RES_402 m38[63D3]			
	R7107 RES_402 m38[12B7]		R2800 RES_402 m38[28C7]	R4353 RES_402 m38[43C7]	R6500 RES_402 m38[65C7]			
	R7108 RES_402 m38[12B7]		R2801 RES_402 m38[28C7]	R4354 RES_402 m38[43C7]	R6501 RES_402 m38[65A7]			
	R7109 RES_402 m38[12B7]		R2900 RES_402 m38[29A3]	R4355 RES_402 m38[43C7]	R6502 RES_1206 m38[65D6]			
	R7126 RES_402 m38[12B7]		R3001 RES_402 m38[30D4]	R4356 RES_402 m38[43C7]	R6503 RES_805 m38[65D5]			
	R7127 RES_402 m38[12A7]		R3009 RES_402 m38[30D4]	R4357 RES_402 m38[43B7]	R6504 RES_805 m38[65C5]			
	R7128 RES_402 m38[12A7]		R3011 RES_402 m38[30C4]	R4402 RES_402 m38[44B3]	R6505 RES_805 m38[65D5]			
	R7129 RES_402 m38[12A7]		R3025 RES_402 m38[30C4]	R4403 RES_402 m38[44B5]	R6506 RES_402 m38[65D6]			
	R7130 RES_402 m38[12A7]		R3035 RES_402 m38[30B4]	R4404 RES_402 m38[44A7]	R6507 RES_805 m38[65B5]			
	R7131 RES_402 m38[13C3]		R3100 RES_402 m38[31C5]	R4407 RES_402 m38[44A7]	R6508 RES_805 m38[65C3]			
	R7132 RES_402 m38[14C3]		R3101 RES_402 m38[31C5]	R4409 RES_402 m38[44B3]	R6509 RES_805 m38[65B5]			
	R7133 RES_402 m38[14C3]		R3300 RES_402 m38[33B6]	R4410 RES_402 m38[44D2]	R6510 RES_1206 m38[65B6]			
	R7134 RES_402 m38[14B6]		R3301 RES_402 m38[33B7]	R4411 RES_402 m38[44D6]	R6511 RES_402 m38[65B6]			
	R7135 RES_402 m38[14B6]		R3302 RES_402 m38[33D4]	R4412 RES_402 m38[44C1]	R6512 RES_805 m38[65C5]			
	R7136 RES_402 m38[14D6]		R3303 RES_402 m38[33C4]	R4413 RES_402 m38[44C3]	R6513 RES_805 m38[65B5]			
	R7137 RES_402 m38[14D6]		R3304 RES_402 m38[33C7]	R4414 RES_402 m38[44C3]	R6514 RES_805 m38[65B4]			
	R7138 RES_402 m38[19A4]		R3400 RES_402 m38[34C5]	R4416 RES_402 m38[44A5]	R6515 RES_805 m38[65C4]			
	R7139 RES_402 m38[19B7]		R3401 RES_402 m38[34B5]	R4450 RES_402 m38[44B3]	R6580 RES_402 m38[65B8]			
	R7140 RES_402 m38[19B7]		R3402 RES_402 m38[34B5]	R4451 RES_402 m38[44B3]	R6581 RES_402 m38[65B7]			
	R7141 RES_402 m38[19B8]		R3403 RES_402 m38[34C5]	R4452 RES_402 m38[44B3]	R6582 RES_402 m38[65A7]			
	R7142 RES_402 m38[20B4]		R3404 RES_402 m38[34C5]	R4453 RES_402 m38[44B3]	R6598 RES_402 m38[65A7]			
	R7143 RES_402 m38[20B4]		R3405 RES_402 m38[34C5]	R4454 RES_402 m38[44B3]	R6599 RES_402 m38[65C7]			
	R7144 RES_402 m38[20A4]		R3406 RES_402 m38[34C5]	R4455 RES_402 m38[44B3]	R6600 RES_402 m38[66C7]			
	R7145 RES_402 m38[20A4]		R3407 RES_402 m38[34B5]	R4602 RES_2512-1 m38[46D5]	R6601 RES_805 m38[66D5]			
	R7146 RES_402 m38[20C7]		R3408 RES_402 m38[34B5]	R4650 RES_402 m38[46C8]	R6602 RES_805 m38[66C4]			
	R7147 RES_402 m38[20B7]		R3409 RES_402 m38[34B5]	R4651 RES_402 m38[46C7]	R6603 RES_805 m38[66D5]			
	R7148 RES_402 m38[20B7]		R3410 RES_402 m38[34B5]	R4652 RES_402 m38[46B8]	R6604 RES_1206 m38[66D5]			
	R7149 RES_402 m38[20C4]		R3411 RES_402 m38[34B5]	R4653 RES_402 m38[46B7]	R6605 RES_402 m38[66D6]			
	R7150 RES_402 m38[21C3]		R3412 RES_402 m38[34B5]	R4654 RES_402 m38[46B7]	R6606 RES_805 m38[66C5]			
	R7151 RES_402 m38[21C4]		R3413 RES_402 m38[34B5]	R4656 RES_2512-1 m38[46D6]	R6607 RES_805 m38[66C3]			
	R7152 RES_402 m38[21D6]		R3414 RES_402 m38[34B5]	R4660 RES_402 m38[46C7]	R6680 RES_402 m38[66D8]			
	R7153 RES_402 m38[21C2]		R3415 RES_402 m38[34B5]	R4661 RES_402 m38[46C7]	R6681 RES_402 m38[66D7]			



XW9314	SHORT_SM	m38[93C7]
XW9320	SHORT_SM	m38[93B7]
XW9324	SHORT_SM	m38[93B7]
XW9330	SHORT_SM	m38[93B7]
XW9345	SHORT_SM	m38[93A7]
Y2600	CRYSTAL_4PIN_SM-LF	m38[26D8]
Y3301	CRYSTAL_5X3_2-SM	m38[33C7]
Y4101	CRYSTAL_4PIN_SM-3	m38[41B5]
Y4400	CRYSTAL_HC49-USMD	m38[44D2]
Y5800	CRYSTAL_SM-3	m38[59B8]
Y6700	CRYSTAL_4PIN_SM-LF	m38[59B7]
ZH500	HOLE_VIA	m38[5C1]
ZH501	HOLE_VIA	m38[5C1]
ZH502	HOLE_VIA	m38[5C1]
ZH503	HOLE_VIA	m38[5C1]
ZH504	HOLE_VIA	m38[5B1]
ZH505	HOLE_VIA	m38[5B1]
ZH506	HOLE_VIA	m38[5B1]
ZH507	HOLE_VIA	m38[5B1]
ZH508	HOLE_VIA	m38[5B1]
ZH509	HOLE_VIA	m38[5B1]
ZH510	HOLE_VIA	m38[5C1]
ZH511	HOLE_VIA	m38[5C1]
ZH512	HOLE_VIA	m38[5C1]
ZH513	HOLE_VIA	m38[5C1]
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ZH516	HOLE_VIA	m38[5B1]
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ZH524	HOLE_VIA	m38[5B1]
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ZH526	HOLE_VIA	m38[5B1]
ZH527	HOLE_VIA	m38[5B1]
ZH528	HOLE_VIA	m38[5B1]
ZH529	HOLE_VIA	m38[5B1]
ZH601	MTGHOLE	m38[6A3]
ZH602	MTGHOLE	m38[6A3]
ZH603	MTGHOLE	m38[6A3]
ZH604	MTGHOLE	m38[6B3]
ZH605	MTGHOLE	m38[6A1]
ZH607	MTGHOLE	m38[9D4]
ZH608	MTGHOLE	m38[9D3]
ZH609	MTGHOLE	m38[9D2]
ZH610	MTGHOLE	m38[9D2]