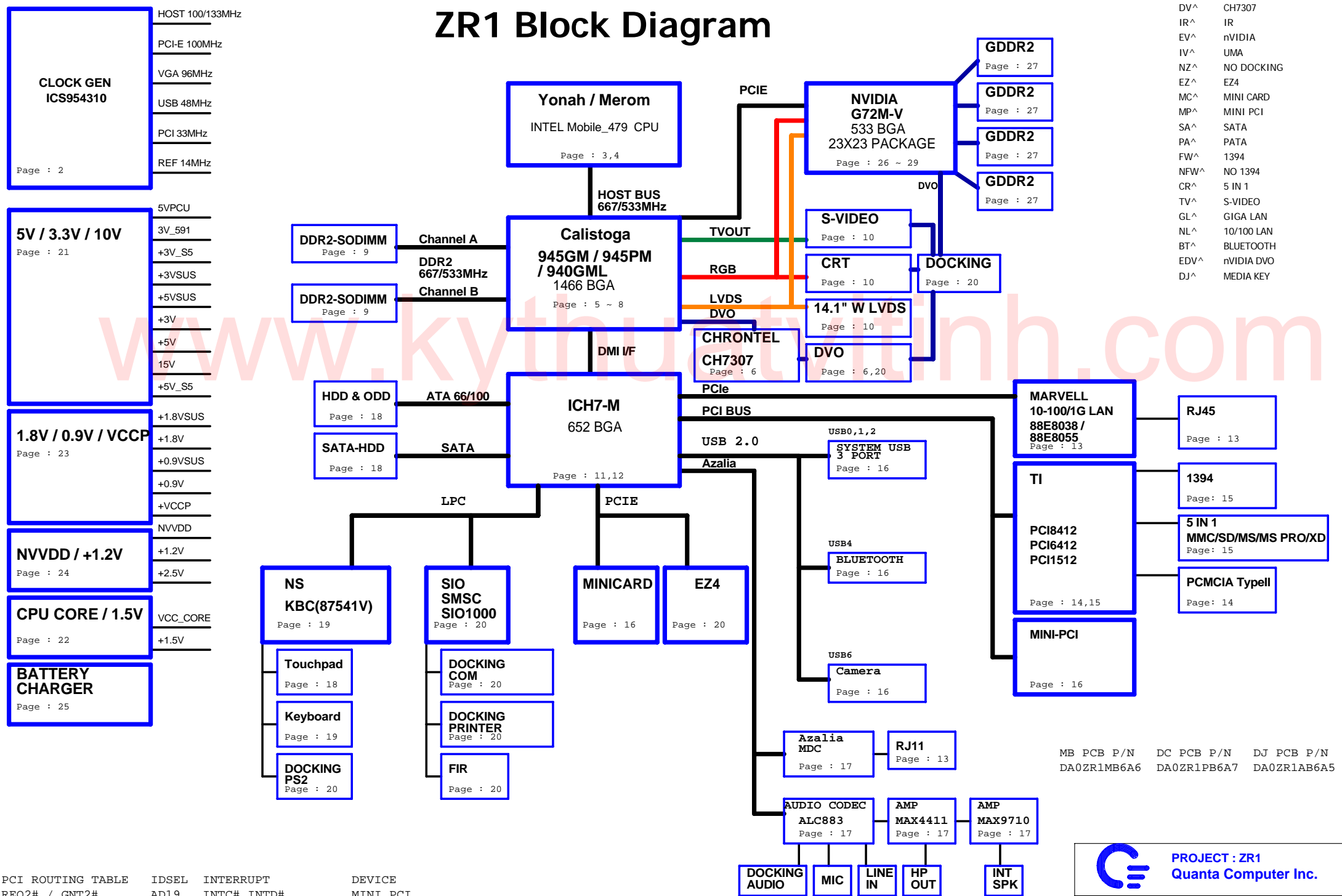



ZR1 Block Diagram



- DV^ CH7307
- IR^ IR
- EV^ nVIDIA
- IV^ UMA
- NZ^ NO DOCKING
- EZ^ EZ4
- MC^ MINI CARD
- MP^ MINI PCI
- SA^ SATA
- PA^ PATA
- FW^ 1394
- NFW^ NO 1394
- CR^ 5 IN 1
- TV^ S-VIDEO
- GL^ GIGA LAN
- NL^ 10/100 LAN
- BT^ BLUETOOTH
- EDV^ nVIDIA DVO
- DJ^ MEDIA KEY

PCI ROUTING TABLE	IDSEL	INTERRUPT	DEVICE
REQ2# / GNT2#	AD19	INTC#, INTD#	MINI PCI
REQ0# / GNT0#	AD25	INTE#, INTF#, INTG#	TI XX12

MB PCB P/N DC PCB P/N DJ PCB P/N
DA0ZR1MB6A6 DA0ZR1PB6A7 DA0ZR1AB6A5

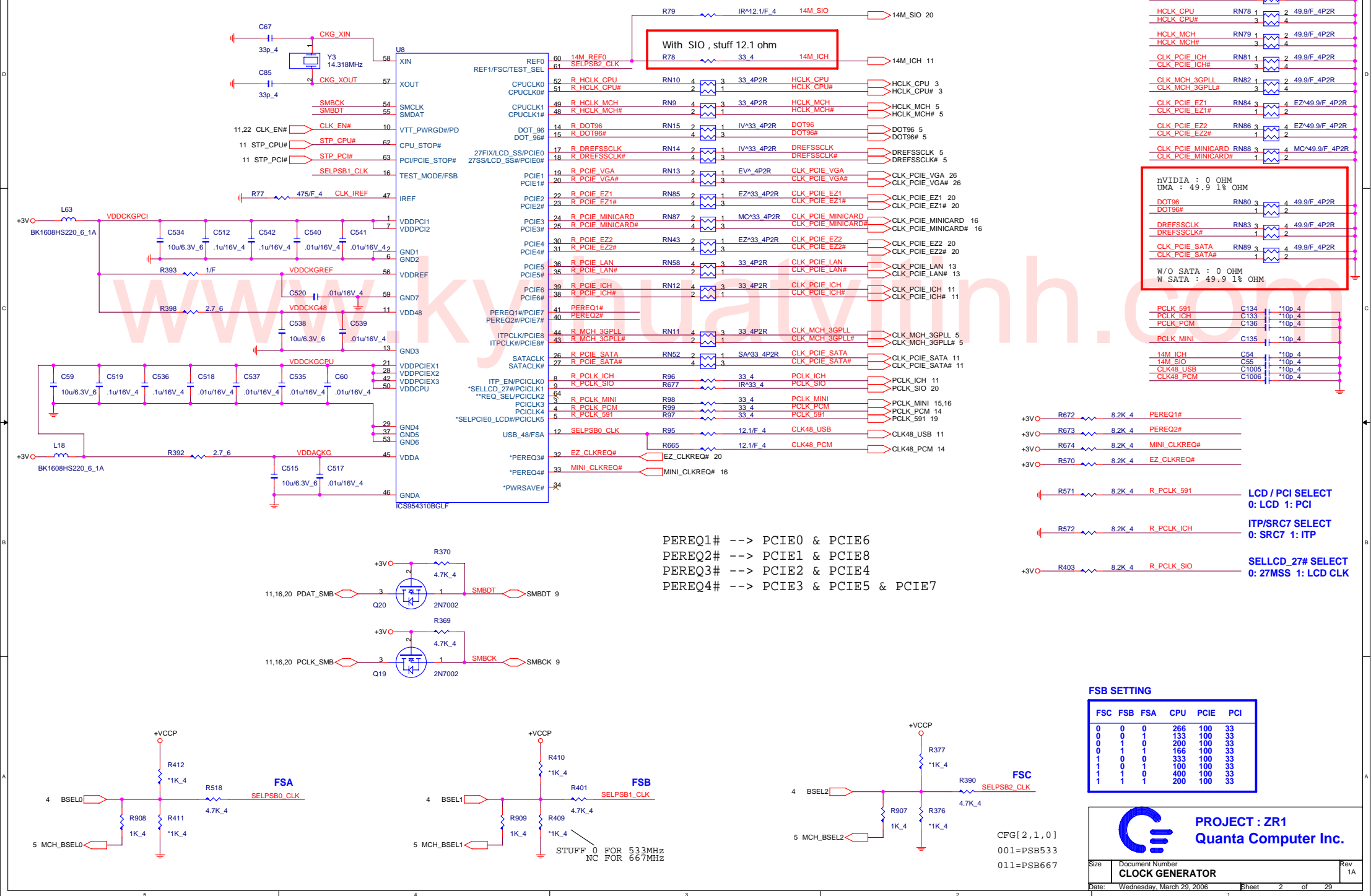


PROJECT : ZR1

Quanta Computer Inc.

Size	Document Number	Rev
	BLOCK DIAGRAM	1A
Date: Wednesday, March 29, 2006	Sheet	1 of 29

CLOCK GENERATOR



FSB SETTING

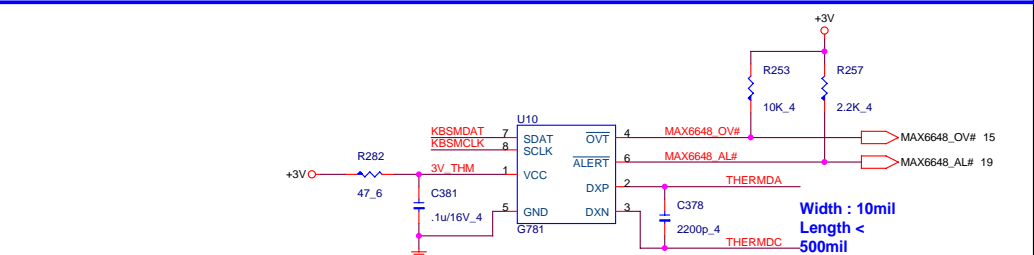
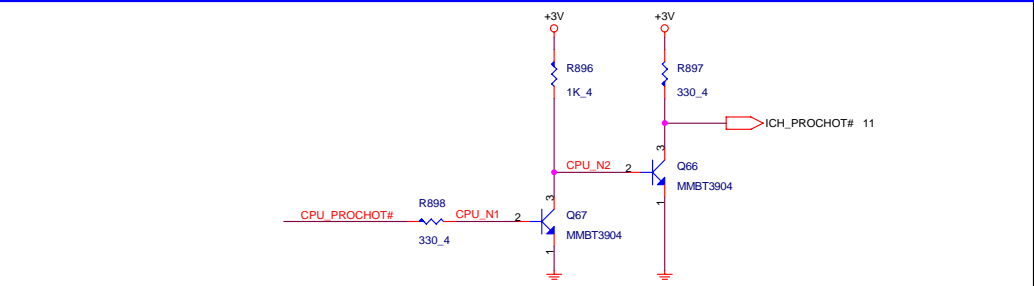
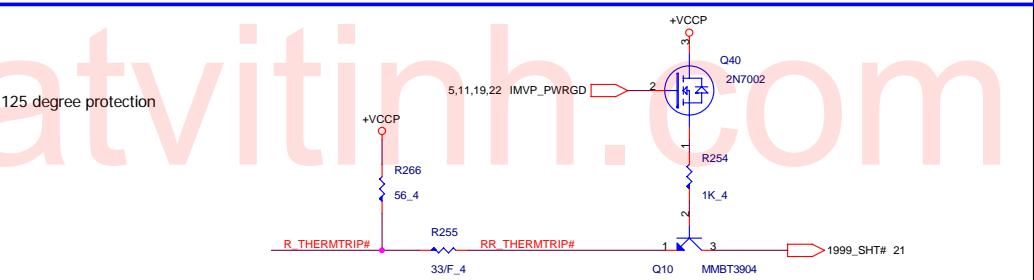
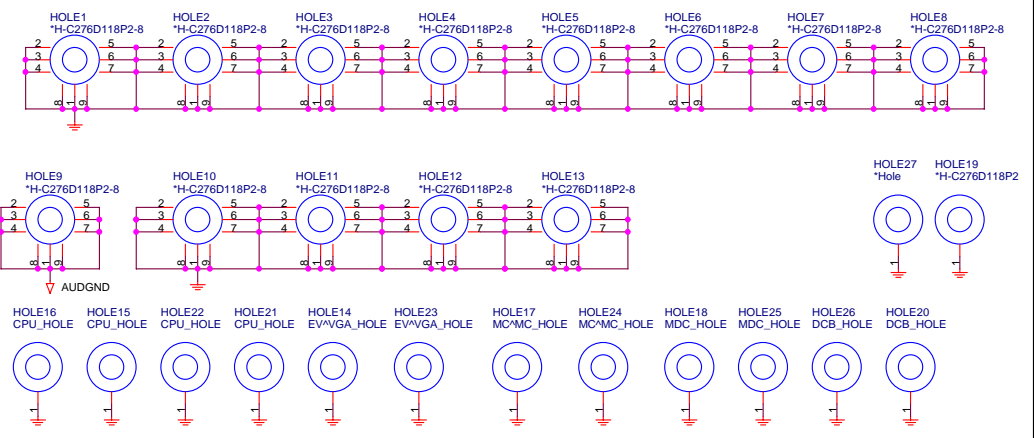
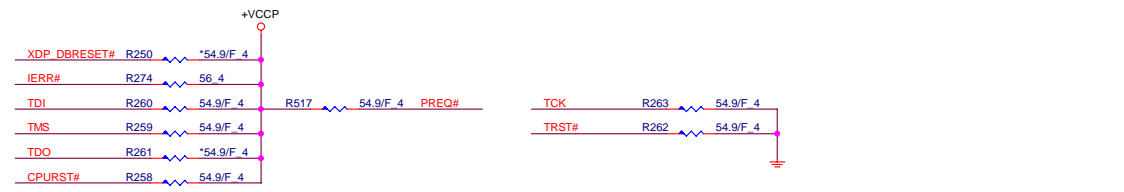
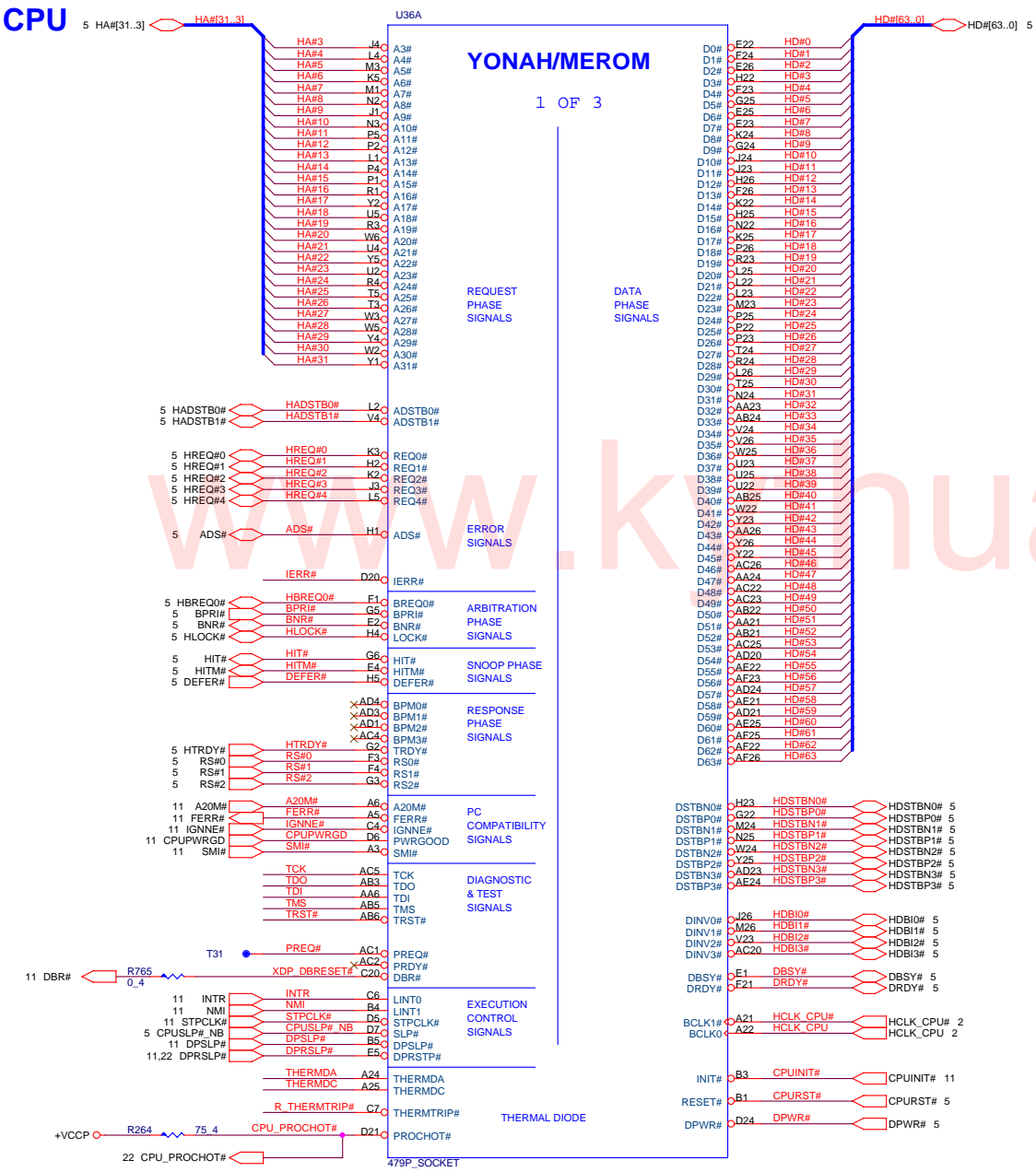
	FSC	FSB	FSA	CPU	PCIE	PCI
0	0	0		266	100	33
0	0	1		133	100	33
0	1	0		200	100	33
0	1	1		166	100	33
1	0	0		333	100	33
1	0	1		100	100	33
1	1	0		400	100	33
1	1	1		200	100	33




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Quanta Computer Inc.

Size	Document Number CLOCK GENERATOR	Rev 1A
Date:	Wednesday, March 29, 2006	Sheet 2 of 29

CPU



MAXIM 6657 : AL006657020
GMT G781 : AL000781101

 PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number CPU (HOST)	Rev 1A
Date:	Wednesday, March 29, 2006	Sheet 3 of 29

CPU

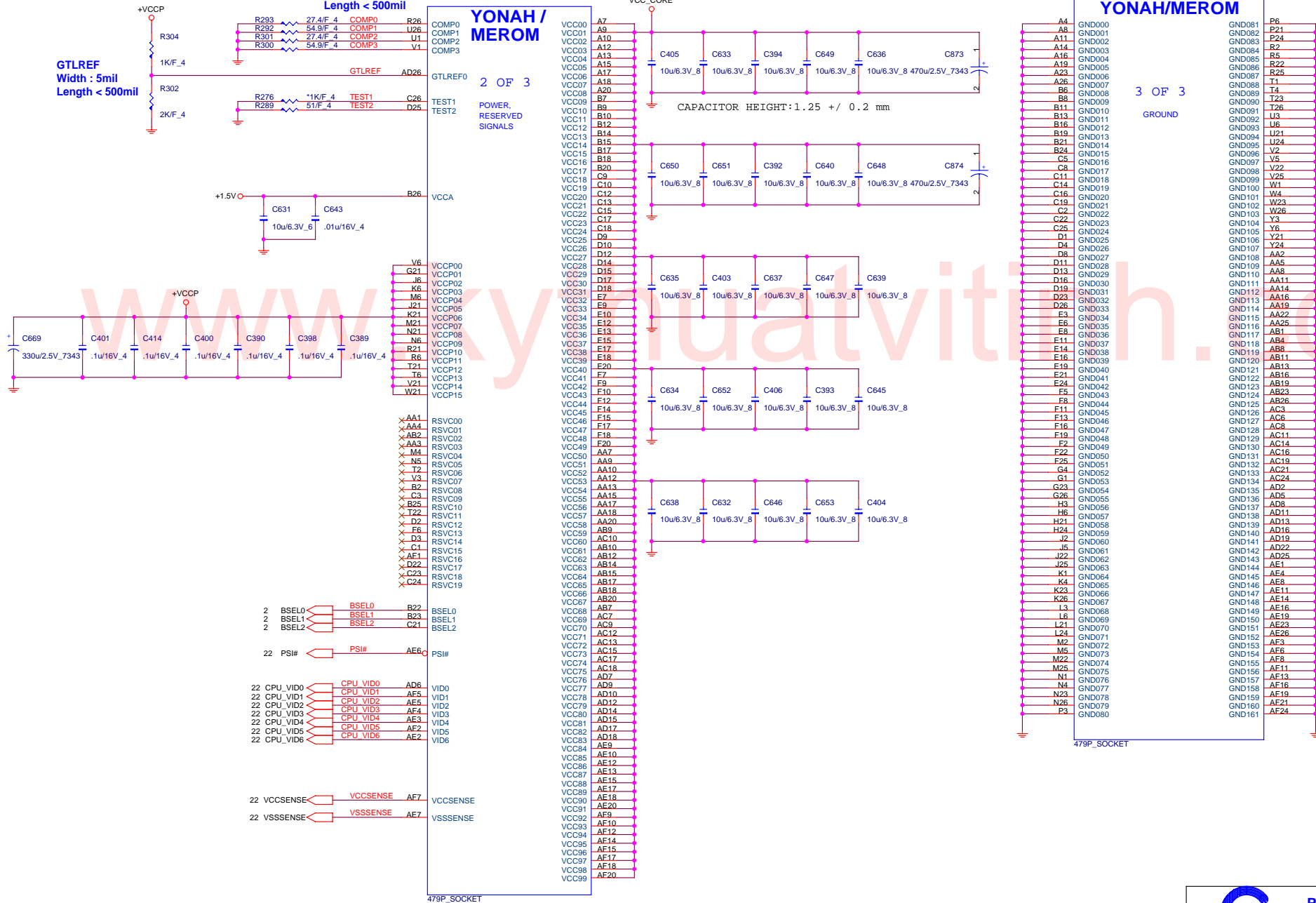
COMP0 - COMP3
Width : 20mil
Length < 500mil

YONAH /
MEROM

2 OF 3
POWER,
RESERVED
SIGNALS

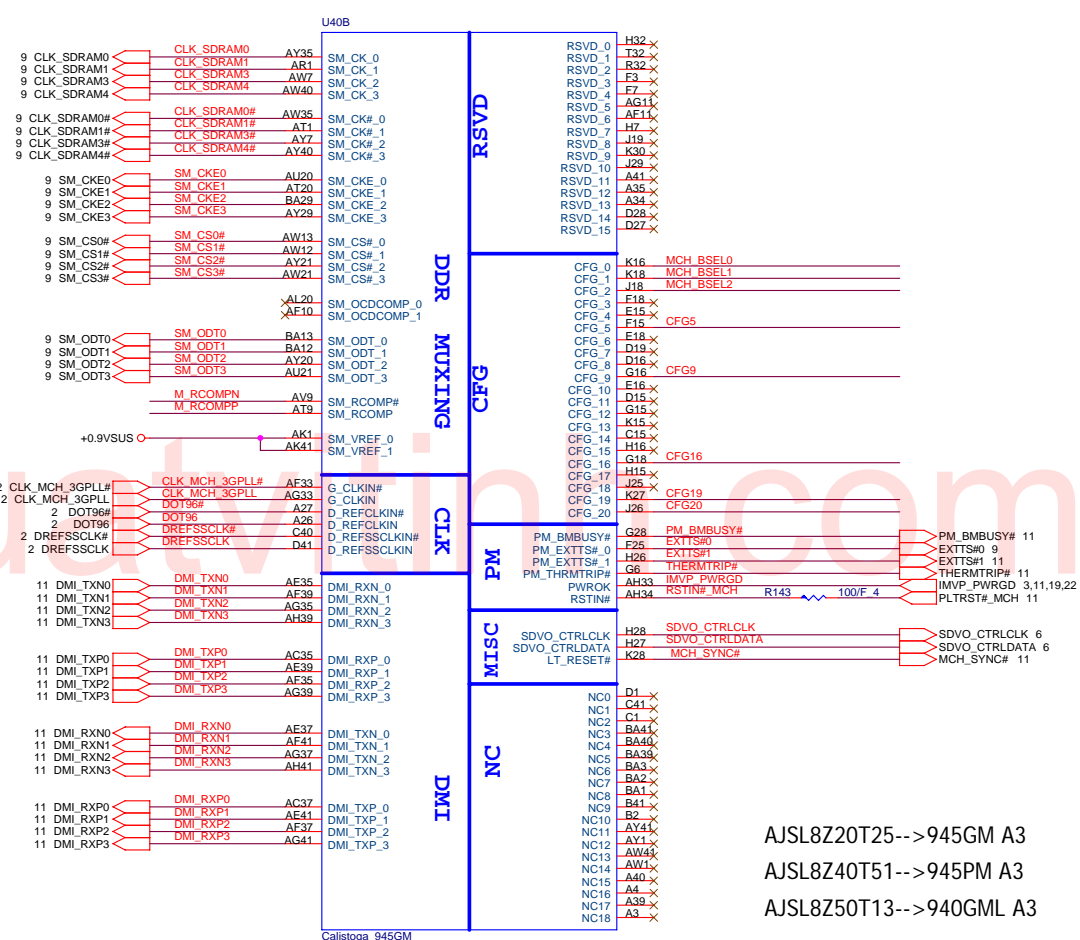
YONAH/MEROM

3 OF 3
GROUND



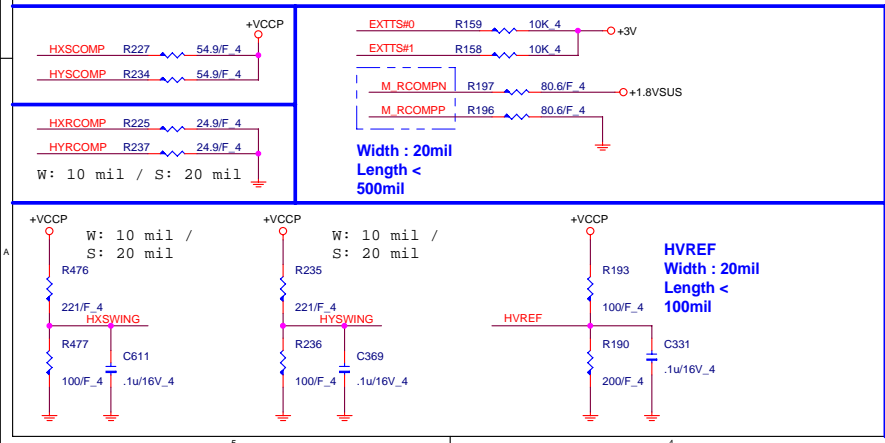
PROJECT : ZR1
Quanta Computer Inc.

NB 945GM/PM/940GML

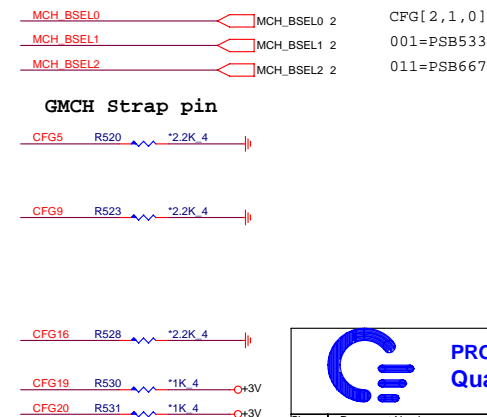


AJSL8Z40T51-->945PM A3

AJSL8Z50T13-->940GML A



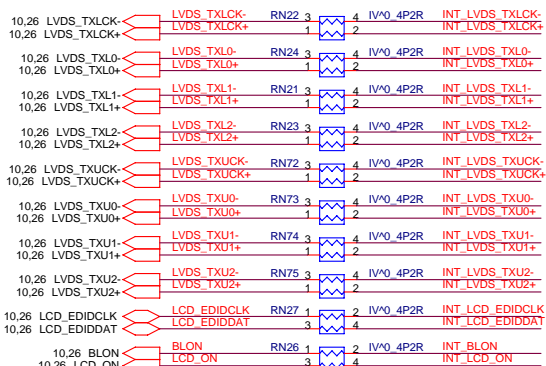
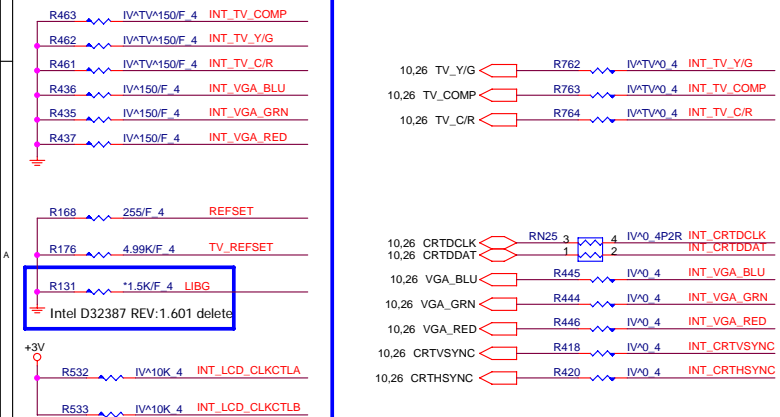
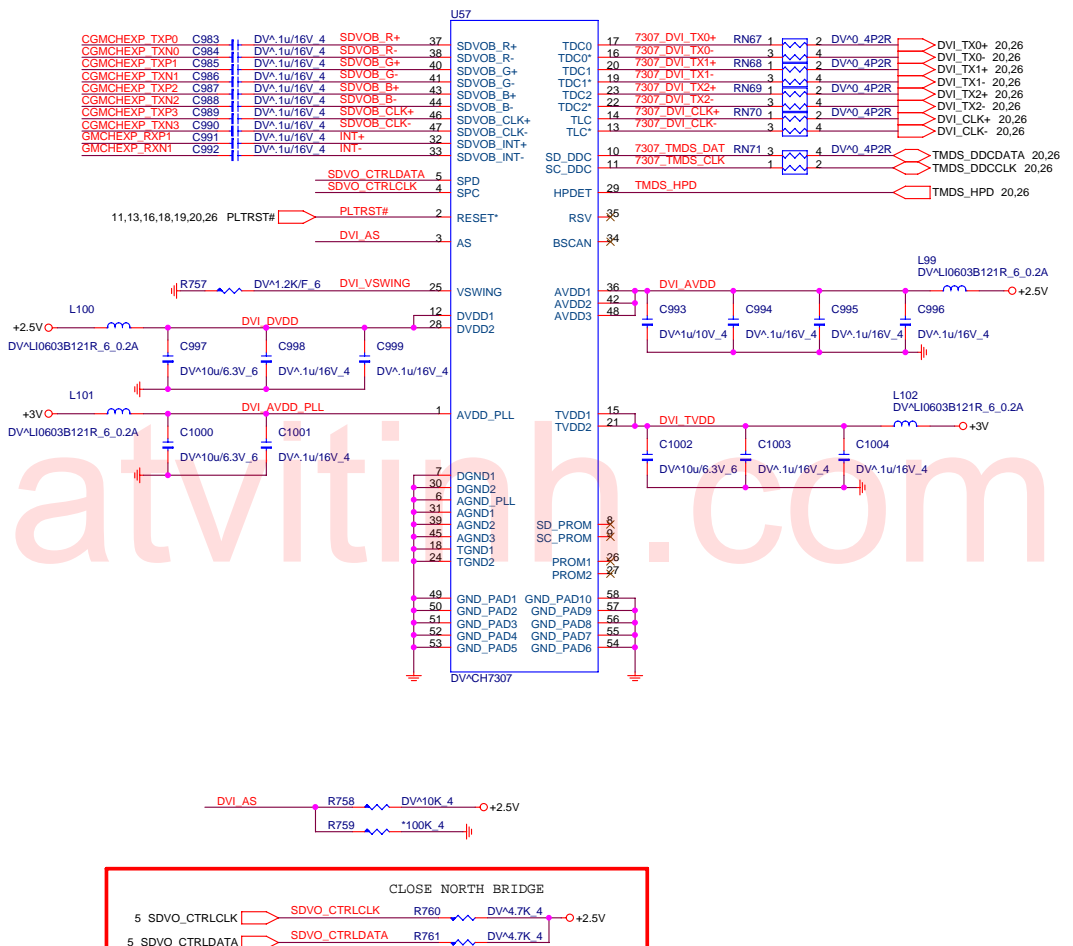
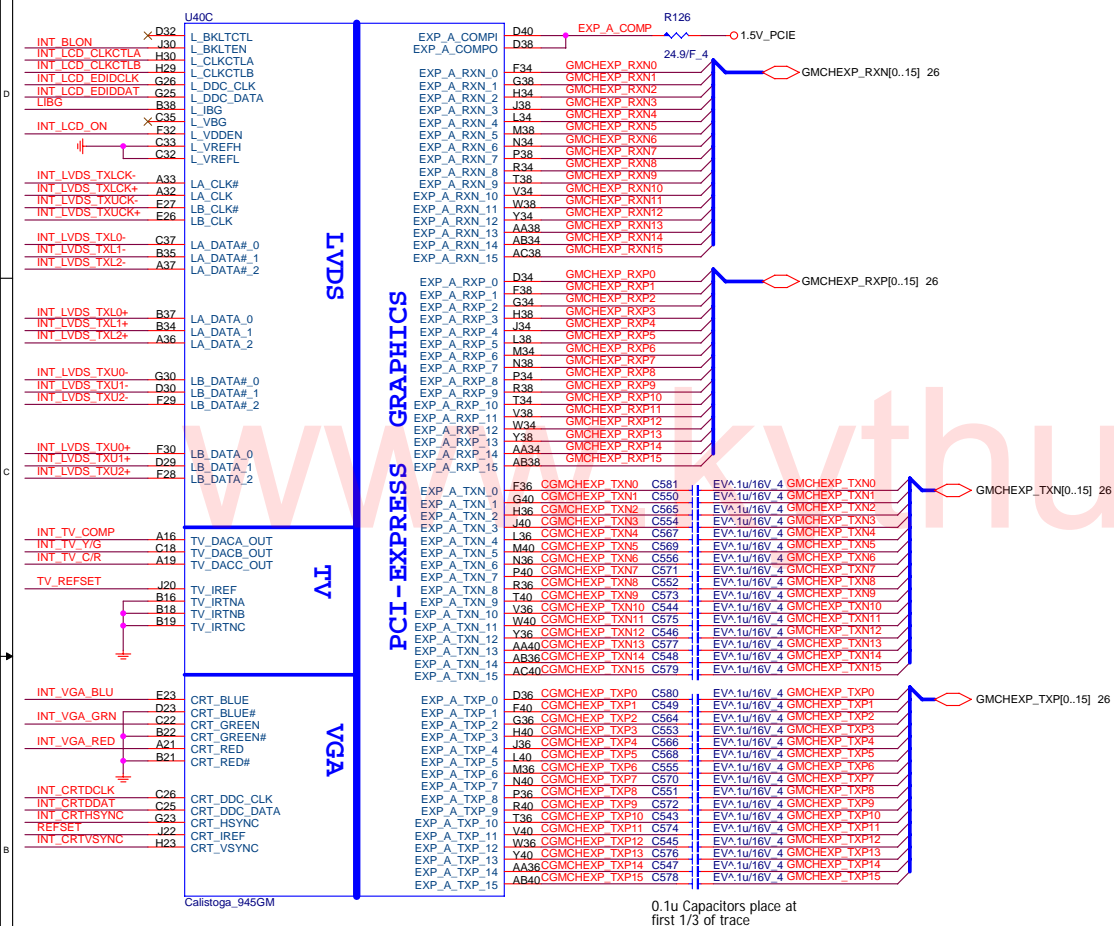
- 1.MCH_CFG_5 : Low = DMI X2, High=DMIX4
- 2.MCH_CFG_6 : Low = Moby Dick, High = Calistoga (Default)
- 3.MCH_CFG_7 : Low = RSVD, High = Mobile CPU
- 4.MCH_CFG_9 PCI Exp Graphics Lane: Low =Reverse lane ,High=Normal
- 5.MCH_CFG_10 Host PLL VCC Select: Low=Reserved, High=Mobility
- 6.MCH_CFG_11: PSB 4x Enable : Low=Rsvd, High=Calistoga.
- 7.MCH_CFG_16 FSB Dnynic ODT: Low = Dnynamic ODT Disabled,
High= Dnynamic ODT Enabled.
- 8.MCH_CFG_18 VCC Select: LOW=1.05V, High=1.5V
- 9.MCH_CFG_19 DMI LANE Reversal:Low=Normal,High=LANES Reversed.
- 10.MCH_CFG_20 PCIE Backward interoperability mode: Low= only SDVO or PCIE x1 is operational (defaults) ,
High=SDVO and PCIE x1 are operation
simultaneously via the PEG port.



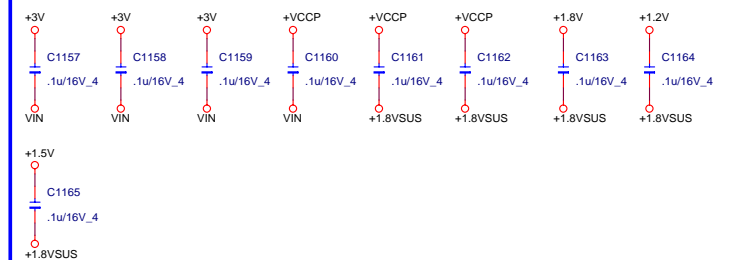
PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number GMCH (HOST / DMI)	Rev 1A
Date:	Wednesday, March 29, 2006	Sheet 5 of 29

DVO_CH7307



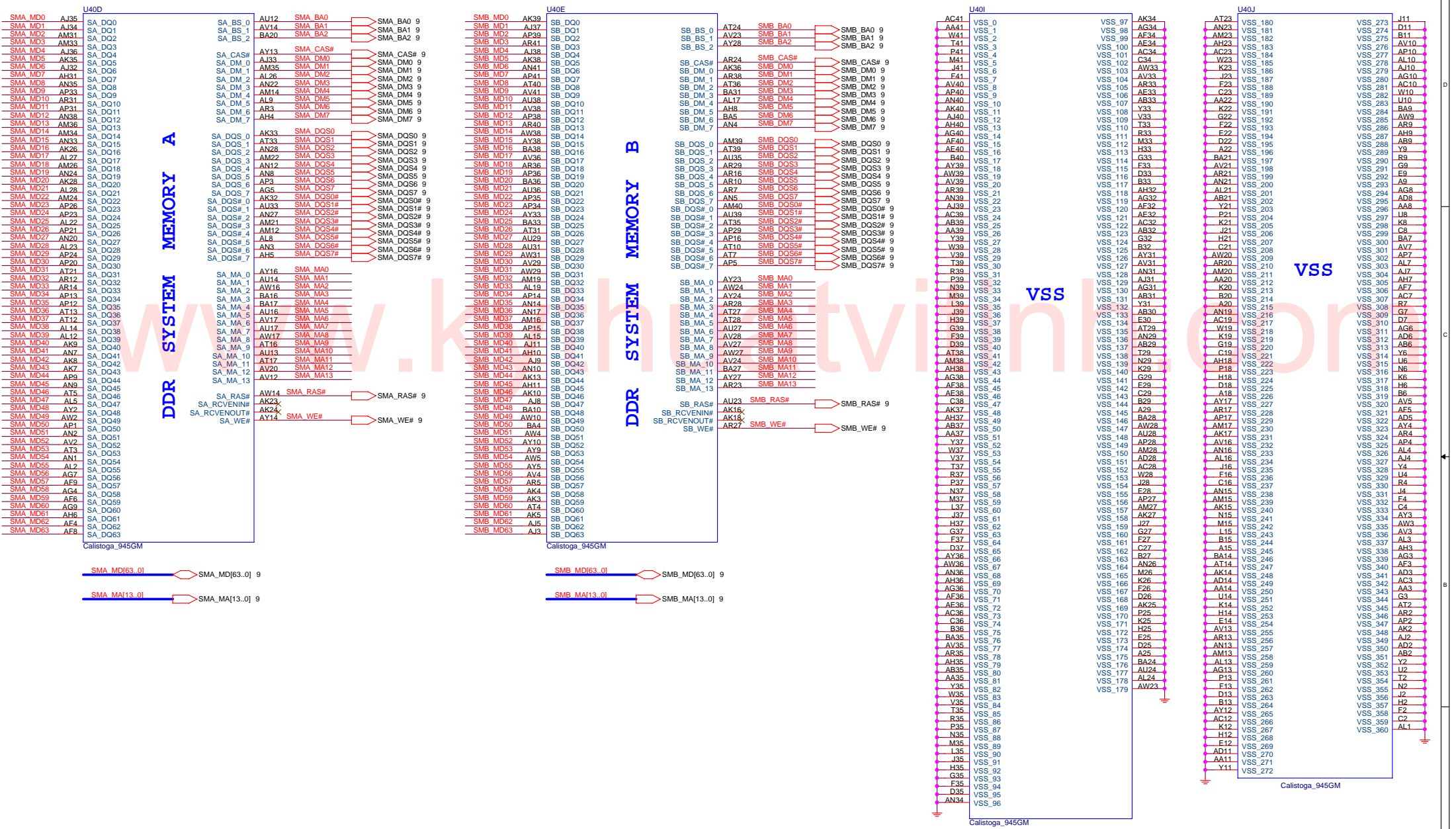
Signal Cross Moat



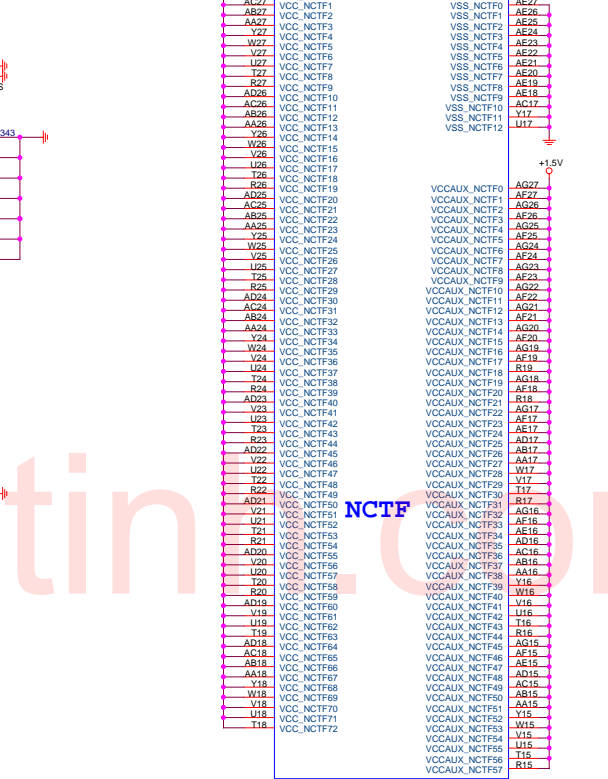
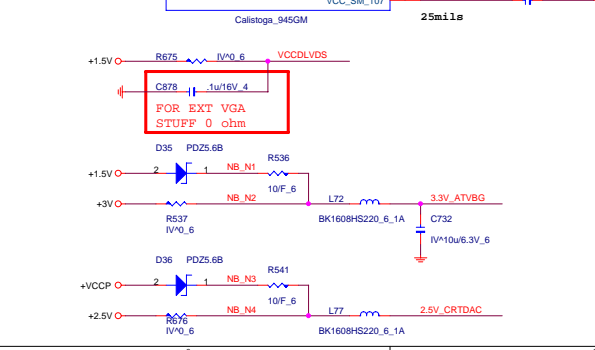
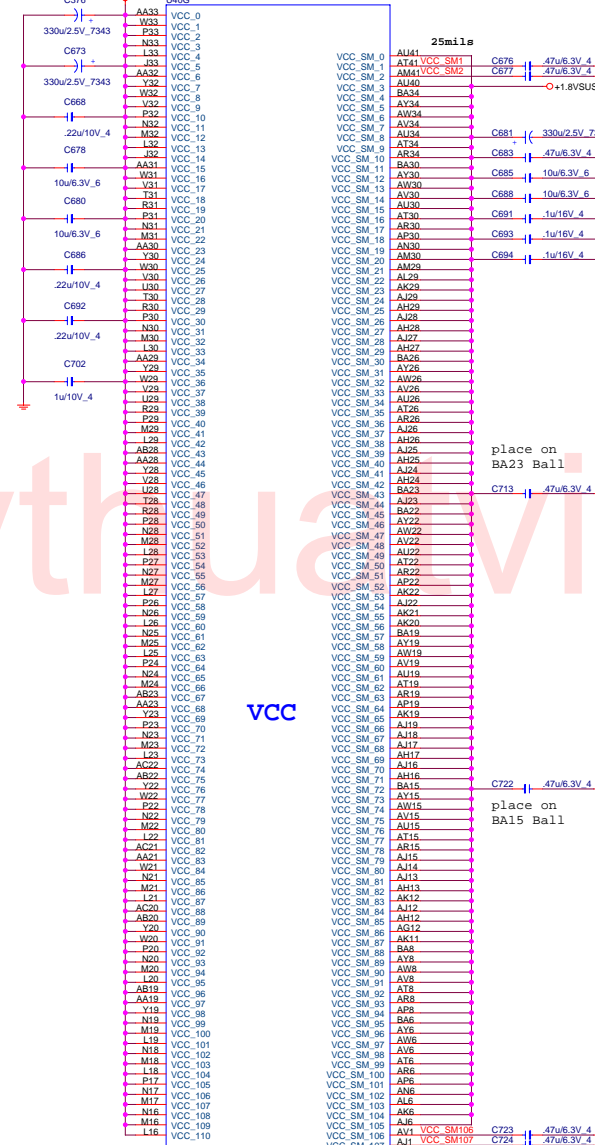
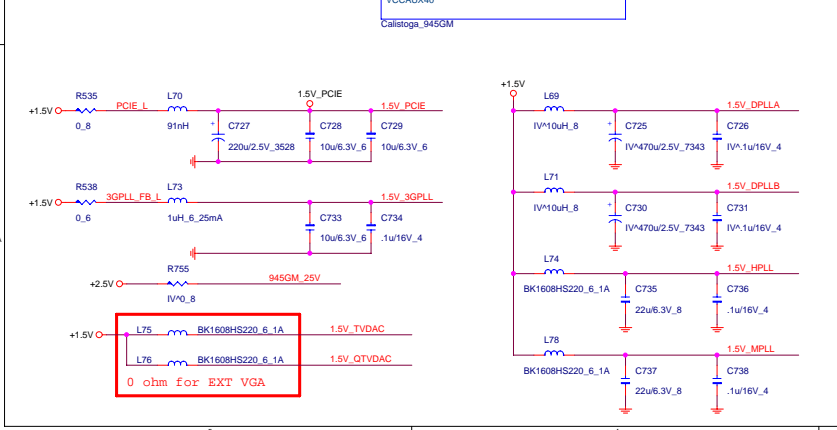
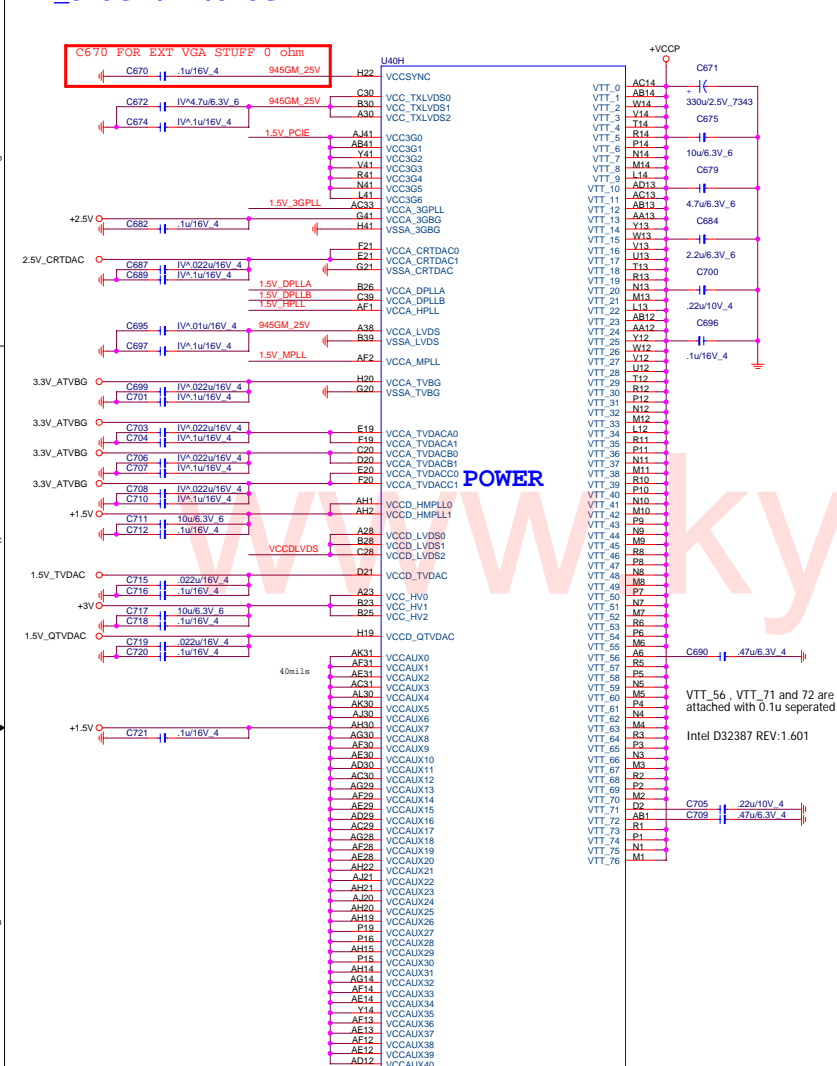
PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number GMCH (VGA)	Rev 1A
Date:	Wednesday, March 29, 2006	Sheet 6 of 29

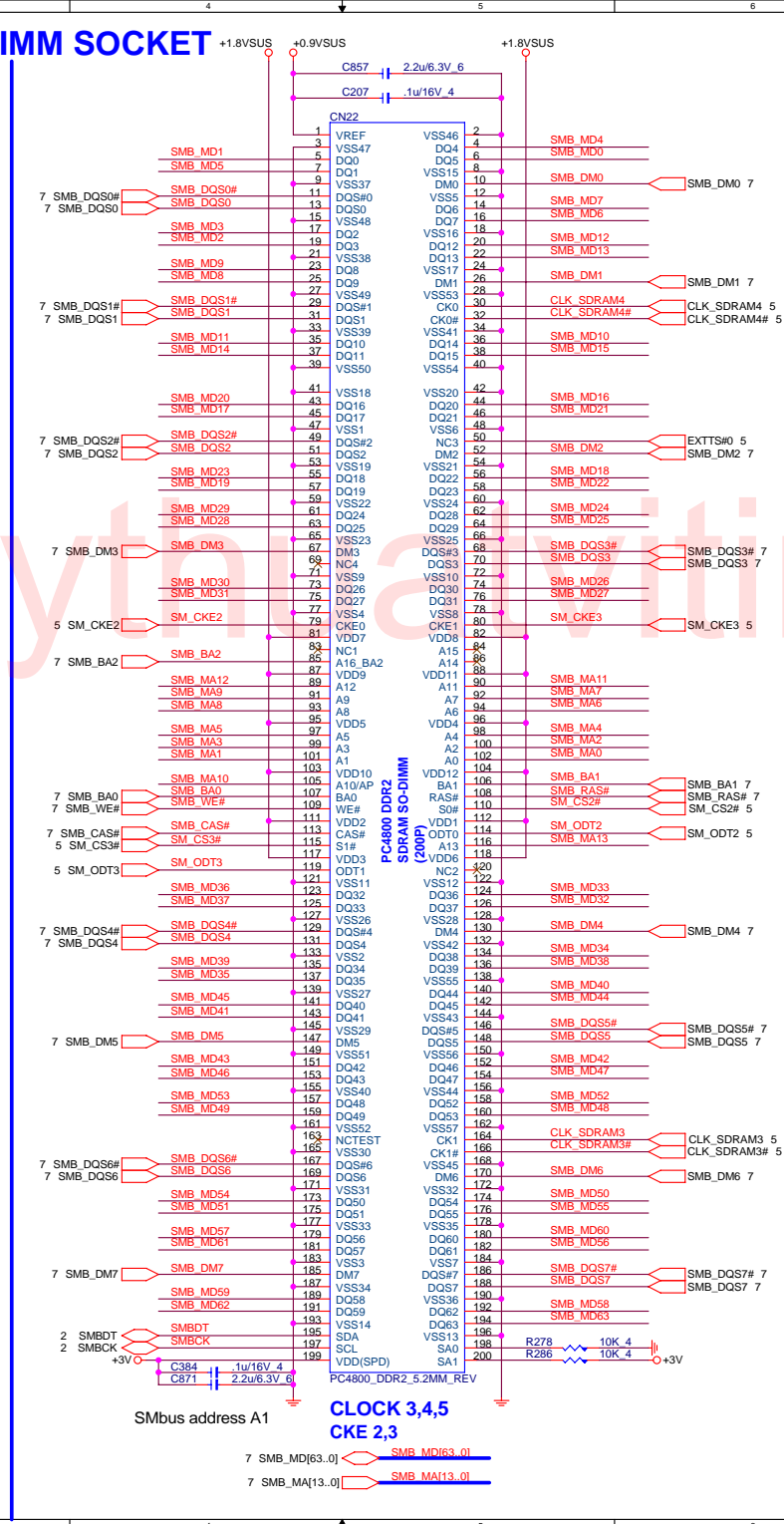
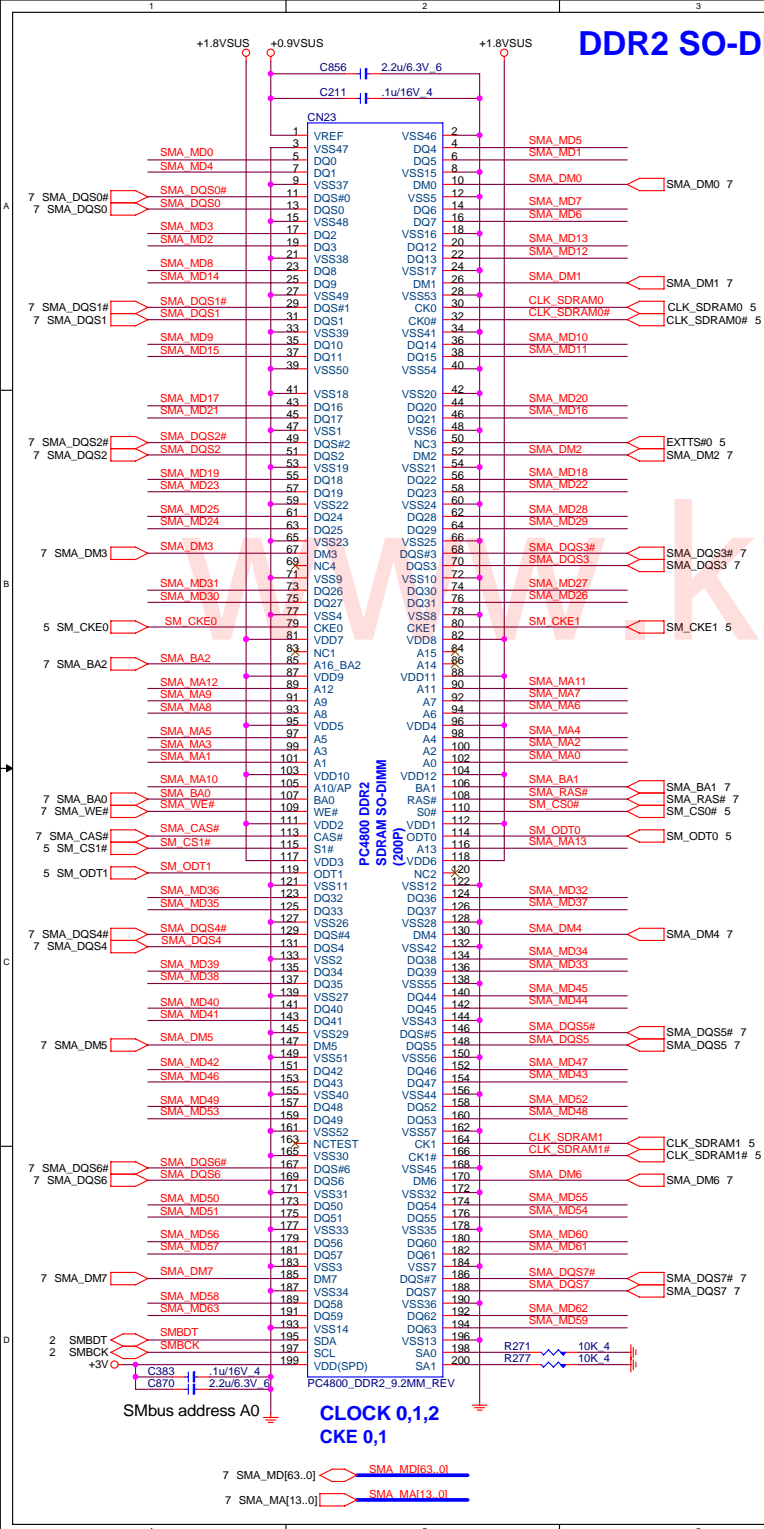
NB_945GM/PM/940GML



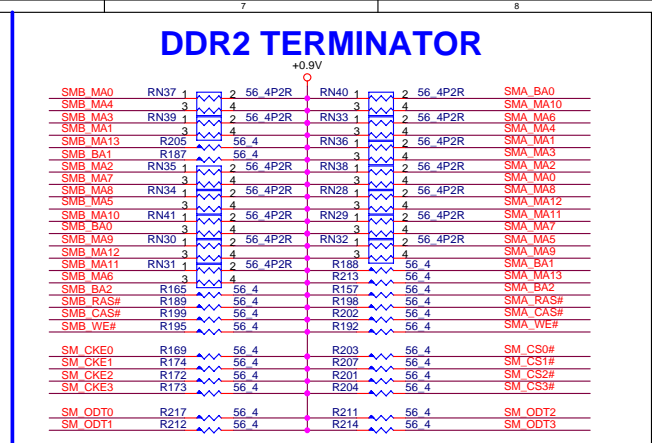
NB 945GM/PM/940GML



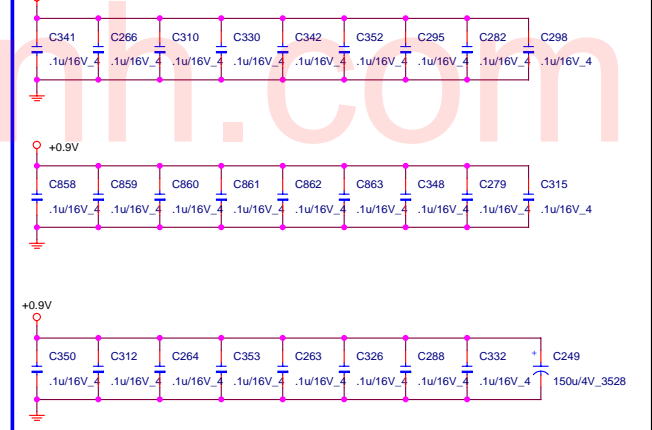
DDR2 SO-DIMM SOCKET



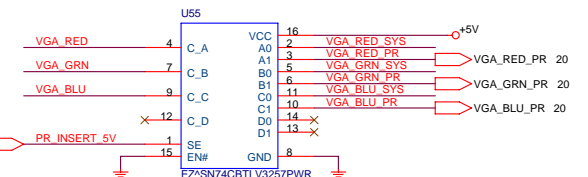
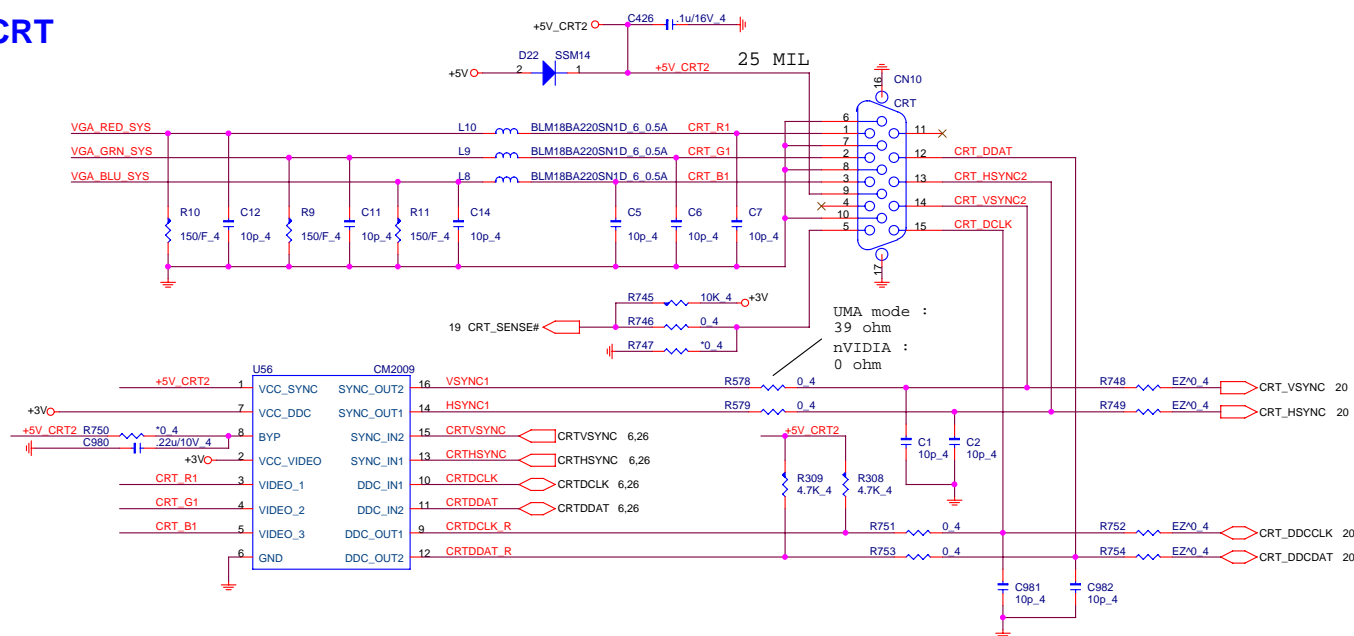
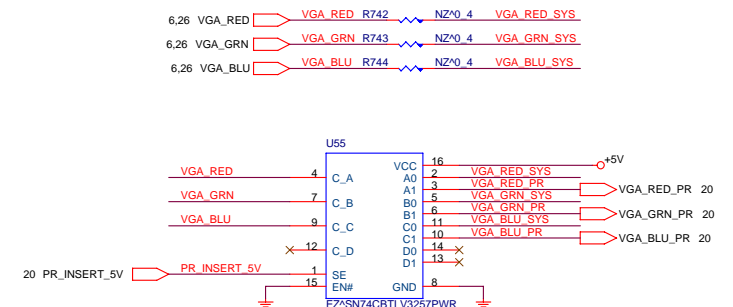
DDR2 TERMINATOR



TERMINATOR DECOUPLING CAPACITOR

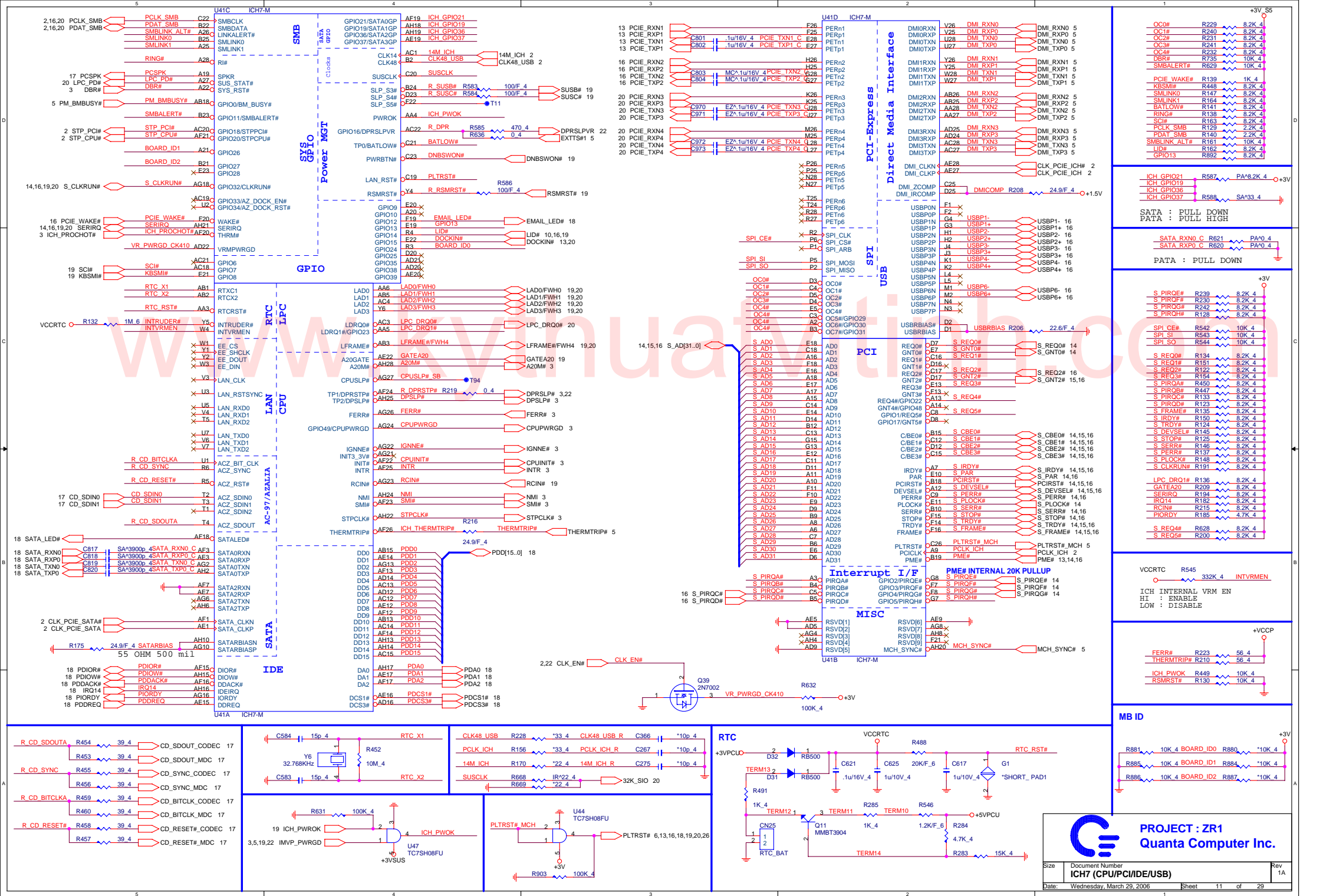


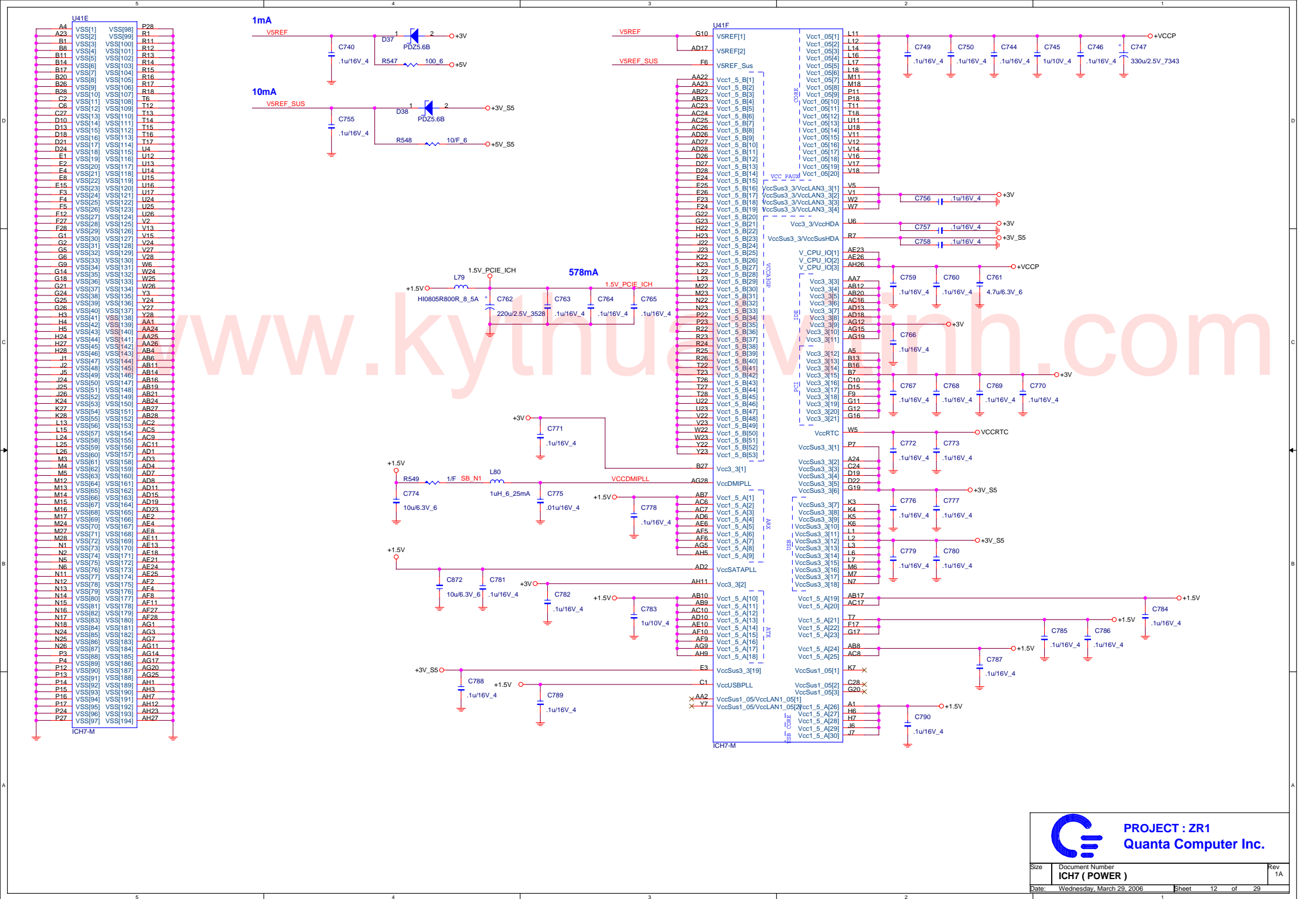
CRT



PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number LCD / CRT / TV	Rev 1A
Date:	Wednesday, March 29, 2006	Sheet 10 of 29

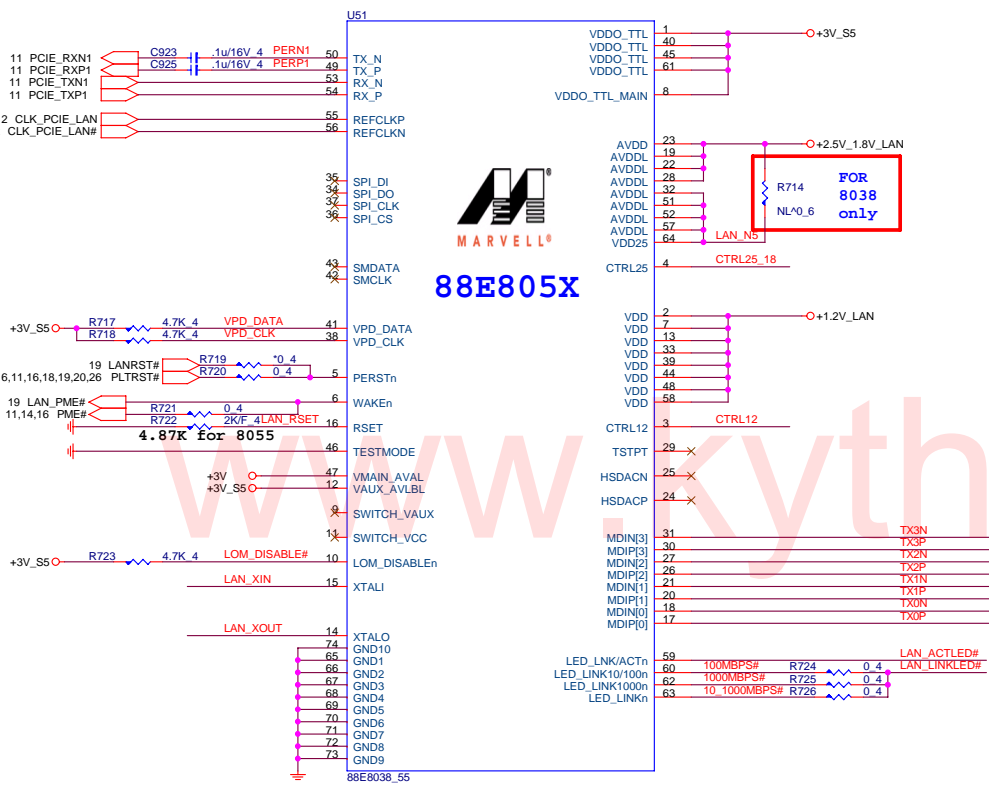




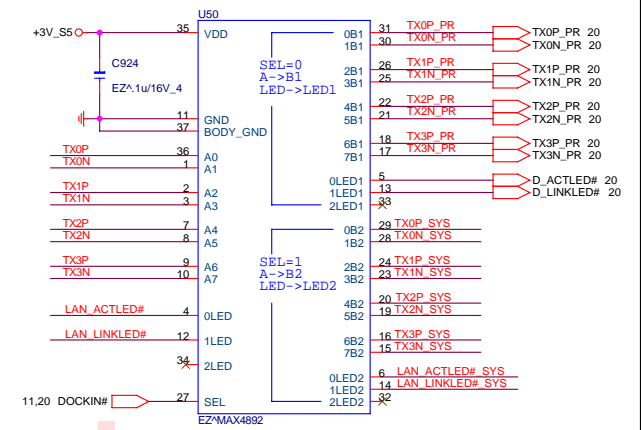
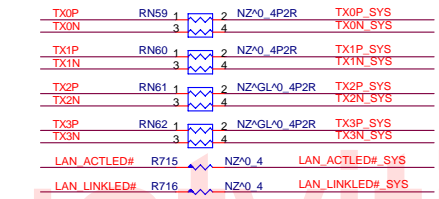
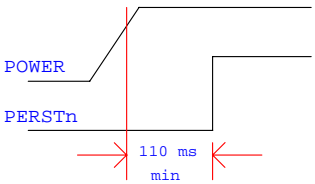
LAN_88E8038/88E8055

10/100 : 88E8038
GIGA : 88E8055

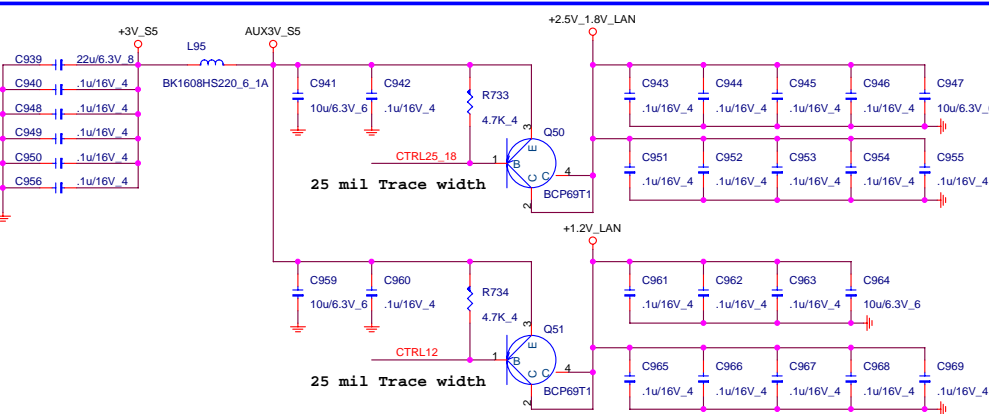
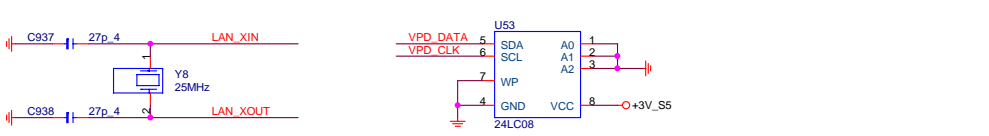
AJ080380000
AJ080550011 A2



RESET TIMING



BOTH HAND FCE
10/100 DB0KN7LAN24 DBED2LLAN05
GiGa DBKN1NLAN03 DB0ZH1LAN06



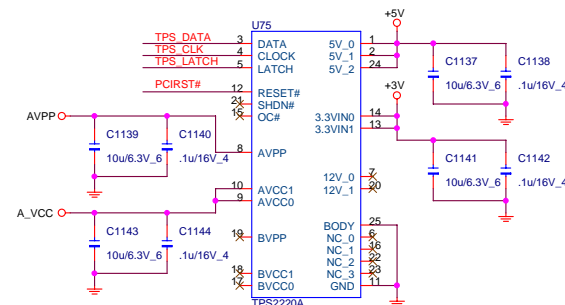
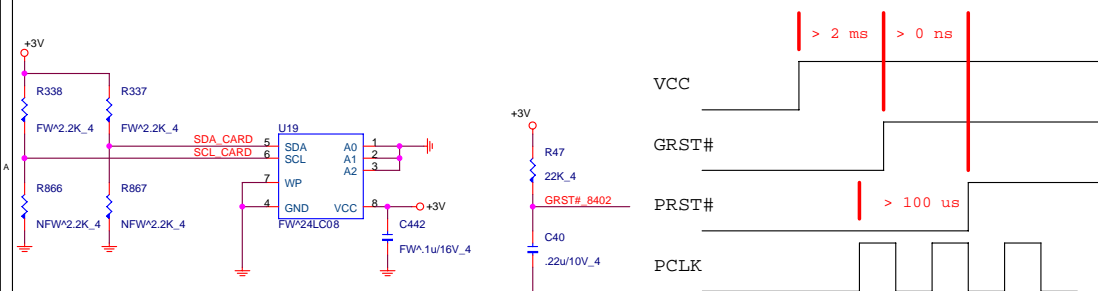
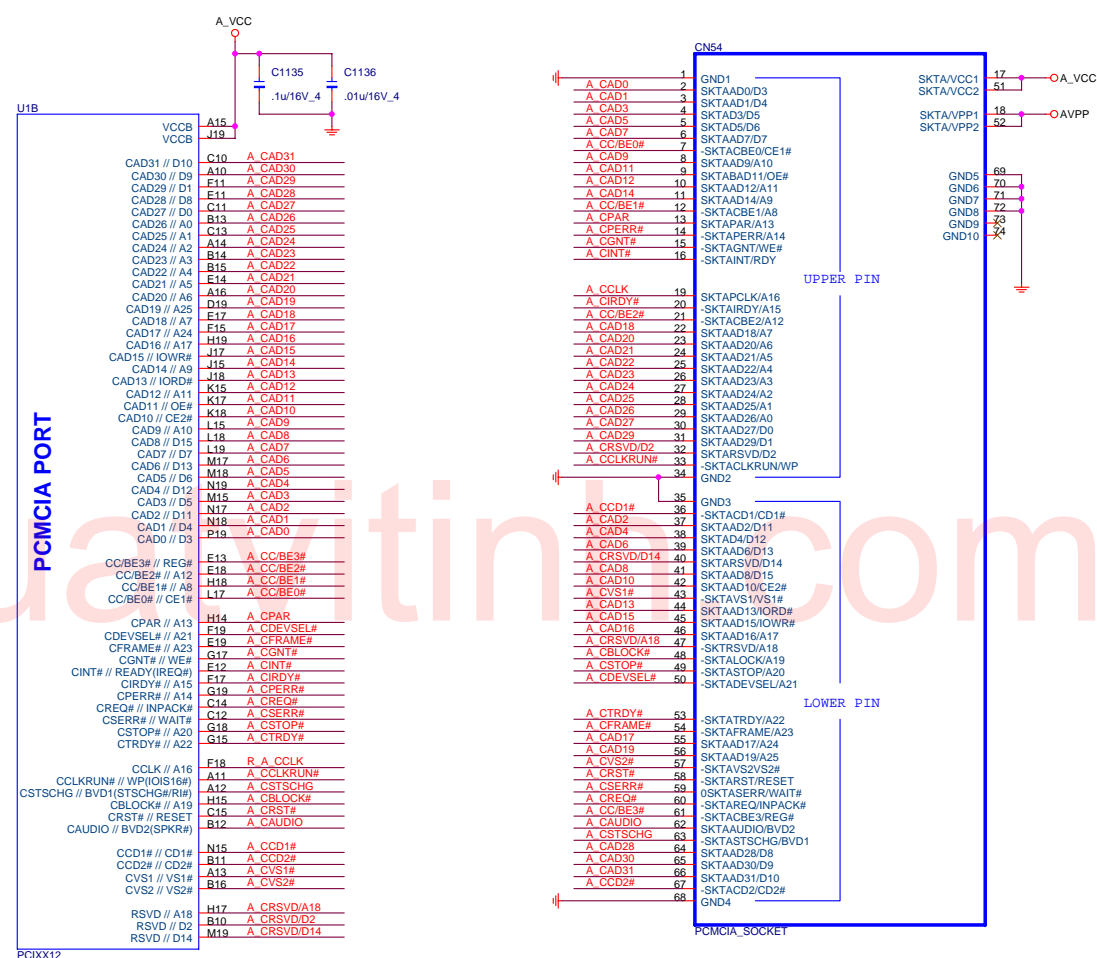
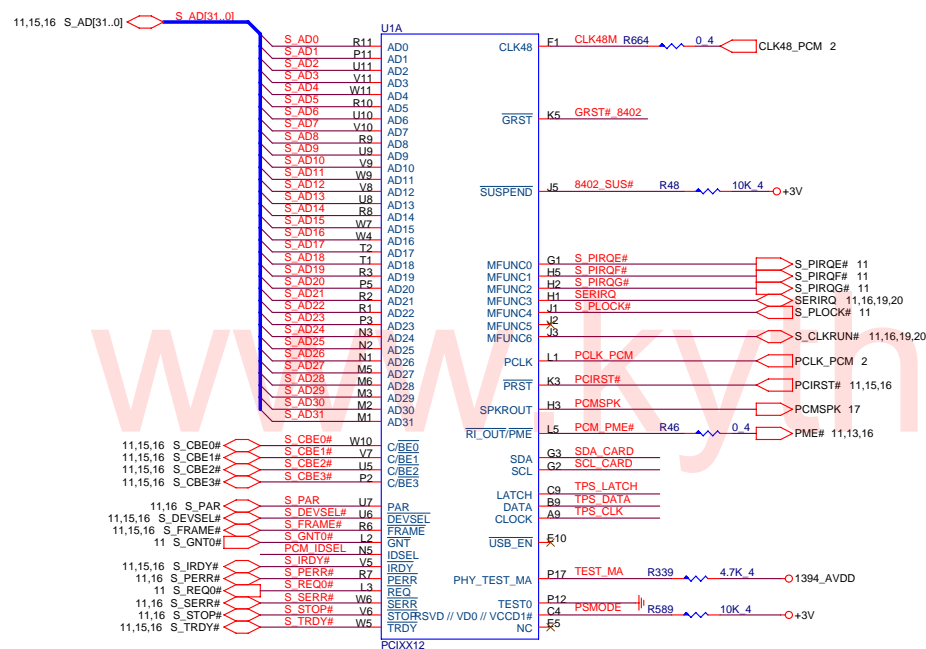
PCIXX12 PCMCIA



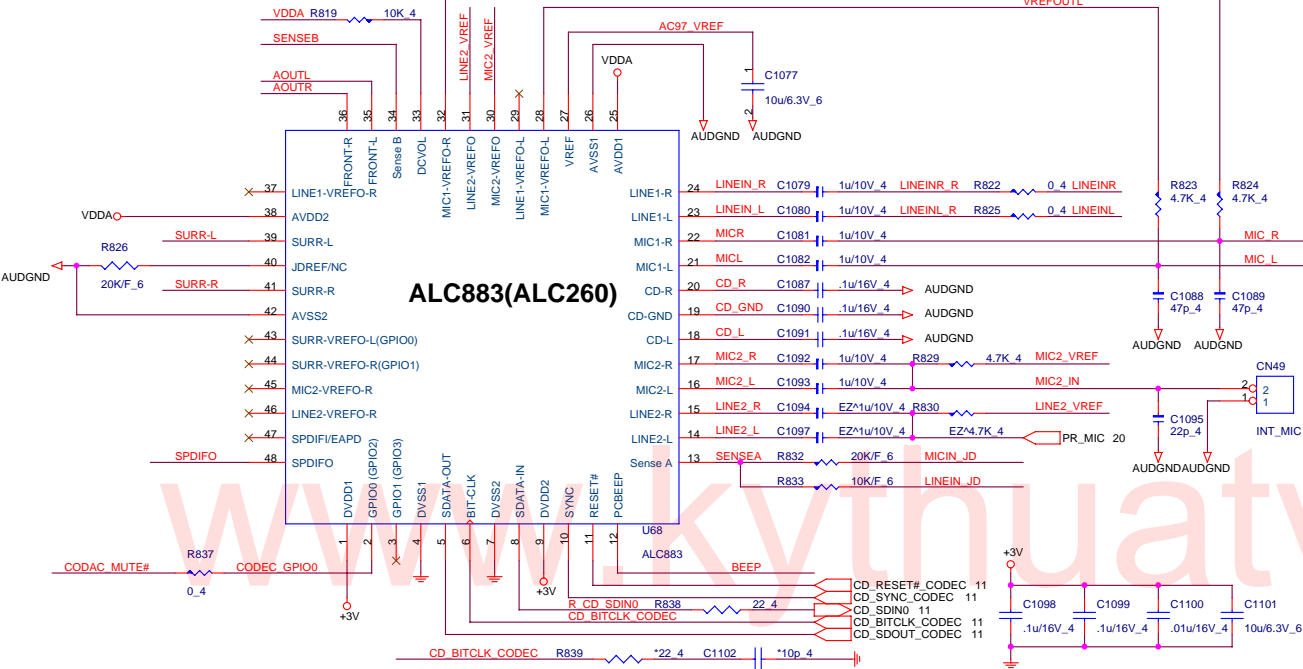
PCI8412 : AJ084120T08 PCMCIA / 1394 / 5 IN 1

PCI6412 : AL064120T04 PCMCIA / 5 IN 1

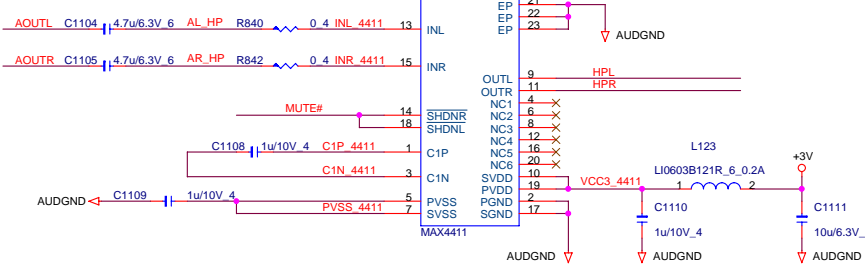
PCI1512 : AJ015120T02 PCMCIA



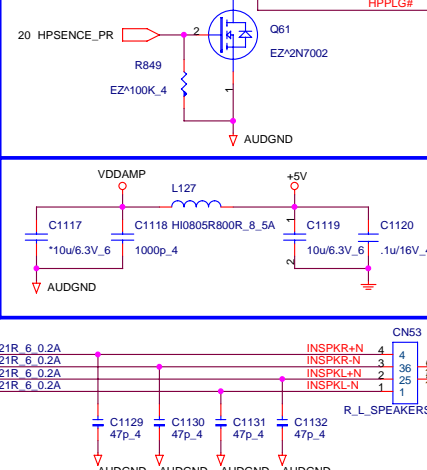
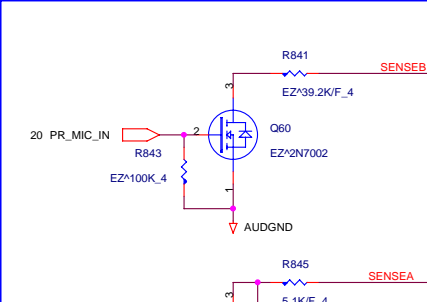
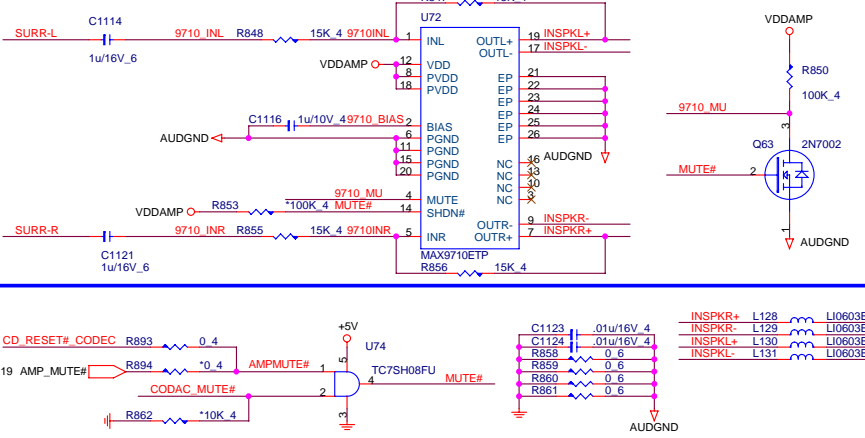
Audio Codec_ALC883



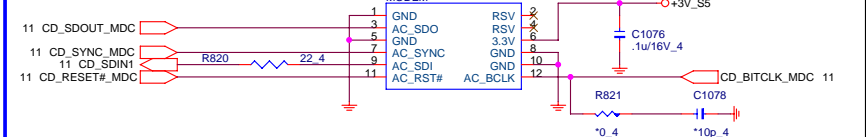
LINE OUT Amplifier



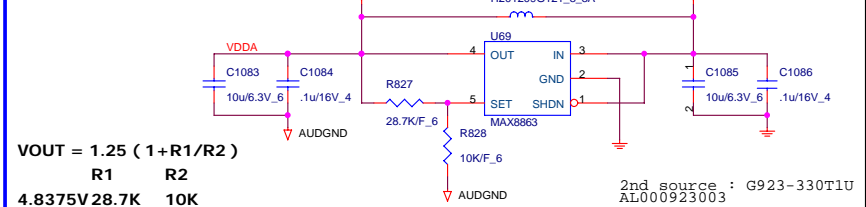
SPEAKER Amplifier



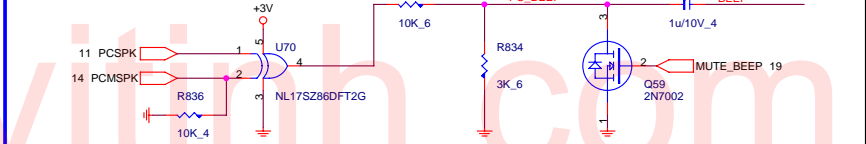
MDC



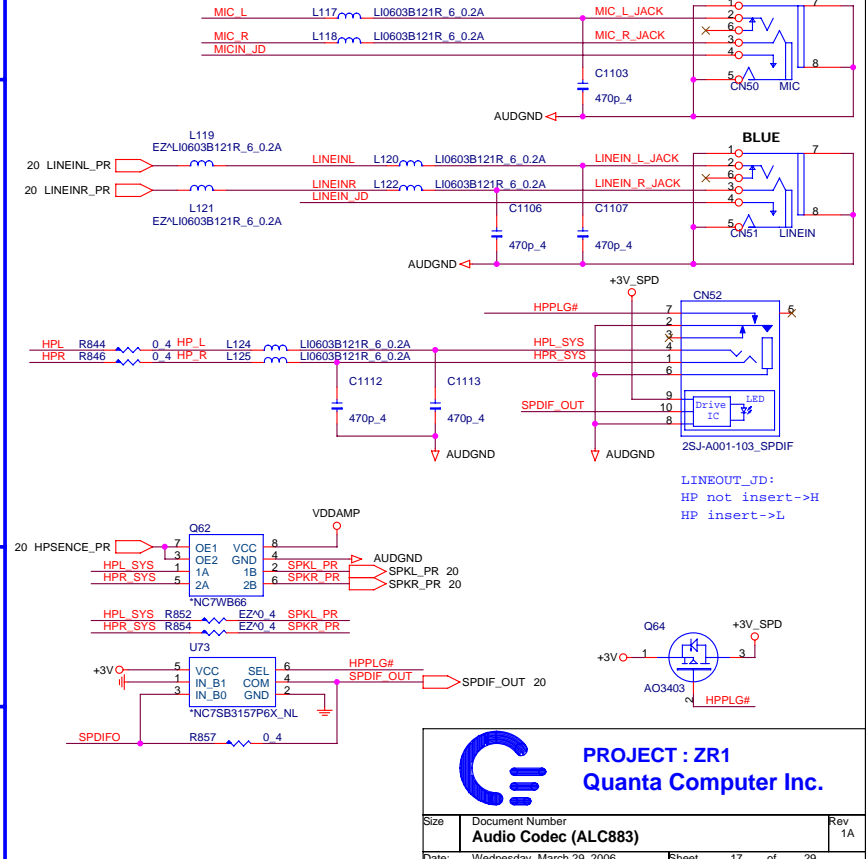
Audio Codec Power



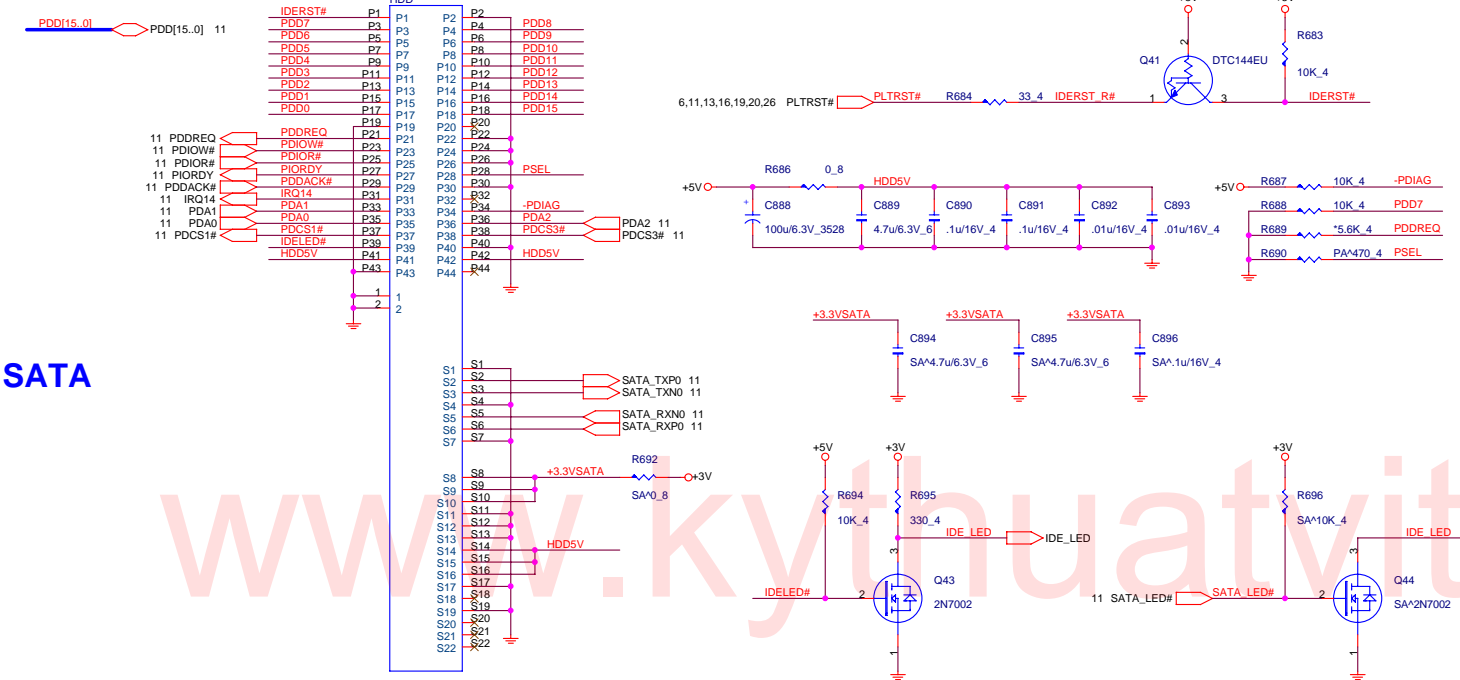
BEEP



Audio Jack

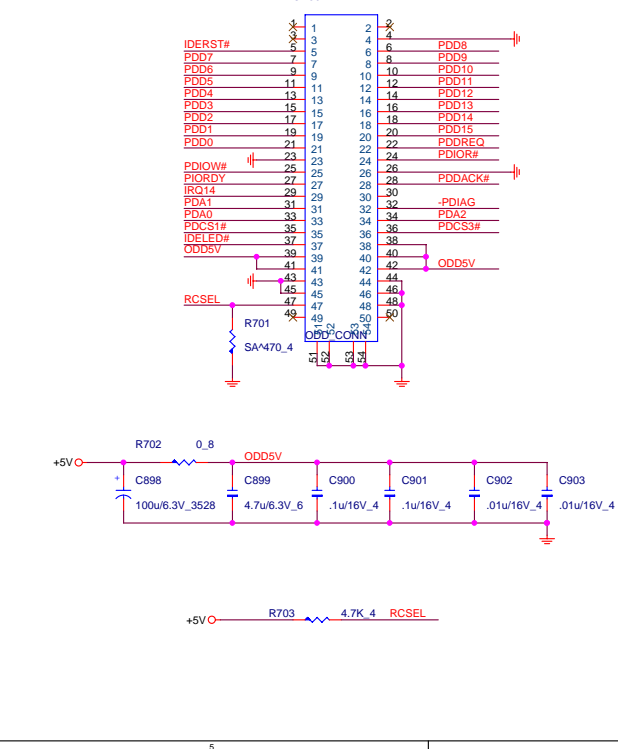


HDD

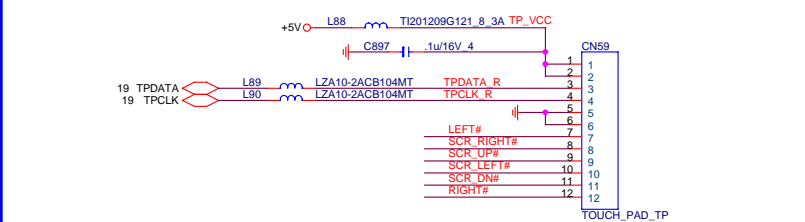


SATA

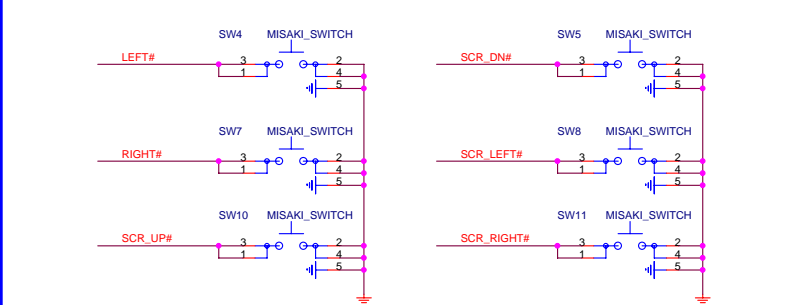
ODD



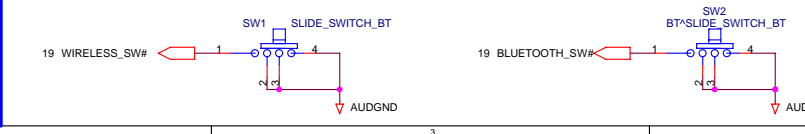
TP CONN



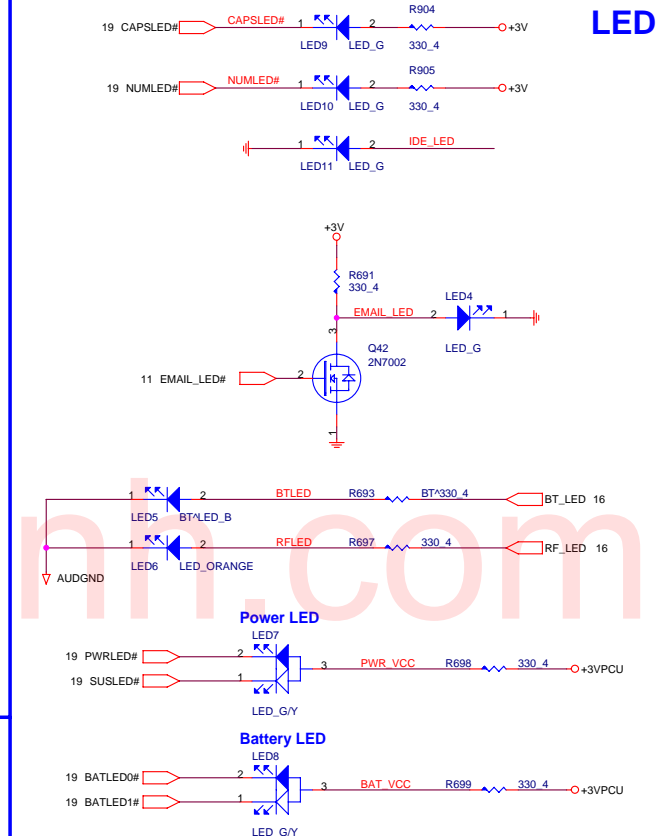
TP SWITCH



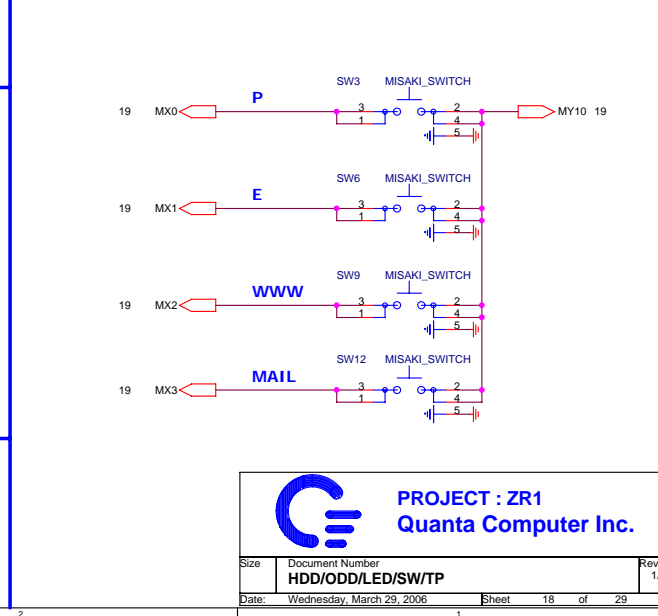
WL & BT SWITCH



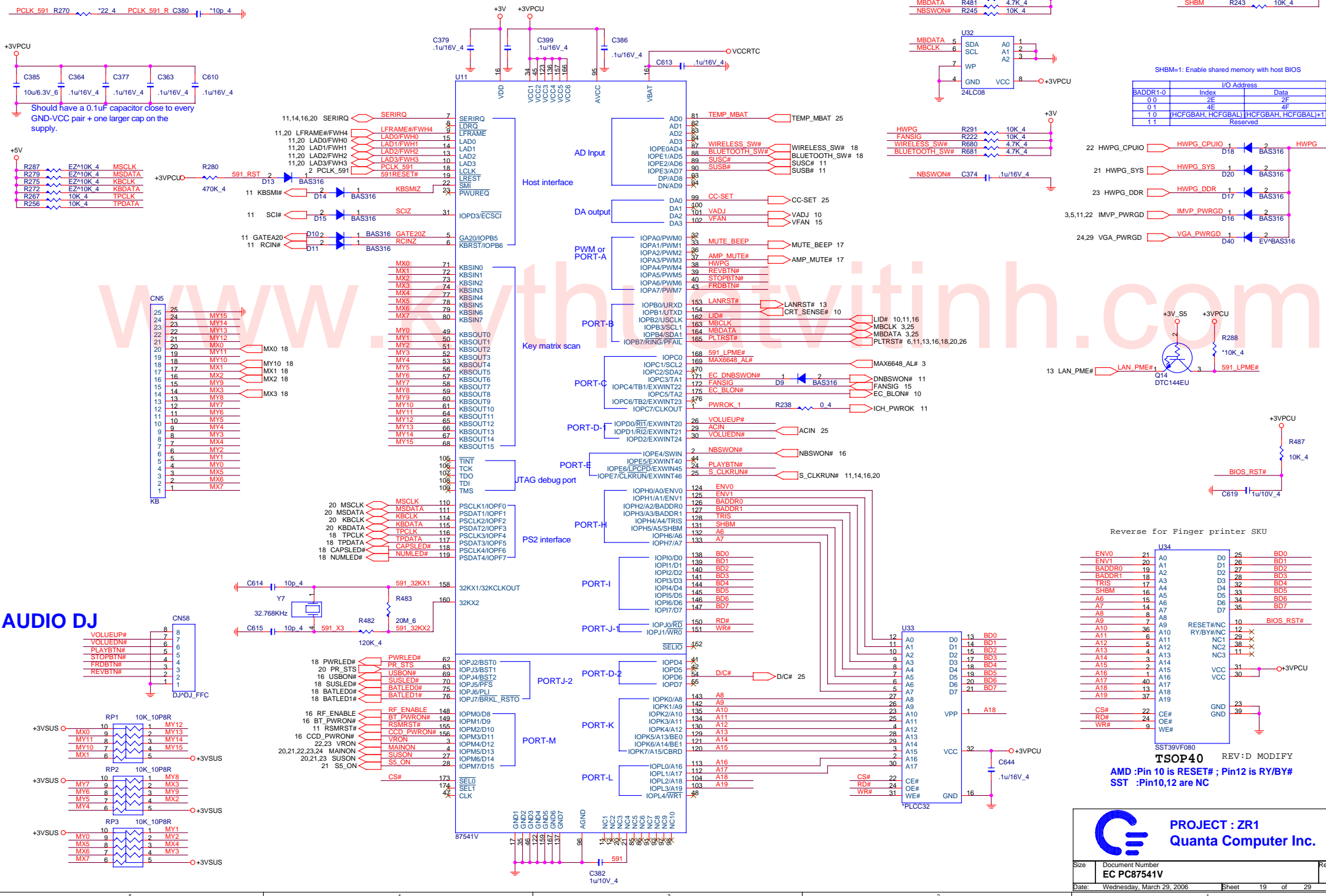
LED



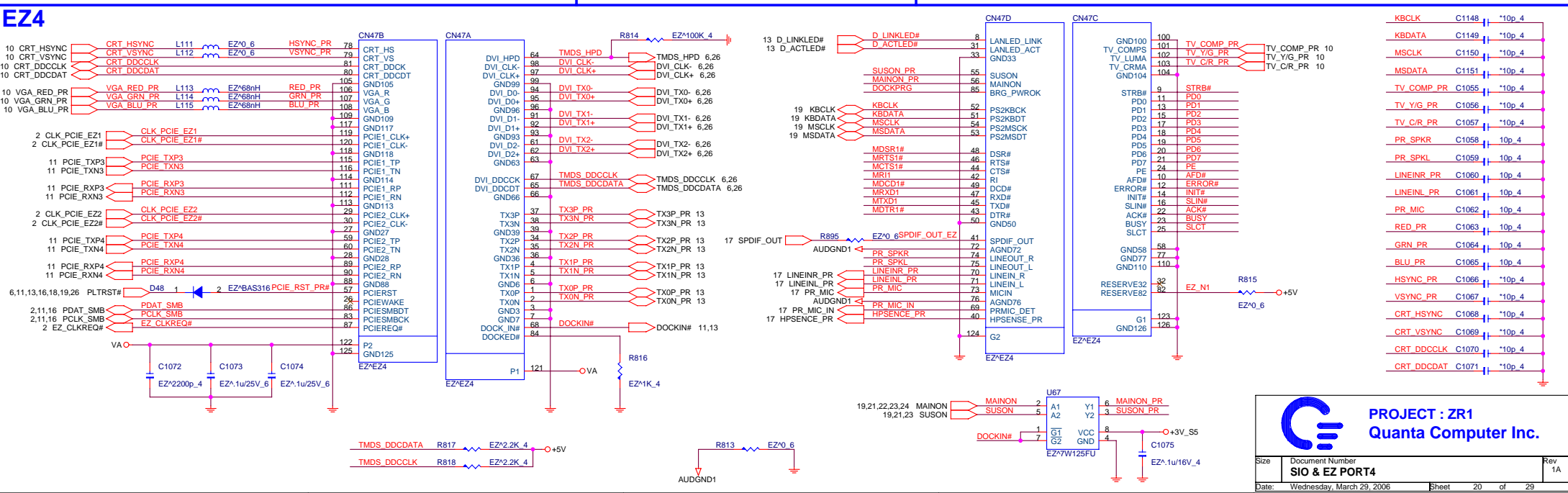
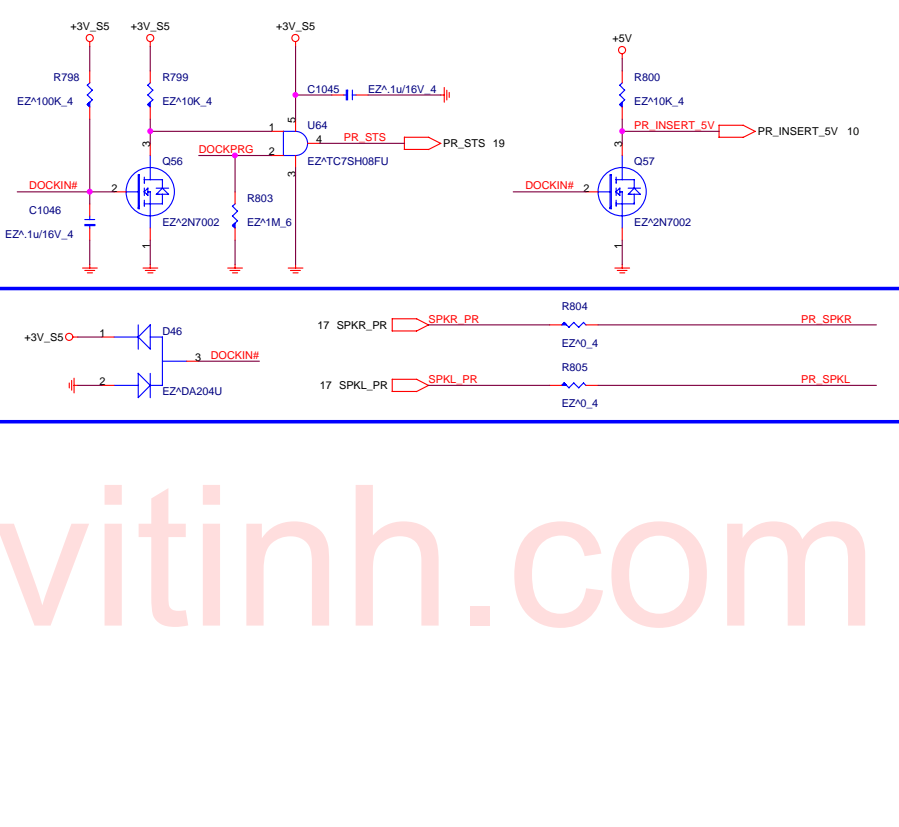
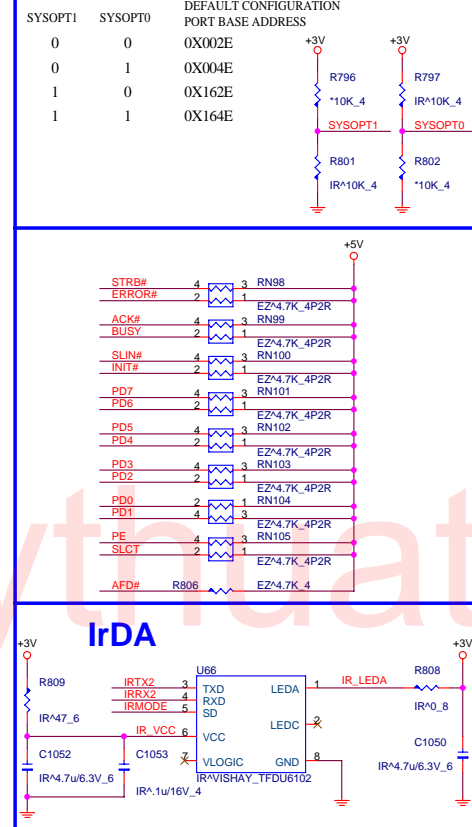
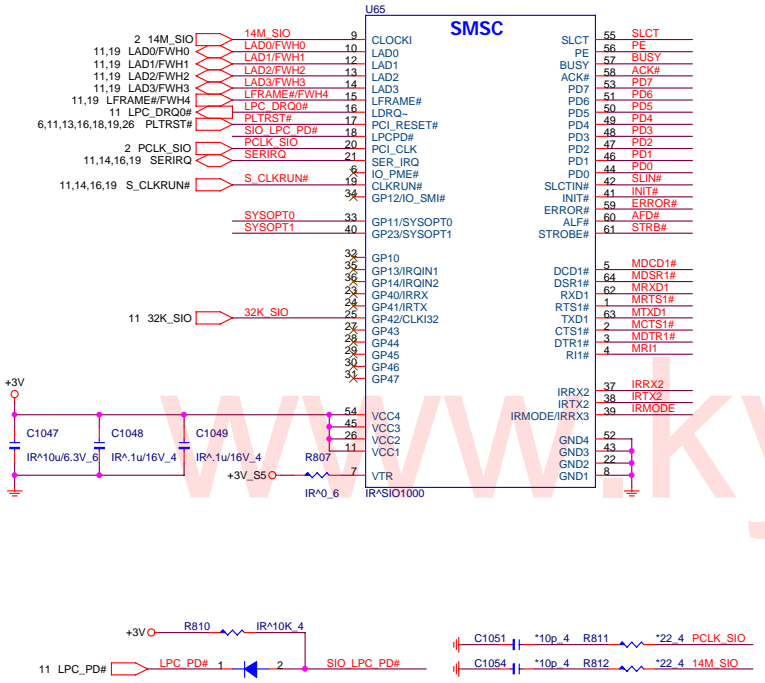
QUICK KEY SWITCH

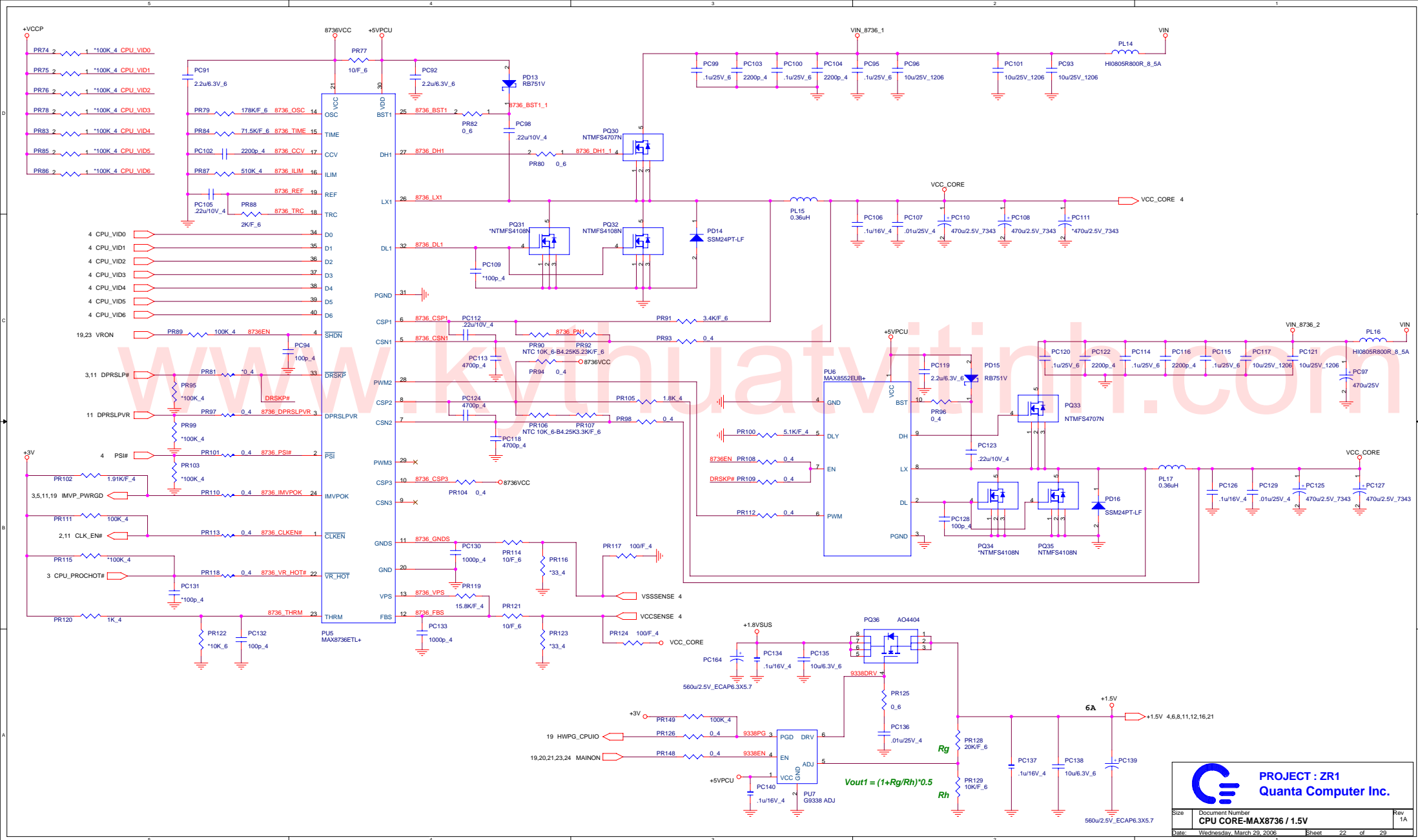


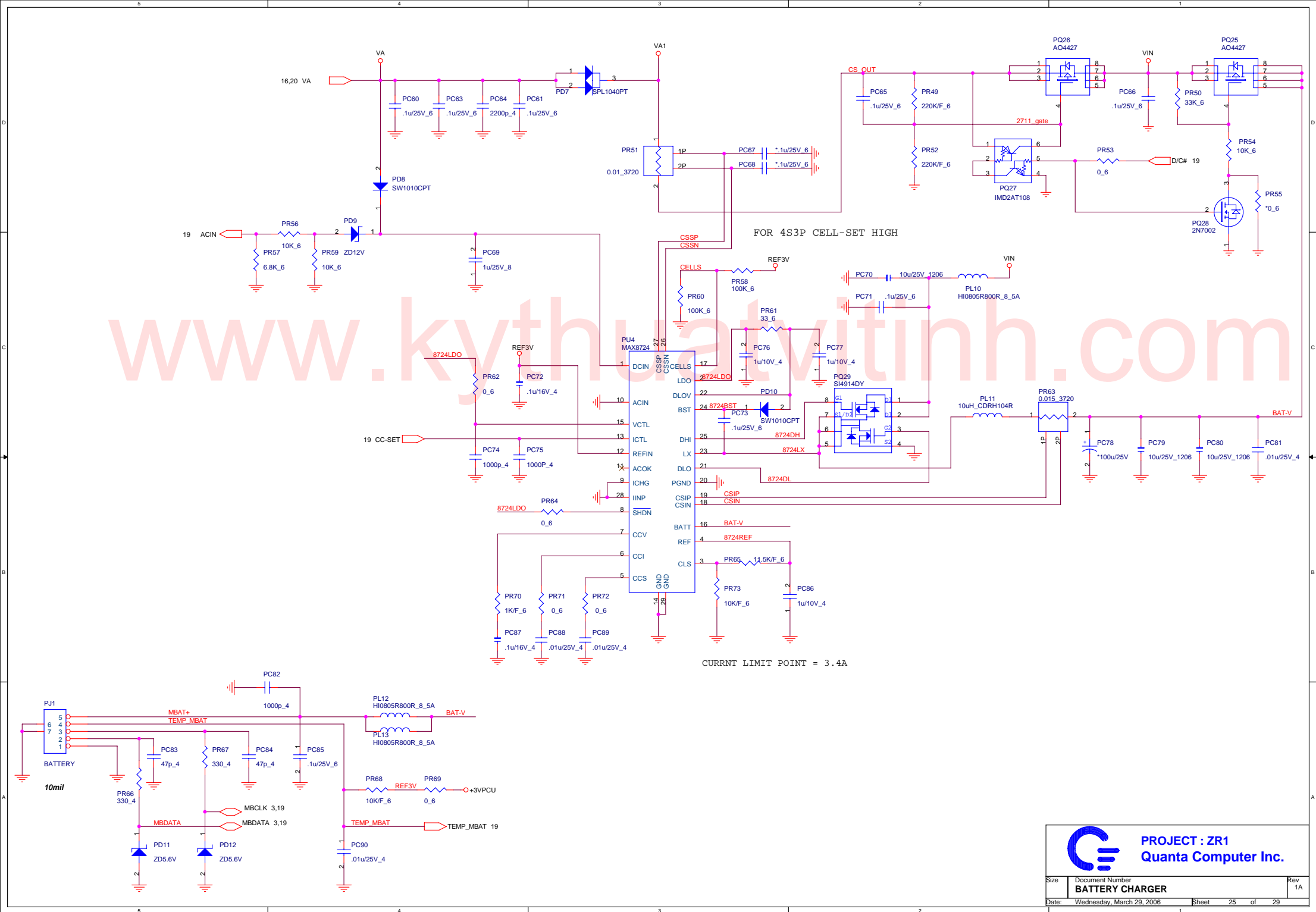
K/B CONTROLLER



SMSC SIO1000







NVIDIA
G72M
G3-64
(23X 23)

