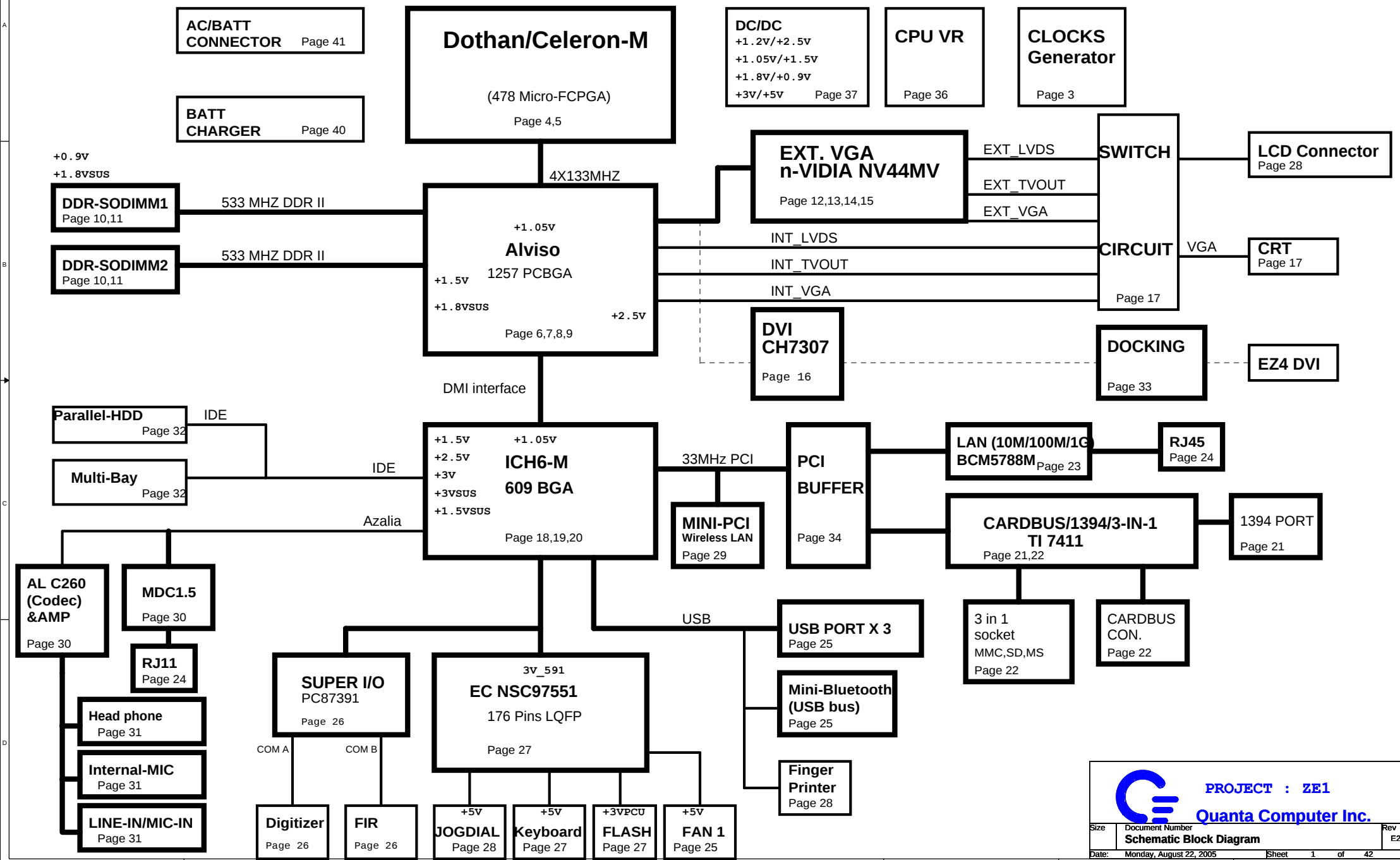


ZE1

REV : E2C



B TEST CHANGE ITMES:

- 06/22 MODIFY LIST**
- Remove all no-use text meno, ie: Rev A2.
 - Page 12. Chage R157, R158, R162~R167 power source from +3V to IFPC_I0VDD.
 - Page 15. Add R615, R616, R614 Q44 and Q43. Follow nVIDIA's suggestion. TMD5 Backdrive Prevention circuit.
 - Page 16. Change single R to RN, R62, R63 => RN16; R56, R57 => RN17; R58, R59 => RN18; R60, R61=> RN19. And all use 0 ohm.
 - Page 24. Swap net of CN10-12 and CN10-13. Wrong connection.
 - Page 25. Change USB power source from 5VPCU to +5VSUS. Wrong design.
 - Page 30. Reserve R617 (stuff), R618 (stuff), C859 (NA), C869 (NA), C861 (NA) and C862 (NA). For noise in EZ4's LINE-IN.

- 06/23 MODIFY LIST**
- Page 23. Delete R36 and R76. VESDChage PWR source from LAN_3V to 3V_LAN directly.
 - Page 23. Delete R40 from TCK.
 - Page 23. Delete R42 from Q9-B and Q9-C.
 - Page 23. Add option resistors in 3VLAN. Add R56 and R57 for BCM4401.
 - Page 23. Add PI-filter for AVDD and separate from VDDP. Add L72.
 - Page 23. Add PI-filter for AVDDL. Add L73 and C863.
 - Page 23. BOM modification. Correct RDAC's resistor from 1.18K to 1.24K (BCM5788).
 - Page 23. BOM modification. R70: stuff; R68: NA. FAE: Support CLKRUN#.

- 06/28 MODIFY LIST**
- Page 28. Delete R112 (pull high/ 10K) and Add R624 (0 ohm) to GND. BOM.
 - Page 28. Add R625 and R626 to GND. ALPS setting. BOM.
 - Page 28. Add R619, R620, R621 and R623 for bypass 3D function. BOM.
 - Page 28. Reserve R624, R622, C864 and U62. Those are for 3D function.

- 06/28 MODIFY LIST**
- Page 13. Update and correct U27 and U28 footprint. Correct DQS and DQM.
 - Page 13. Re-route MDA[0..63], QSA[0..7] and -DQMA[0..7]
 - Page 9. C272 change from CH73301M8B9 to CH7474M8801(LOW profile). BOM

	R51, R48	R56, R57	R38	R97, R97	Q5, Q9	U7	U8
BCM5788A	STUFF	NA	1.24K	STUFF	STUFF	NA	
BCM4401	NA	STUFF	1.27K	NA	NA	NA	STUFF
NOTE	+3VRLN	3V_LAN	RDAC		AT93C46	24LC128	

B2 TEST CHANGE ITMES:

- 07/19 MODIFY LIST**
- Page 25. Change U32, U58 and U33 to AL000547001 (GMT).
Change C489, C800 and C490 to CH71001M393 (100uF).
Del R299, R296, R562, R555, R292 and R294.
 - Page 25. Add USB PWR control (SUSON) circuit. Add R554 (10K) and Q39(DTC144EUA)
 - Page 42. Add Semtech solution in Track-point.
Add U63, U66, U65, U64, C866, C868, C867, C870, C872, C871, R637, R638, R643, R642, R645, R644, R647, R641, R646, R640, R639, R638.
 - Page 22. Chane CN13 from 4 in1 to 3 in 1. Re-routing it.
 - Page 28. BOM delete D4. Acer: no action in LID.

- 07/26 MODIFY LIST**
- Page 32. Add HOLE45 for MDC.
 - Page 32. Add HOLE46 for VRAM. (NO SPACE!!!)
 - Page 32. Add clip's PAD. P5, P6 and P7.

- 07/29 MODIFY LIST**
- Page 27. Add PULL HIGH 10K/ R649 to 3V. (EC)
 - Page 40. BOM: Remove PR149 and Stuff PR147. (EC)
 - Page 40. Delete PC981, PU10, PC97, PC98 and PC99. REFP => VIN; REF3V => 3V_591. (EC and PWR)
REFP in PD19-1 and PC130-1; REF3V in PU13-12, PR40-1, PR64-1 and PU14-5.
 - Page 40. Delete PR134 and PR139. Then SHORT them. (PWR)
 - Page 40. Change PQ52-3 from PU13-13(CC-SET) to PU13-8. And change PR87 from 0 ohm to 10K.(PWR)
 - Page 28. Udate CN7's footprint to 88264-06XX-6P-R
 - Page 28. Udate CN8's footprint to 88264-10XX-10P-R
 - Page 28. Udate CN3's footprint to 88264-04XX-4P-L
 - Page 39. Delete JP1. Change +1VS_55 to +1.5V_55.
 - Page 39. Delete JP2 and JP5. Change VCCP-OUT to +VCCP.
 - Page 31. Reserve and Stuff R650 and R651. For Fine-tune the value of headphone.

- 07/30 MODIFY LIST**
- Page 35. Disable NVVDD & +1.2V for INT VGA cost-down
 - Page 35. change R128 from 100K to 97.6Kohm, R127 from 34K to 100Kohm for power fine-tune VGA voltage
 - Page 42. Change trackpointer function from ALPS to SEMTECH
 - Page 12. Change EKT BLON resistor R446 form 10K to 1K for boot white screen
 - Page 04. Disable R596 for Battery only can't boot

C TEST CHANGE ITMES:

- 08/9 MODIFY LIST**
- Page 26. Modify serial port function for EZ4 (MRXD1 & MTXD1).
 - Page 25. Change USB Power on voltage from +5VSUS to 5VPCU
 - Page 25. Modify all USB power switch OC# pull-up 10k to +3VSUS
- 08/10 MODIFY LIST**
- Page 26. Add GND in C577 & C578 for FIR
 - Page 33. Remove LAN's reserve capacitors(C817/C513/C542/C546/C554/C555/C525/C523/C533/C530) by EMI engineer confirm

- 08/11 MODIFY LIST**
- Page 36. CPU CORE Power(PU11's pin25) add another CAP 0.022UF(C170) by power engineer request
 - Page 23. Add two new CAP 0.1UF(C873 & C874) for AVDD_LAN & +1.8V_1.2V_LAN by EMI request

- 08/12 MODIFY LIST**
- Page 23. Change R38 from 1.24k to 1.18k for LAN droop & template
 - Page 17. Change L59/L60/L61 from FBM-10-160808-470(47ohm) to BLM188A220(22ohm) for EA measure fail
 - Page 28. Modify Bluetooth led can't light for FOXCONN Bluetooth module (Add R652 & R653)
 - Page 31. Modify Line-in noise change R650 &R651 from 0ohm to 1kohm
 - Page 30. Modify Line-in noise add C859 &C861 (0.1UF)
 - Page 31. Add AND gate(U67) & R654(0 ohm) for popo sound (delete D29 & D30 & R560)
 - Page 31. Add MOSFET (Q48&Q49) for headphone popo sound, and reserve Q46/Q47 for speaker popo sound issue
 - Page 33. Change L33/L34/L36 from 22ohm BEAD to short

- 08/13 MODIFY LIST**
- Page 26. Change R449 from 1k ohm to 4.7k ohm for SIO

- 08/18 MODIFY LIST**
- Page 17. Change R399/R400/R401/R387/R388/R390 size from 0402 to 0603 for EMI request
 - Page 31. Add Q50 & R656(10K) for Normal close type audio jack issue (Reserve)
 - Page 32. Change Q13 from Transistor to MOS and R110 (47k to 10K) & C139(0.22U to 0.1U) and add R657 (OR) from EC (154pin) for Lite_ON_ODD issue
 - Page 33. Add C875/C876/C877/C878 (12pf) for Docking DVI EMI issue(Reserve)

- 08/19 MODIFY LIST**
- Page 21. Change 1394 connector(CN28) type form SMD to DIP for SMT request
 - Page 28. Update CN7's footprint to 88264-06XX-6P-R-ZE1 & CN8's footprint to 88264-10XX-10P-R-ZE1 for SMT request (Increase PAD length 0.2mm)
 - Page 25. Update CN5's footprint to SM088-SURS-8P-R-ZE1 for SMT request (Increase PAD length 0.2mm)
 - Page 41. Add new CAP(PC171/PC172) in BAT-V plane for EMI request
 - Page 23. Add new CAP(C879/C880) in AVDD_LAN plane for EMI request
 - Page 23. Add new CAP(C881/C882) in +3V plane for EMI request
 - Page 33. Change C1/C2 from TOP to BOT side for Mechanical request

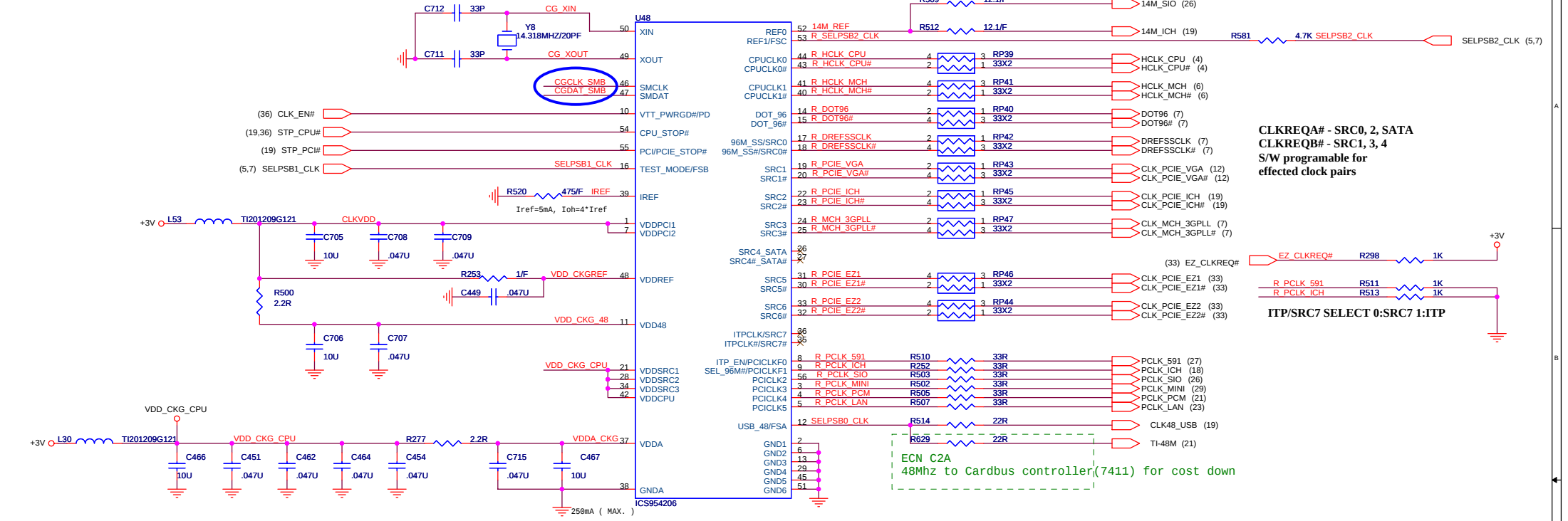
06/27 LAYOUT SWAP

- Pin JDM1.152 moved to net R_B_M042.
Pin JDM2.152 moved to net R_B_M046.
Pin JDM2.151 moved to net R_B_M054.
Pin JDM2.151 moved to net R_B_M048.
Pin JDM1.58 moved to net R_B_M053.
Pin JDM2.6 moved to net R_B_M052.
Pin JDM2.179 moved to net R_B_M041.
Pin JDM2.155 moved to net R_B_M061.
Pin JDM2.44 moved to net R_B_M041.
Pin JDM2.43 moved to net R_B_M021.
Pin JDM1.56 moved to net R_B_M048.
Pin JDM2.154 moved to net R_B_M042.
Pin JDM1.57 moved to net R_B_M022.
Pin JDM2.152 moved to net R_B_M051.
Pin JDM2.179 moved to net R_B_M049.
Pin JDM1.184 moved to net R_B_M059.
Pin JDM2.152 moved to net R_B_M058.
Pin JDM1.58 moved to net R_B_M059.
Pin JDM2.152 moved to net R_B_M051.
Pin JDM2.7 moved to net R_B_M02.
Pin JDM2.152 moved to net R_B_M057.
Pin JDM1.7 moved to net R_B_M043.
Pin JDM2.157 moved to net R_B_M043.
Pin JDM1.7 moved to net R_B_M05.
Pin JDM2.157 moved to net R_B_M057.
Pin JDM2.207 moved to net R_B_M051.
Pin JDM1.158 moved to net R_B_M048.
Pin JDM2.158 moved to net R_B_M052.
Pin JDM2.177 moved to net R_B_M058.
Pin JDM1.158 moved to net R_B_M052.
Pin JDM2.5 moved to net R_B_M031.
Pin JDM1.61 moved to net R_B_M059.
Pin JDM2.179 moved to net R_B_M047.
Pin JDM2.57 moved to net R_B_M022.
Pin JDM1.154 moved to net R_B_M043.
Pin JDM2.158 moved to net R_B_M058.
Pin JDM1.62 moved to net R_B_M054.
Pin JDM2.63 moved to net R_B_M052.
Pin JDM1.182 moved to net R_B_M057.
Pin JDM1.152 moved to net R_B_M046.
Pin JDM1.151 moved to net R_B_M047.
Pin JDM1.126 moved to net R_B_M052.
Pin JDM1.157 moved to net R_B_M052.
Pin JDM1.182 moved to net R_B_M058.
Pin JDM1.179 moved to net R_B_M059.
Pin JDM2.177 moved to net R_B_M059.
Pin JDM1.4 moved to net R_B_M059.
Pin JDM2.158 moved to net R_B_M057.
Pin JDM1.63 moved to net R_B_M028.
Pin JDM2.187 moved to net R_B_M051.
Pin JDM2.64 moved to net R_B_M025.
Pin JDM1.187 moved to net R_B_M059.
Pin JDM1.64 moved to net R_B_M025.
Pin RPL4 moved to net R_B_M047.
Pin RPL4 moved to net RM_C529.
Pin RPL2 moved to net M_C072.
Pin RPL2 moved to net R_B_M043.
Pin RPL2 moved to net R_B_SPA5A9.
Pin RPL2 moved to net R_B_BMW5A9.
Pin RPL2 moved to net M_C072.
Pin RPL2 moved to net R_B_M047.
Pin RPL2 moved to net R_B_M043.
Pin RPL2 moved to net R_B_M047.
Pin RPL2 moved to net CK63.
Pin RPL4 moved to net R_B_M049.
Pin RPL2 moved to net R_B_M047.
Pin RPL2 moved to net R_B_M047.
Pin RPL2 moved to net R_B_M047.
Pin RPL4 moved to net R_B_M049.
Pin RPL2 moved to net R_B_M047.
Pin RPL2 moved to net R_B_M047.
Pin RPL4 moved to net R_B_M049.
Pin JDM1.59 moved to net R_B_M022.
Pin JDM1.46 moved to net R_B_M059.
Pin JDM2.189 moved to net R_B_M059.
Pin JDM2.189 moved to net R_B_M019.
Pin JDM2.23 moved to net R_B_M012.
Pin JDM2.189 moved to net R_B_M047.
Pin JDM1.39 moved to net R_B_M044.
Pin JDM1.174 moved to net R_B_M059.
Pin JDM2.63 moved to net R_B_M059.
Pin JDM2.45 moved to net R_B_M049.
Pin JDM2.189 moved to net R_B_M059.
Pin JDM1.179 moved to net R_B_M041.
Pin JDM1.42 moved to net R_B_M021.
Pin JDM1.159 moved to net R_B_M052.
Pin JDM2.153 moved to net R_B_M047.
Pin JDM2.158 moved to net R_B_M058.
Pin JDM1.189 moved to net R_B_M059.
Pin JDM1.6 moved to net R_B_M041.
Pin JDM2.4 moved to net R_B_M02.
Pin JDM1.177 moved to net R_B_M054.
Pin JDM2.62 moved to net R_B_M028.
Pin JDM2.194 moved to net R_B_M050.
Pin JDM1.181 moved to net R_B_M049.
Pin JDM2.46 moved to net R_B_M047.
Pin JDM1.441 moved to net R_B_M052.
Pin JDM2.189 moved to net R_B_M059.
Pin JDM2.174 moved to net R_B_M052.
Pin JDM1.46 moved to net R_B_M047.
Pin JDM2.22 moved to net R_B_M058.
Pin JDM2.19 moved to net R_B_M07.
Pin JDM2.56 moved to net R_B_M022.
Pin JDM2.179 moved to net R_B_M059.
Pin JDM1.129 moved to net R_B_M057.



PROJECT : ZE1
Quanta Computer Inc.

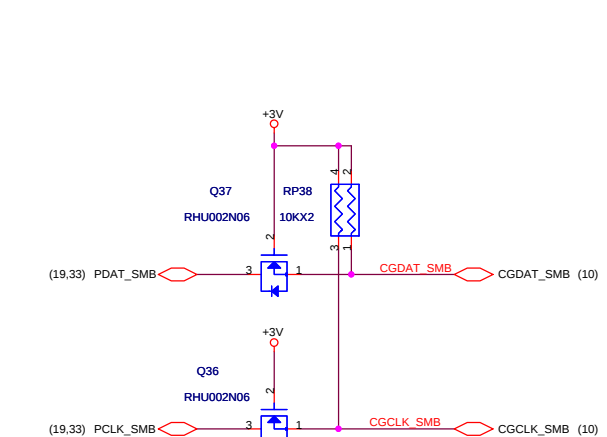
Clock gen



CLKREQA# - SRC0, 2, SATA
CLKREQB# - SRC1, 3, 4
S/W programmable for
effected clock pairs

EZ_CLKREQ# R298 1K
R_PCLK_591 R511 1K
R_PCLK_ICH R513 1K
ITP/SRC7 SELECT 0:SRC7 1:ITP

SM Bus



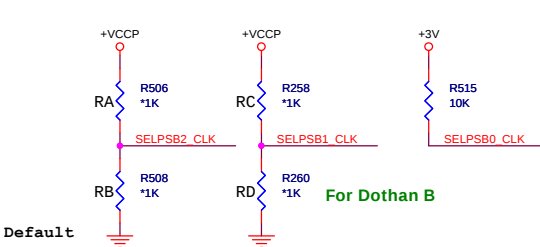
Speed setting

Resistor Stuff Table

	RA	RB	RC	RD
Dothan A 400	V	X	X	V
Dothan A 533	X	V	X	V
Dothan B	X	X	X	X

Clock Gen. Frequency Selection Table

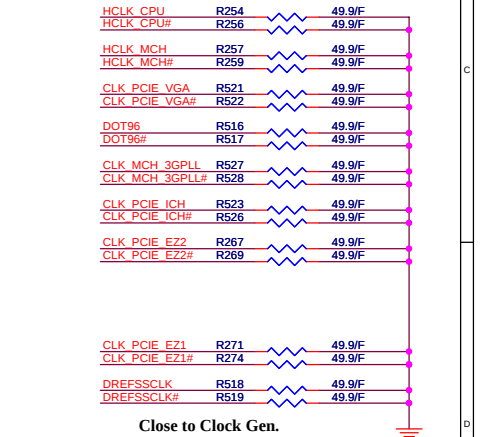
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33



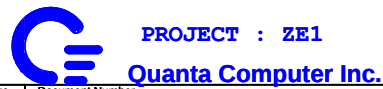
DOTHAN BSEL Output Value

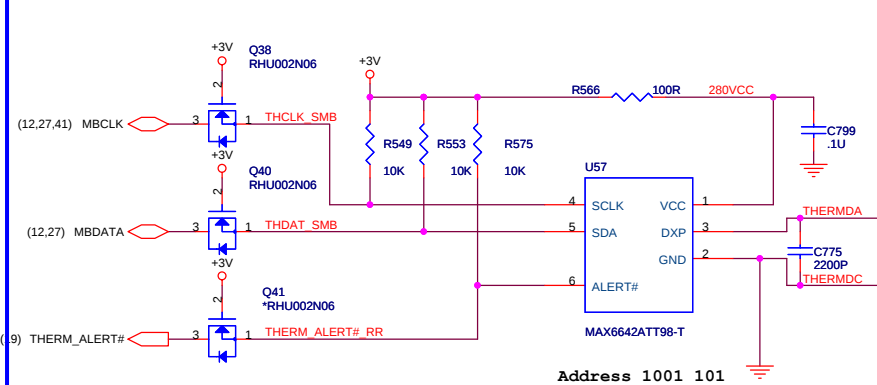
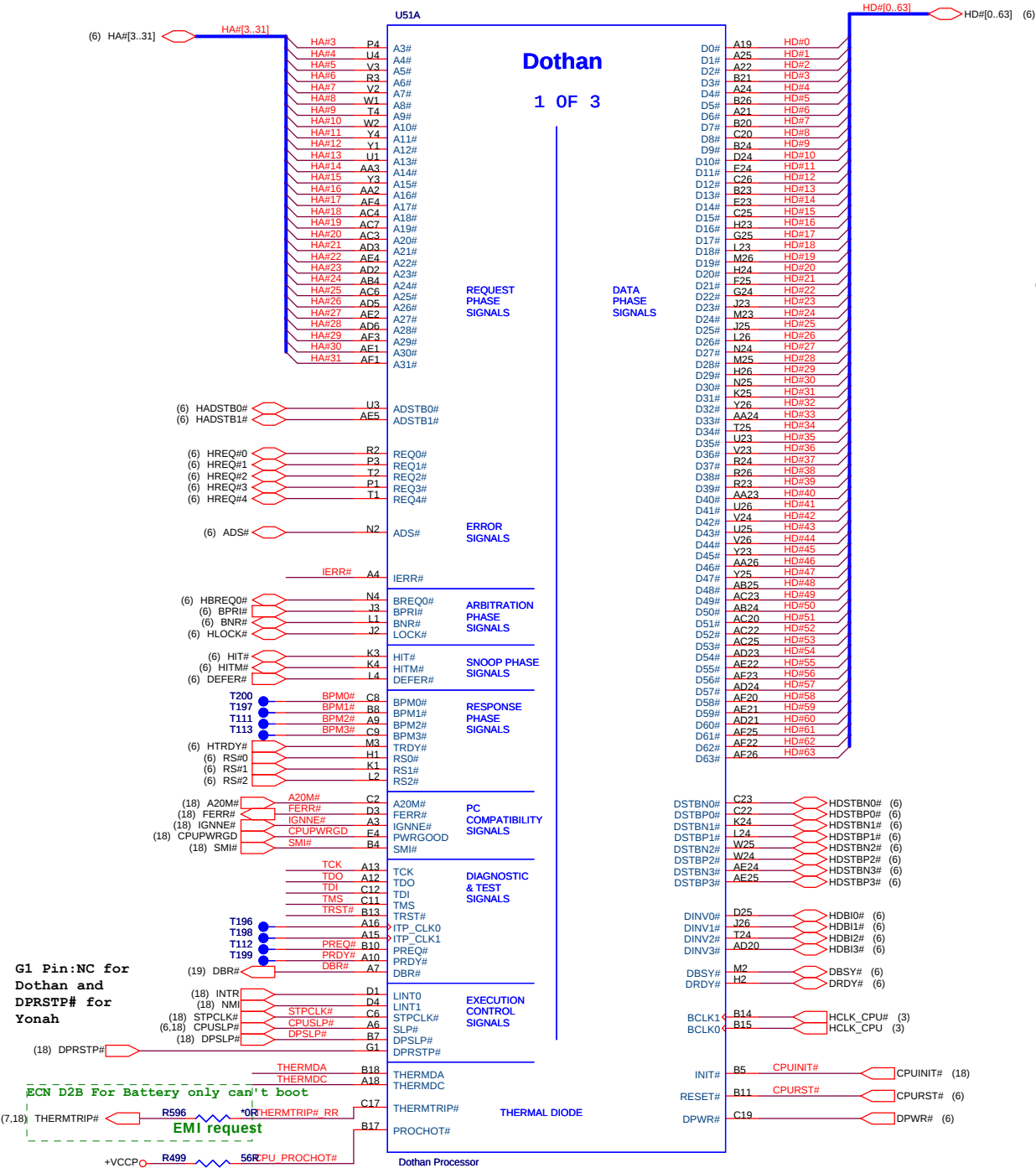
FSB Frequency	DOTHAN A-Step		DOTHAN B-Step	
	BSEL1	BSEL0	BSEL1	BSEL0
400 MHz	0	0	0	1
533 MHz	0	1	0	0

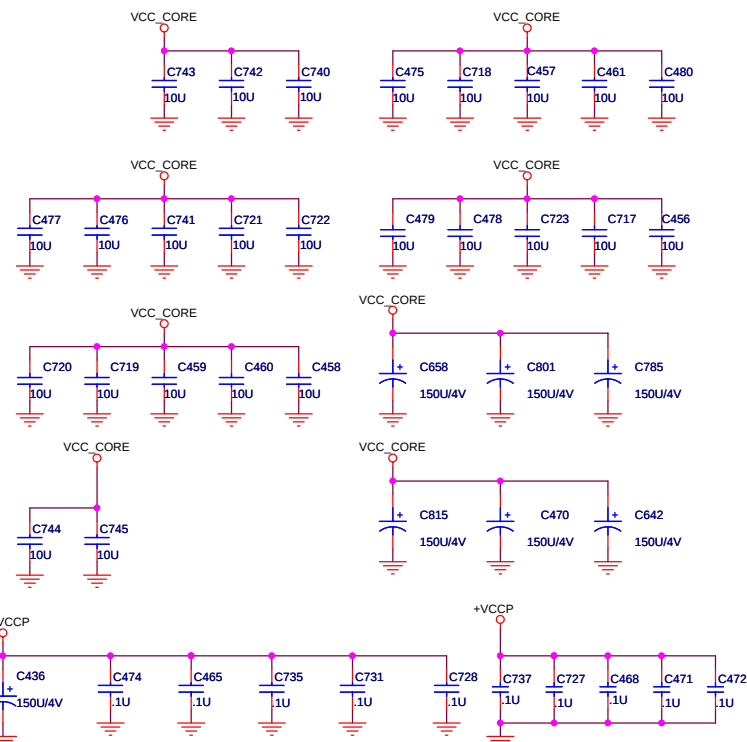
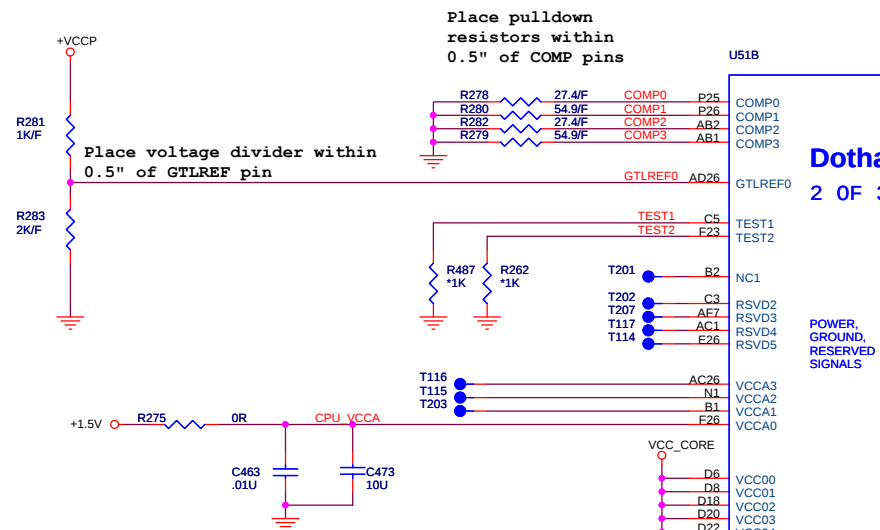
Clock terminator



Close to Clock Gen.







DOOTHAN-A NC
DOOTHAN-B POP

(36) CPU_VID0
(36) CPU_VID1
(36) CPU_VID2
(36) CPU_VID3
(36) CPU_VID4
(36) CPU_VID5

(37) SELPSB2_CLK
(37) SELPSB1_CLK

SELPSB2_CLK R498
SELPSB1_CLK R497

(36) PSI#
ECN C2A
Reserve for Power saving

Dothan
3 OF 3

POWER, GROUND AND NC

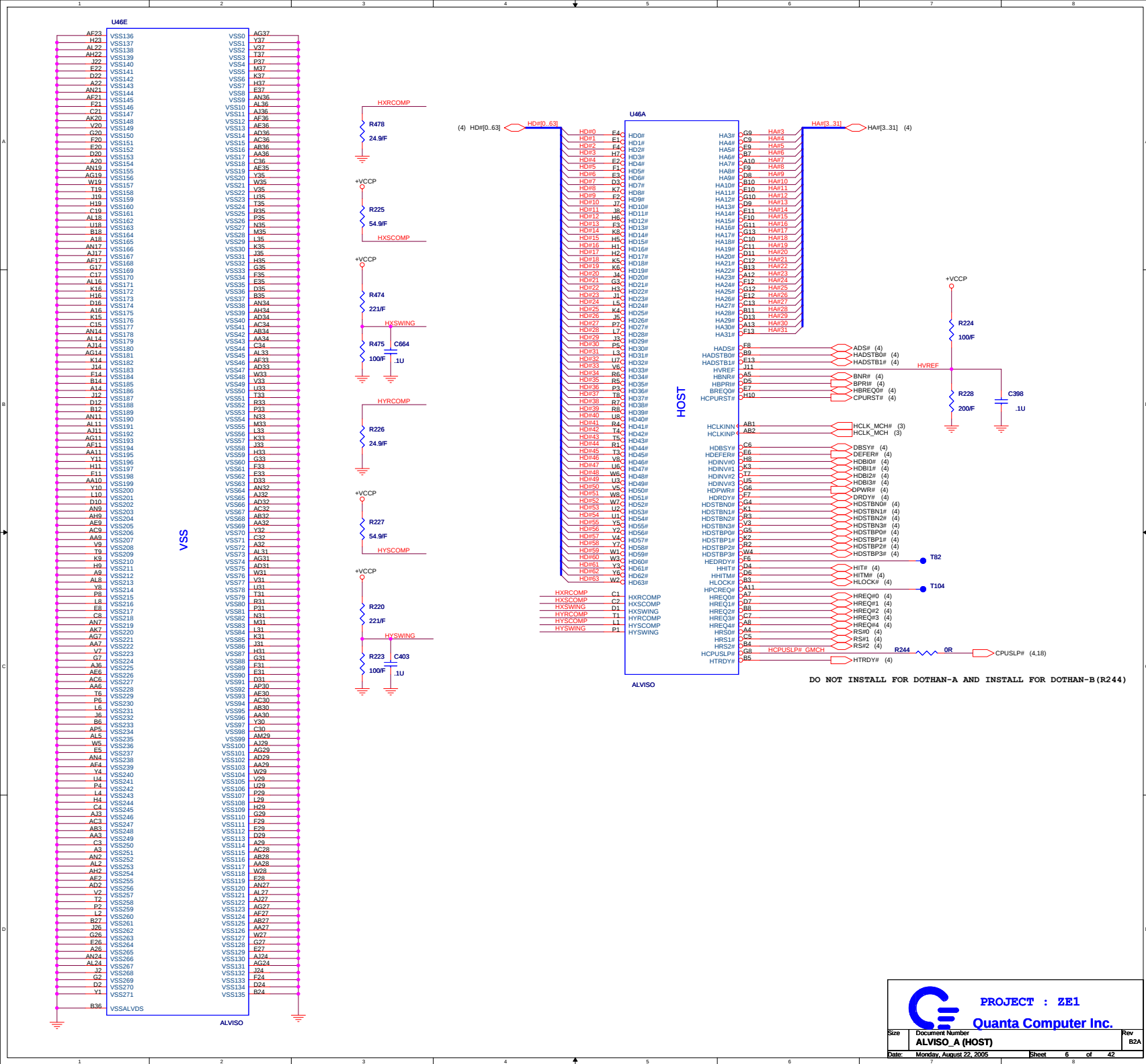
VID

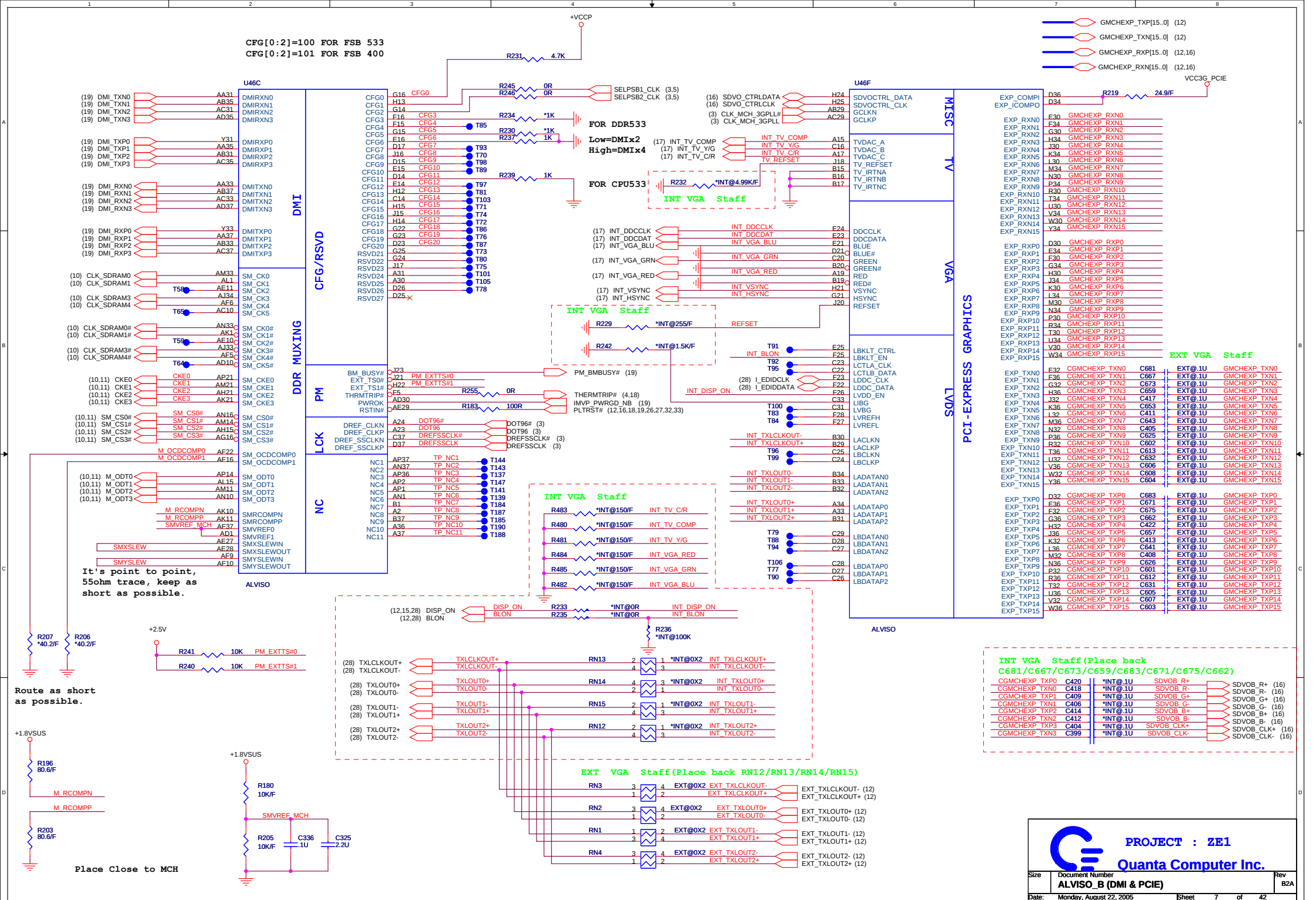
Dothan Processor

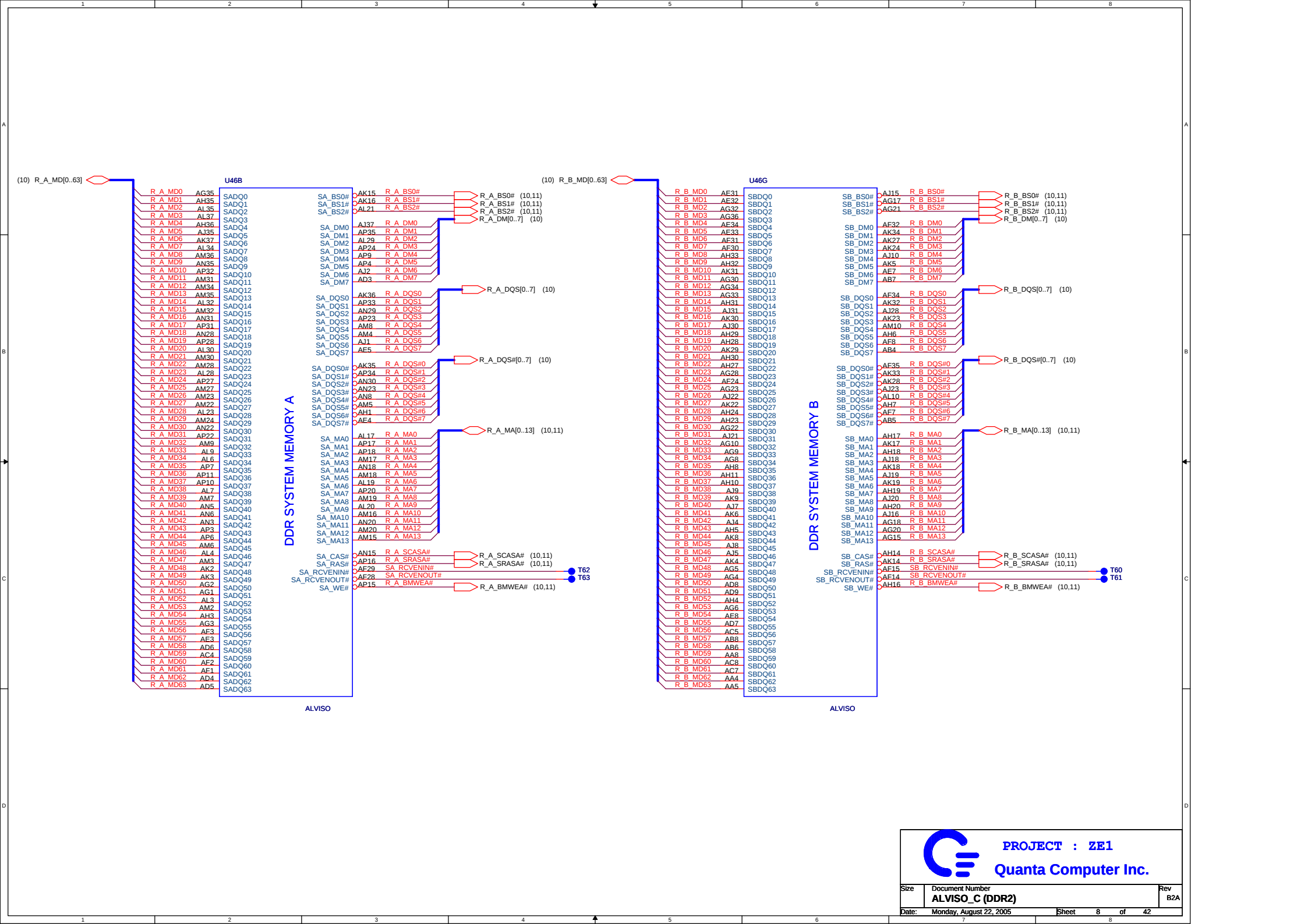
Dothan Processor



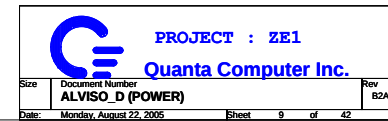
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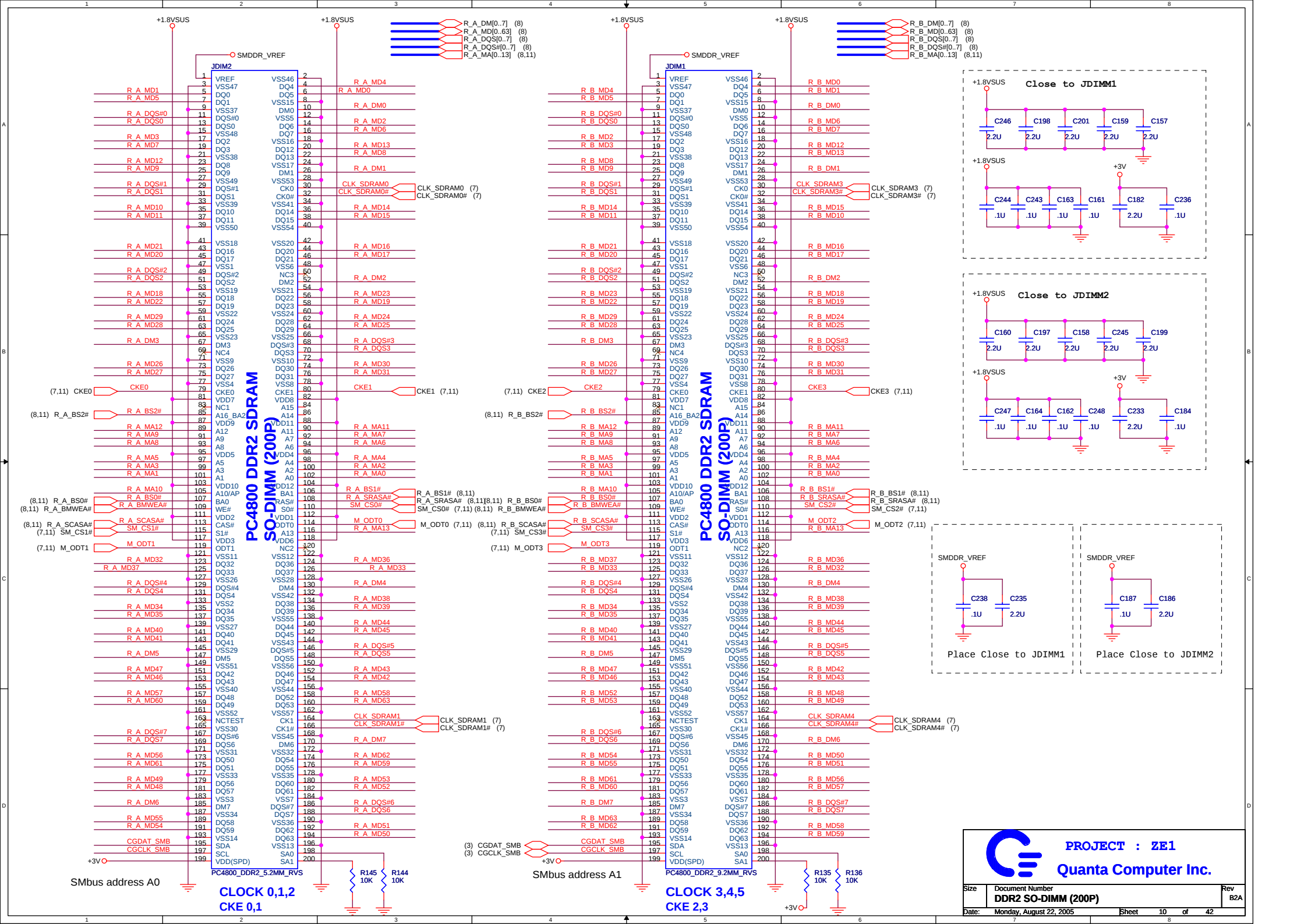


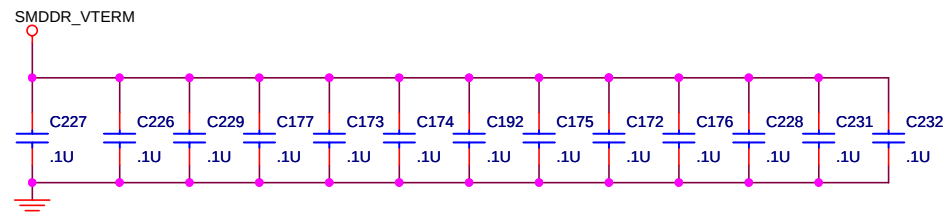




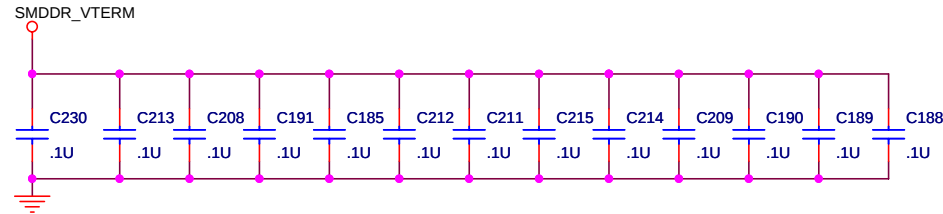
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Quanta Computer Inc.







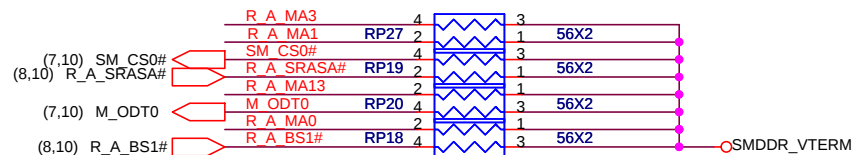
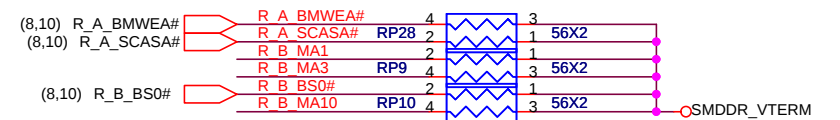
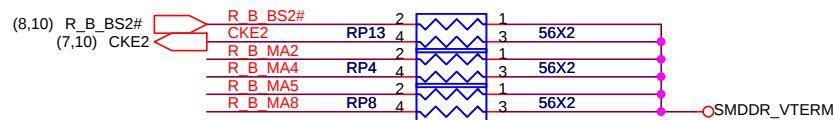
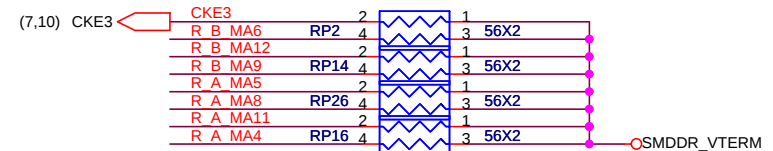
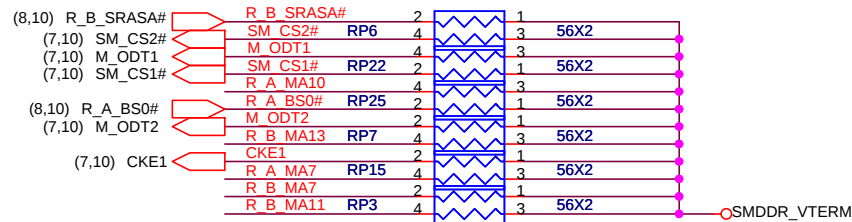
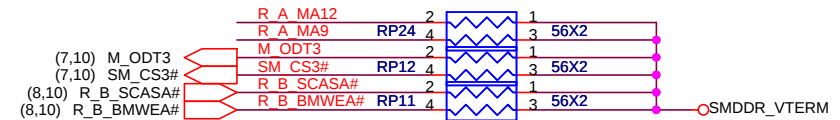
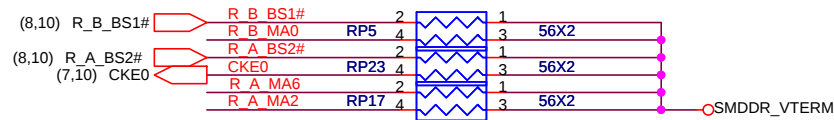
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM



Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM

R_A_MA[0..13] (8,10)

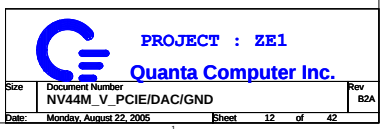
R_B_MA[0..13] (8,10)

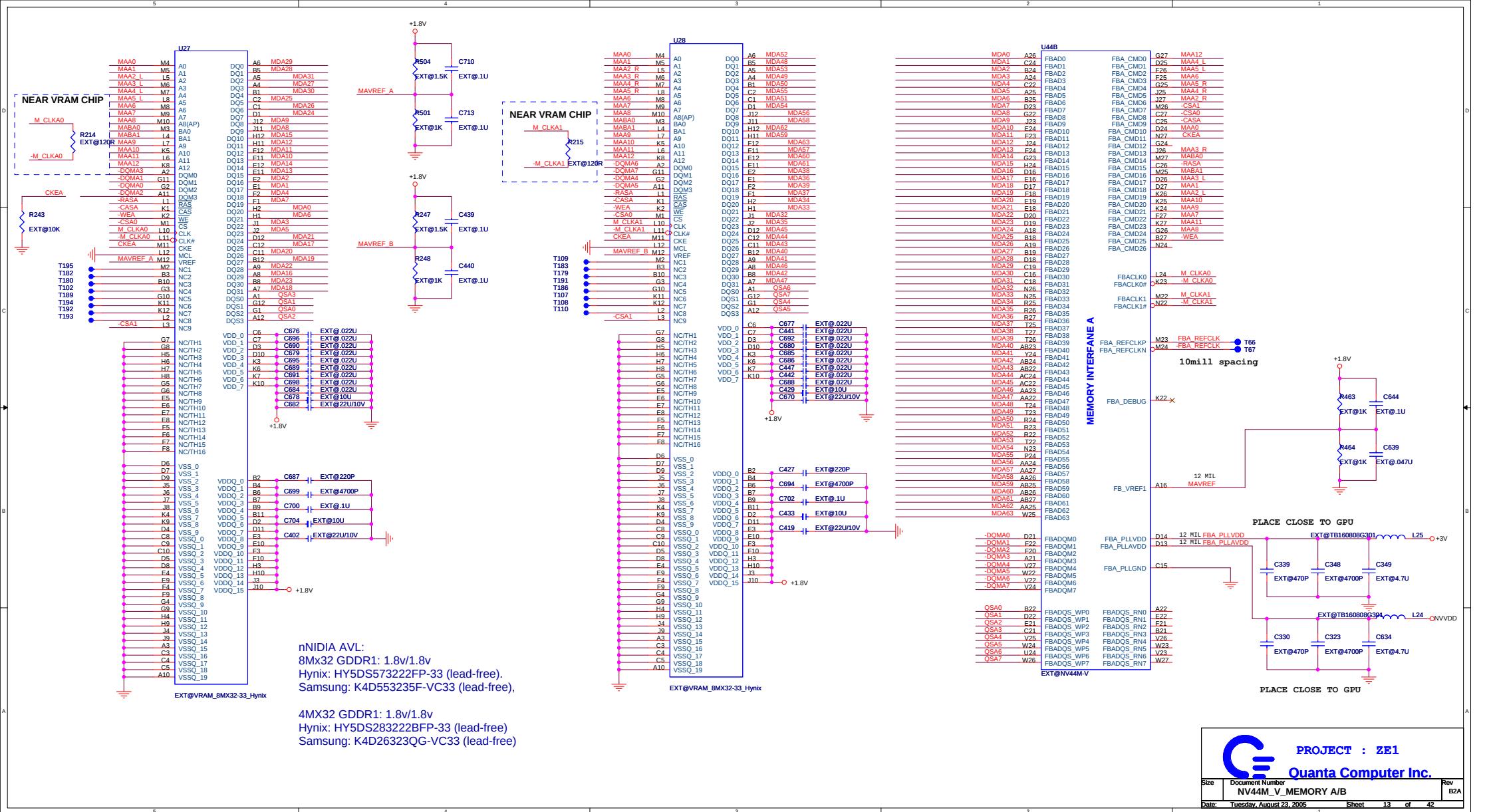


PROJECT : ZE1

Quanta Computer Inc.

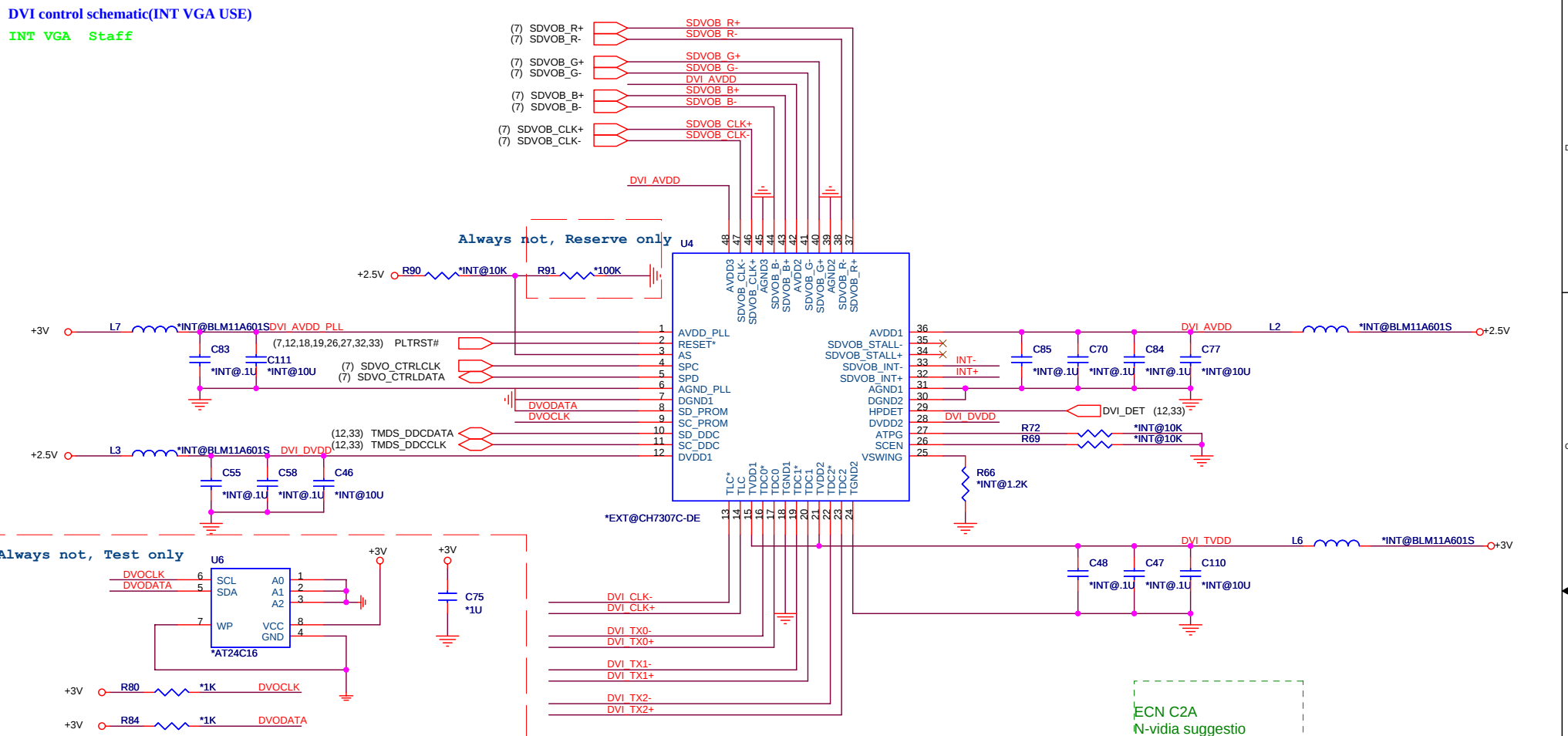
Size	Document Number	Rev
	DDR2 RES. ARRAY	B2A
Date:	Monday, August 22, 2005	Sheet 11 of 42



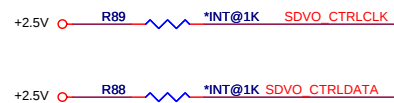
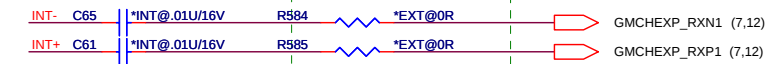
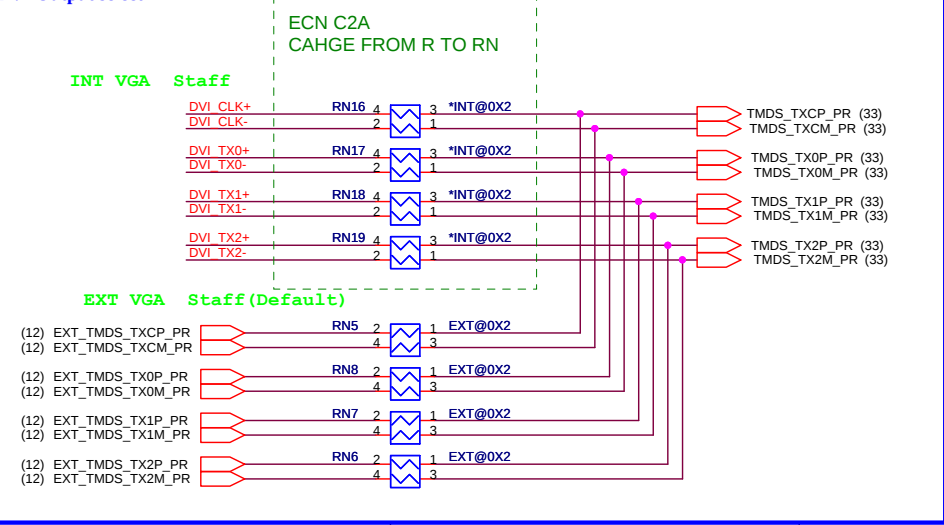


DVI control schematic(INT VGA USE)

INT VGA Staff



DVI Output select

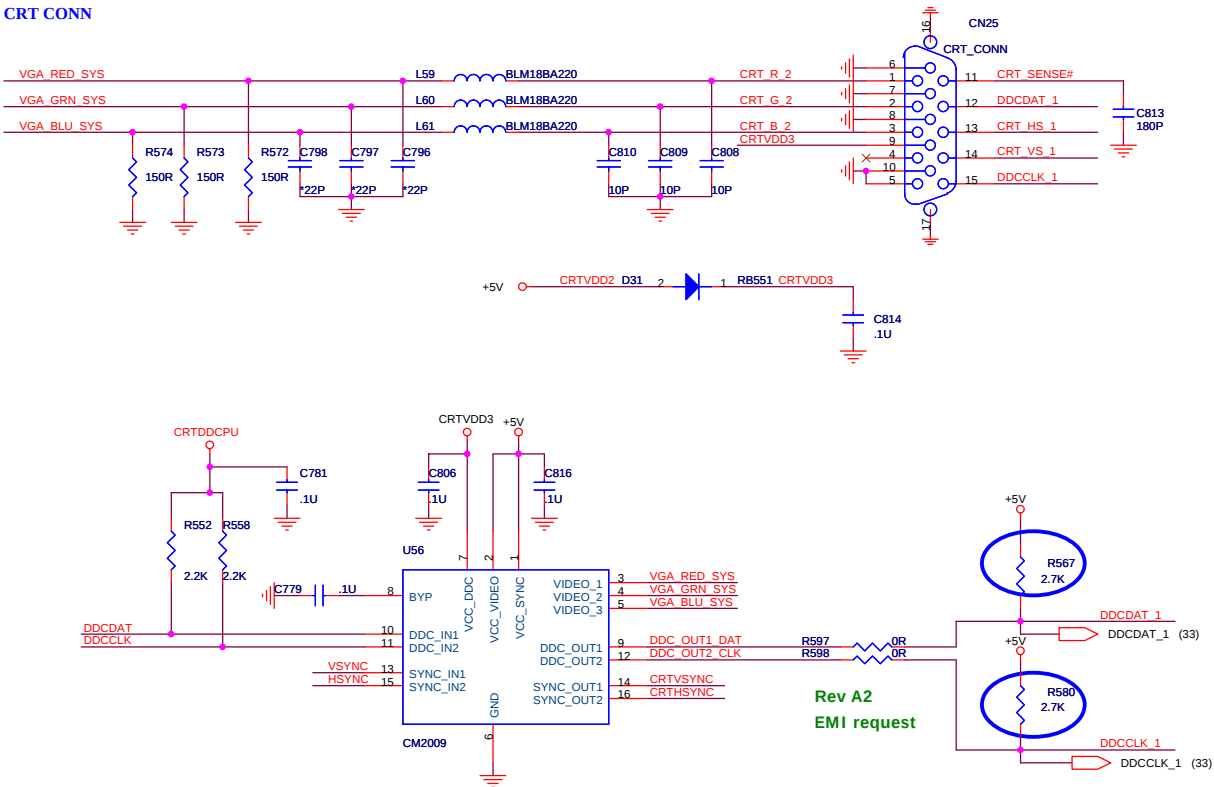


PULL Low For DVO Not Present (Internal PULL LOW In 915GM)
+2.5V 250mA
+3V 190mA

PROJECT : ZE1
Quanta Computer Inc.

Size	Document Number	Rev
	INT. DVI	B2A
Date:	Monday, August 22, 2005	Sheet 16 of 42

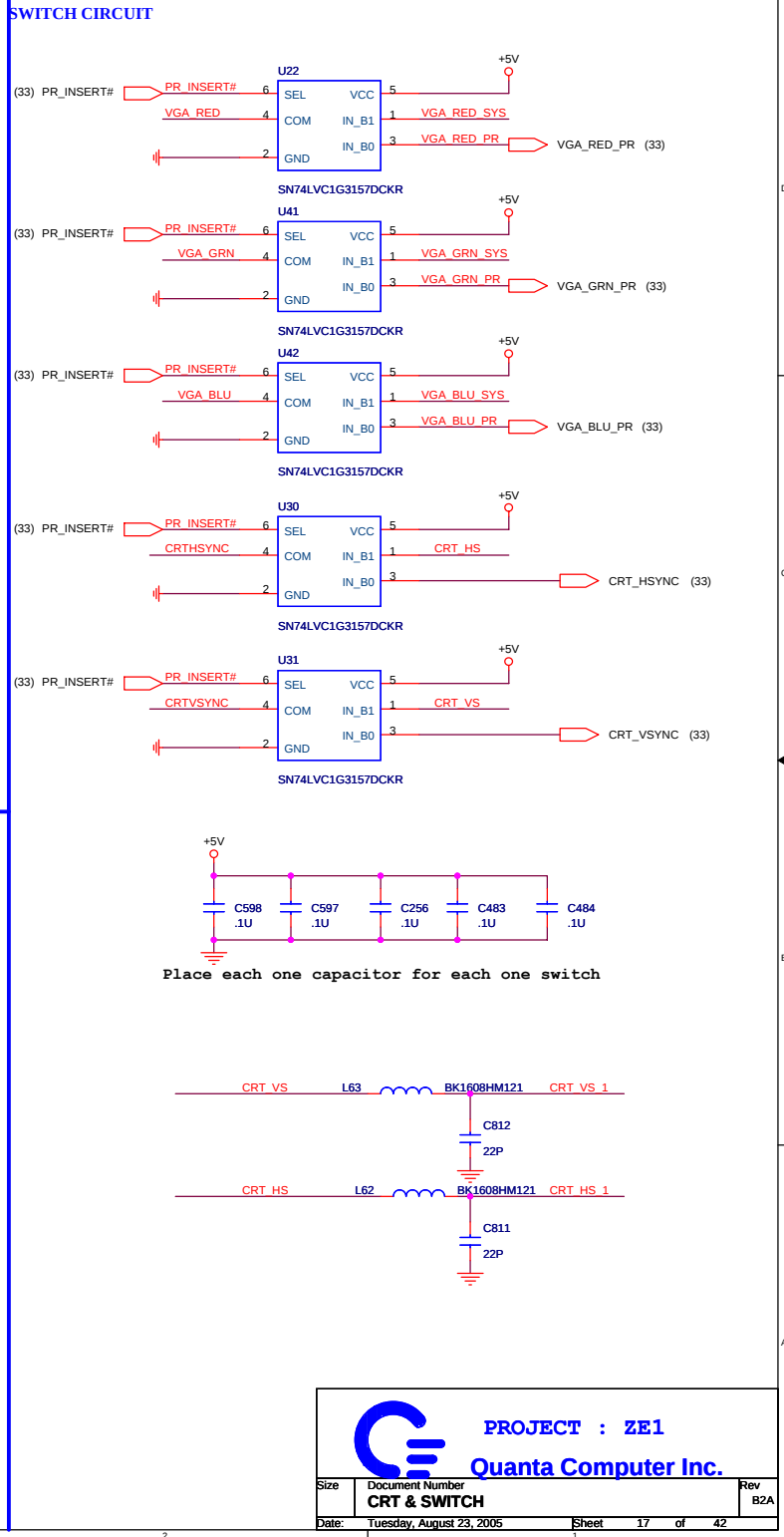
CRT CONN

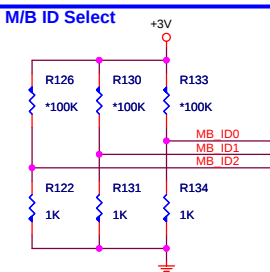
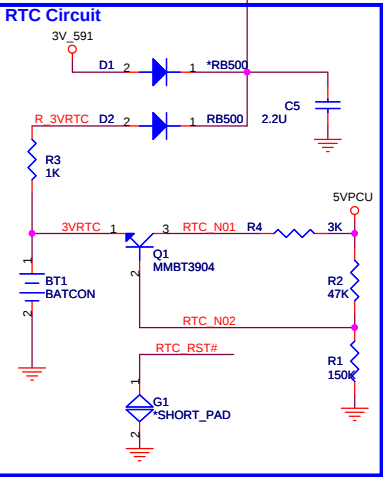


CRT Select

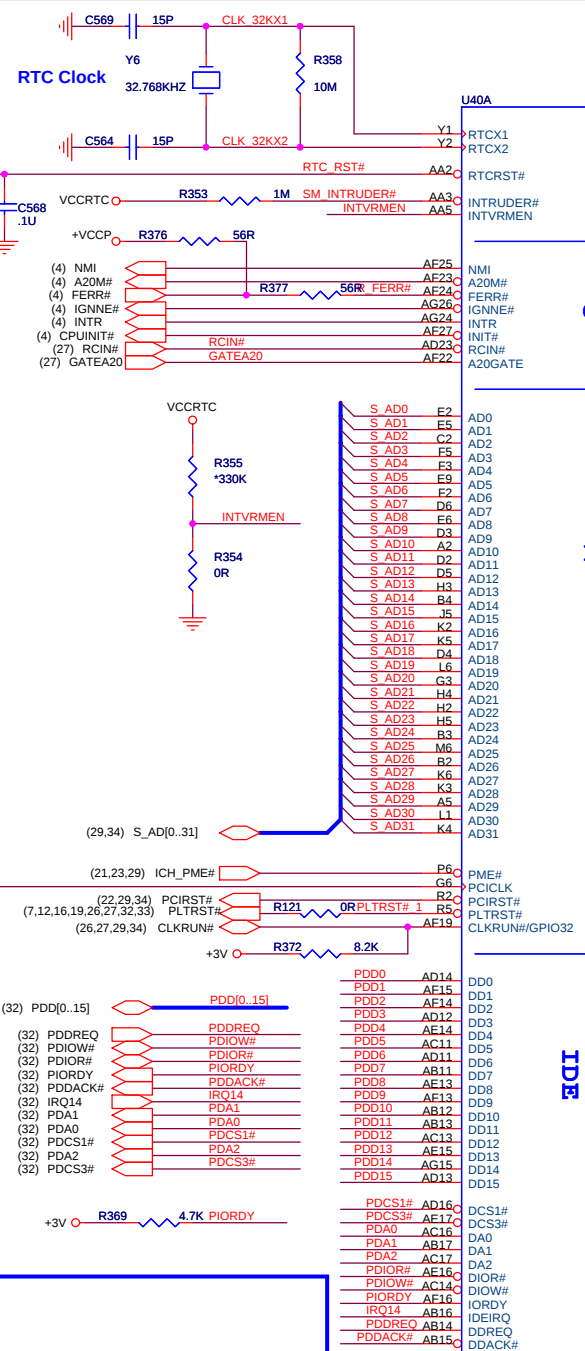


SWITCH CIRCUIT





MB_ID2	MB_ID1	MB_ID0	MB TYPE
0	0	0	TYPE 1
0	0	1	TYPE 2
0	1	0	TYPE 3
0	1	1	TYPE 4
1	0	0	TYPE 5



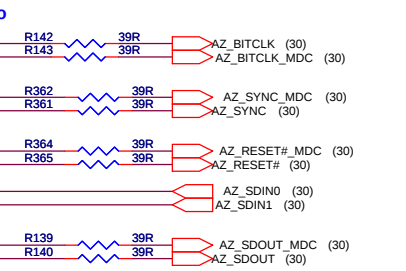
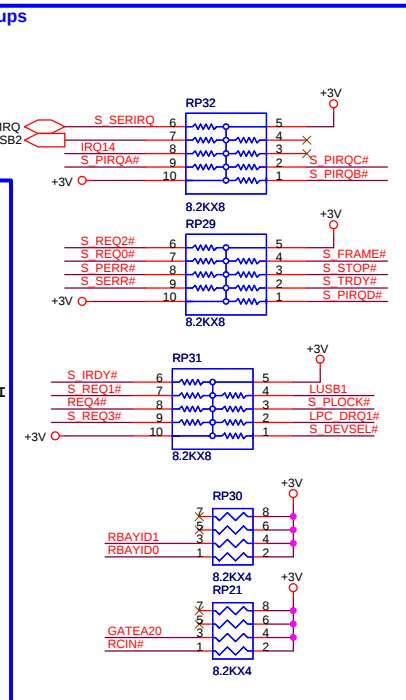
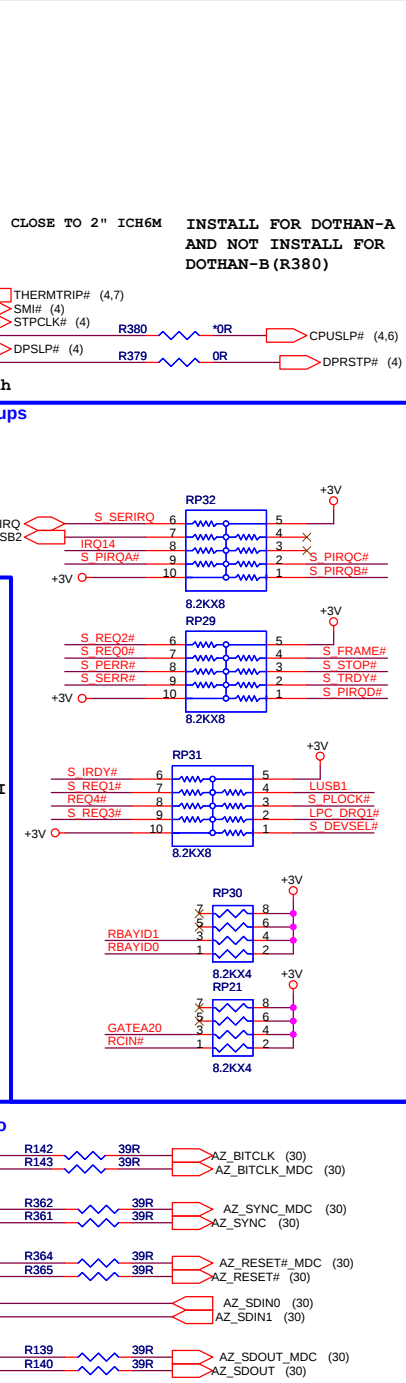
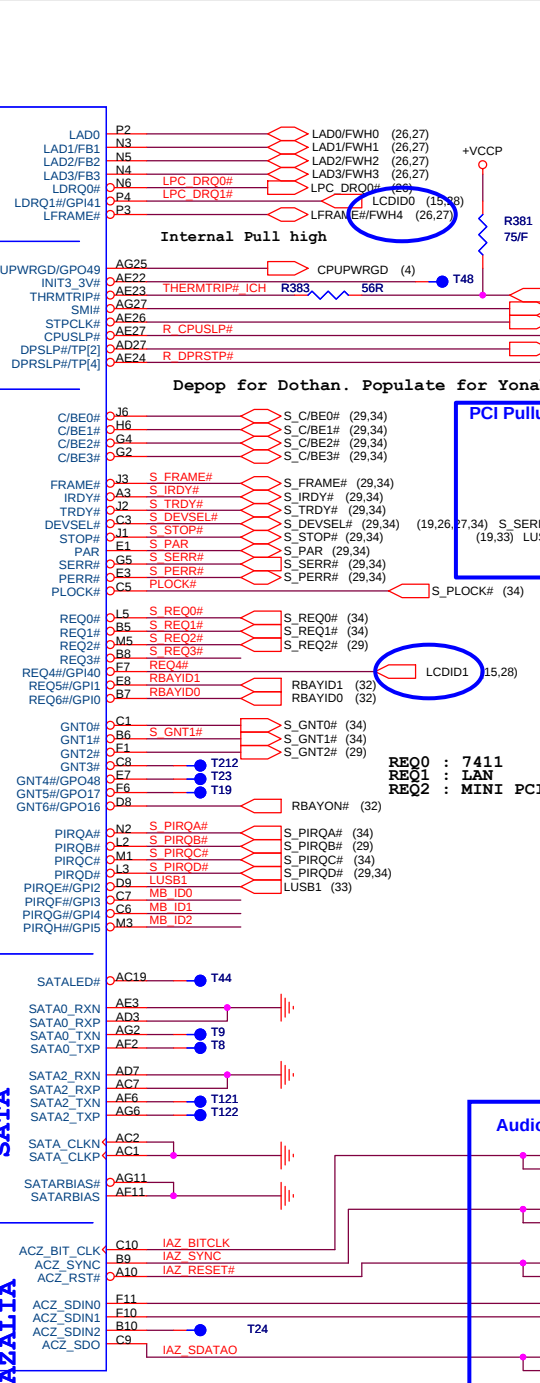
CPU

PCI

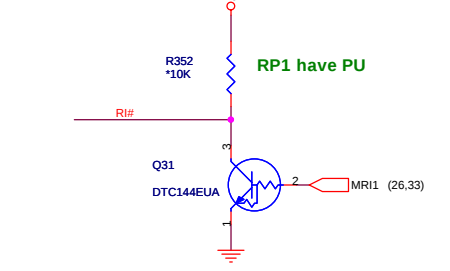
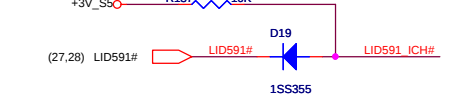
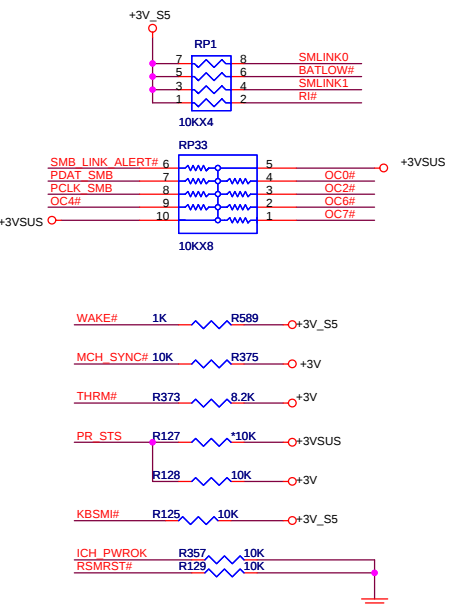
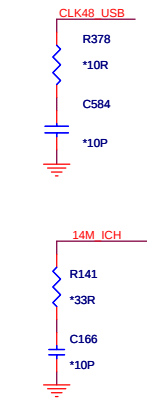
IDE

SATA

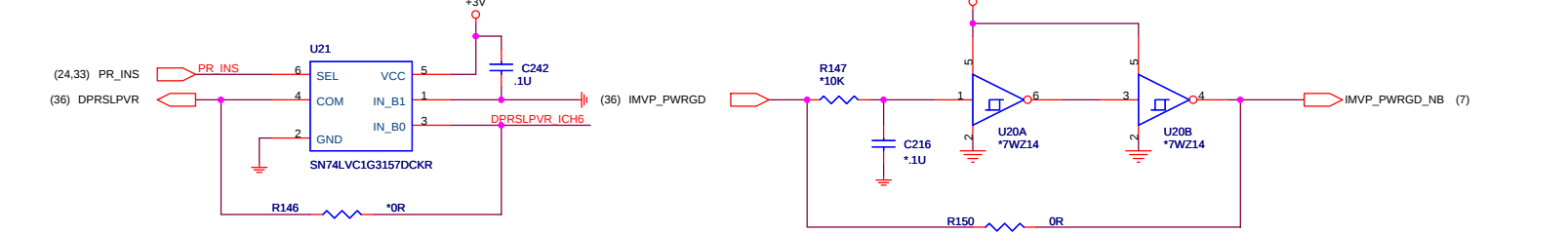
AC-97/AZALIA



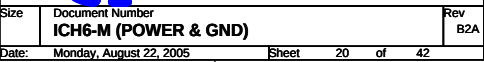
PROJECT : ZE1
Quanta Computer Inc.



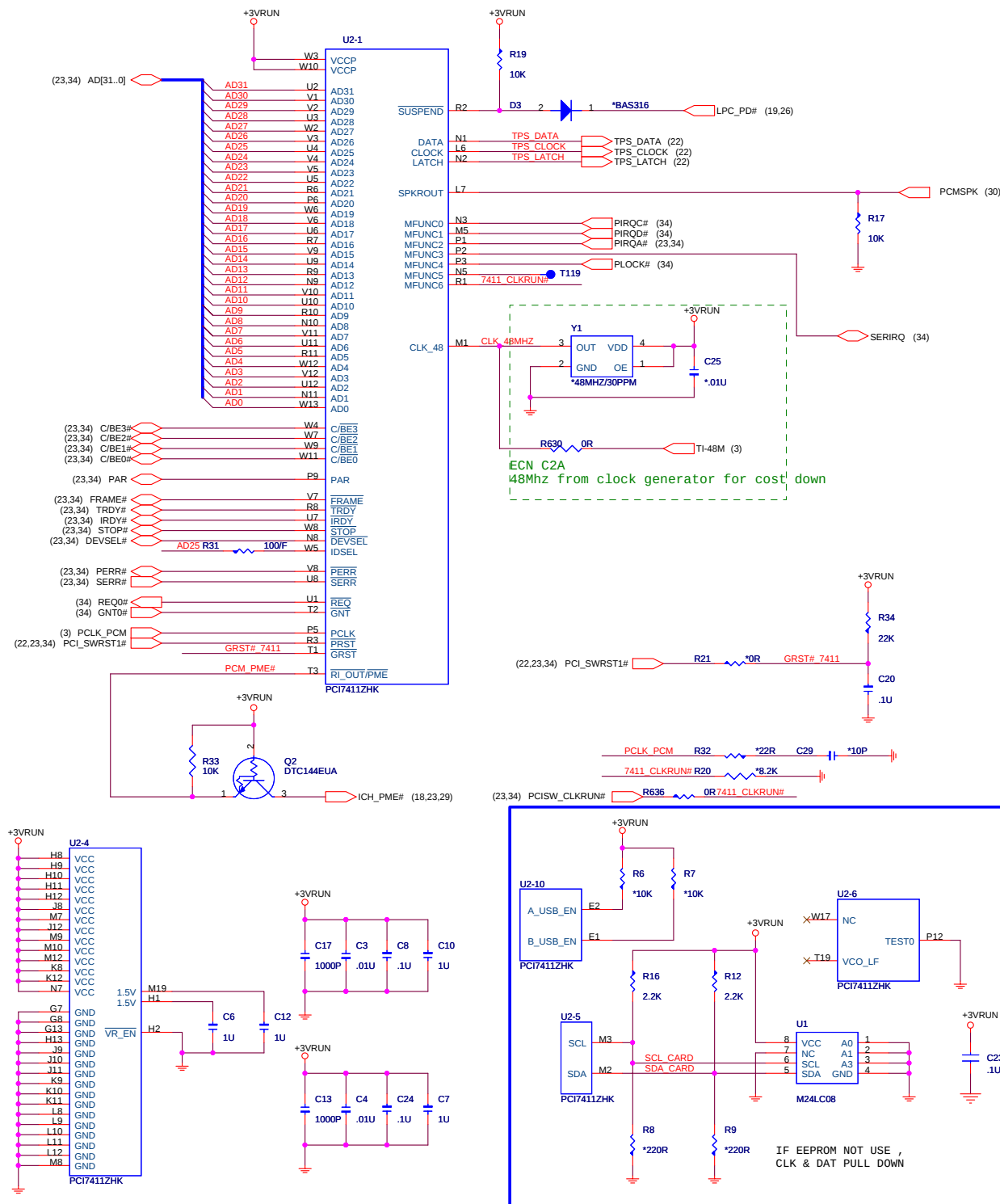
Power good to SB and NB/Deeper Sleep Voltage control for C4



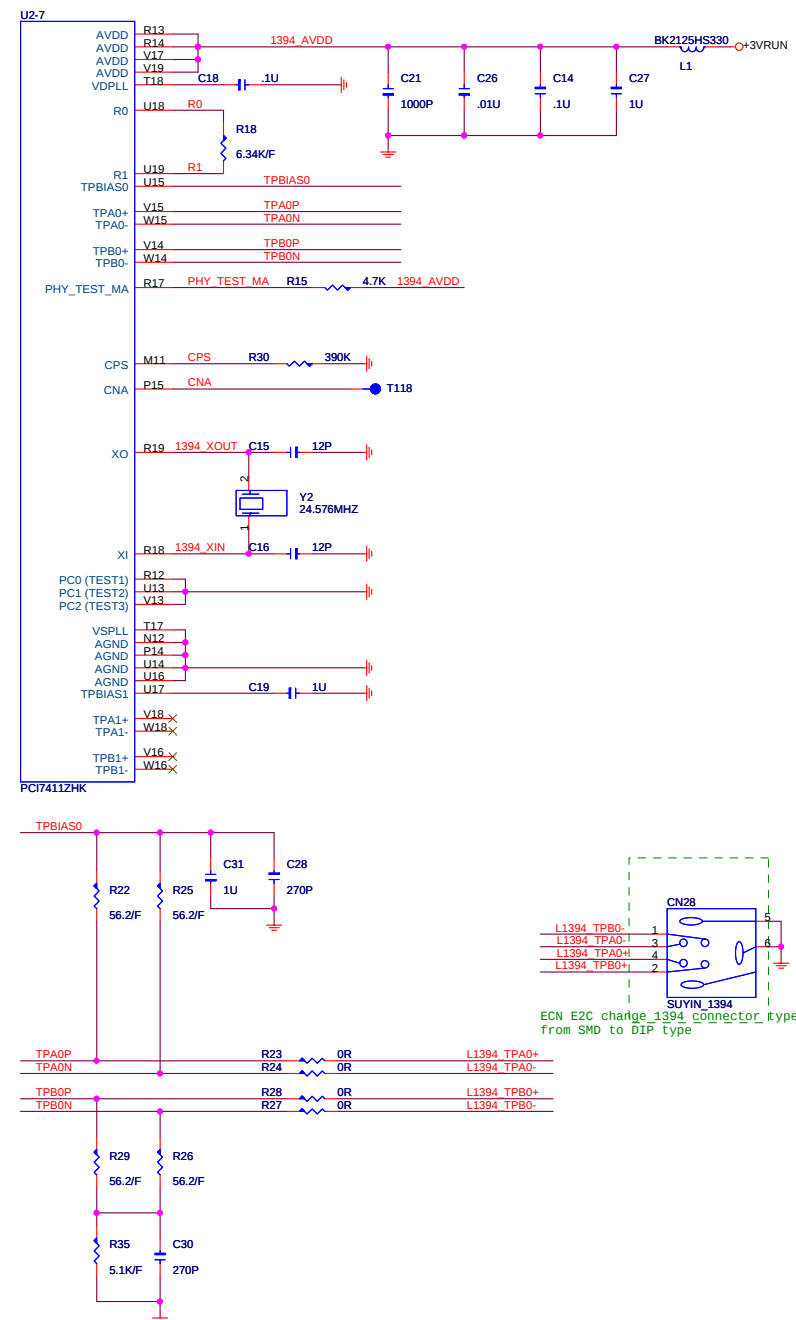
PROJECT : ZE1
Quanta Computer Inc.



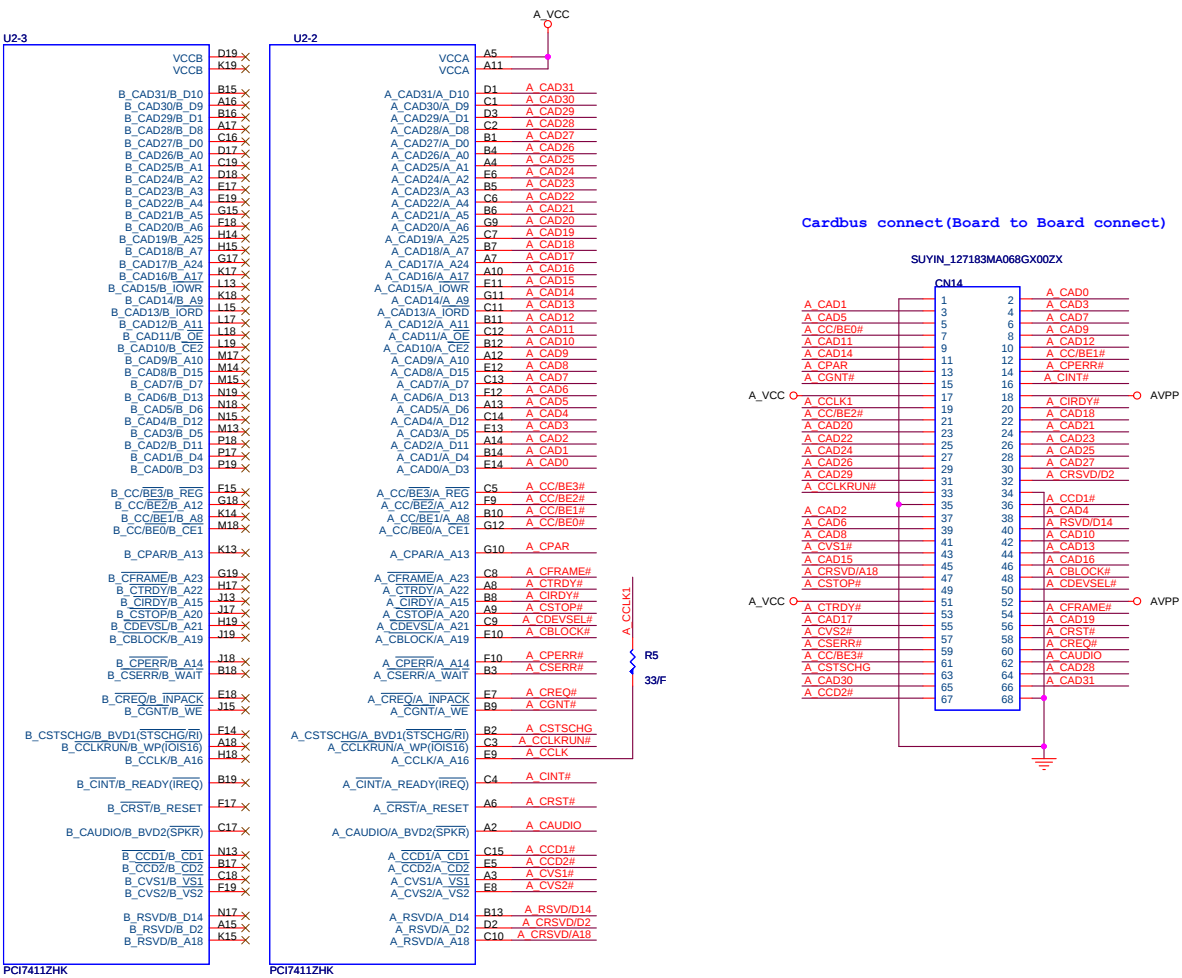
7411 PCI Interface



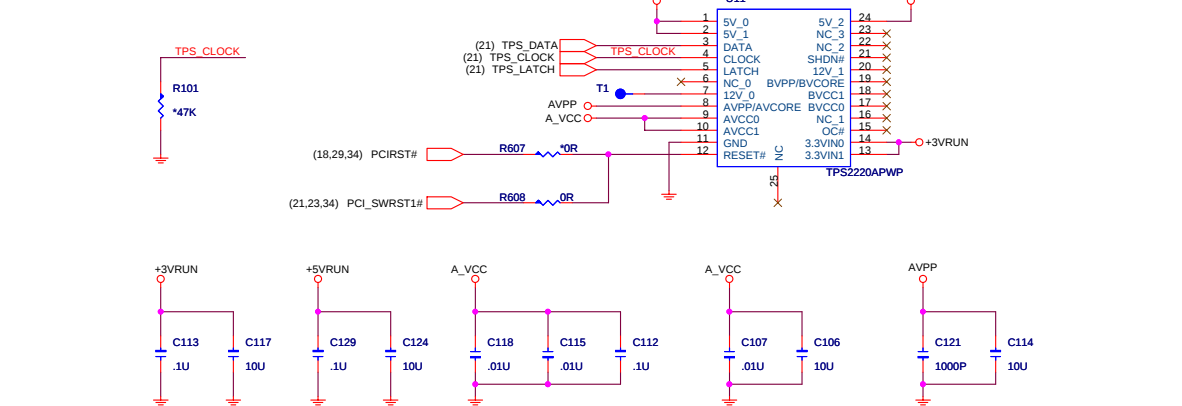
1394 Interface (CONN/EEPROM)



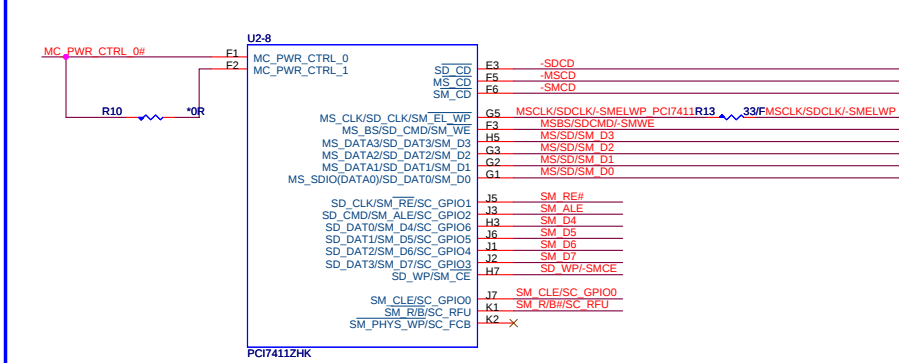
Cardbus Interface



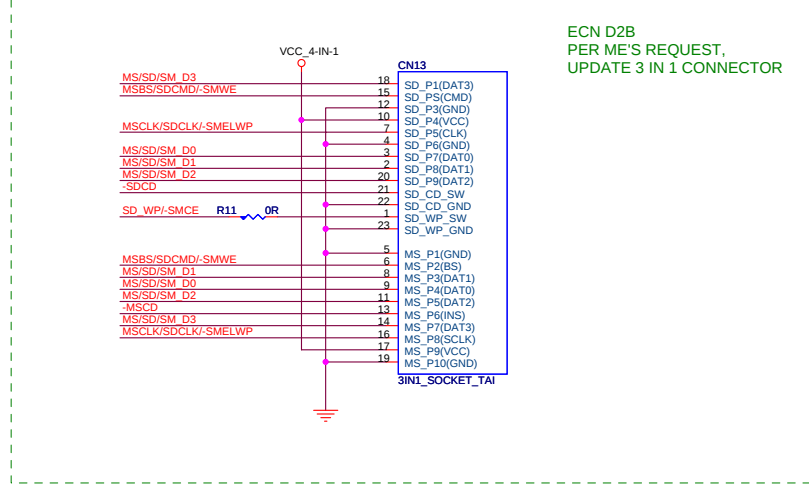
Cardbus Power



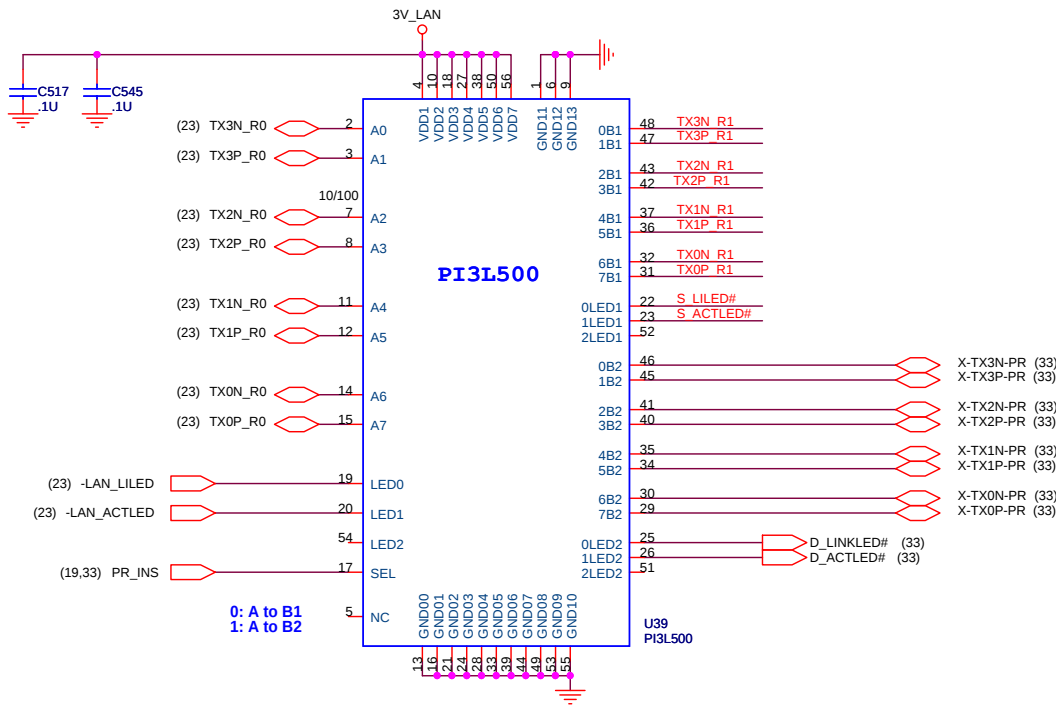
Card Reader Interface



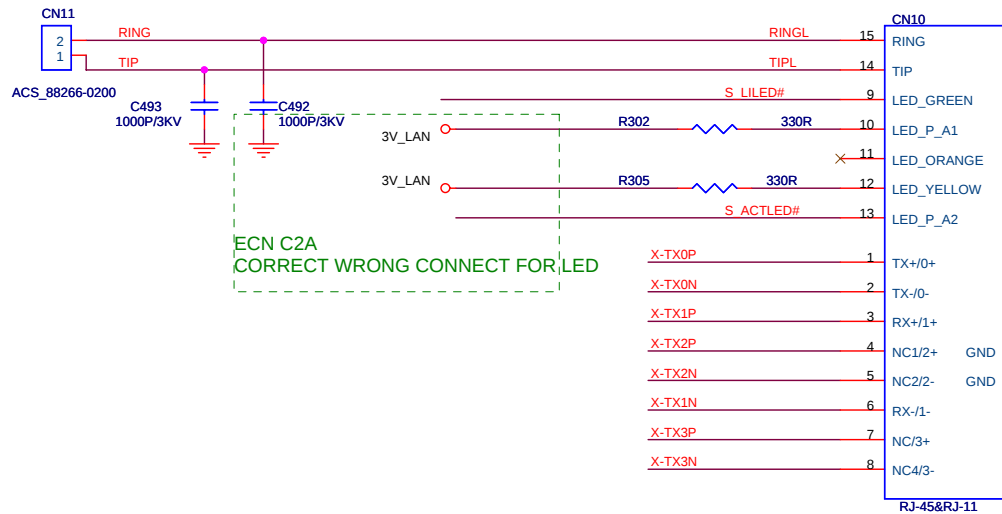
3 IN 1 CARD READER(SD/MMC/MS/MS Pro)



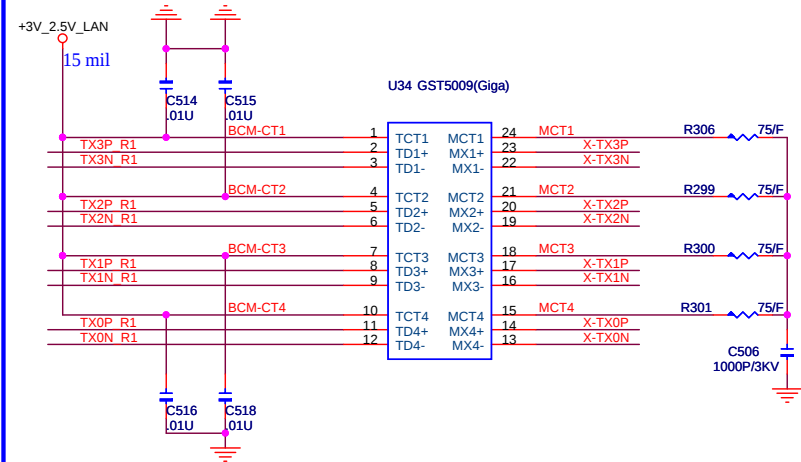
LAN Switch



Modem conn and LAN connect

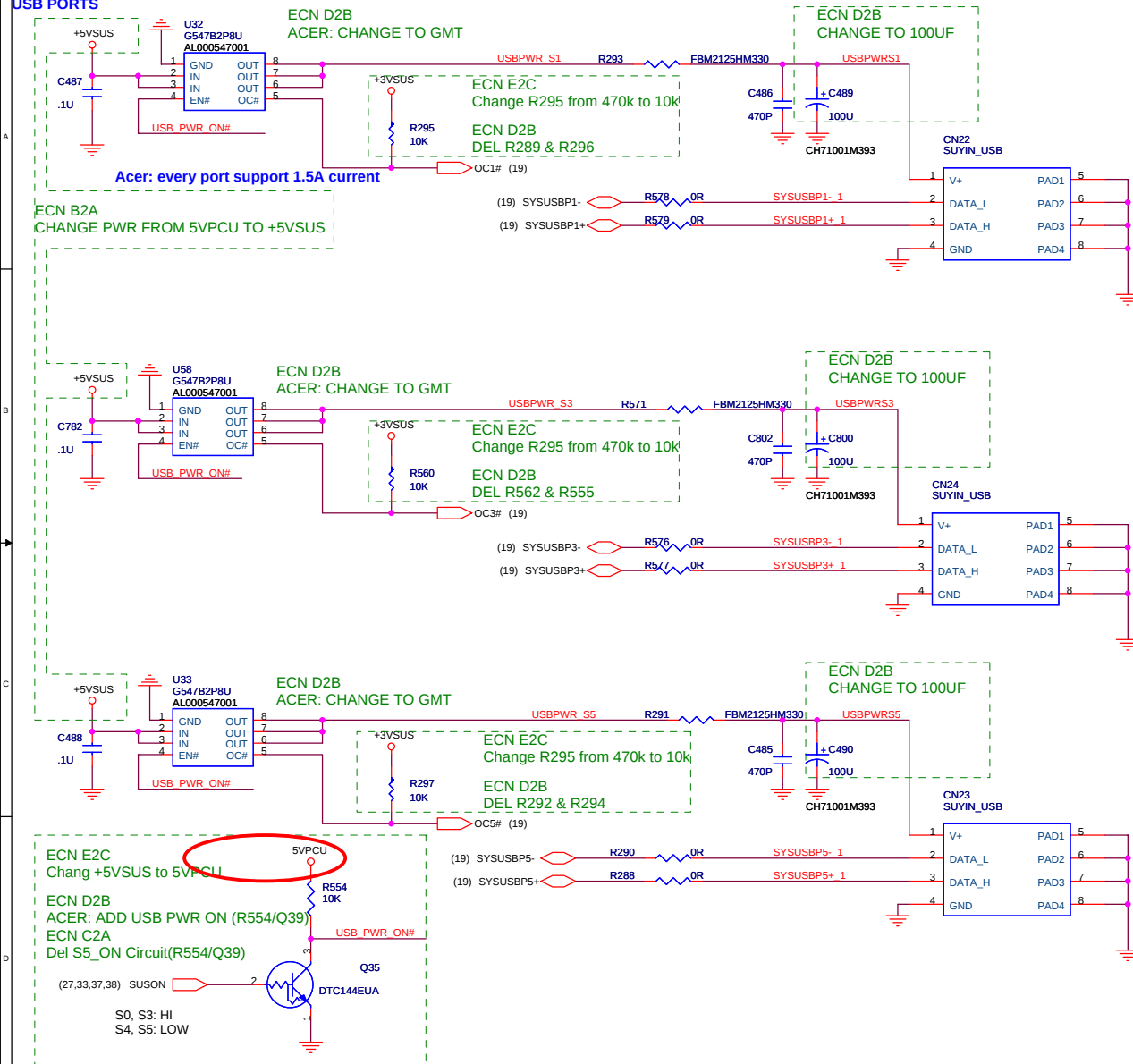


LAN transformer

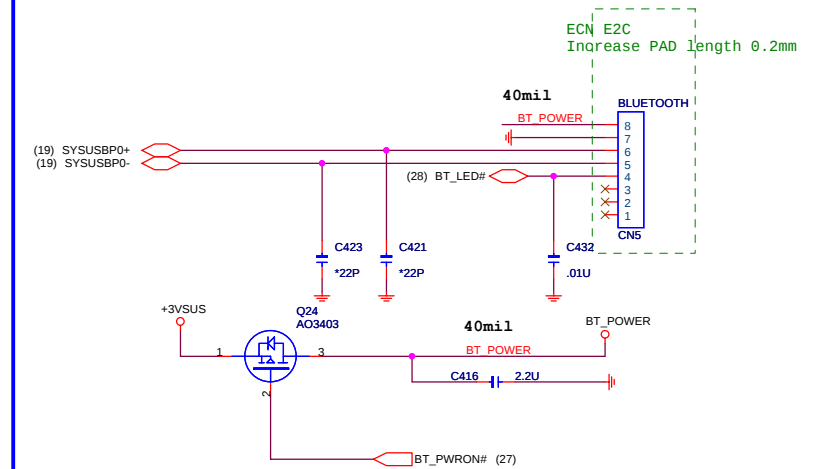


PROJECT : ZE1
Quanta Computer Inc.

USB PORTS

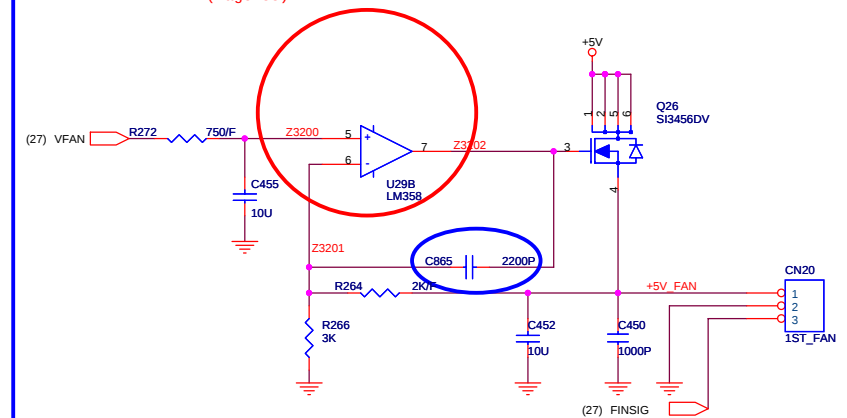


Bluetooth



FAN

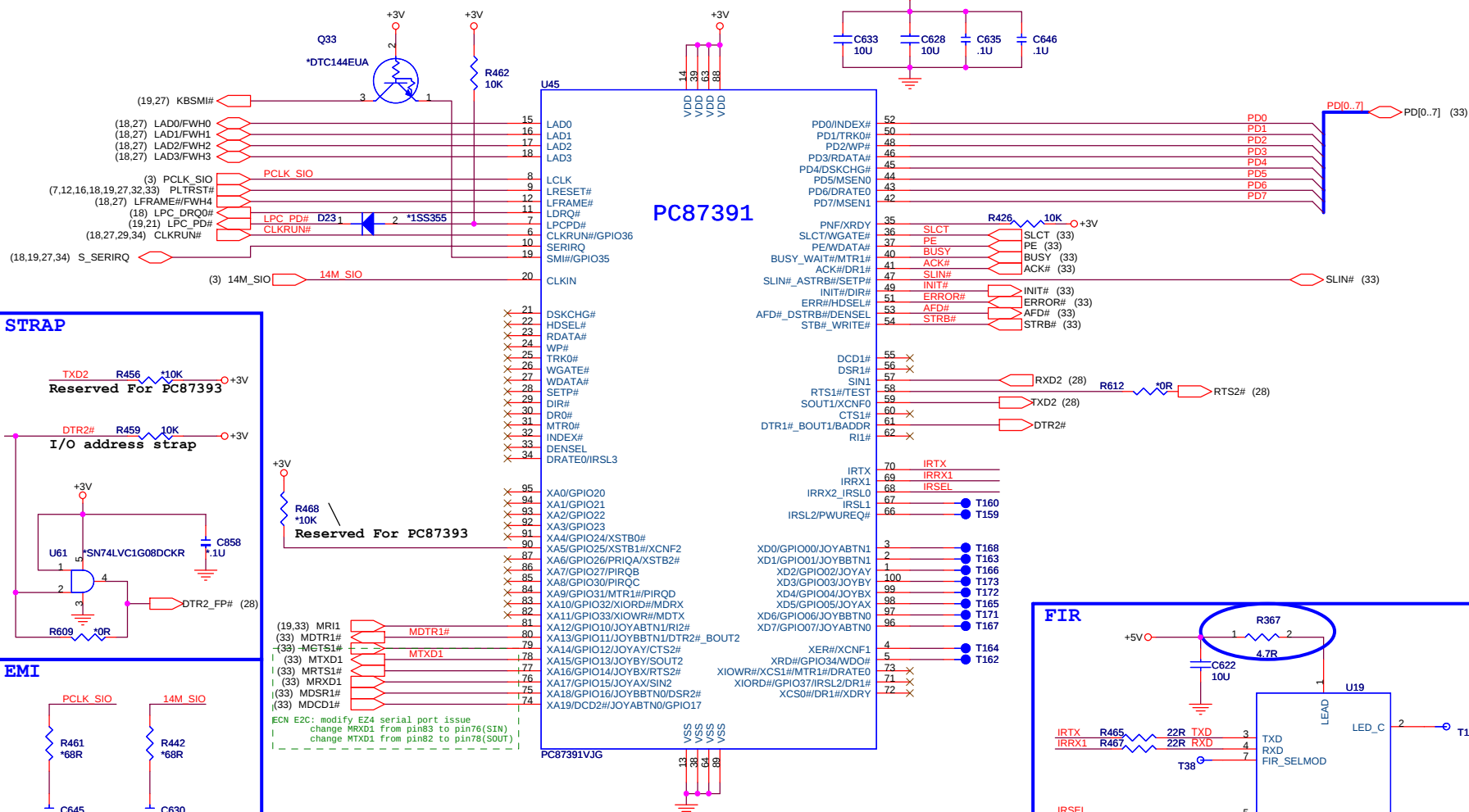
U29 use together with Transfer 2.5V circuit
(Page 38)



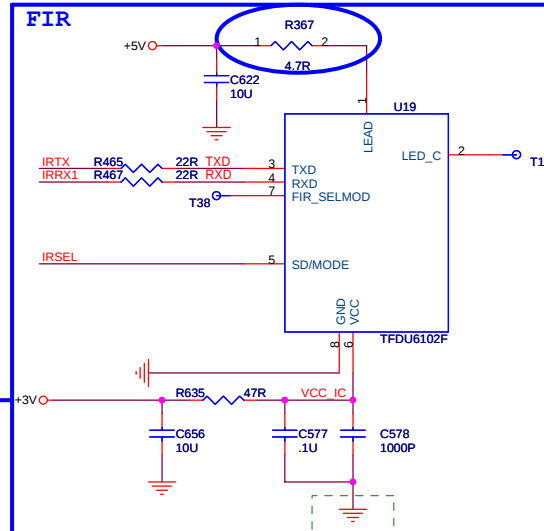
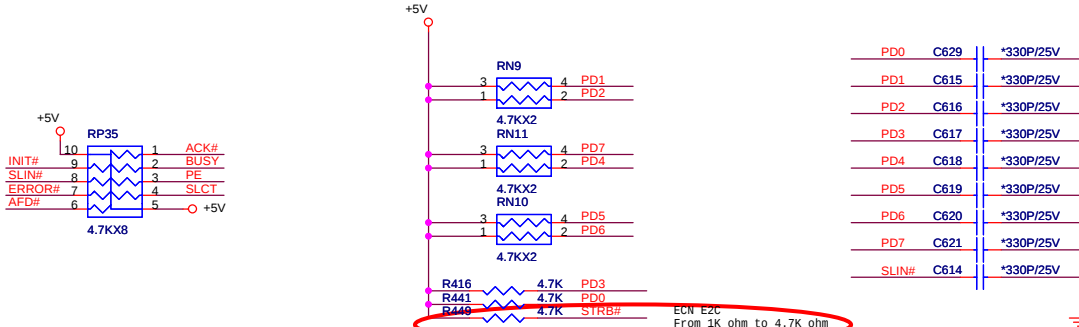
PROJECT : ZE1
Quanta Computer Inc.

Size	Document Number	Rev
	USB PORTS & BLUETOOTH & FAN	D2B
Date:	Monday, August 22, 2005	Sheet 25 of 42

SIO (LPT/Serial port/Digitizer/FIR)



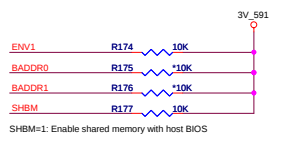
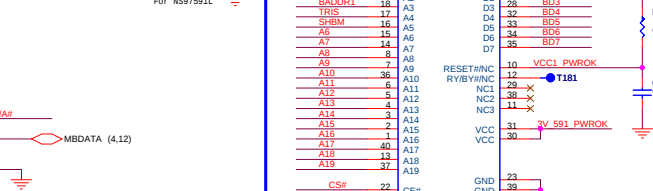
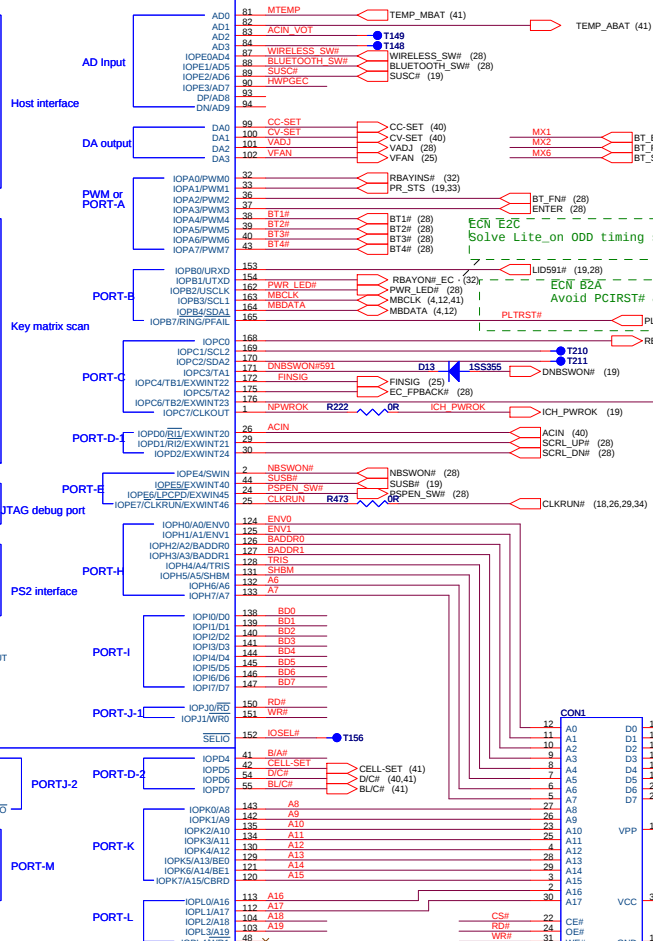
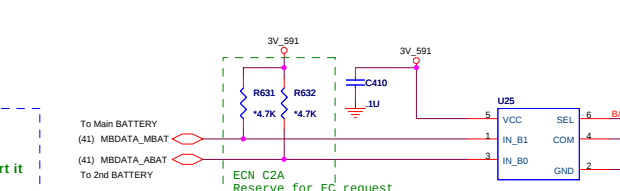
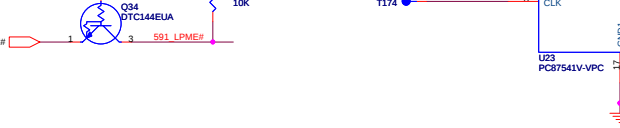
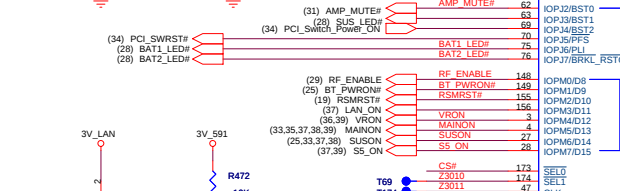
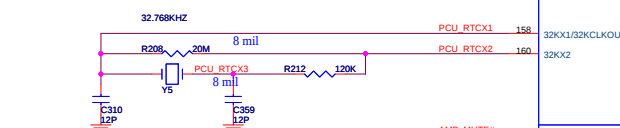
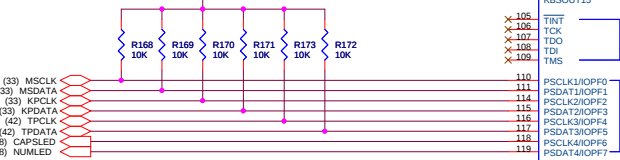
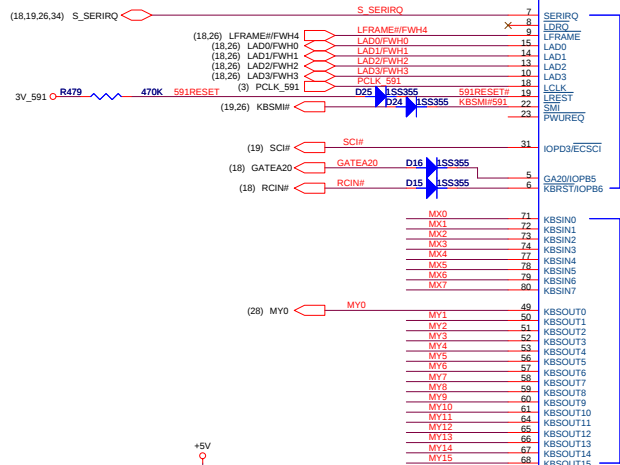
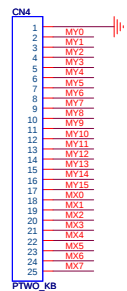
LPT PU



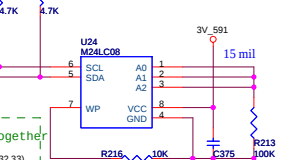
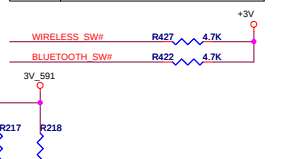
PROJECT : ZE1
Quanta Computer Inc.

Size	Document Number PC87391, FIR, DIGITIZER	Rev B2A
Date:	Monday, August 22, 2005	Sheet 26 of 42

KEYBOARD



I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL)	(HCFGBAH, HCFGBAL)+1
1 1	Reserved	



ECN D2B
EC: ADD PULL-HIGH 10K



Rev A2
EC Remove Hold pin function.Because no pin to support it

request

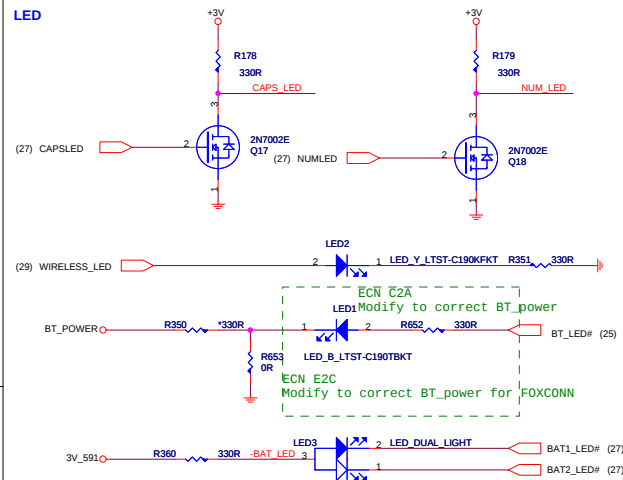
 PROJECT : ZE1
Quanta Computer Inc.



Size	Document Number PCU(PC87591) & FLASH			
Order	Transmit	Argument	25	20000
	Element	25	4	43

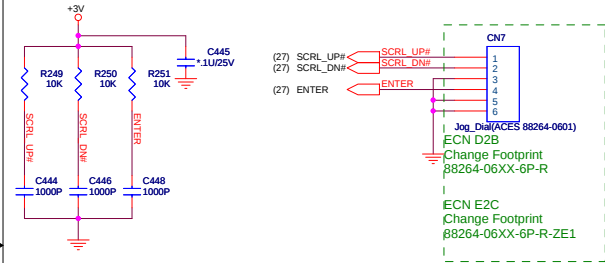
Date.	Tuesday, August 23, 2005	Sheet	27	Of	42
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LED

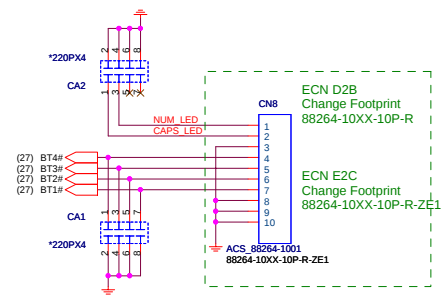


**TRACK POINT CIRCUITS
MOVE TO PAGE 42**

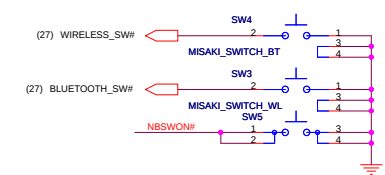
Jogdial & CONN.



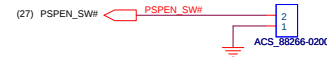
Quick Button Board



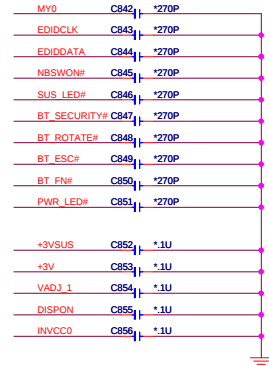
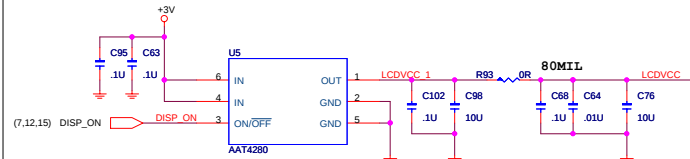
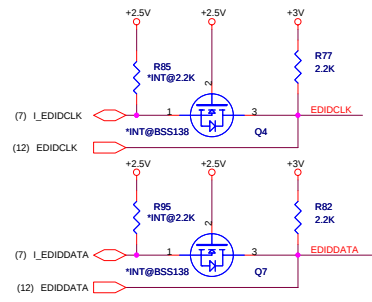
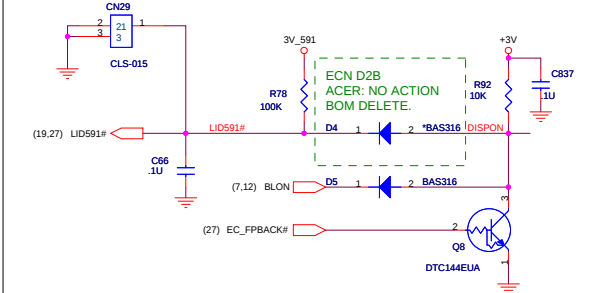
Switch



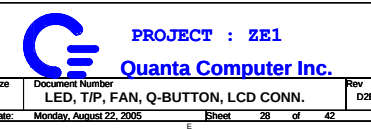
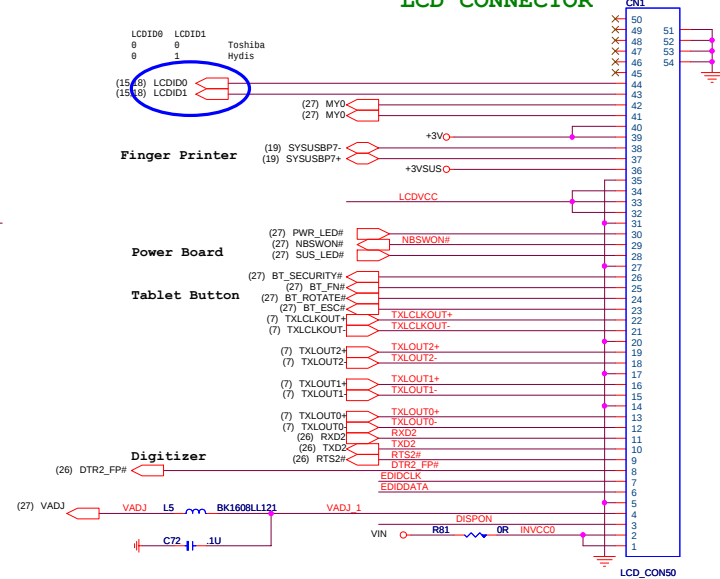
Pressure Sensitive Pen Detect

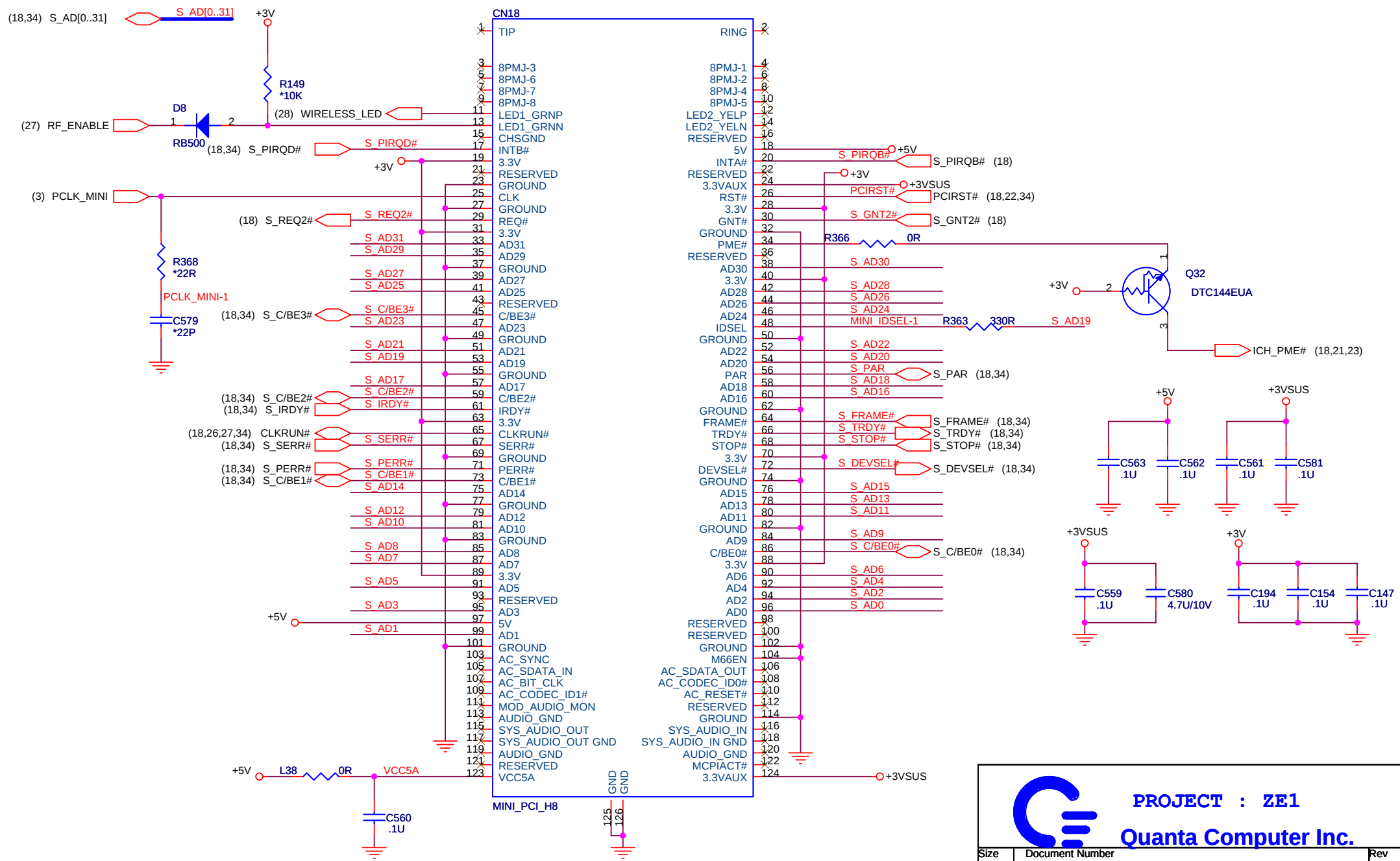


LID/Display on

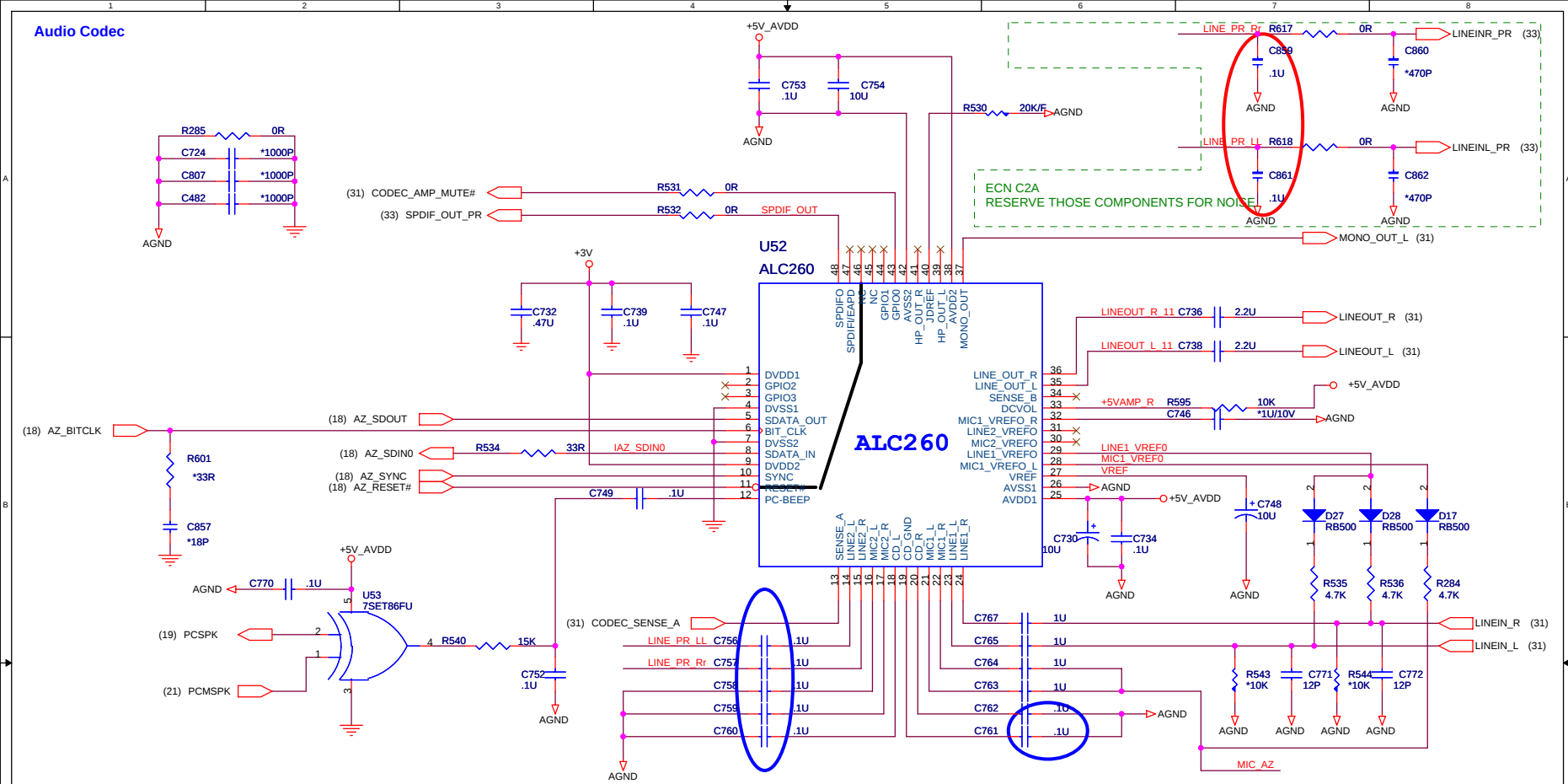


LCD CONNECTOR

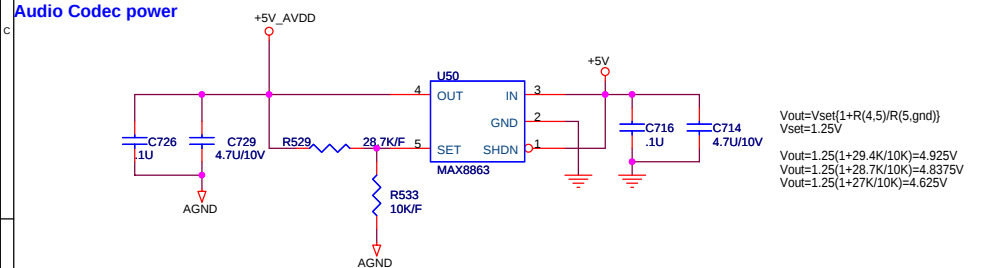




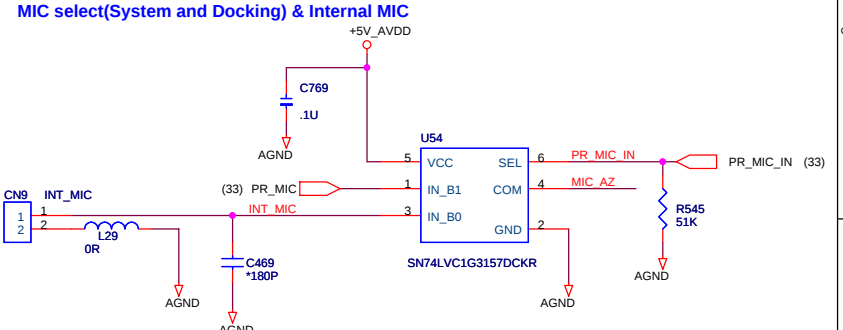
Audio Codec



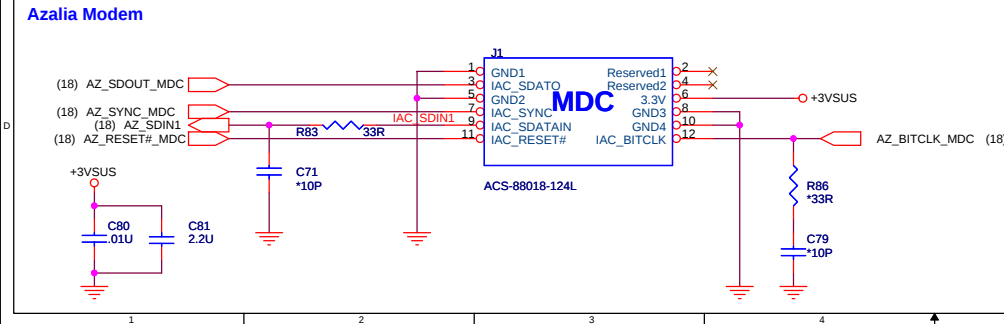
Audio Codec power



MIC select(System and Docking) & Internal MIC

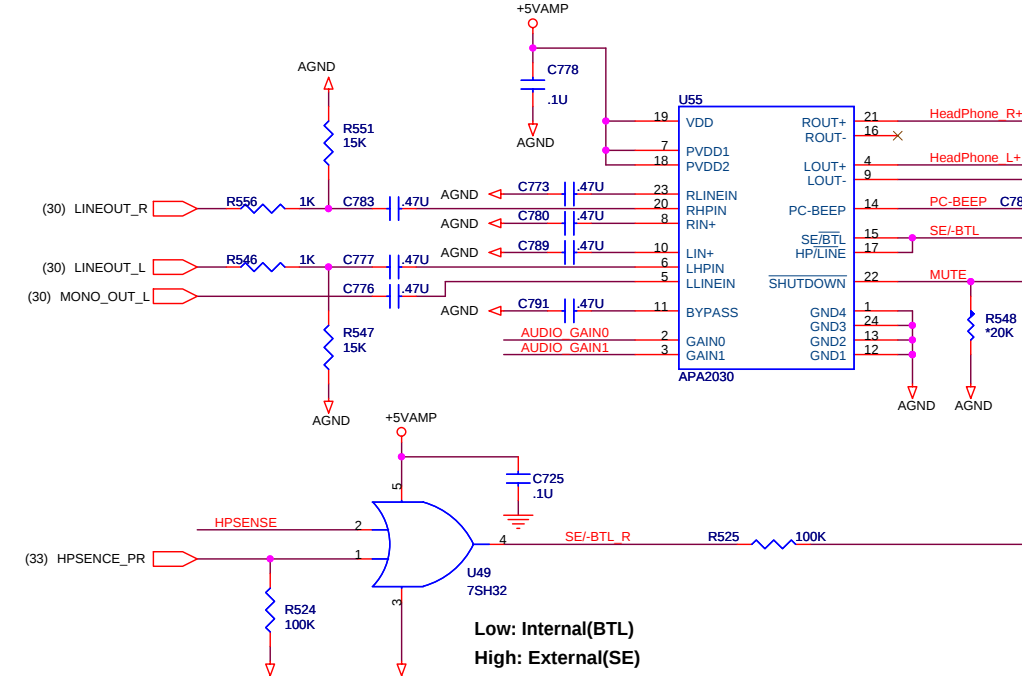


Azalia Modem

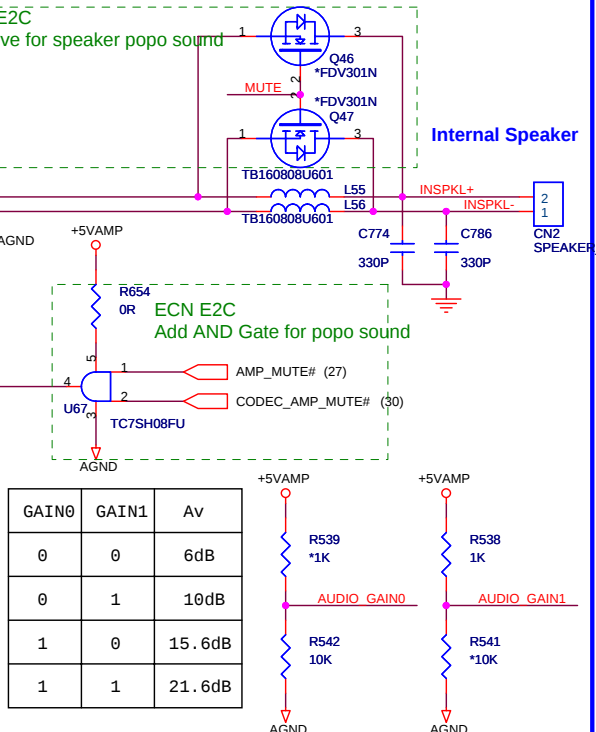


SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

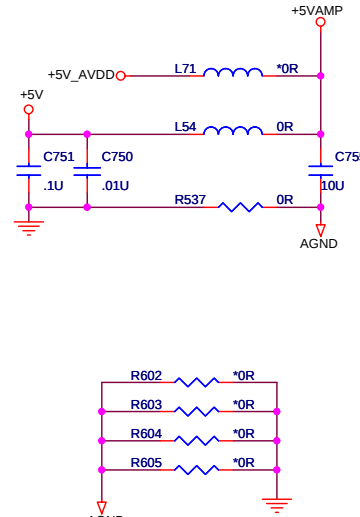
Audio Amplifier



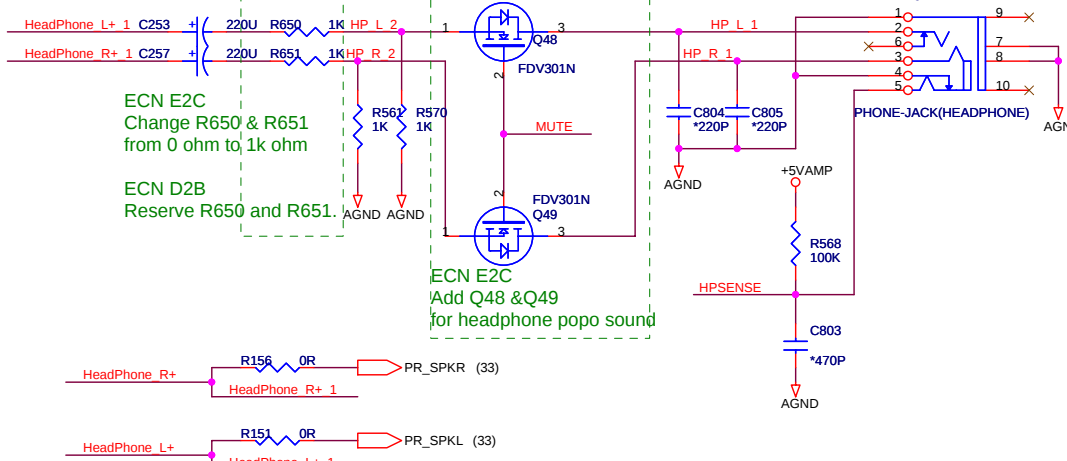
ECN E2C
Reserve for speaker popo sound



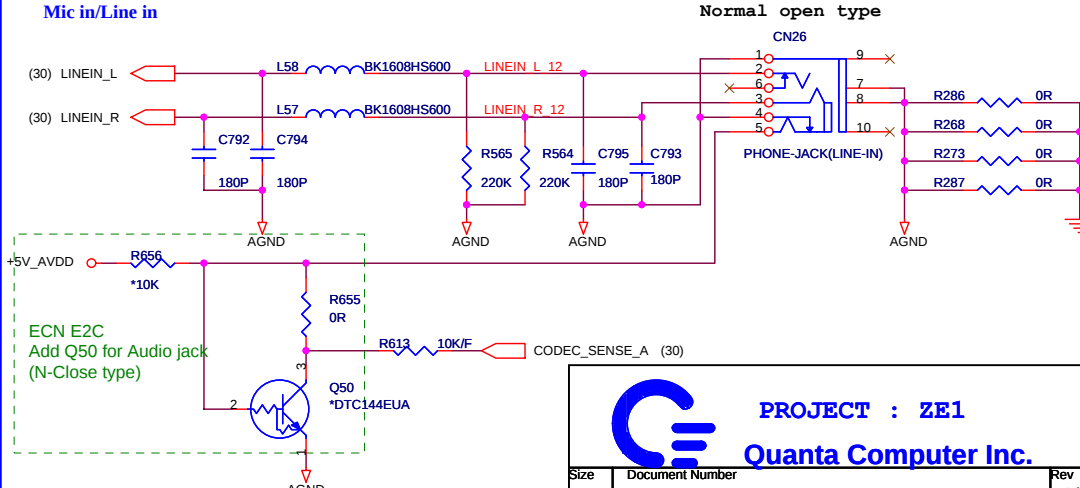
AMP power



Head phone/Line out



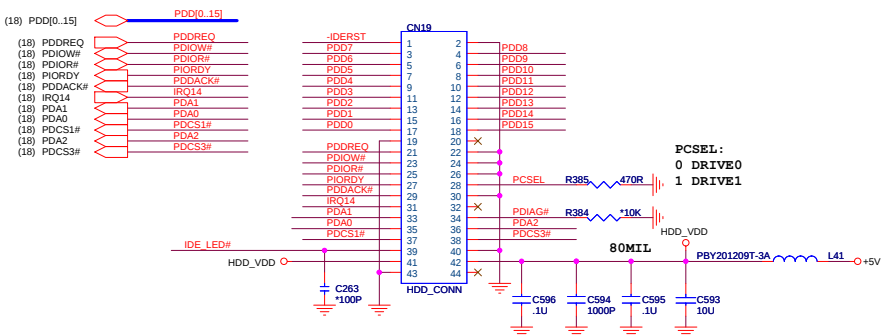
Mic in/Line in



PROJECT : ZE1

Size	Document Number AUDIO AMP & JACK	Rev D2
Date	Monday, August 22, 2005	Sheet 31 of 42

(18) PDD[0..15]		PDD[0..15]
(18) PDDREQ		PDDREQ
(18) PDIOW#		PDIOW#
(18) PDIOR#		PDIOR#
(18) PIORDY		PIORDY
(18) PDDACK#		PDDACK#
(18) IRQ14		IRQ14
(18) PDA1		PDA1
(18) PDA0		PDA0
(18) PDCS1#		PDCS1#
(18) PDA2		PDA2
(18) PDCS3#		PDCS3#

[illegible]

(19) RST_HDD#

(7,12,16,18,19,26,27,33) PLTRST#

R153

R154

OR

*OR

Q16

DTC144EUA

+3V

+5V

R155

10K

3

1

2

4

1

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4

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12

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The schematic diagram illustrates the internal circuitry of the PDC1431T evaluation board. It features two PIC16601 microcontrollers (PDC1431T and PDC1431T) and a PIC16601. The board is powered by a 5V supply and has a ground connection. The schematic is divided into two main sections: the left side for the PIC16601 and the right side for the PDC1431T.

Left Section (PIC16601):

- Power and Ground:** 5V supply connected to pin 36 (VCC_1 VCC_2) and pin 48 (VCC_1 VCC_2). Ground connections are at pins 12 (GND_1GND_2) and 24 (GND_1GND_2).
- Microcontroller Pins:**
 - PDD5# (pin 2), PDD15# (pin 3), PDD7# (pin 4), PDD10# (pin 5), PDD4# (pin 6), NC_1# (pin 1), NC_2# (pin 13), PDD3# (pin 14), PDD9# (pin 15), PDD2# (pin 16), PDD1# (pin 17), PDD14# (pin 18), LBAYON_HDD# (pin 35), PDD13# (pin 19), PDCS3# (pin 20), PDCS1F# (pin 21), PDC3# (pin 22), PDC4# (pin 23), LBAYON_HDD# (pin 47), GND_1GND_2 (pin 12), GND_1GND_2 (pin 24).
- Other Components:**
 - Resistors: R590 (10K), R591 (22K), R592 (10K).
 - Capacitors: C838 (1U).
 - Diodes: D1 (1N4148), D2 (1N4148).

Right Section (PDC1431T):

- Power and Ground:** 5V supply connected to pin 24 (VCC). Ground connections are at pins 12 (GND) and 24 (GND).
- Microcontroller Pins:**
 - PDA2# (pin 2), PDA1# (pin 3), PDCW# (pin 4), PDCY# (pin 5), IRQ1# (pin 6), PDDREQ# (pin 7), PDDACK# (pin 8), RST_RBAY0# (pin 11), LBAYON_HDD# (pin 1), LBVIAS (pin 2), ION (pin 3), FST6800 (pin 4), RST_RBAY0# (pin 11), LBAYON_HDD# (pin 1), LBVIAS (pin 2), ION (pin 3), FST6800 (pin 4).
- Other Components:**
 - Resistors: R592 (10K).
 - Capacitors: C838 (1U).
 - Diodes: D1 (1N4148), D2 (1N4148).

BAY ID STATUS

RBYAID0	RBYAID1	STATUS
0	1	HDD
1	0	CD/DVD

ECN C2A
Need change to Green color

5V

3V

R111 1k

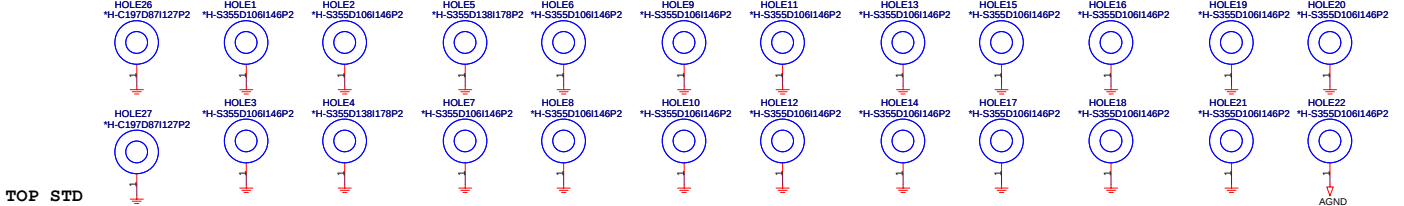
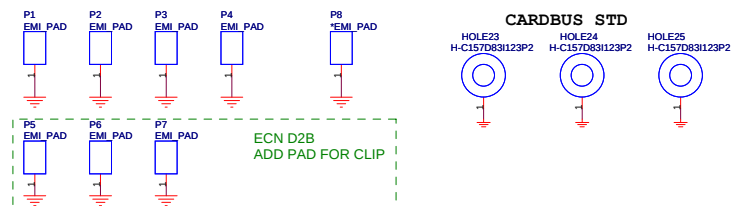
R120 4.7k

IDE_LED

LED4

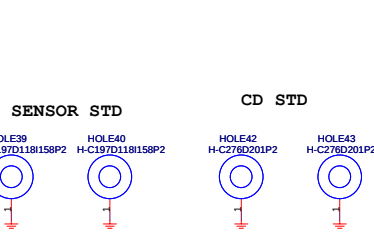
LED_GREEN

Q14 DTC144EUA



HOLE28
H-C197D1221162P2

HOLE29
H-C197D1221162P2



HOLE42
H-C276D201P2

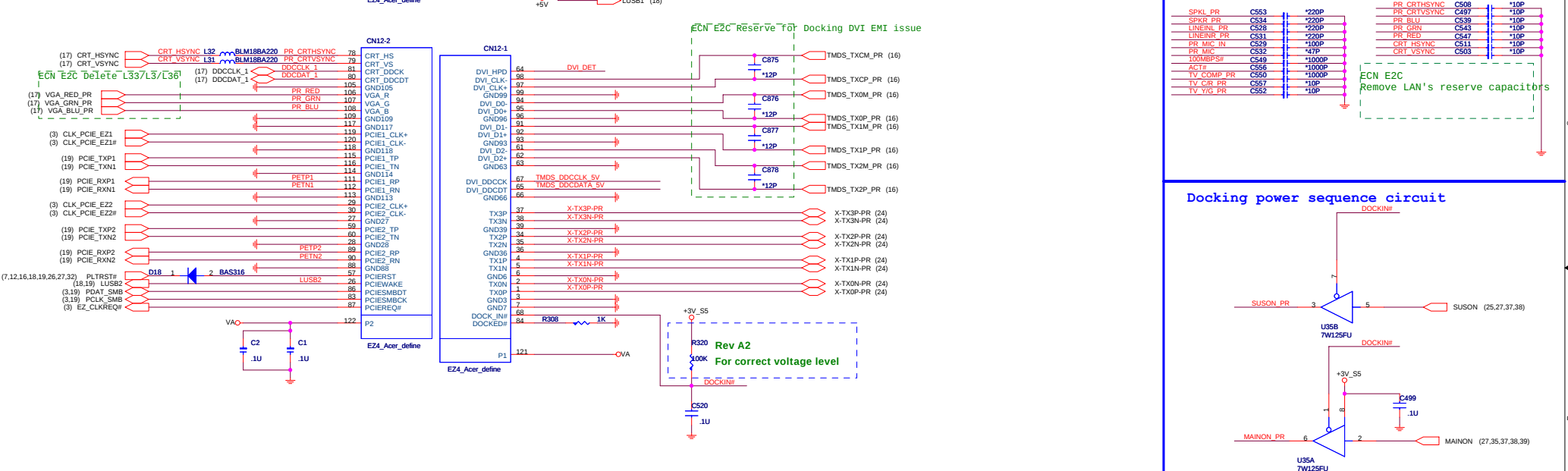
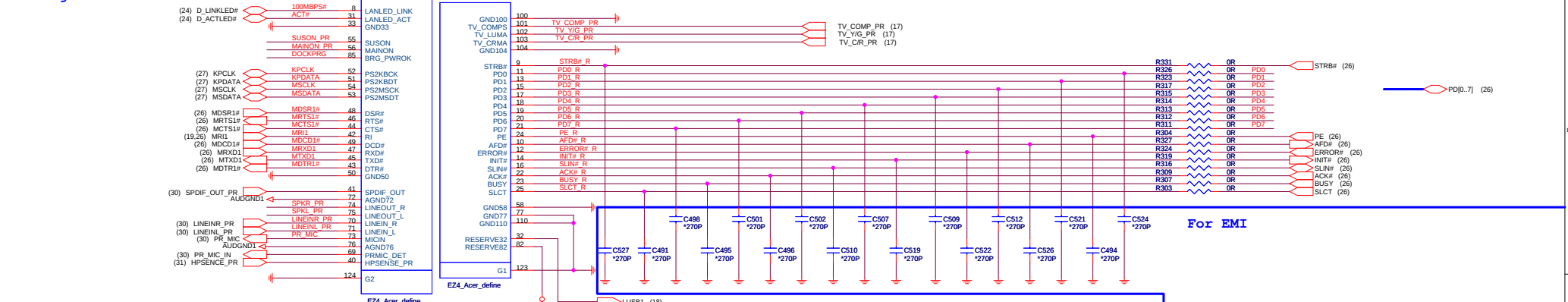
HOLE43
H-C276D201P2

HOLE44
H-C236D157P2

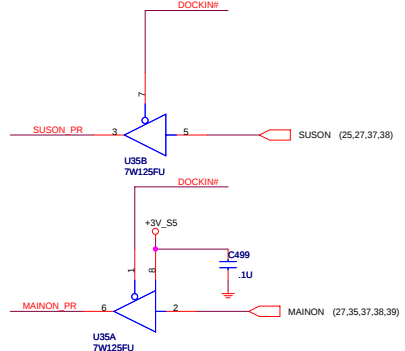
HOLE45
H-C236D157P2

ECN D2
ADD ON
FOR M

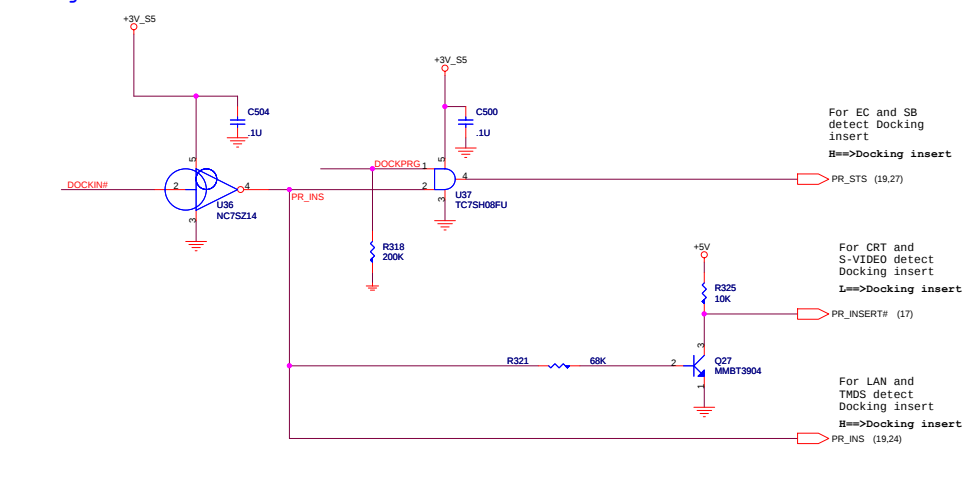
Docking Interface



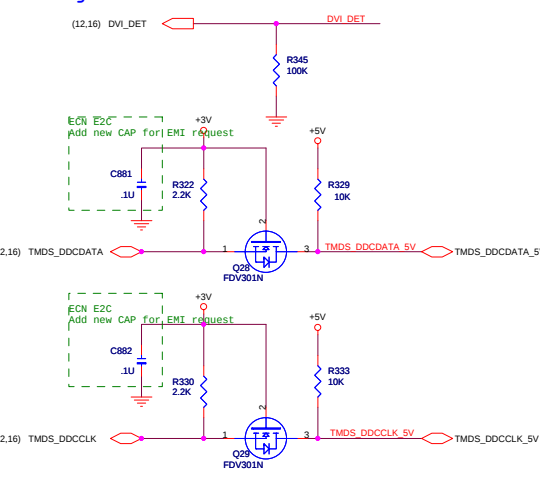
Docking power sequence circuit



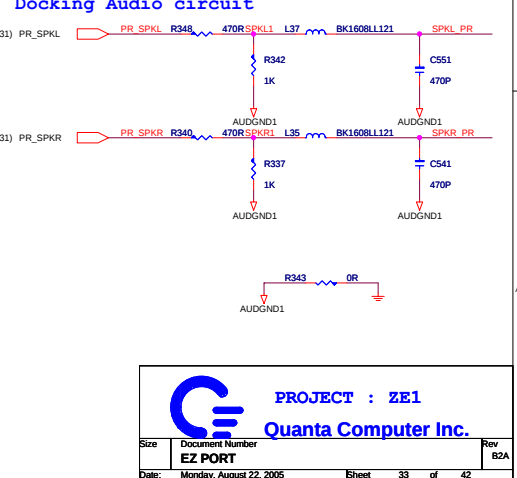
Docking Insert circuit



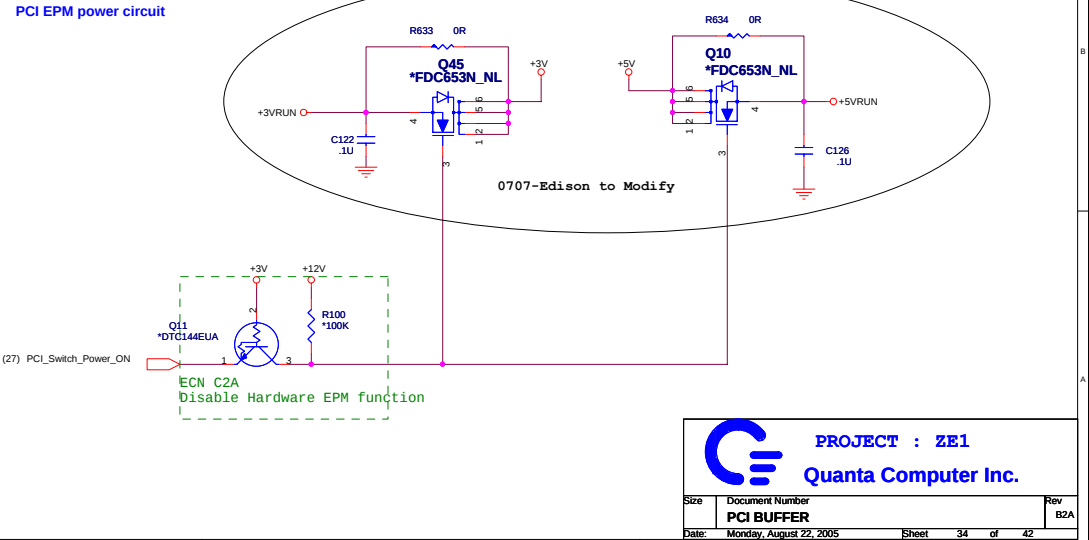
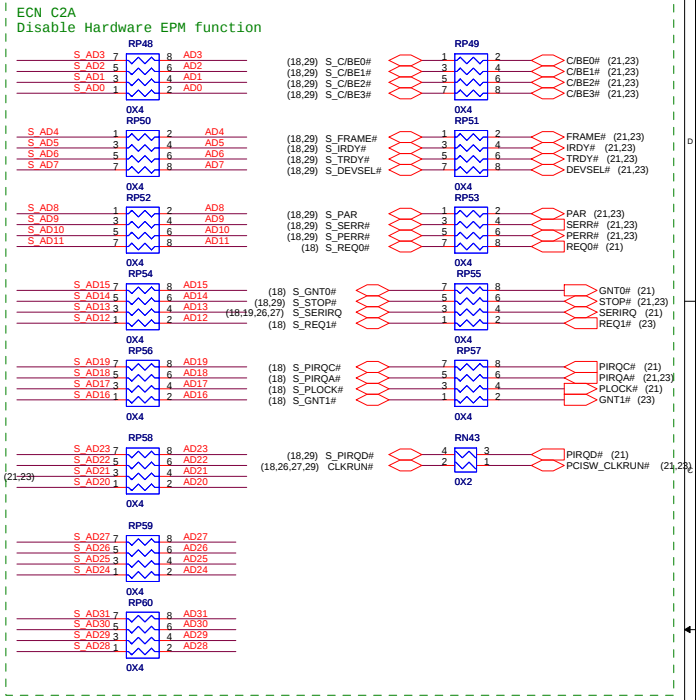
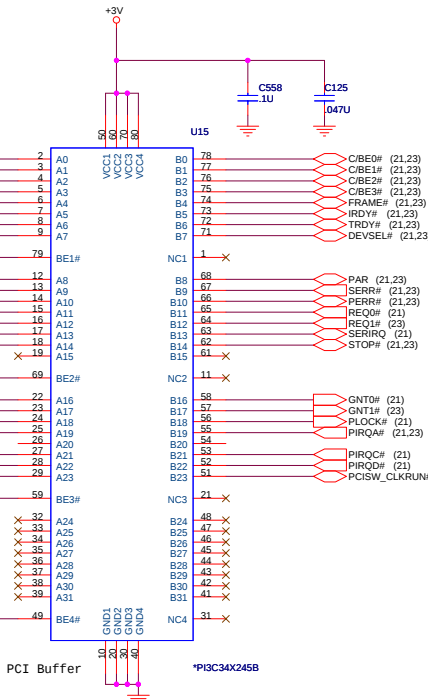
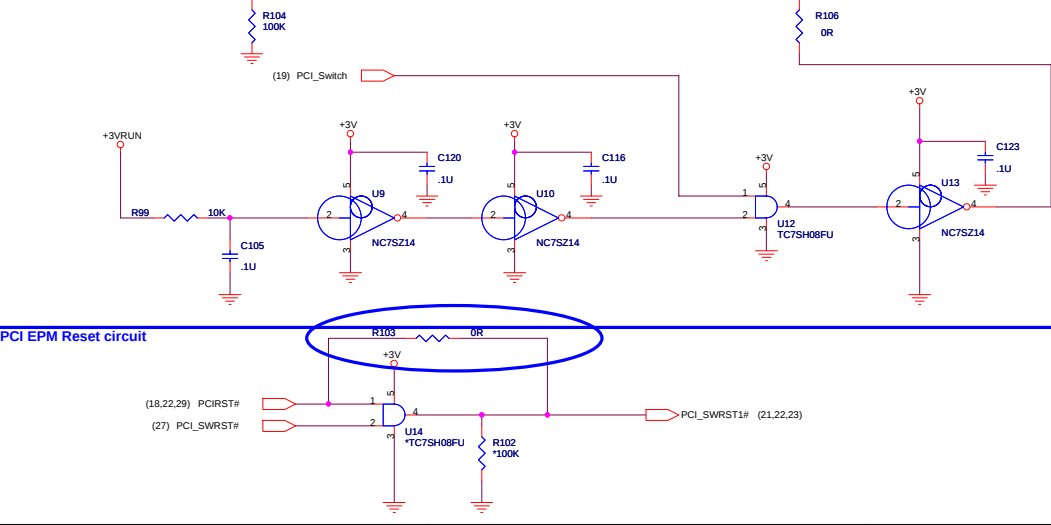
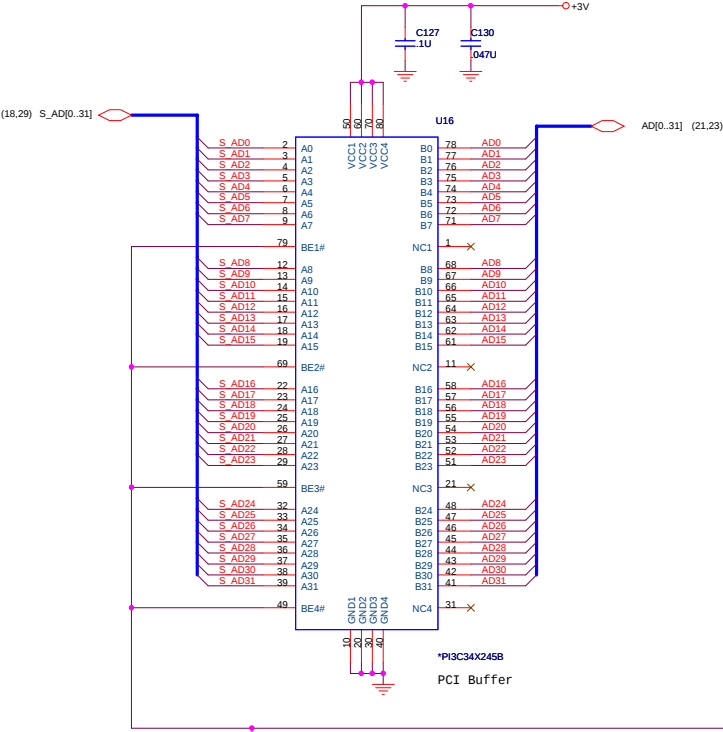
Docking TMDS circuit

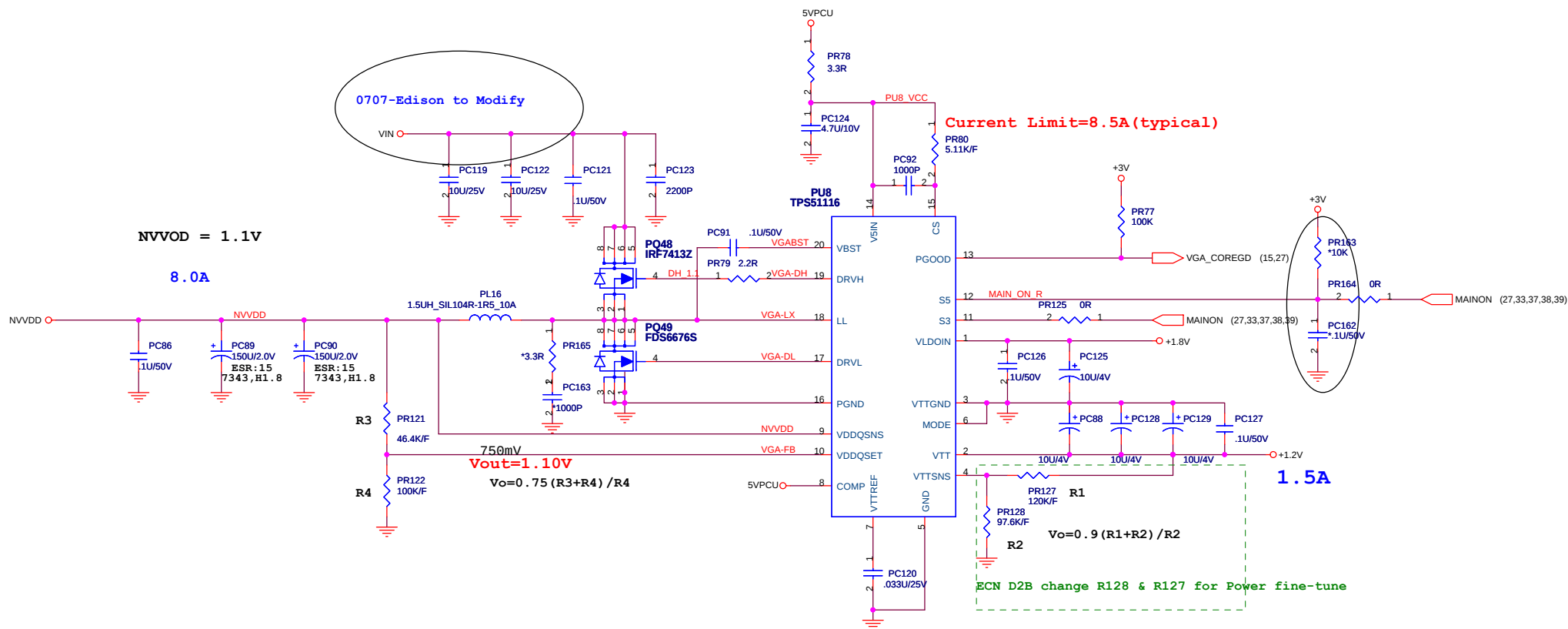


Pushing Audio Signals

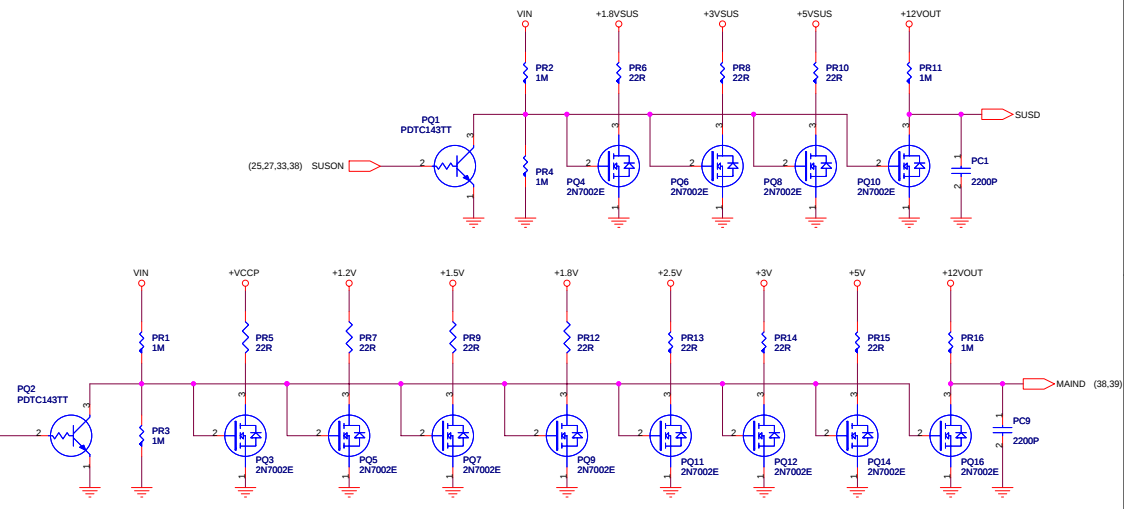
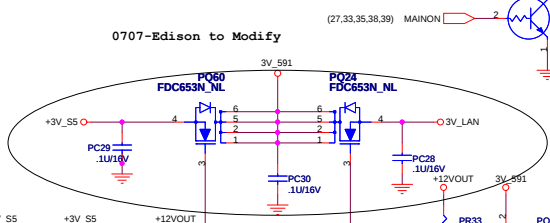
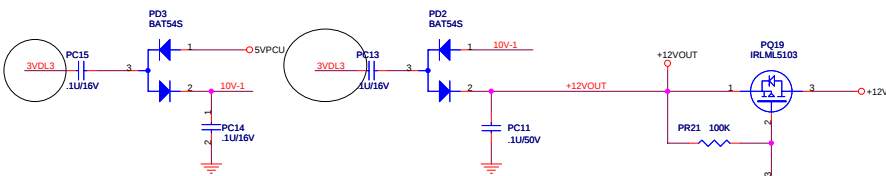
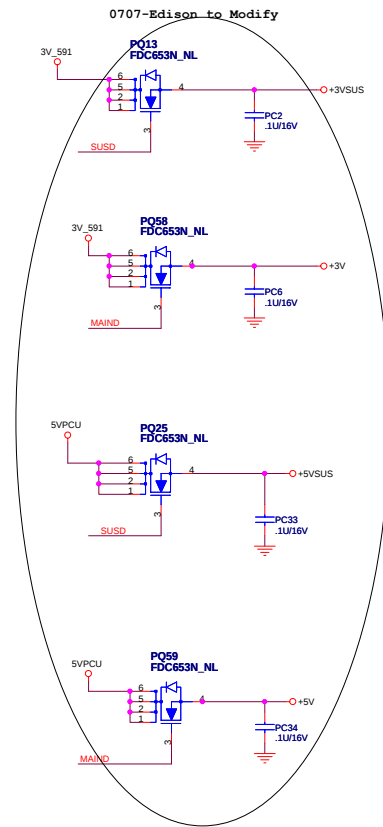
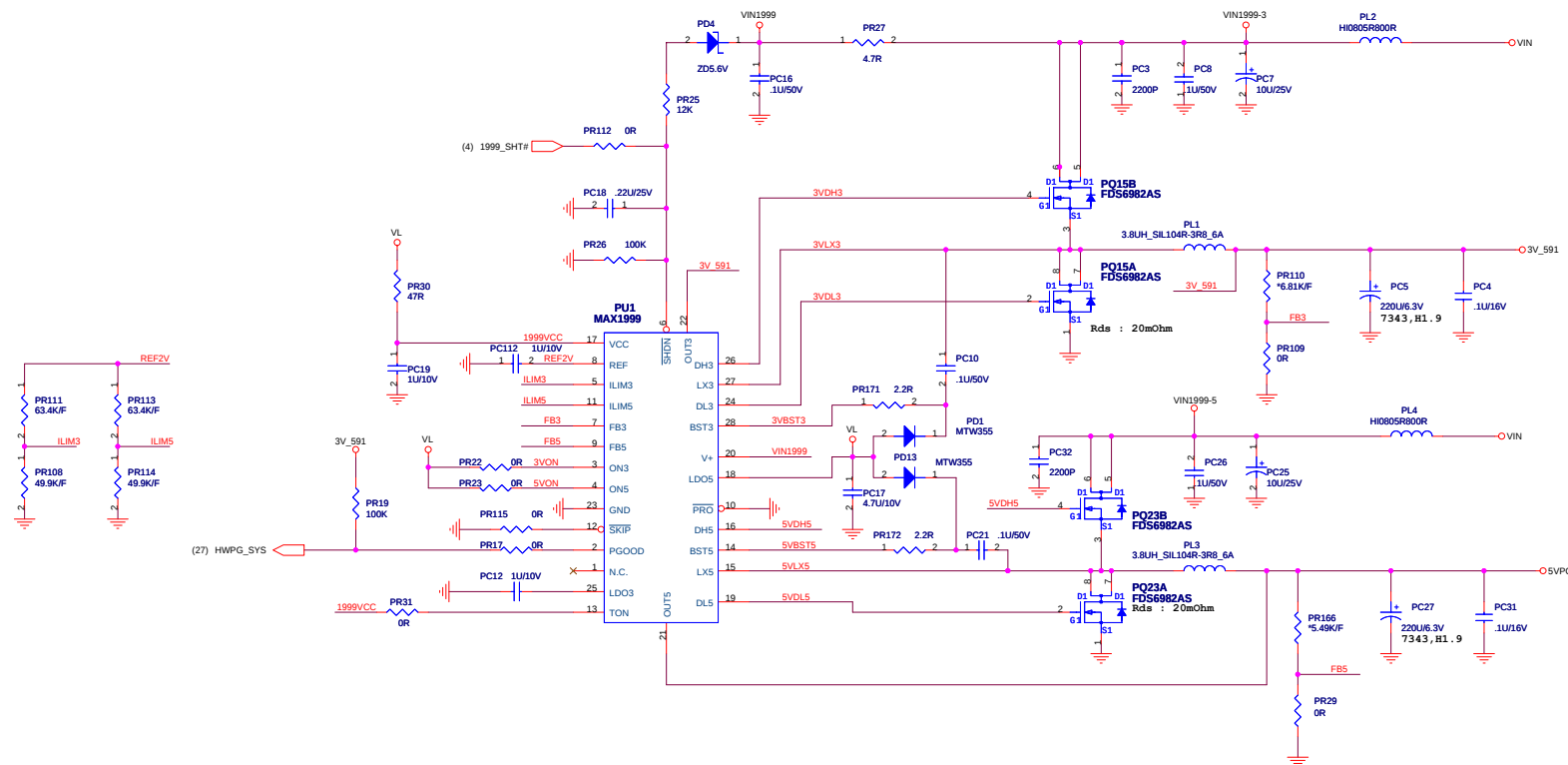


PCI EPM Buffer

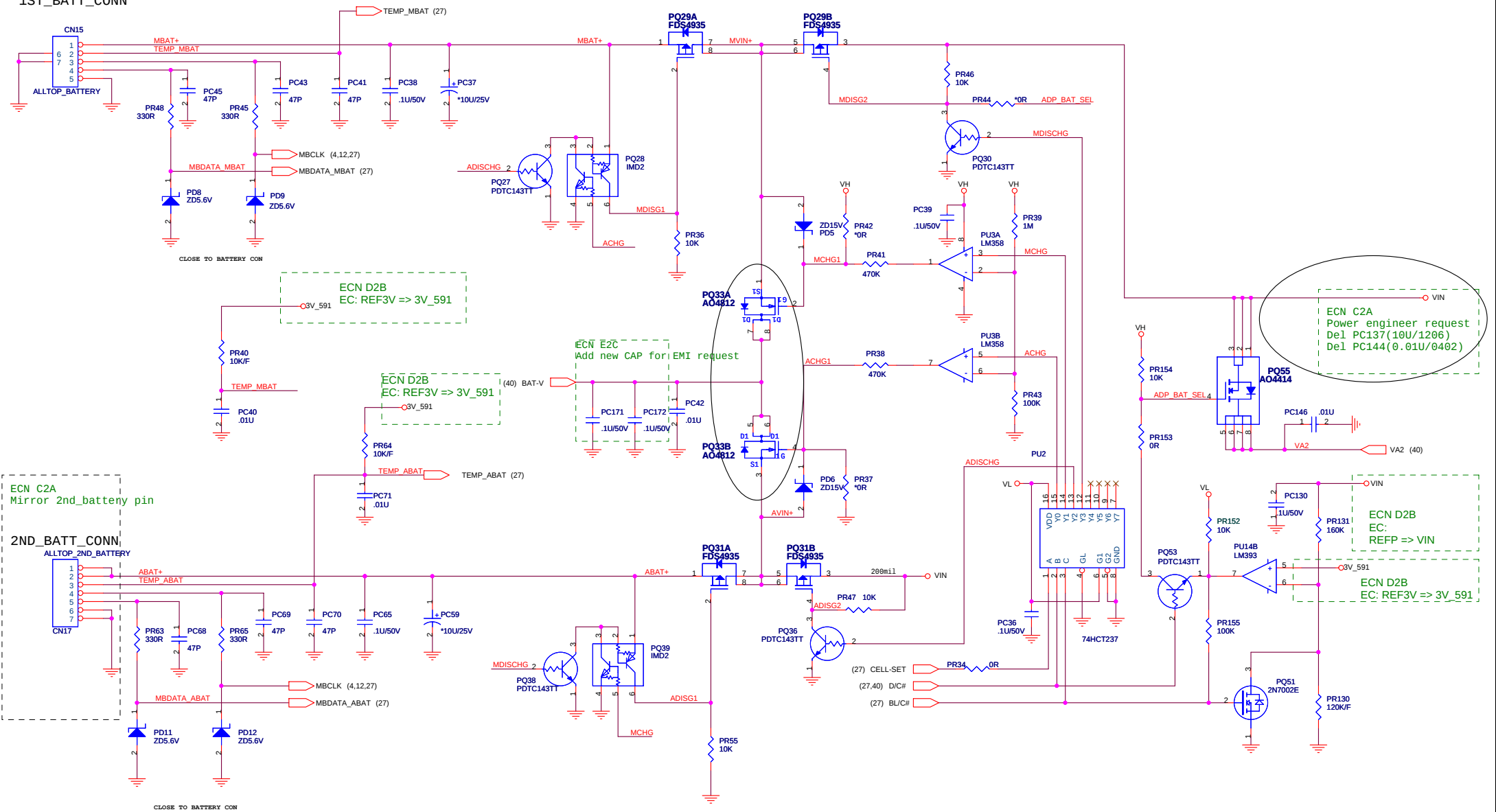




PROJECT : ZE1
Quanta Computer Inc.



1ST_BATT_CONN



2ND_BATT_CONN

