MODEL:	REV:	CHANGE LIST:	PAGE	FROM	TO
ZI2 MB	1A	FIRST RELEASE : ECN NO. E200302-3095	1	1A	
	1B	MODIFY BOM : ECN NO. E200303-0277	2	1A	
		PAGE05 : R495 CHANGE TO 127 ohm 1%	3	3B	
		PAGE06 : C604 & C654 CHANGE TO 220uF / 2.5V	4	3A	
		PAGE18 : D4 CHANGE TO CH715F	5	1B	
		PAGE26 : D35 DON'T STUFF			
		PAGE33 : PU6 CHANGE TO MAX1907AETL , PQ15 & PQ23 CHANGE TO FDB7045L , PC46 CHANGE TO 220pF			
		PAGE36 : PU13 CHANGE TO LM358AM			
	1C	MODIFY BOM : ECN NO. E200303-2680	6	2A	
		PAGE03 : Q40 CHANGE FOOTPRINT FOR SMT ISSUE	7	1C	
		PAGE06 : SOLVE FOR I2C BUS CAN'T READ DATA	8	3B	
		PAGE07 : C560 CHANGE FOOTPRINT FOR ASSEMBLY ISSUE	9	3A	
		PAGE08 : SOLVE FOR PCIRST SIGNAL NOT GOOD ISSUE	10	1C	
		PAGE09 : STUFF R634 FOR LAN WORKING	11	1A	
		PAGE10 : DDR ADDRESS LINE CHANGE & ADD EMI SOLUTION	12	3B	3C
		PAGE12 : SOLVE FOR I2C BUS CAN'T READING	13	3B	
		PAGE14 : U10 CHANGE TO CH7011A-T & CHANGE Q86,Q87 FOR SMBUS READING	14	3B	
		PAGE15 : SOLVE FOR LCD BACKLIGHT OFF ISSUE	15	3A	
		PAGE16 : UPDATE 4 IN 1 BOARD'S ALCOCATION HOLE SIZE	16	3A	
		PAGE18 : SOLVE FOR 4 IN 1 CARD READER MODULE READING SIGNAL	17	3B	
		PAGE19 : CHANGE LCD CONNECTOR FOOTPRINT FOR SMT ISSUE	18	3B	
		PAGE21 : ADD Q88 , Q89	19	3B	
		PAGE23 : ADD EMI SOLUTION	20	3B	
		PAGE24 : SOLVE FOR AUDIO QUALITY	21	3B	
		PAGE25 : ADD EMI SOLUTION & SOLVE FOR AUDIO QUALITY	22	3B	
		PAGE28 : SOLVE FOR USB2.0 ISSUE	23	3A	
		PAGE27 : CHANGE Q23 FOOTPRINT FOR SMT ISSUE	24	3B	
		PAGE29 : ADD EMI SOLUTION	25	3B	
		PAGE30 : ADD EMI SOLUTION	26	3A	
		PAGE32 : PQ6 CHANGE TO SI4810DY	27	3B	
	2A	MODIFY BOM : ECN NO. E200303-3536	28	3A	
		PAGE06 : ADD R480 FOR TV-OUT	29	3B	
		PAGE21 : DON'T STUFF R510 FOR MINI PCI LAN CARD DON'T WORK	30	1C	
		PAGE27 : DON'T STUFF R150 FOR PWROK SIGNAL QUALITY	31	3A	
	3A	MODIFY BOM : ECN NO. E200304-2184	32	1C	
		PAGE03 : SOLVE LEAKAGE CURRENT ISSUE (Q40)	33	1B	
		PAGE04 : CHANGE FOOTPRINT FOR SMT ISSUE	34	1A	
		PAGE09 : CHANGE R460 FOR USB2.0 EYE DIAGRAM	35	3B	
		PAGE12 : ADD CLK BYPASS CAPACITOR FOR SIGNAL QUALITY	36	1B	
		PAGE14 : SOLVE FOR TV-OUT NOT GOOD ISSUE	37	1A	
		PAGE15 : ADD ACER'S REQUEST (HOT-KEY FOR BACKLIGHT)			
		PAGE16 : ADD FINGER FOR ASSEMBLY ISSUE			
		PAGE19 : SOLVE LEAKAGE CURRENT ISSUE (Q57)			
		PAGE23 : CHANGE PART FOR ASSEMBLY ISSUE			
		PAGE24 : SOLVE AUDIO NOISE & VOLUME ISSUE			
		PAGE25 : SOLVE AUDIO NOISE ISSUE			
		PAGE26 : CHANGE PART BY PURCHASE DEPT REQUEST			
		PAGE27 : SOLVE LEAKAGE CURRENT ISSUE (Q23)			
		PAGE28 : CONNECT USB HUB OVER CURRENT SIGNAL 1&2 TOGETHER			
		PAGE29 : DELETE UNUSED NET			
		PAGE31 : CHANGE PART FOR ASSEMBLY ISSUE			
	3B	MODIFY BOM : ECN NO. E200305-1836 & E200305-2668 & E200306-0261			
		PAGE03 : DELETE ITP & SOLVE THERMAL SIGNAL MIXER			
		PAGE08 : ENHANCE PCIRST SIGNAL QUALITY			
		PAGE12 : ADD SUSC# FOR CLOCK POWERDOWN			
		PAGE13 : SOLVE FOR ACER'S LCD MONITOR 1280X1024 NO DISPLAY ISSUE			
		PAGE14 : SOLVE FOR VIEW SONIC'S MONITOR DISPLAY NO GOOD ISSUE			
		PAGE17 : ENHANCE IDE RST SIGNAL			
		PAGE18 : IMPROVE ICH_PME# SIGNAL			
		PAGE19 : PREVENT SUPPEND LEAKAGE CURRENT ISSUE			
		PAGE20 : IMPROVE ICH_PME# SIGNAL			
		PAGE21 : IMPROVE ICH_PME# SIGNAL			
		PAGE22 : SOLVE WAKE UP ON LAN ISSUE			
		PAGE24 : SOLVE MIC SMALL VOLUME ISSUE			
		PAGE25 : SOLVE PCMCIA DOS TEST NO SOUND ISSUE			
		PAGE27 : SOLVE WAKE UP ON LAN ISSUE			
		PAGE29 : PREVENT THERMAL SIGNAL UNPROBABLY WORK ISSUE			
		PAGE35 : PREVENT DTC144EUA TRANSISTOR BAD CAUSE HDD NO POWER & REBOOT FAIL ISSUE			
	3C	MODIFY BOM : ECN NO. E200306-1724			
		PAGE12 : MODIFY R244 TO 22 OHM 1% FOR USB CLOCK TIMING ISSUE			

PROJECT : ZI2 Quanta Computer Inc.	PROJECT : ZI2	APPROVE BY:LAWRENCE CHAO	DRAWING BY:JACKY CHENG	REV : 3C
	MB ASSY'S P/N : 31ZI2MB0007	PROJECT LEADER: JIM HSU	DOCUMENT NO: 204	DATE :2003/06/18

COVER SHEET 1 OF 1

ZI2 BLOCK DIAGRAM

CENTRINO

	PIRQ#	REQ#/GNT#	IDSEL
VGA	X	X	X
CARDBUS	C, (B)	3	22
IEEE 1394	B	2	23
MINI-PCI	E, F	1	20
LAN	D	0	18
USB 2.0	X	X	X

DC/DC
1.25VDDR
2.5VDDR
PG 31
1.5V/1.8V
PG 32
System Power MAX1632
PG 34

BATT SELECTOR PG 37

BATT CHARGER AC/BATT CONNECTOR PG 36

CPU CORE PG 33

Banias
(Micro-FCPGA)
PG 3,4

CLOCKS
PG 12

Port Replicator
PS2 X2
1S1P
LINE-IN
LINE-OUT
MICROPHONE-IN
USB 1.1 X2
CRT
S-VIDEO
LAN
PG 30

LVDS
Panel Connector PG 15

4X100MHZ

DDR-SODIMM1 PG 10

DDR-SODIMM2 PG 10

266/200 MHZ DDR

Montara-GM MGM
732 Micro-FCBGA
PG 5,6,7

DVOB
Chrontel 7009
TV EN-CONDE
PG 14

S-Video
SEE I/O Board PG 13

VGA PG 13

DDR-Termiation PG 11

66(266)MHZ, 1.8V HUB I/F

ICH4-M
421 BGA
PG 8,9

33MHZ, 3.3V PCI

Primary IDE HDD PG 17

ATA 66/100

Secondary IDE - Swap Bay (Optical Drive, 2ND HDD) PG 17

IDE BUS BUFFER PG 17

LAN BCM4401 PG 22

MINI-PCI SOCKET PG 21

CARDBUS OZ711EC1 PG 18

IEEE-1394 TSB43AB21 PG 20

RJ11 **RJ45** PG 16

CALEXICO PG 21

CARDBUS SLOT SCR SLOT PG 18

1394 CONN. PG 20

4-IN-1 SLOT PG 19

USB 2.0

USB connec*4 PG 28

LED/B Connector PG 19

3.3V LPC, 33MHz

AC LINK
USB 1.1

MDC CONNECTOR PG 21

AC LINK

SIO PC87391
100 Pins TQFP
PG 26

PC87591
176 Pins LQFP
PG 27

AUDIO CODEC PG 24

AUDIO AMP TPA0312 PG 25

HEAD PHONE
LINE IN
EXT. /INT. MIC
PG 24, 25

FIR PG 26

Parallel PG 26

Touchpad PG 19

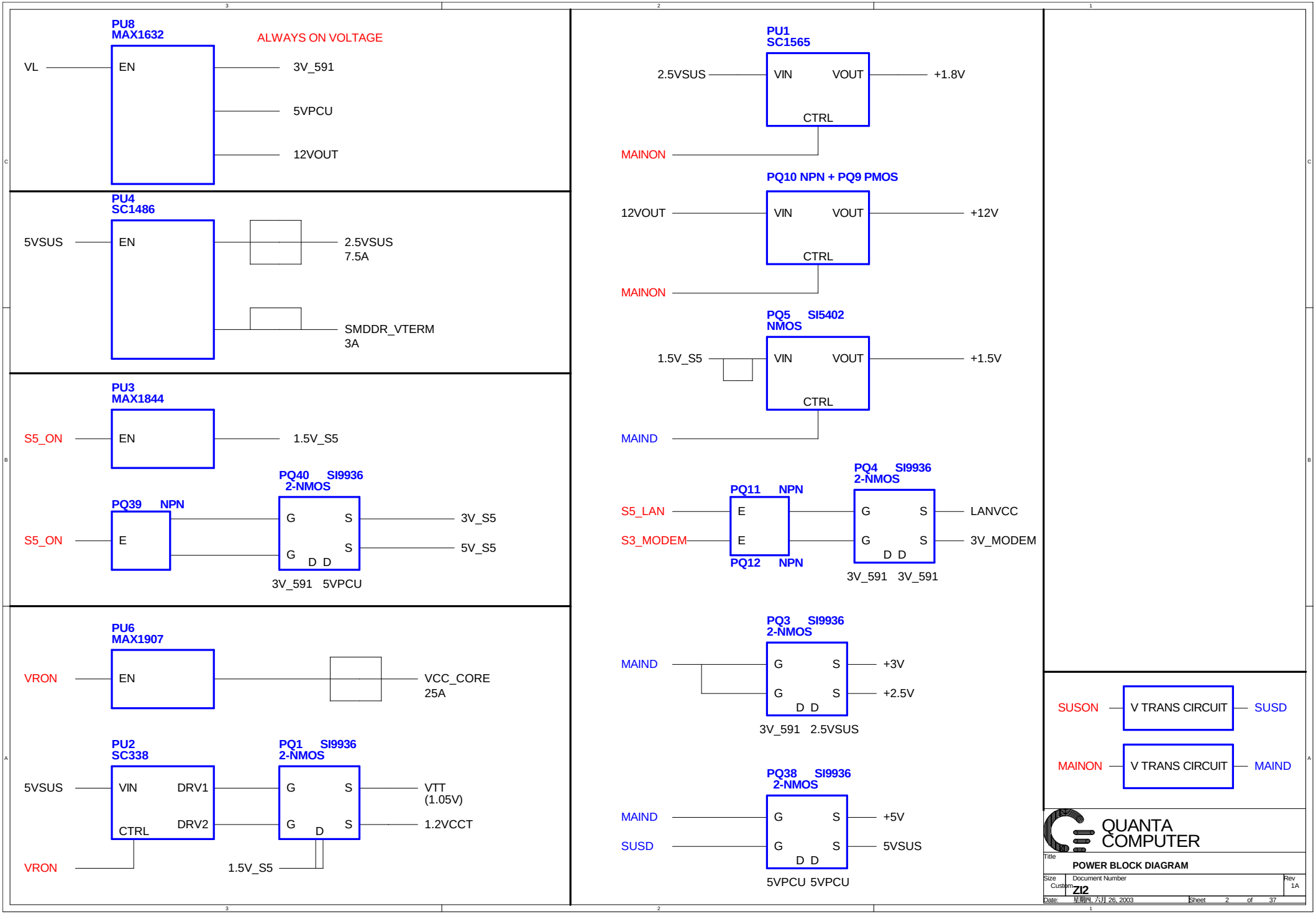
Keyboard PG 29

FLASH PG 27

FAN 1,2 PG 29

QUANTA COMPUTER

Title	Schematic Block Diagram1	
Size	Document Number: ZI2	Rev 1A
Date: 星期四, 六月 26, 2003	Sheet 1 of 37	


PU8
MAX1632
ALWAYS ON VOLTAGE
VL
EN
3V_591
5VPCU
12VOUT
PU4
SC1486
5VSUS
EN
2.5VSUS
7.5A
SMDDR_VTERM
3A
PU3
MAX1844
S5_ON
EN
1.5V_S5
PQ40 SI9936
2-NMOS
PQ39 NPN
S5_ON
E
G
S
G
S
D D
3V_S5
5V_S5
3V_591 5VPCU
PU6
MAX1907
VRON
EN
VCC_CORE
25A
PU2
SC338
PQ1 SI9936
2-NMOS
5VSUS
VIN
DRV1
CTRL
DRV2
G
S
G
S
D
VTT
(1.05V)
1.2VCCT
VRON
1.5V_S5
PU1
SC1565
2.5VSUS
VIN
VOUT
+1.8V
CTRL
MAINON
PQ10 NPN + PQ9 PMOS
12VOUT
VIN
VOUT
+12V
CTRL
MAINON
PQ5 SI5402
NMOS
1.5V_S5
VIN
VOUT
+1.5V
CTRL
MAIND
PQ4 SI9936
2-NMOS
PQ11 NPN
S5_LAN
S3_MODEM
E
E
G
S
G
S
D D
LANVCC
3V_MODEM
PQ12 NPN
3V_591 3V_591
PQ3 SI9936
2-NMOS
MAIND
G
S
G
S
D D
+3V
+2.5V
3V_591 2.5VSUS
PQ38 SI9936
2-NMOS
MAIND
SUSD
G
S
G
S
D D
+5V
5VSUS
5VPCU 5VPCU
SUSON
V TRANS CIRCUIT
SUSD
MAINON
V TRANS CIRCUIT
MAIND
QUANTA
COMPUTER
Title
POWER BLOCK DIAGRAM
Size
Custom
Document Number
ZI2
Rev
1A
Date:
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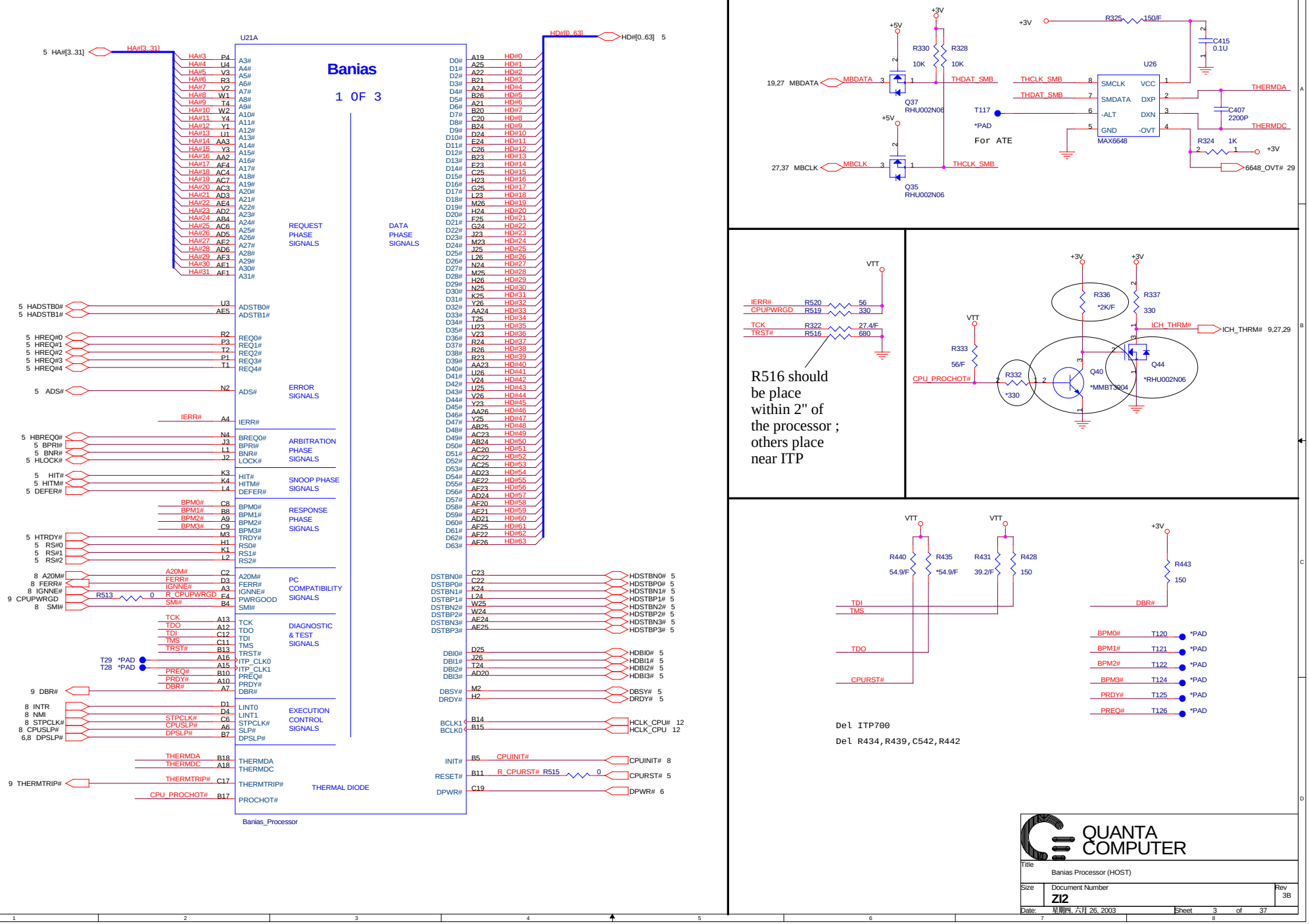
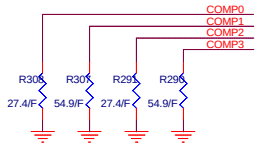
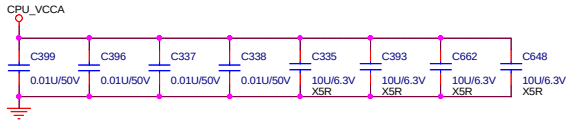
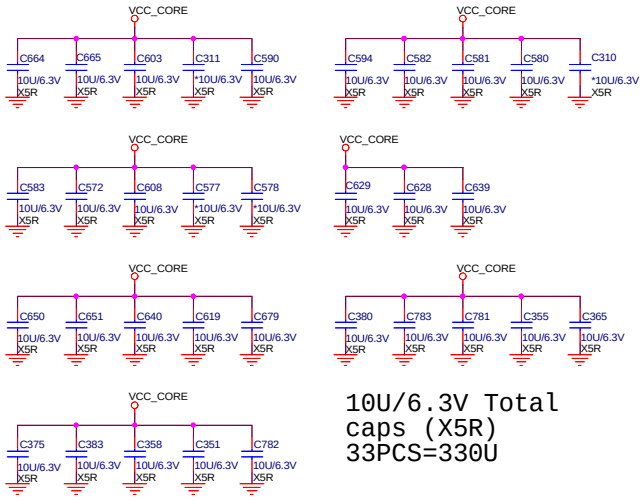
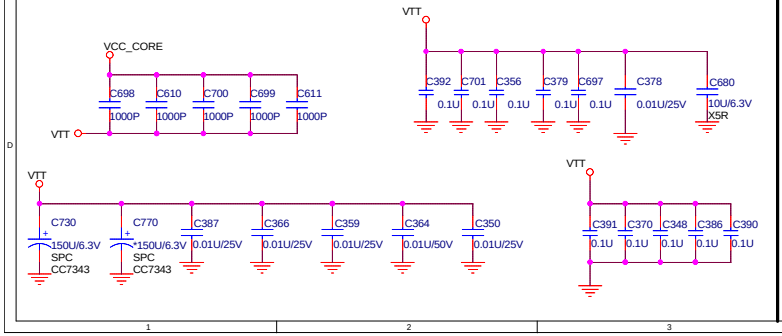

U21A
Banias
1 OF 3
REQUEST PHASE SIGNALS
DATA PHASE SIGNALS
ERROR SIGNALS
ARBITRATION PHASE SIGNALS
SNOOP PHASE SIGNALS
RESPONSE PHASE SIGNALS
PC COMPATIBILITY SIGNALS
DIAGNOSTIC & TEST SIGNALS
EXECUTION CONTROL SIGNALS
THERMAL DIODE
Banias_Processor
+5V
+3V
MBDATA
MBCLK
THDAT_SMB
THCLK_SMB
Q37 RHU002N06
Q35 RHU002N06
*PAD
For ATE
MAX6648
SMCLK
SMDATA
-ALT
GND
VCC
DXP
DXN
-OVT
THERMDA
THERMDC
6648_OVT# 29
VTT
IERR#
CPUPWRGD
TCK
TRST#
R516 should be place within 2" of the processor ; others place near ITP
CPU_PROCHOT#
ICH_THRM#
Q40 *MMBT3904
Q44 *RHU002N06
TDI
TMS
TDO
CPURST#
DBR#
Del ITP700
Del R434,R439,C542,R442
QUANTA COMPUTER
Title
Banias Processor (HOST)
Size
Document Number
ZI2
Rev
3B
Date: 星期四, 六月 26, 2003
Sheet 3 of 37

These 4 Resistors need to place with 0.5" of CPU.
Comp0,2 trace need to be zo=27.4 ohm,
comp1,3 traces need to be zo=55 ohm.







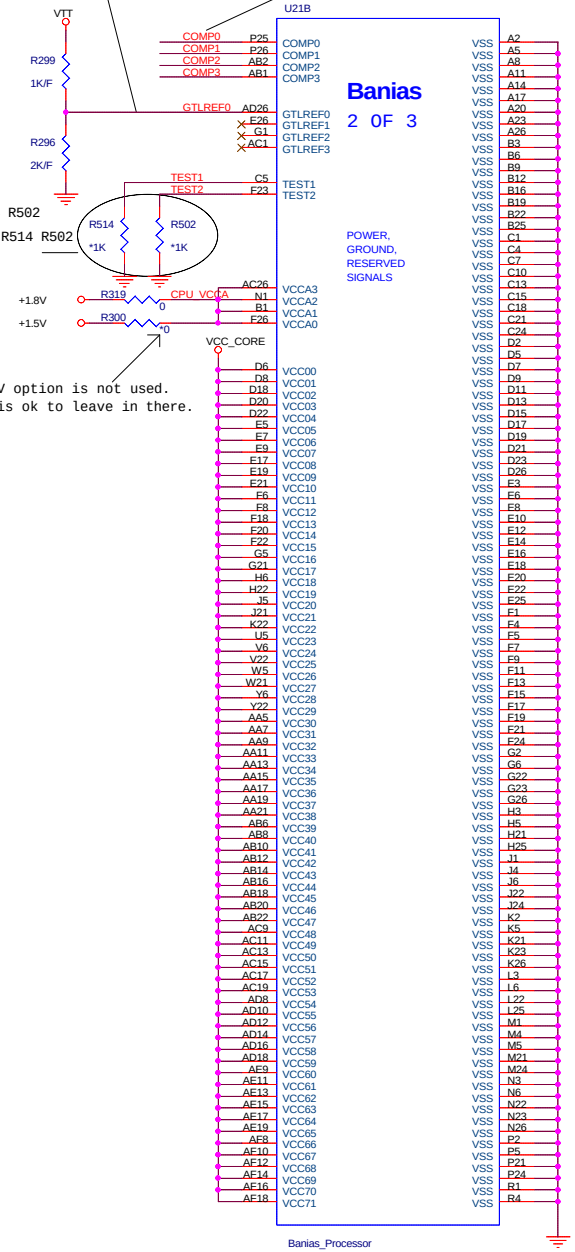


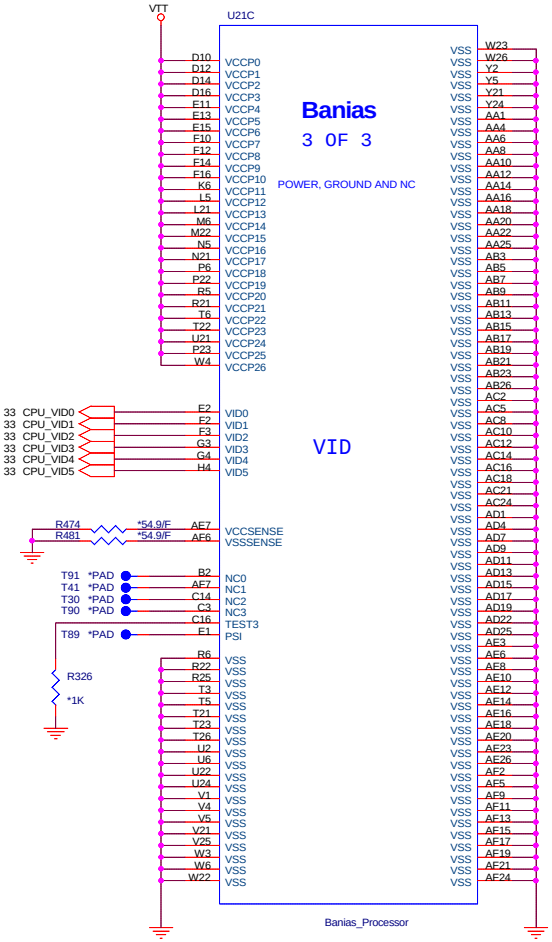
GTLREF: 2/3 VCCP+-2%
T<0.5" 25 mils space

25 MILS (>50 MILS PREFFERD) SPACE
T < 0.5"

A0 CPU install R514 R502
A1 CPU not install R514 R502

1.5V option is not used.
It is ok to leave in there.





*
VCC,BOOT 1.2V
VCC_CORE 1.468V
(DEEPER SLEEP) 0.956V
VTT 1.05V
VCCA 1.8V

QUANTA COMPUTER

Title	Banias Processor (POWER)	
Size	Document Number ZI2	Rev 3A
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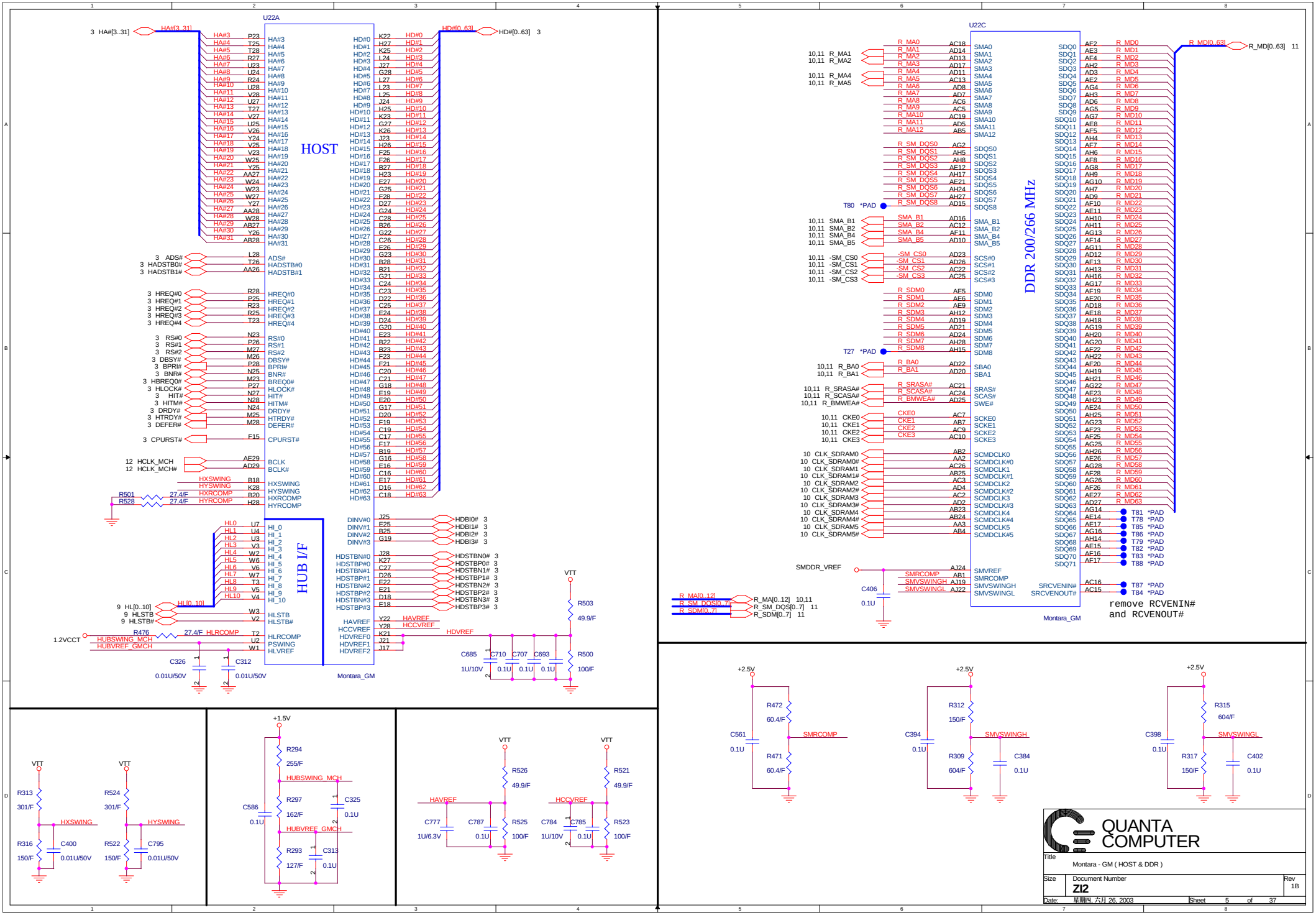
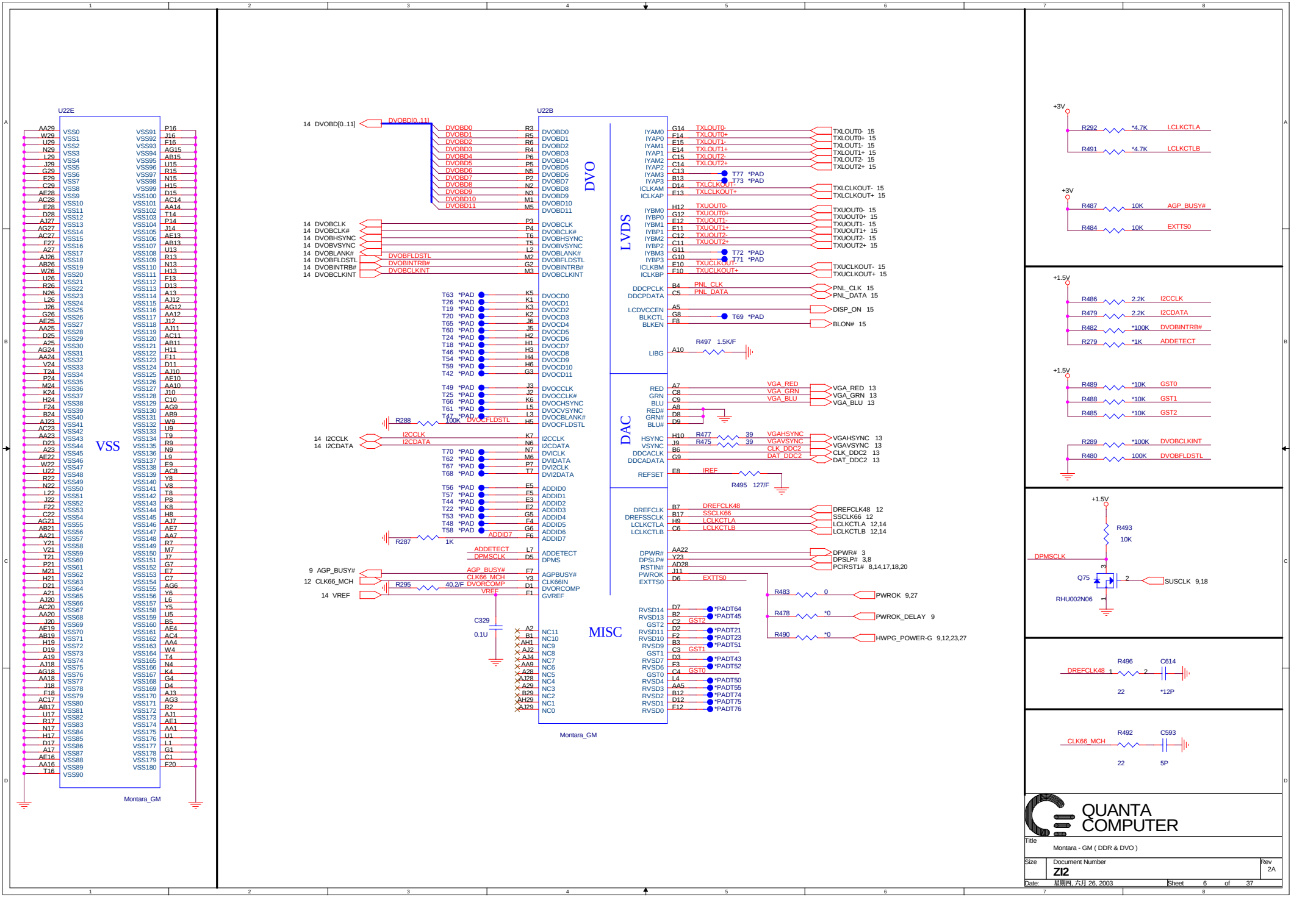


Title	Montara - GM (HOST & DDR)	
Size	Document Number ZI2	Rev 1B
Date: 星期四, 六月 26, 2003	Sheet 5 of 37	

QUANTA COMPUTER


U22E
VSS
Montara_GM
U22B
DVO
LVDS
DAC
MISC
Montara_GM
14 DVOBD[0..11]
DVOBD[0..11]
14 DVOBCLK
14 DVOBCLK#
14 DVOBHSYNC
14 DVOBVSYNC
14 DVOBLANK#
14 DVOBFLDSTL
14 DVOBINTRB#
14 DVOBCLKINT
DVOBFLDSTL
DVOBINTRB#
DVOBCLKINT
R288 100K DVOCFLDSTL
14 I2CCLK
14 I2CDATA
I2CCLK
I2CDATA
R287 1K
ADDETECT
DPMSCLK
9 AGP_BUSY#
12 CLK66_MCH
14 VREF
AGP_BUSY#
CLK66_MCH
R295 40.2/F DVORCOMP
VREF
C329 0.1U
TXLOUT0- 15
TXLOUT0+ 15
TXLOUT1- 15
TXLOUT1+ 15
TXLOUT2- 15
TXLOUT2+ 15
TXLCLKOUT- 15
TXLCLKOUT+ 15
TXUOUT0- 15
TXUOUT0+ 15
TXUOUT1- 15
TXUOUT1+ 15
TXUOUT2- 15
TXUOUT2+ 15
TXUCLKOUT- 15
TXUCLKOUT+ 15
PNL_CLK 15
PNL_DATA 15
DISP_ON 15
BLON# 15
R497 1.5K/F
VGA_RED 13
VGA_GRN 13
VGA_BLU 13
VGAHSYNC 13
VGAVSYNC 13
CLK_DDC2 13
DAT_DDC2 13
R495 127/F
DREFCLK48 12
SSCLK66 12
LCLKCTLA 12,14
LCLKCTLB 12,14
DPWR# 3
DPSLP# 3,8
PCIRST1# 8,14,17,18,20
EXTTS0
R483 0 PWROK 9,27
R478 *0 PWROK_DELAY 9
R490 *0 HWPG_POWER-G 9,12,23,27
+3V
R292 *4.7K LCLKCTLA
R491 *4.7K LCLKCTLB
R487 10K AGP_BUSY#
R484 10K EXTTS0
+1.5V
R486 2.2K I2CCLK
R479 2.2K I2CDATA
R482 *100K DVOBINTRB#
R279 *1K ADDETECT
R489 *10K GST0
R488 *10K GST1
R485 *10K GST2
R289 *100K DVOBCLKINT
R480 100K DVOBFLDSTL
+1.5V
R493 10K
DPMSCLK
Q75
RHU002N06
SUSCLK 9,18
R496 C614
DREFCLK48
22 *12P
R492 C593
CLK66_MCH
22 5P
QUANTA COMPUTER
Title
Montara - GM (DDR & DVO)
Size
Document Number
ZI2
Rev 2A
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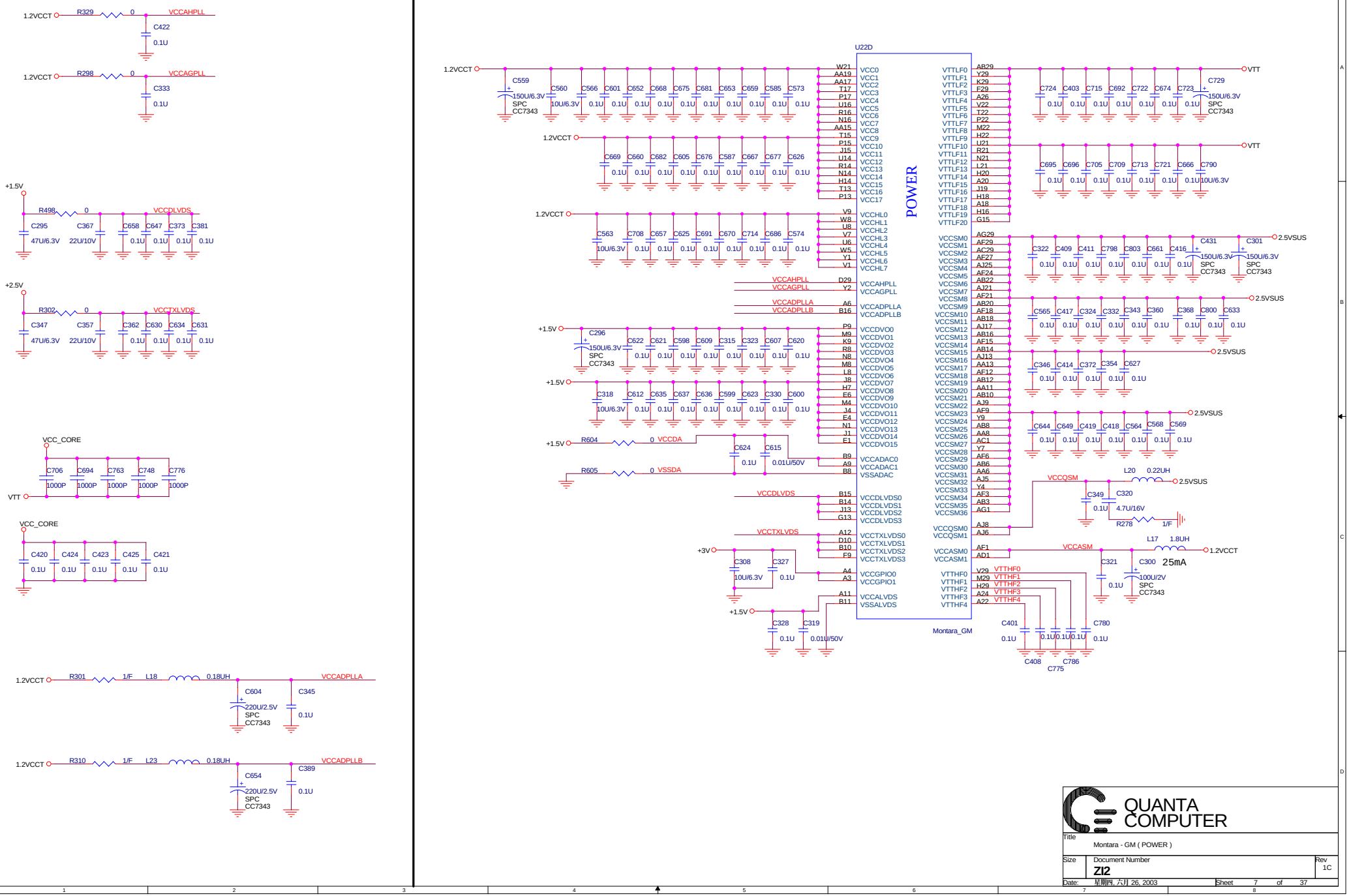
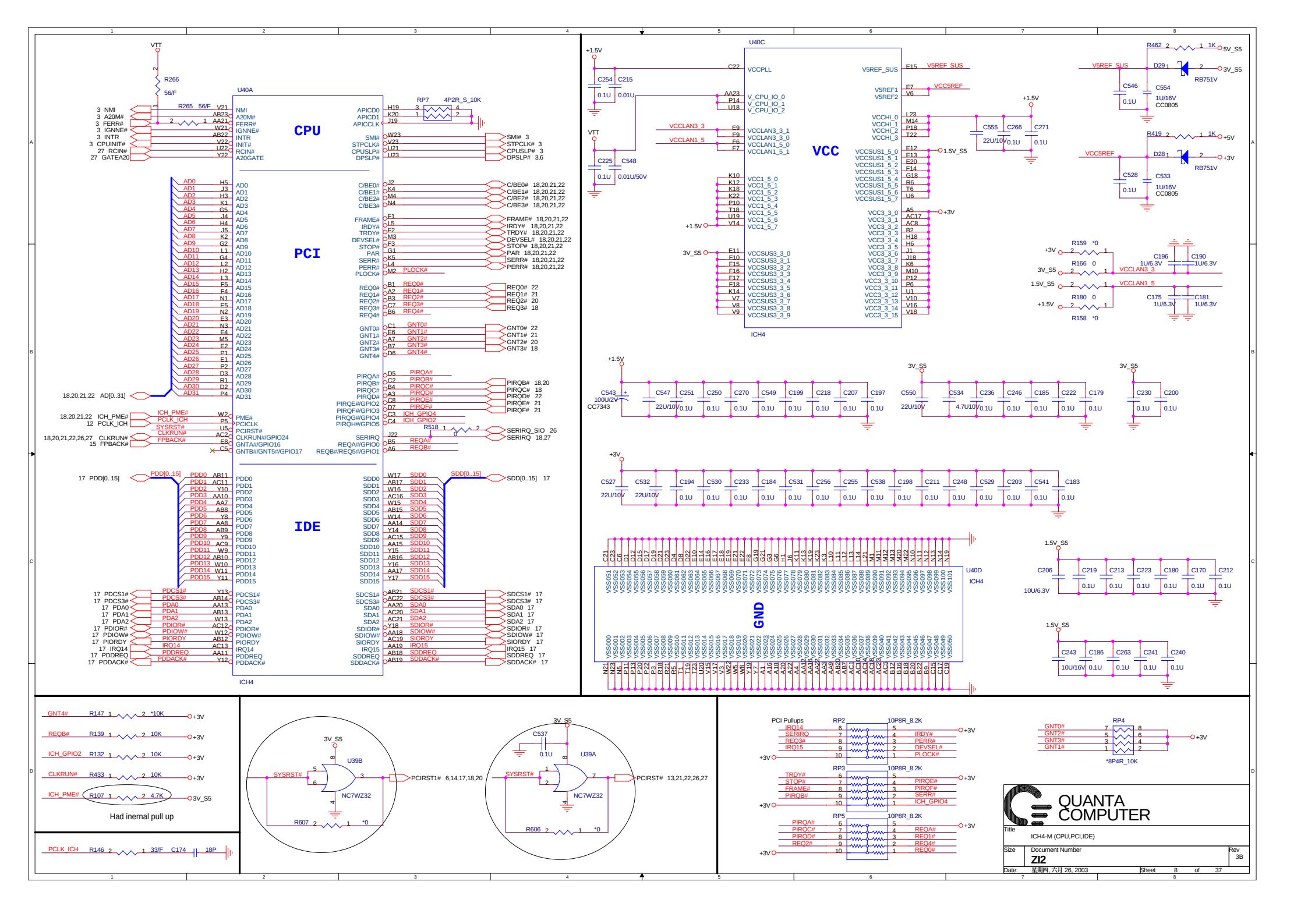


Title	Montara - GM (POWER)		
Size	Document Number		Rev
	ZI2		1C
Date:	星期四, 六月 26, 2003	Sheet 7 of 37	


CPU
PCI
IDE
VCC
GND
ICH4
U40A
U40C
U40D
R266
56/F
RP7
4P2R_S_10K
R265
R147 *10K
R139 10K
R132 10K
R433 10K
R107 4.7K
Had inernal pull up
R146 33/F
C174 18P
U39B
U39A
NC7WZ32
R607 *0
R606 *0
PCI Pullups
RP2
RP3
RP5
10P8R_8.2K
RP4
*8P4R_10K
R462 1K
R419 1K
R159 *0
R166 0
R180 0
R158 *0
R518 0
RB751V
QUANTA COMPUTER
Title
ICH4-M (CPU,PCI,IDE)
Size
Document Number
ZI2
Rev
3B
Date: 星期四, 六月 26, 2003
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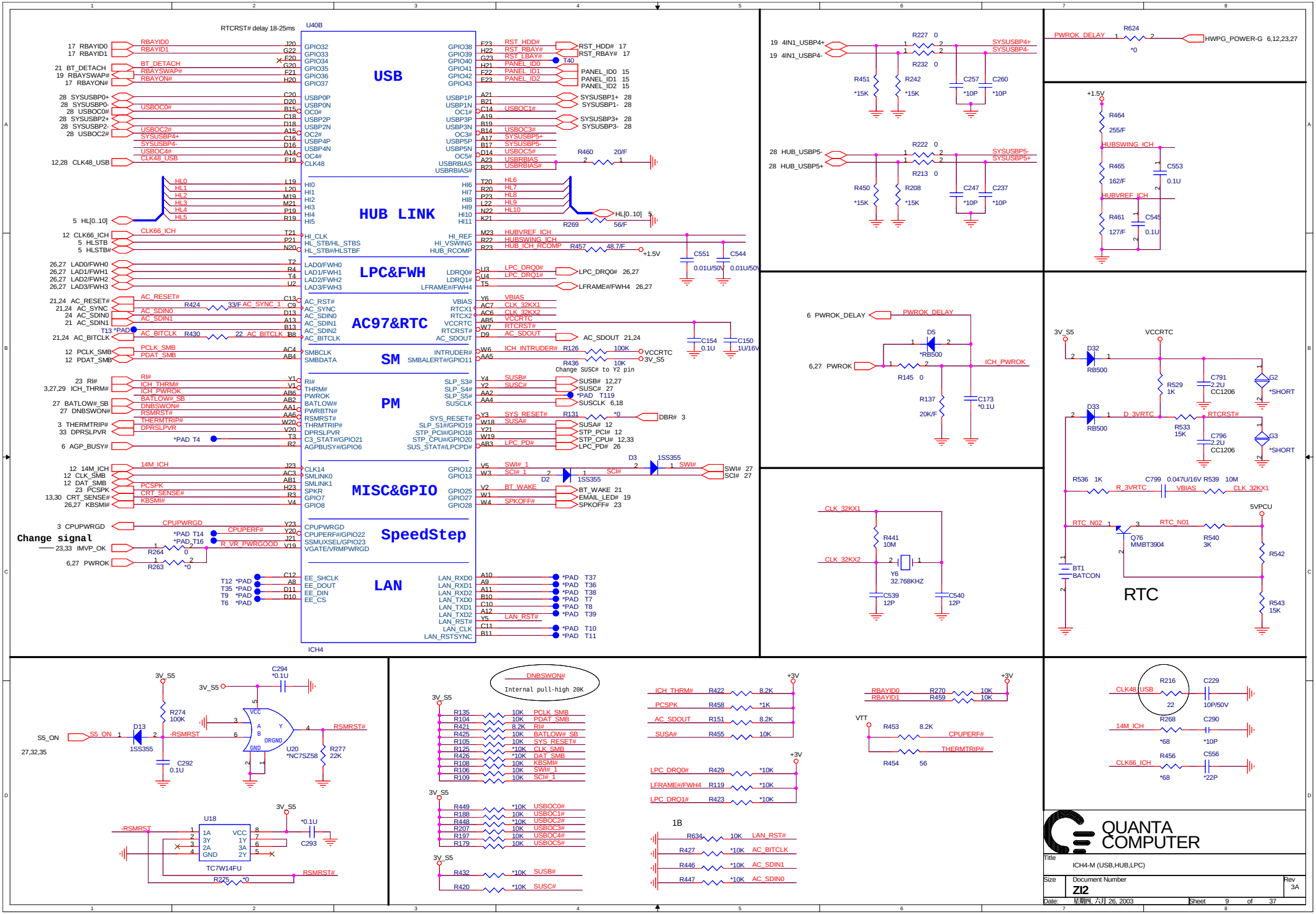
RTCRST# delay 18-25ms
U40B
USB
HUB LINK
LPC&FWH
AC97&RTC
SM
PM
MISC&GPIO
SpeedSteP
LAN
ICH4
Change signal
RTC
QUANTA COMPUTER
Title
ICH4-M (USB,HUB,LPC)
Size
Document Number
ZI2
Rev
3A
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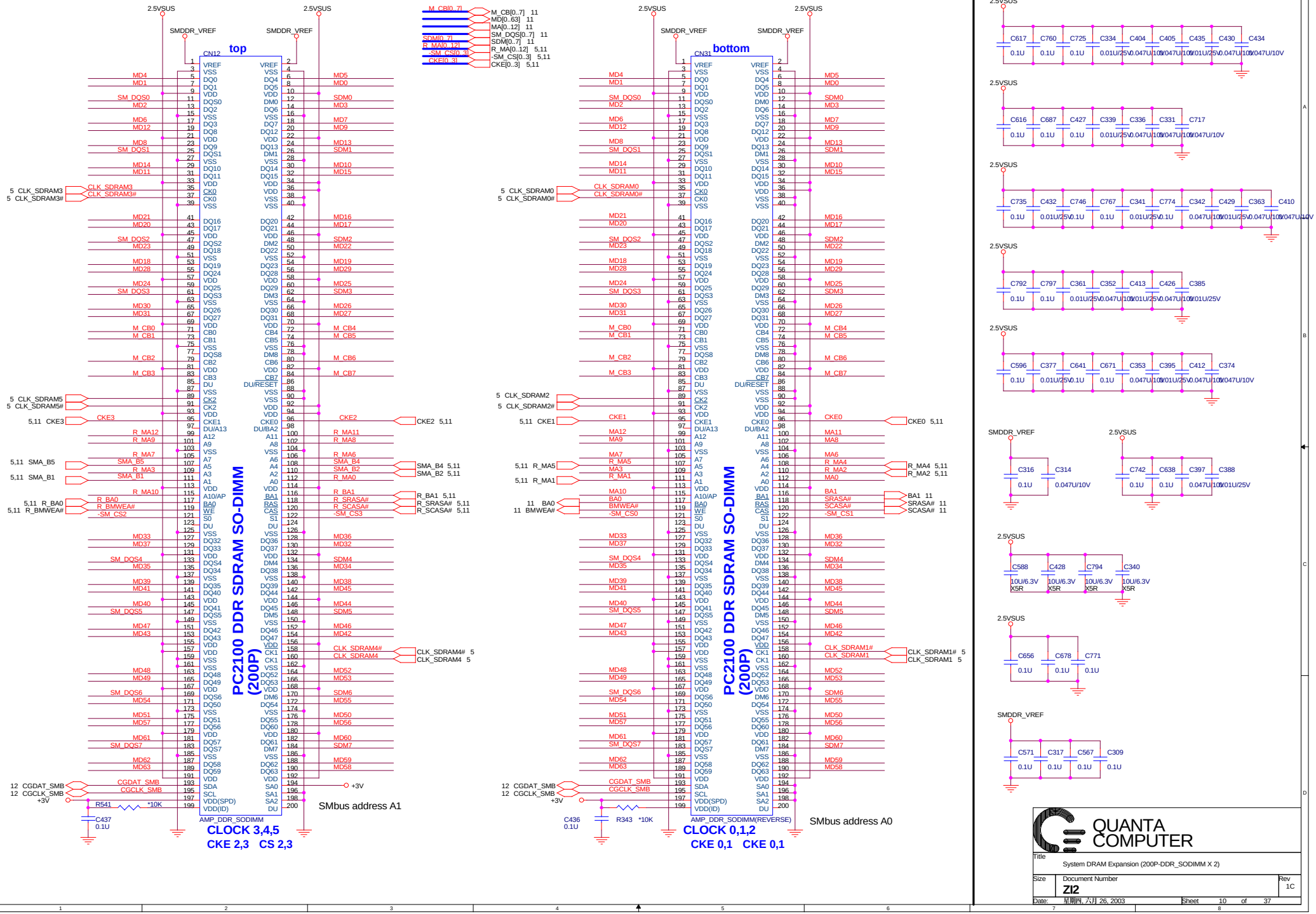
top
bottom
CN12
CN31
PC2100 DDR SDRAM SO-DIMM (200P)
AMP_DDR_SODIMM
AMP_DDR_SODIMM(REVERSE)
2.5VSUS
SMDDR_VREF
+3V
M_CB[0..7] 11
MD[0..63] 11
MA[0..12] 11
SM_DQS[0..7] 11
SDM[0..7] 11
R_MA[0..12] 5,11
-SM_CS[0..3] 5,11
CKE[0..3] 5,11
CLOCK 3,4,5
CKE 2,3 CS 2,3
CLOCK 0,1,2
CKE 0,1 CKE 0,1
SMbus address A1
SMbus address A0
R541 *10K
R343 *10K
C437 0.1U
C436 0.1U
C617 C760 C725 C334 C404 C405 C435 C430 C434
C616 C687 C427 C339 C336 C331 C717
C735 C432 C746 C767 C341 C774 C342 C429 C363 C410
C792 C797 C361 C352 C413 C426 C385
C596 C377 C641 C671 C353 C395 C412 C374
C316 C314
C742 C638 C397 C388
C588 C428 C794 C340
C656 C678 C771
C571 C317 C567 C309
QUANTA COMPUTER
System DRAM Expansion (200P-DDR_SODIMM X 2)
ZI2
Rev 1C
星期四, 六月 26, 2003
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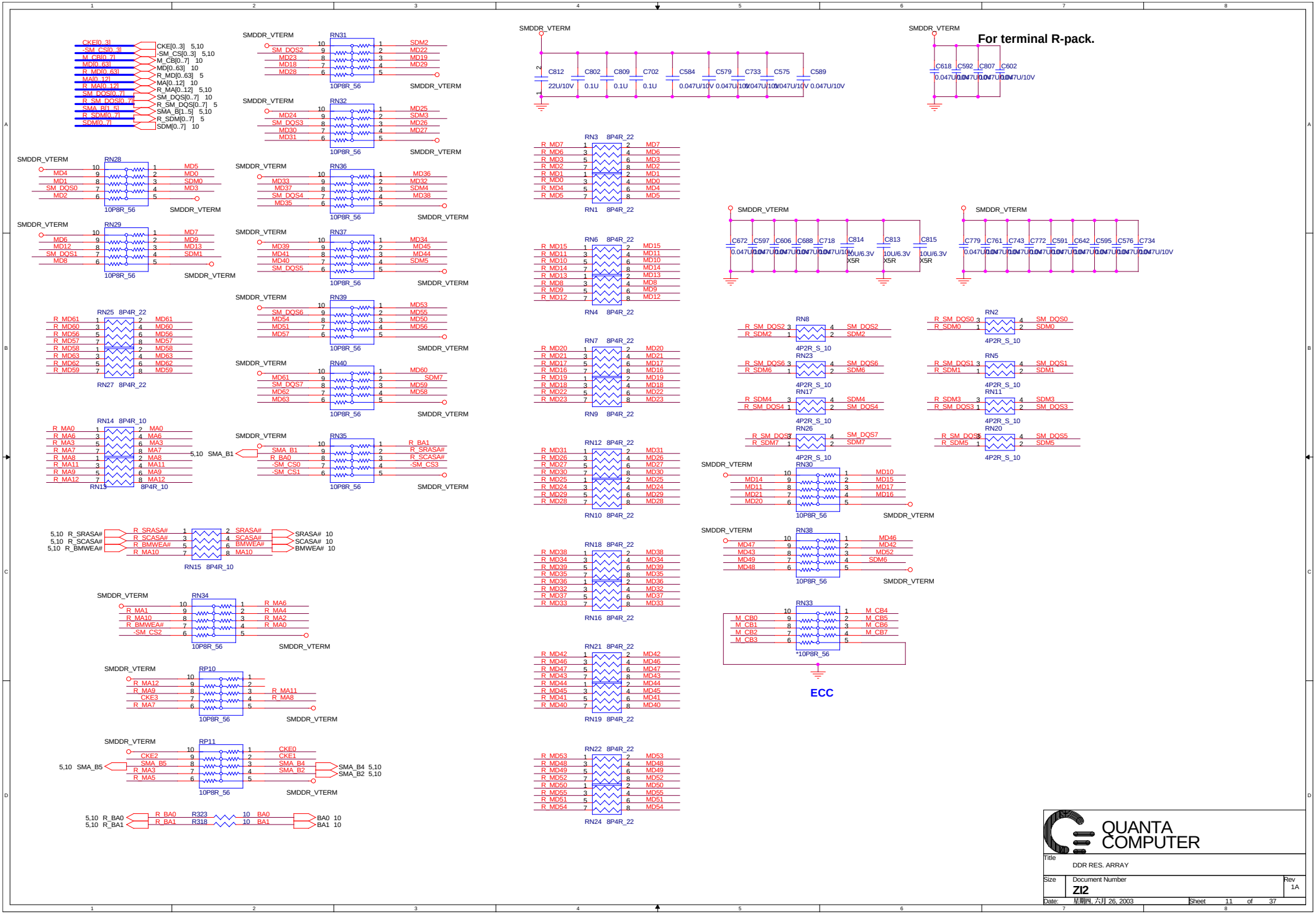
CKE[0..3] 5,10
-SM_CS[0..3] 5,10
M_CB[0..7] 10
MD[0..63] 10
R_MD[0..63] 5
MA[0..12] 10
R_MA[0..12] 5,10
SM_DQS[0..7] 10
R_SM_DQS[0..7] 5
SMA_B[1..5] 5,10
R_SDM[0..7] 5
SDM[0..7] 10
SMDDR_VTERM
RN31
SM_DQS2 MD22 MD23 MD19 MD18 MD29 MD28
10P8R_56
RN32
MD24 MD25 SM_DQS3 MD26 MD30 MD27 MD31
RN28
MD4 MD5 MD1 MD0 SM_DQS0 MD3 MD2
RN36
MD33 MD36 MD37 MD32 SM_DQS4 MD34 MD35 MD38
RN29
MD6 MD7 MD12 MD9 SM_DQS1 MD13 MD8
RN37
MD39 MD34 MD41 MD45 MD40 MD44 SM_DQS5 SDM5
RN25 8P4R_22
R_MD61 MD61 R_MD60 MD60 R_MD56 MD56 R_MD57 MD57 R_MD58 MD58 R_MD63 MD63 R_MD62 MD62 R_MD59 MD59
RN27 8P4R_22
RN39
SM_DQS6 MD53 MD54 MD55 MD51 MD50 MD57 MD56
RN40
MD61 MD60 SM_DQS7 MD59 MD62 MD58 MD63
RN14 8P4R_10
R_MA0 MA0 R_MA6 MA6 R_MA3 MA3 R_MA7 MA7 R_MA8 MA8 R_MA11 MA11 R_MA9 MA9 R_MA12 MA12
RN13 8P4R_10
5,10 SMA_B1
RN35
SMA_B1 R_BA1 R_BA0 R_SRASA# -SM_CS0 R_SCASA# -SM_CS1 -SM_CS3
5,10 R_SRASA#
5,10 R_SCASA#
5,10 R_BWEA#
R_SRASA# SRASA# 10
R_SCASA# SCASA# 10
R_BWEA# BWEA# 10
R_MA10 MA10
RN15 8P4R_10
RN34
R_MA1 R_MA6 R_MA10 R_MA4 R_BWEA# R_MA2 -SM_CS2 R_MA0
RP10
R_MA12 R_MA11 R_MA9 R_MA8 CKE3 R_MA7
RP11
CKE2 CKE0 CKE1 SMA_B5 SMA_B4 SMA_B4 5,10 R_MA3 SMA_B2 SMA_B2 5,10 R_MA5
5,10 R_BA0
5,10 R_BA1
R_BA0 R323 10 BA0
R_BA1 R318 10 BA1
BA0 10
BA1 10
SMDDR_VTERM
C812 C802 C809 C702 C584 C579 C733 C575 C589
22U/10V 0.1U 0.1U 0.1U 0.047U/10V 0.047U/10V 0.047U/10V 0.047U/10V 0.047U/10V
For terminal R-pack.
C618 C592 C807 C602
0.047U/10V
RN3 8P4R_22
R_MD7 MD7 R_MD6 MD6 R_MD3 MD3 R_MD2 MD2 R_MD1 MD1 R_MD0 MD0 R_MD4 MD4 R_MD5 MD5
RN1 8P4R_22
C672 C597 C606 C688 C718 C814 C813 C815
10U/6.3V X5R
C779 C761 C743 C772 C591 C642 C595 C576 C734
RN6 8P4R_22
R_MD15 MD15 R_MD11 MD11 R_MD10 MD10 R_MD14 MD14 R_MD13 MD13 R_MD8 MD8 R_MD9 MD9 R_MD12 MD12
RN4 8P4R_22
RN8
R_SM_DQS2 SM_DQS2 R_SDM2 SDM2
4P2R_S_10
RN2
R_SM_DQS0 SM_DQS0 R_SDM0 SDM0
RN7 8P4R_22
R_MD20 MD20 R_MD21 MD21 R_MD17 MD17 R_MD16 MD16 R_MD19 MD19 R_MD18 MD18 R_MD22 MD22 R_MD23 MD23
RN9 8P4R_22
RN23
R_SM_DQS6 SM_DQS6 R_SDM6 SDM6
RN5
R_SM_DQS1 SM_DQS1 R_SDM1 SDM1
RN17
R_SDM4 SDM4 R_SM_DQS4 SM_DQS4
RN11
R_SDM3 SDM3 R_SM_DQS3 SM_DQS3
RN12 8P4R_22
R_MD31 MD31 R_MD26 MD26 R_MD27 MD27 R_MD30 MD30 R_MD25 MD25 R_MD24 MD24 R_MD29 MD29 R_MD28 MD28
RN10 8P4R_22
RN26
R_SM_DQS7 SM_DQS7 R_SDM7 SDM7
RN20
R_SM_DQS5 SM_DQS5 R_SDM5 SDM5
RN30
MD14 MD10 MD11 MD15 MD21 MD17 MD20 MD16
10P8R_56
RN18 8P4R_22
R_MD38 MD38 R_MD34 MD34 R_MD39 MD39 R_MD35 MD35 R_MD36 MD36 R_MD32 MD32 R_MD37 MD37 R_MD33 MD33
RN16 8P4R_22
RN38
MD47 MD46 MD43 MD42 MD49 MD52 MD48 SDM6
RN21 8P4R_22
R_MD42 MD42 R_MD46 MD46 R_MD47 MD47 R_MD43 MD43 R_MD44 MD44 R_MD45 MD45 R_MD41 MD41 R_MD40 MD40
RN19 8P4R_22
RN33
M_CB0 M_CB4 M_CB1 M_CB5 M_CB2 M_CB6 M_CB3 M_CB7
*10P8R_56
ECC
RN22 8P4R_22
R_MD53 MD53 R_MD48 MD48 R_MD49 MD49 R_MD52 MD52 R_MD50 MD50 R_MD55 MD55 R_MD51 MD51 R_MD54 MD54
RN24 8P4R_22
QUANTA COMPUTER
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DDR RES. ARRAY
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1A
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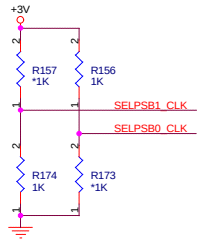
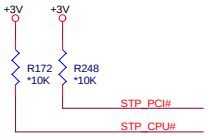
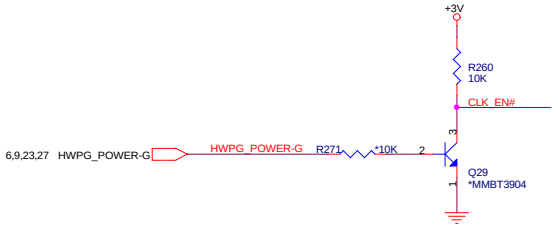
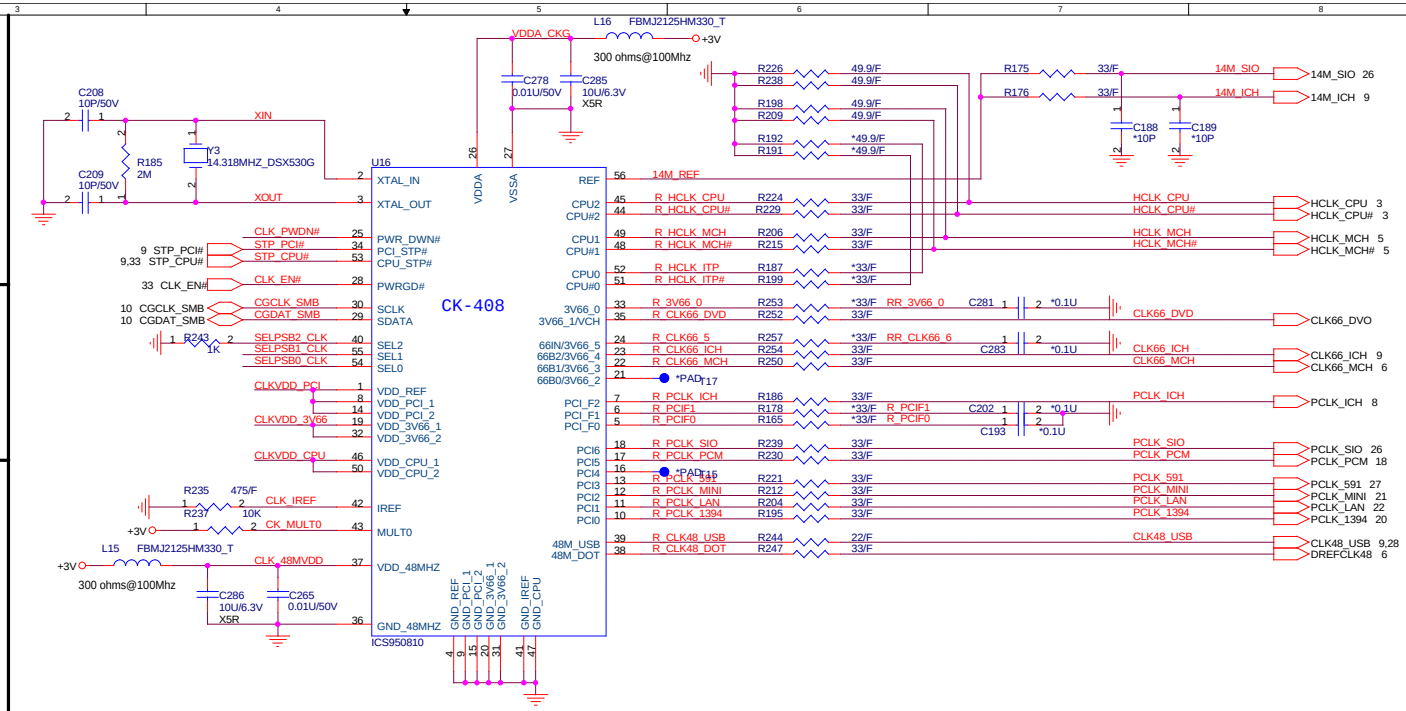
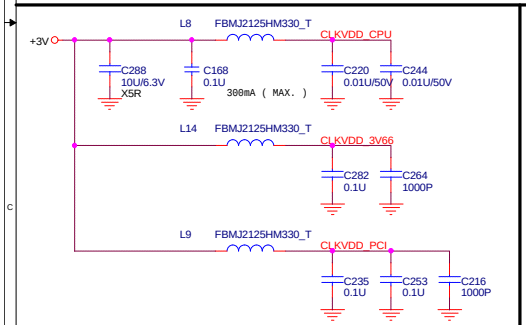
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input

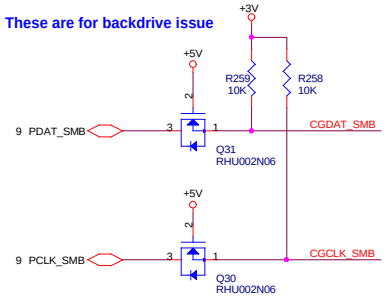


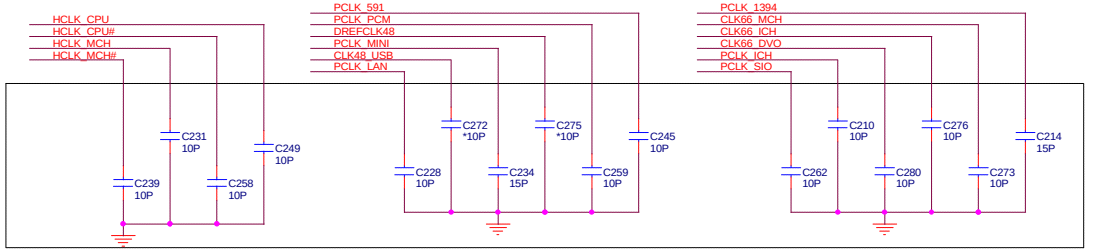


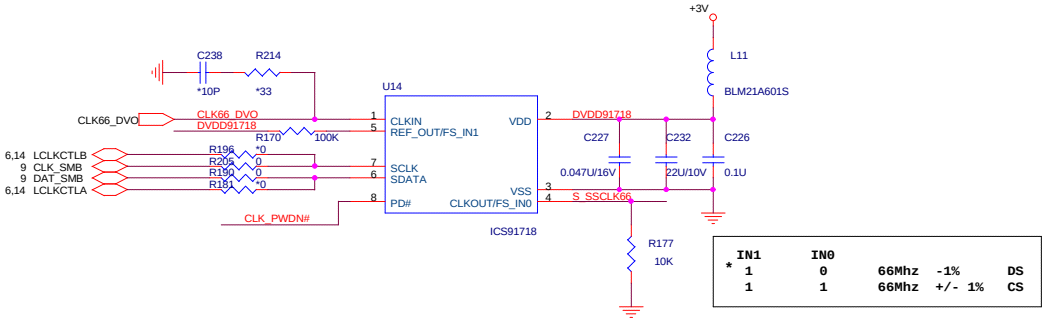


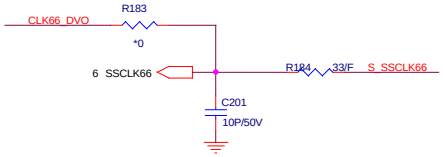


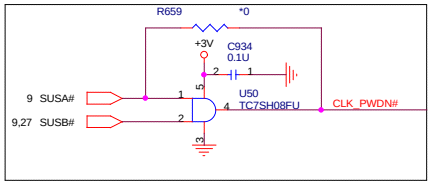












QUANTA COMPUTER

Title: CLOCK GENERATOR

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Custom	ZI2	3C

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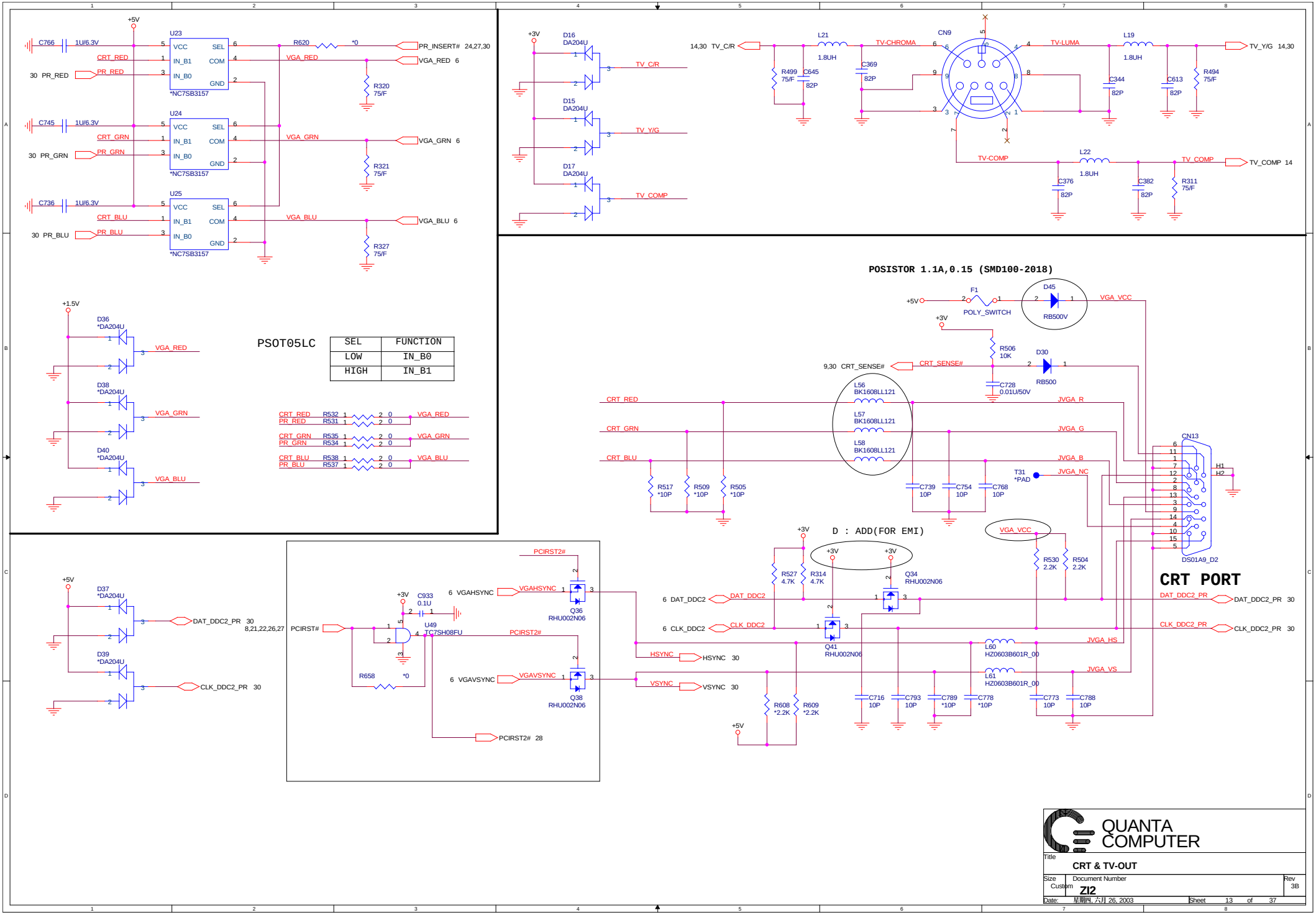
U23
U24
U25
*NC7SB3157
VCC
SEL
IN_B1
COM
IN_B0
GND
C766 1U/6.3V
C745 1U/6.3V
C736 1U/6.3V
CRT_RED
CRT_GRN
CRT_BLU
PR_RED
PR_GRN
PR_BLU
30 PR_RED
30 PR_GRN
30 PR_BLU
R620 *0
VGA_RED
VGA_GRN
VGA_BLU
PR_INSERT# 24,27,30
VGA_RED 6
VGA_GRN 6
VGA_BLU 6
R320 75/F
R321 75/F
R327 75/F
D16 DA204U
D15 DA204U
D17 DA204U
TV_C/R
TV_Y/G
TV_COMP
14,30 TV_C/R
L21 1.8UH
L19 1.8UH
L22 1.8UH
TV-CHROMA
TV-LUMA
TV-COMP
CN9
R499 75/F
C645 82P
C369 82P
C344 82P
C613 82P
R494 75/F
C376 82P
C382 82P
R311 75/F
TV_Y/G 14,30
TV_COMP 14
D36 *DA204U
D38 *DA204U
D40 *DA204U
PSOT05LC
SEL FUNCTION
LOW IN_B0
HIGH IN_B1
CRT_RED R532 PR_RED R531
CRT_GRN R535 PR_GRN R534
CRT_BLU R538 PR_BLU R537
POSISTOR 1.1A,0.15 (SMD100-2018)
F1 POLY_SWITCH
D45 RB500V
VGA_VCC
R506 10K
D30 RB500
9,30 CRT_SENSE#
C728 0.01U/50V
L56 BK1608LL121
L57 BK1608LL121
L58 BK1608LL121
JVGA_R
JVGA_G
JVGA_B
JVGA_NC
T31 *PAD
R517 *10P
R509 *10P
R505 *10P
C739 10P
C754 10P
C768 10P
CN13
DS01A9_D2
H1
H2
D : ADD(FOR EMI)
Q34 RHU002N06
Q41 RHU002N06
R527 4.7K
R314 4.7K
R530 2.2K
R504 2.2K
DAT_DDC2_PR 30
CLK_DDC2_PR 30
JVGA_HS
JVGA_VS
L60 HZ0603B601R_00
L61 HZ0603B601R_00
HSYNC 30
VSYNC 30
R608 *2.2K
R609 *2.2K
C716 10P
C793 10P
C789 *10P
C778 *10P
C773 10P
C788 10P
CRT PORT
D37 *DA204U
D39 *DA204U
DAT_DDC2_PR 30 8,21,22,26,27
CLK_DDC2_PR 30
PCIRST#
PCIRST2#
PCIRST2# 28
C933 0.1U
U49 TC7SH08FU
R658 *0
6 VGAHSYNC
6 VGAVSYNC
VGAHSYNC
VGAVSYNC
Q36 RHU002N06
Q38 RHU002N06
6 DAT_DDC2
6 CLK_DDC2
DAT_DDC2
CLK_DDC2
QUANTA COMPUTER
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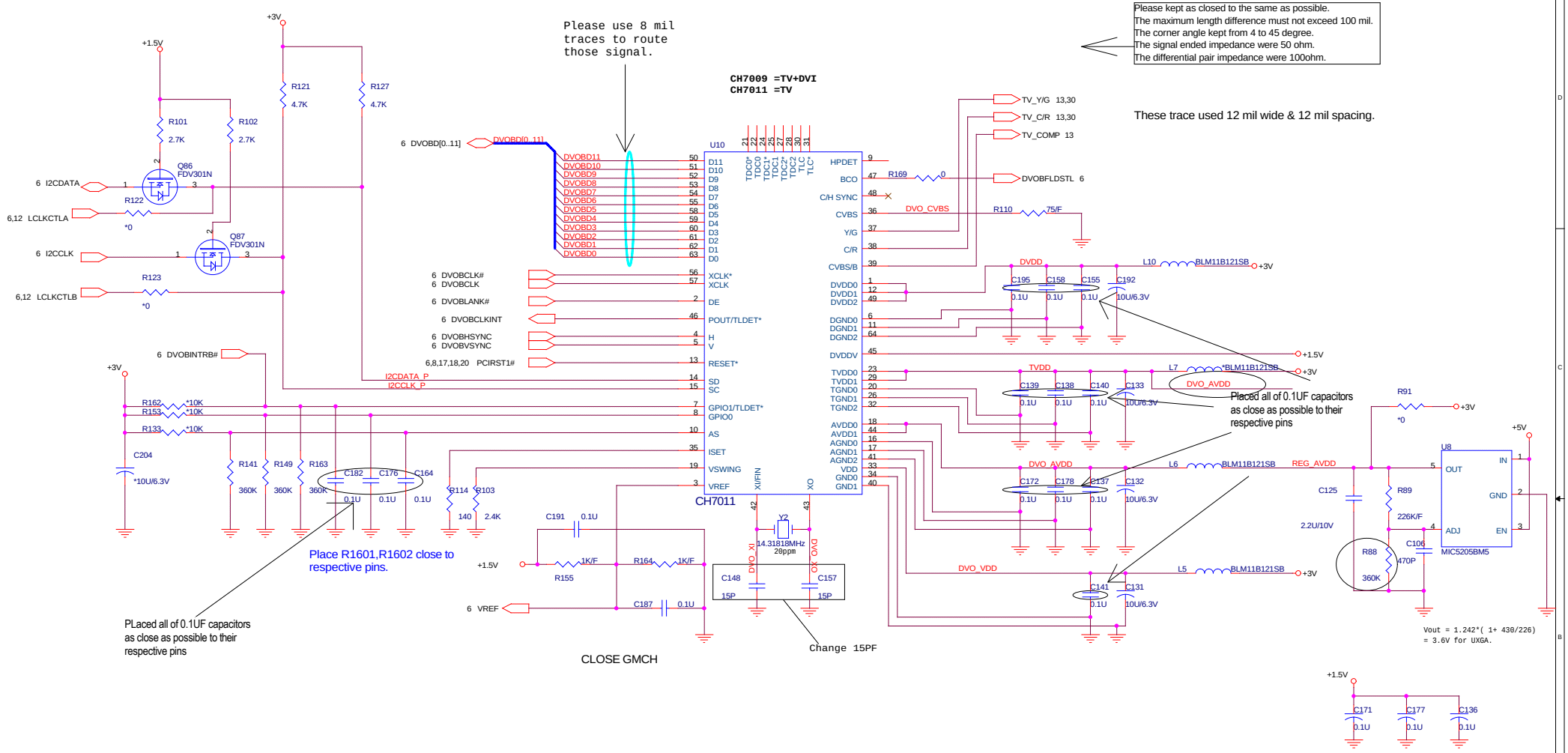

DVOBCLK#	66MHz
DVOBCLK	66MHz
DVOBLANK#	56KHz
DVOBCLKINT	66MHz
DVOBHSYNC	56KHz
DVOBVSYNC	60Hz

QUANTA COMPUTER

Title	DVO (DVI + S_VIDEO)	
Size	Document Number ZI2	Rev 3B
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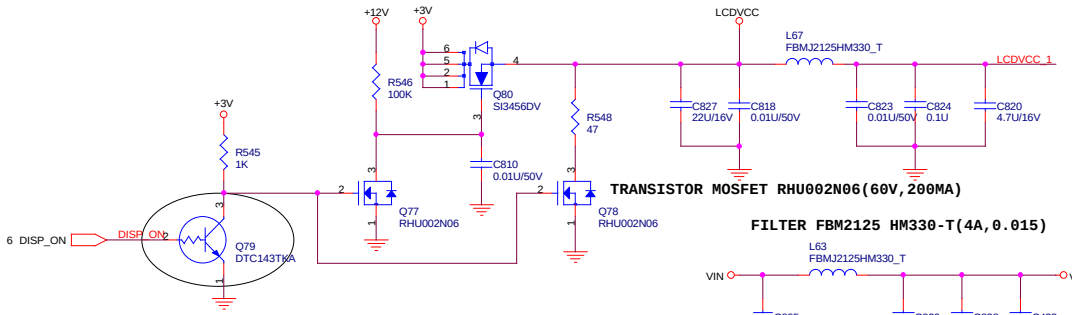
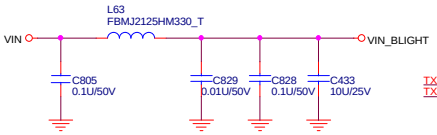
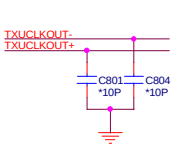
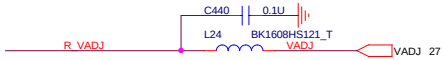
FILTER FBM2125 HM330-T(4A,0.015)

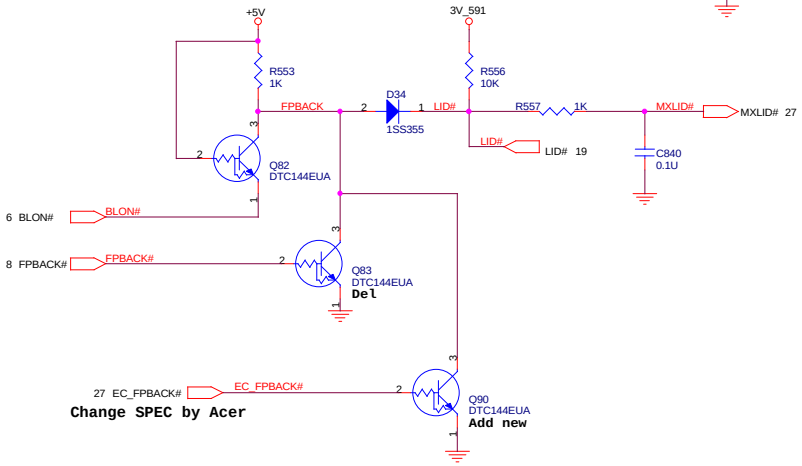


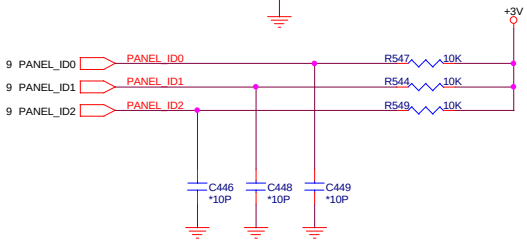
FILTER FBM2125 HM330-T(4A,0.015)





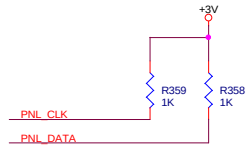


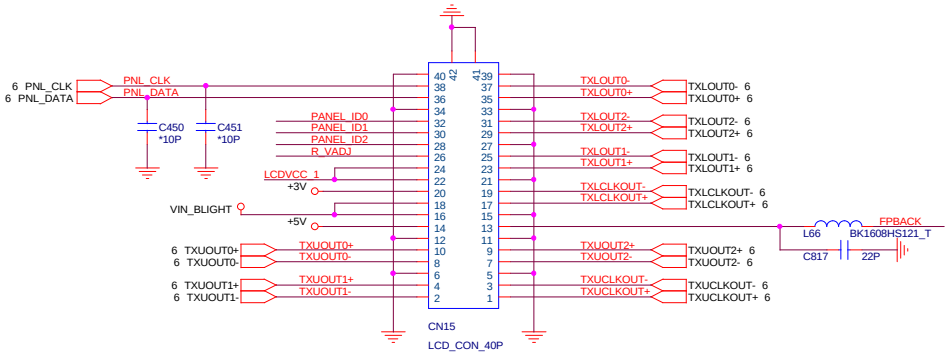




PANEL ID:

		PANEL_ID2	PANEL_ID1	PANEL_ID0
ID3	XGA	0	1	1
ID4	UXGA	1	0	0
ID5	SXGA+	1	0	1
ID6	SXGA	1	1	0
ID7	NC	1	1	1





QUANTA COMPUTER

Title	LCD CONN	
Size: Custom	Document Number: ZI2	Rev: 3A
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H15 *HOLE_C118D118N HOLE-C118D118N
H7 *R59X157DO20X100P2 HOLE-R59X157DO20X100P2
H3 *S394D106P2 HOLE-S394D106P2
H6 *TS315BS394D106P2 HOLE-TS315BS394D106P2
H10 TC315BS394D197P2 HOLE-TC315BS394D197P2
H21 TC236B157D118P2 HOLE-TC236BC157D118P2
H12 TR335X276BS393D118P2 HOLE-TR285X276BC236D118P2
H5 *C236D118P2 HOLE-C236D118P2
H36 C236D157P2 HOLE-C236D157P2
H24 C236D157P2 HOLE-C236D157P2
H17 C236D157P2 HOLE-C236D157P2
H31 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H28 *TC315BS395D110P2_A HOLE-TC315BS395D110P2-A
H1 *TC315BS395D106P2 HOLE-TC315BS395D106P2
AUDGND
H13 *HOLE_C118D118N HOLE-C315D315N
H16 *TS315BS394D106P2 HOLE-TS315BS394D106P2
H29 TC315BS394D197P2 HOLE-TC315BS394D197P2
H22 TC236B157D118P2 HOLE-TC236BC157D118P2
H11 TR335X276BS393D118P2 HOLE-TR285X276BC236D118P2
H2 *C236D118P2 HOLE-C236D118P2
H26 C236D157P2 HOLE-C236D157P2
H14 C236D157P2 HOLE-C236D157P2
H32 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H27 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H4 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H38 *TS315BS394D106P2 HOLE-TS315BS394D106P2
H25 TC236B157D118P2 HOLE-TC236BC157D118P2
H8 *C236D118P2 HOLE-C236D118P2
H33 *C236D157P2 HOLE-C236D157P2
H18 C236D157P2 HOLE-C236D157P2
H35 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H30 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H9 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H34 *C236D157P2 HOLE-C236D157P2
H23 C236D157P2 HOLE-C236D157P2
H37 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H19 *TC315BS395D106P2 HOLE-TC315BS395D106P2
H20 *TC315BS395D106P2 HOLE-TC315BS395D106P2

C887 0.01U/50V C472 0.01U/50V
+3V_2.5V_LAN
DB00A2LAN01(24ST0023P) is for 10/100
DB00A2LAN10(GS5019P) is for giga
U32
BCM-CT1 BCM-CT2
TX0P_RR TX0N_RR TX1P_RR TX1N_RR TX2P_RR TX2N_RR TX3P_RR TX3N_RR
TCT1 TD1+ TD1- TCT2 TD2+ TD2- TCT3 TD3+ TD3- TCT4 TD4+ TD4-
MCT1 MX1+ MX1- MCT2 MX2+ MX2- MCT3 MX3+ MX3- MCT4 MX4+ MX4-
MCT1_R595 MCT2_R596 MCT3_R597 MCT4_R598 75/F
X-TX0P R591 0 X-TX0N R592 0 X-TX1P R593 0 X-TX1N R594 0
X-TX0P-PR X-TX0N-PR X-TX1P-PR X-TX1N-PR
X-TX0P-PR 30 X-TX0N-PR 30 X-TX1P-PR 30 X-TX1N-PR 30
X-TX2P X-TX2N X-TX3P X-TX3N
*GS5019P
C888 0.01U/50V C889 0.01U/50V
LAN_1
C473 1000P/2KV CC1808
GND_LAN_CHASIS

U31
BCM-CT1 TX0P_RR TX0N_RR TX1P_RR TX1N_RR BCM-CT2
CT TD+ TD- CT RD+ RD- CT CT TX+ TX- RX+ RX- CT
MCT1 X-TX0P X-TX0N X-TX1P X-TX1N MCT2
ATPL_119

PAD31 *EMIPAD_S
PAD39 FDET3001015
PAD49 FDET3001015
For LAN cable latch For LAN cable latch

CN23
X-TX0P X-TX0N X-TX1P X-TX2P X-TX2N X-TX1N X-TX3P X-TX3N
TX+/0+ TX-/0- RX+/1+ NC1/2+ NC2/2- RX-/1- NC3/+ NC4/3- TIP RING
CATHODE1 GREEN ANODE1 CATHODE2 YELLOW ANODE2 GND1 GND2
R582 *0 LAN_1000LED# 22
R584 0 LAN_100LED# 22,30
R603 *0 LAN_LILED# 22
RJ45_LANVCC1 R381 330 LANVCC
R602 *330 LAN_1000LED# 22
RJ45_LANVCC2 R380 330 LANVCC
LAN_ACTLED# 22,30
LAN_MODEM_CONN
CN36 TIP_RJ11 RING_RJ11 MDC
C475 1000P/3KV CC4520 C476 1000P/3KV CC4520
This pin will interfere with screw hole, change to GND.
PAD37 *EMIPAD_S GND_LAN_CHASIS
C474 10U/10V C890 1000P C894 1000P C892 10U/10V C893 1000P C891 1000P
R601 0 R600 0 R599 0
GND_LAN_CHASIS

CN34 TX0P_RR TX0N_RR TX1P_RR TX1N_RR TX2P_RR TX2N_RR TX3P_RR TX3N_RR
*85204_0800
CN35 TX0P_RR TX0N_RR TX1P_RR TX1N_RR
53398_0410_4P

PAD10 FDET2002016
PAD11 FDET2002016
PAD41 FDET2002016
PAD17 FDZI1001016
PAD13 FDZI1001016
PAD24 *EMIPAD_S
PAD46 FDZI1001016
PAD43 FDZI1001016
PAD44 FDZI1001016
PAD22 *FDZI1001016
PAD26 *EMIPAD_S
PAD38 *EMIPAD_S
PAD35 FDZI1001016
PAD45 FDZI1001016
PAD34 *FDZI1001016
PAD20 *EMIPAD_S
PAD18 *EMIPAD_S
PAD32 *EMIPAD_S
PAD19 *EMIPAD_S
PAD29 FDZI1001016
PAD16 *FDCA1002017
PAD28 *EMIPAD_S
PAD30 *EMIPAD_S
PAD25 *EMIPAD_S
PAD36 *EMIPAD_S
PAD27 *EMIPAD_S
PAD15 FDZI1001016
PAD21 *EMIPAD_S
PAD33 *EMIPAD_S

QUANTA COMPUTER

Title	RJ11 & RJ45 & HOLES	
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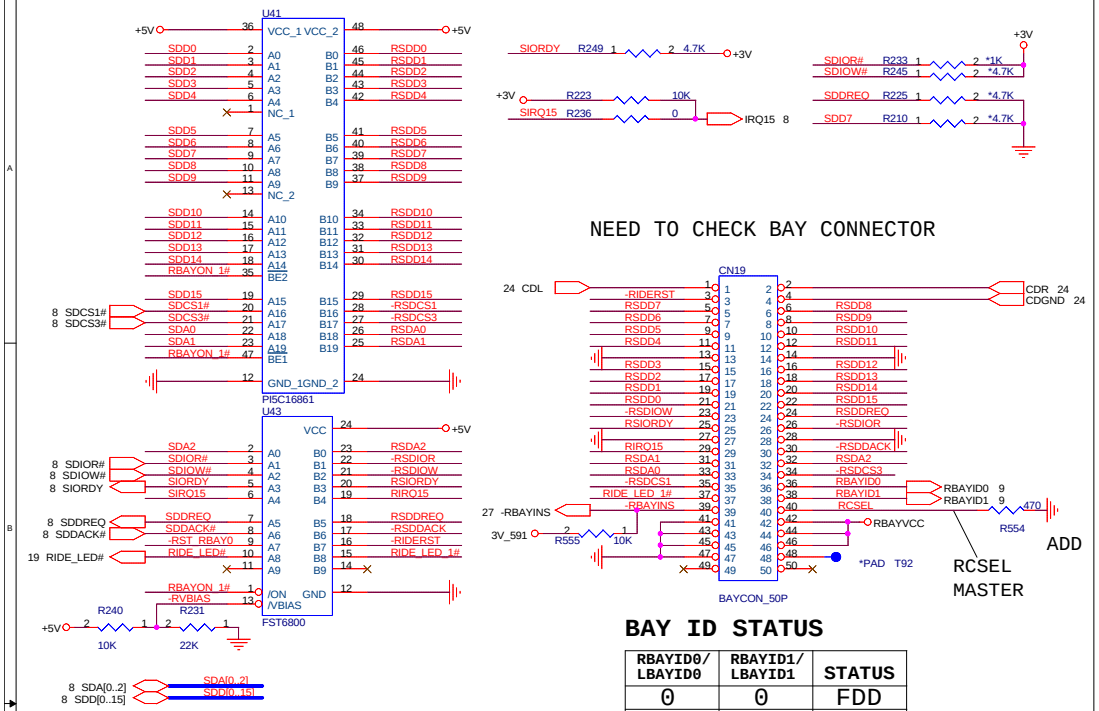
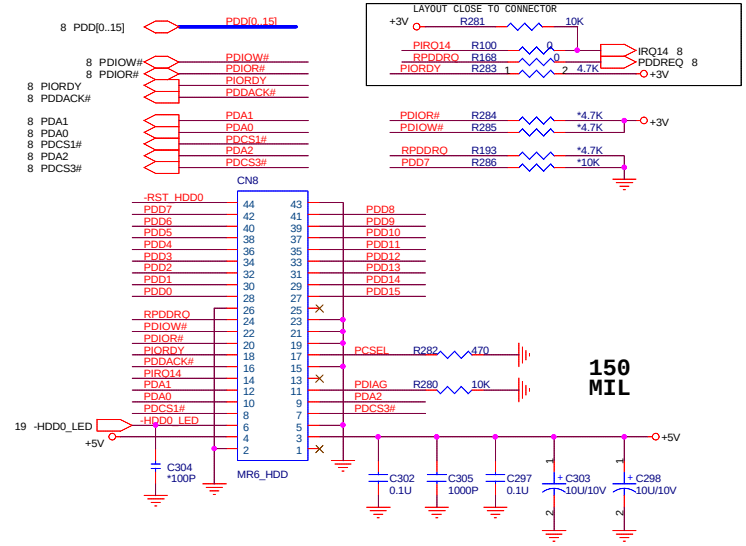


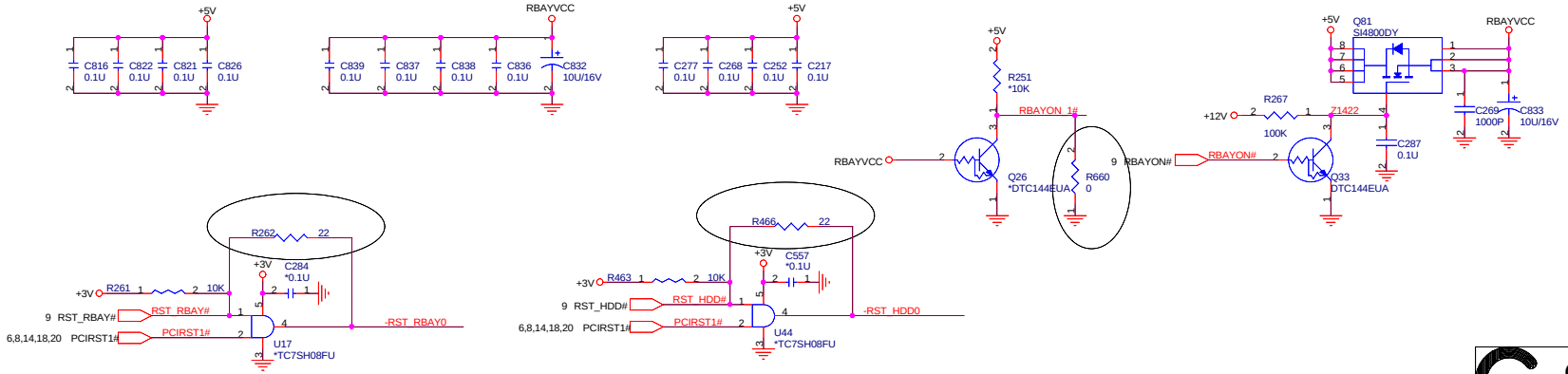
BAY ID STATUS

RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	0	FDD
0	1	HDD
1	0	CD/DVD

SWAP BAY: OPTICAL DRIVE, 2ND BATTERY, 2ND HDD



HDD CONNECTOR



SWAP BAY POWER CONTROL& RESET

QUANTA COMPUTER

Title: DEVICE BAY

Size: Custom | Document Number: ZI2 | Rev: 3B

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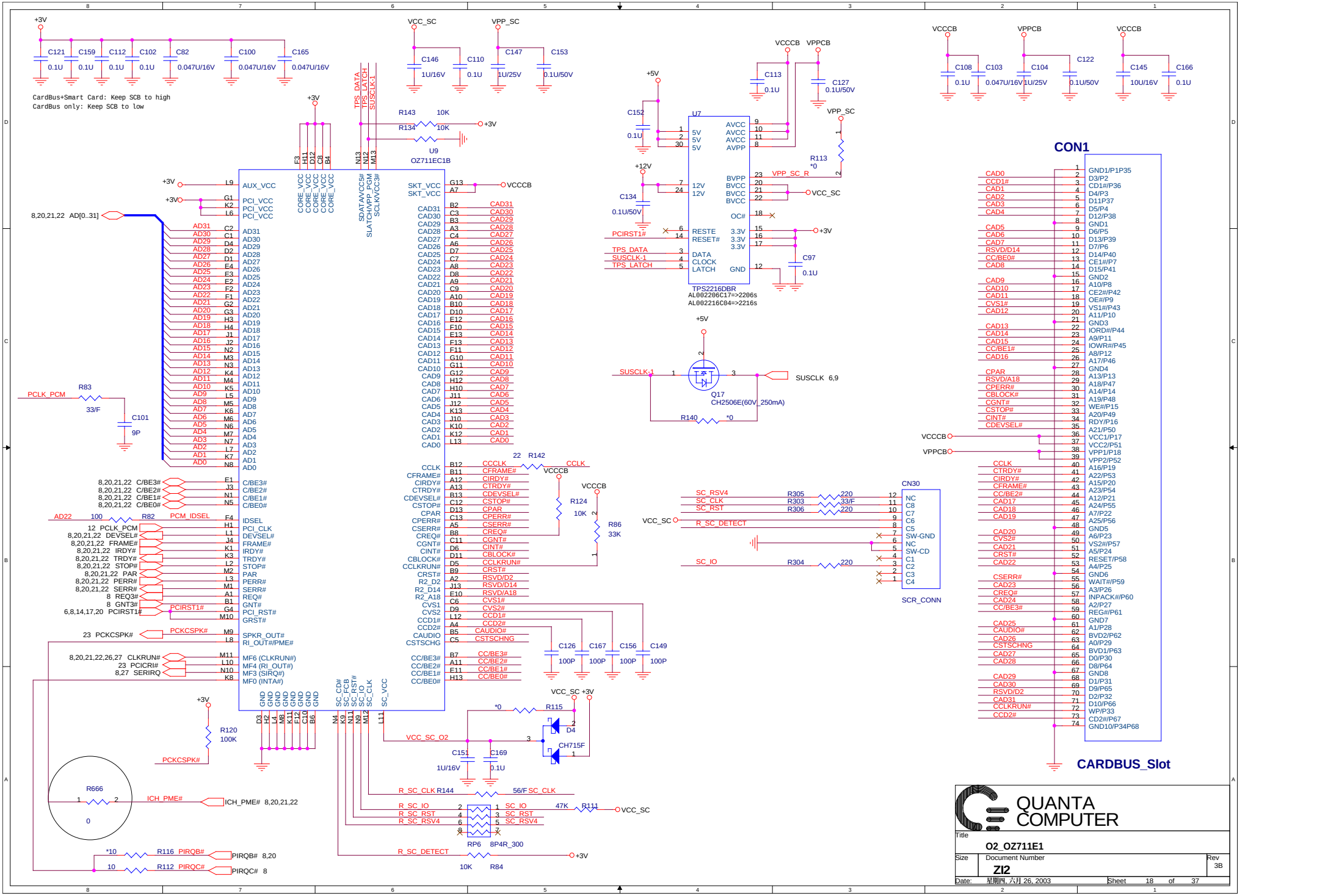
CardBus+Smart Card: Keep SCB to high
CardBus only: Keep SCB to low
U9
OZ711EC1B
U7
TPS2216DBR
AL002206C17=>2206s
AL002216C04=>2216s
Q17
CH2506E(60V_250mA)
CON1
CARDBUS_Slot
CN30
SCR_CONN
RP6 8P4R_300
D4
CH715F
QUANTA COMPUTER
Title
O2_OZ711E1
Size
Document Number
ZI2
Rev
3B
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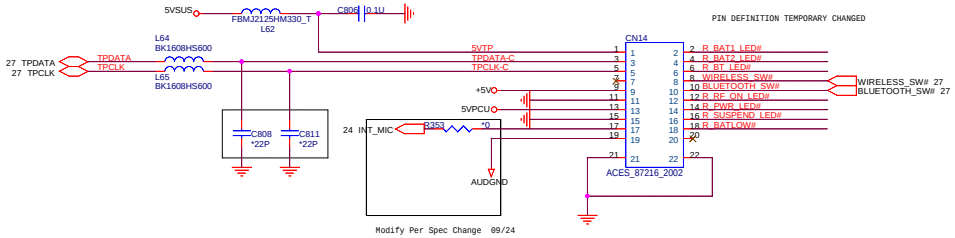
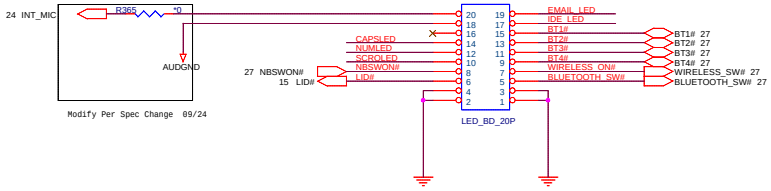
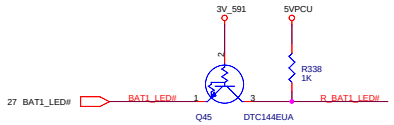
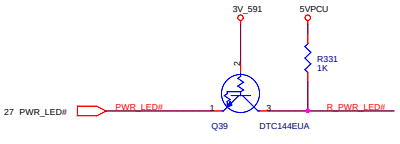
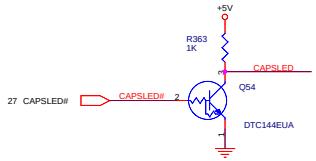
12 MIL

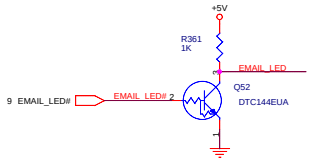


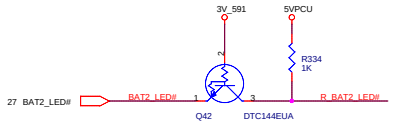


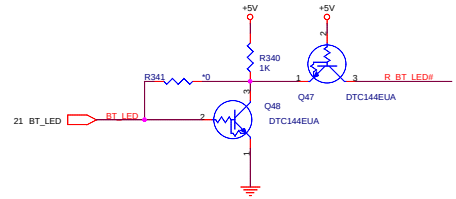


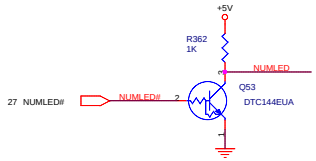


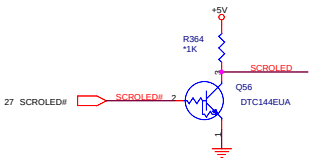


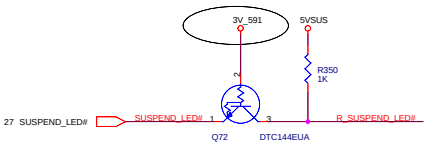


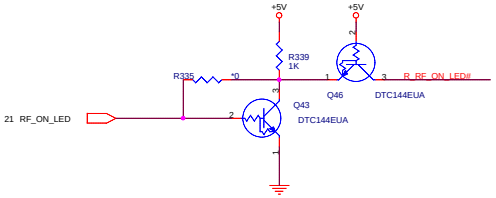


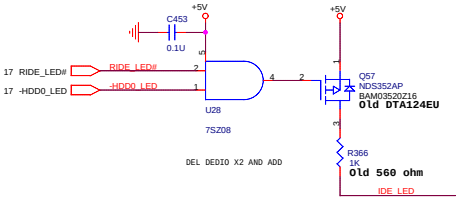


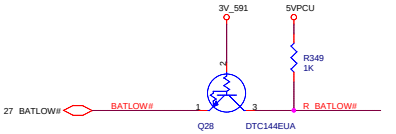






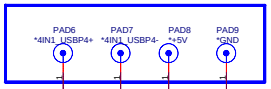


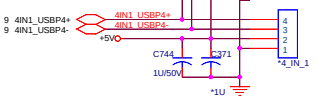


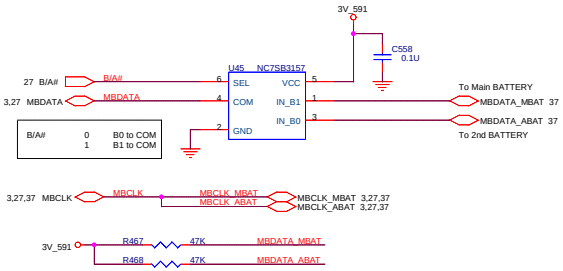


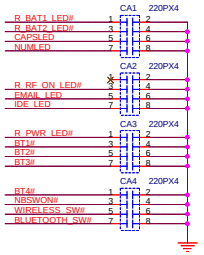
IDE LED CONTROL LOGIC

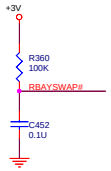
Don't separate this four PAD for the function as a CONN

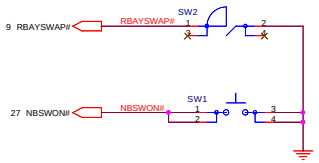












QUANTA COMPUTER

Title	4-IN-1 MEMORY CARD	
Size C	Document Number ZI2	Rev 3B
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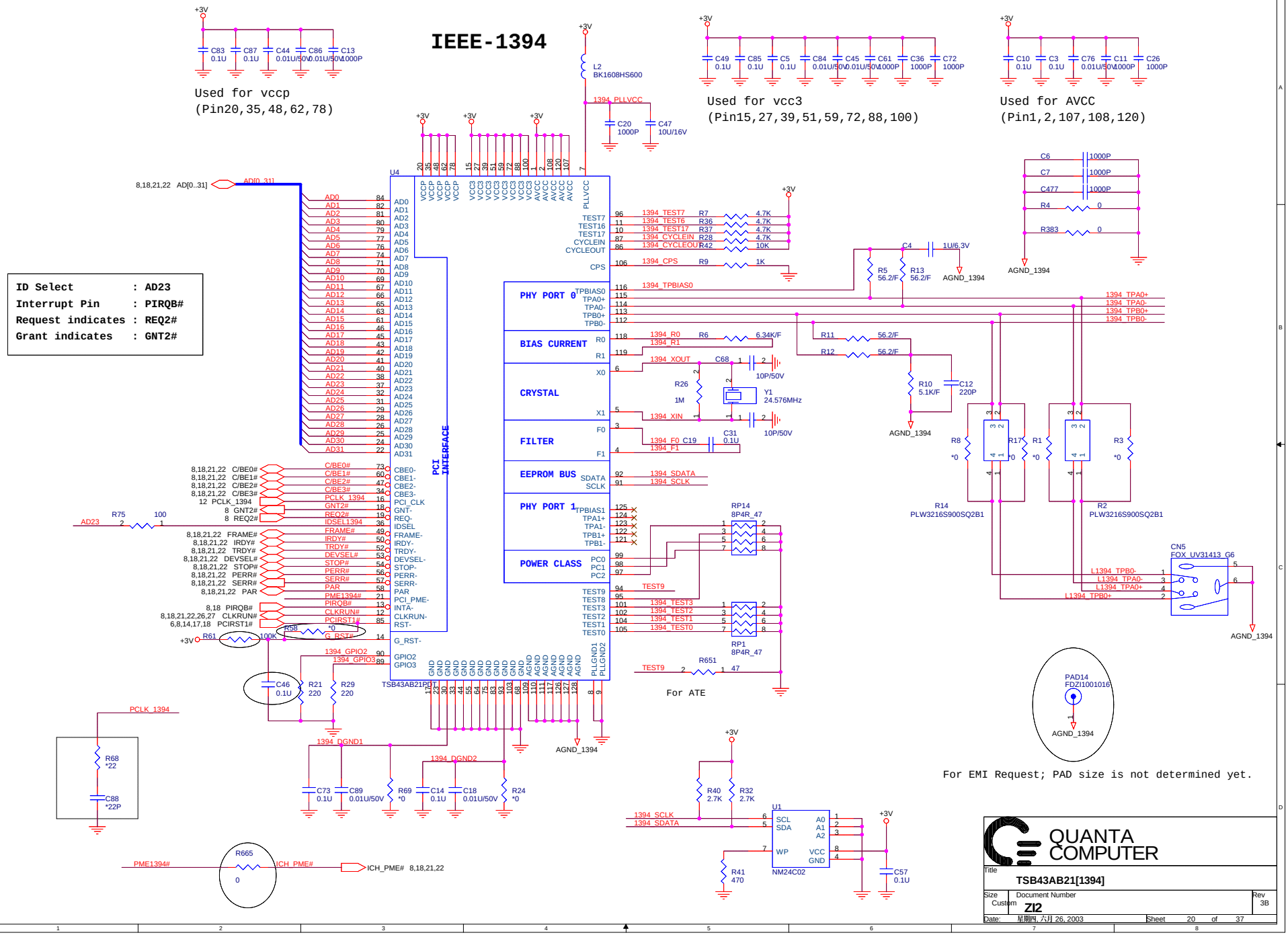
IEEE-1394


Used for vccp
(Pin20,35,48,62,78)
Used for vcc3
(Pin15,27,39,51,59,72,88,100)
Used for AVCC
(Pin1,2,107,108,120)
ID Select : AD23
Interrupt Pin : PIRQB#
Request indicates : REQ2#
Grant indicates : GNT2#
U4
TSB43AB21PDT
PCI INTERFACE
PHY PORT 0
BIAS CURRENT
CRYSTAL
FILTER
EEPROM BUS
PHY PORT 1
POWER CLASS
Y1 24.576MHz
For ATE
For EMI Request; PAD size is not determined yet.
U1 NM24C02
CN5 FOX_UV31413_G6
PAD14 FDZI1001016
QUANTA COMPUTER
TSB43AB21[1394]
Size Custom
Document Number ZI2
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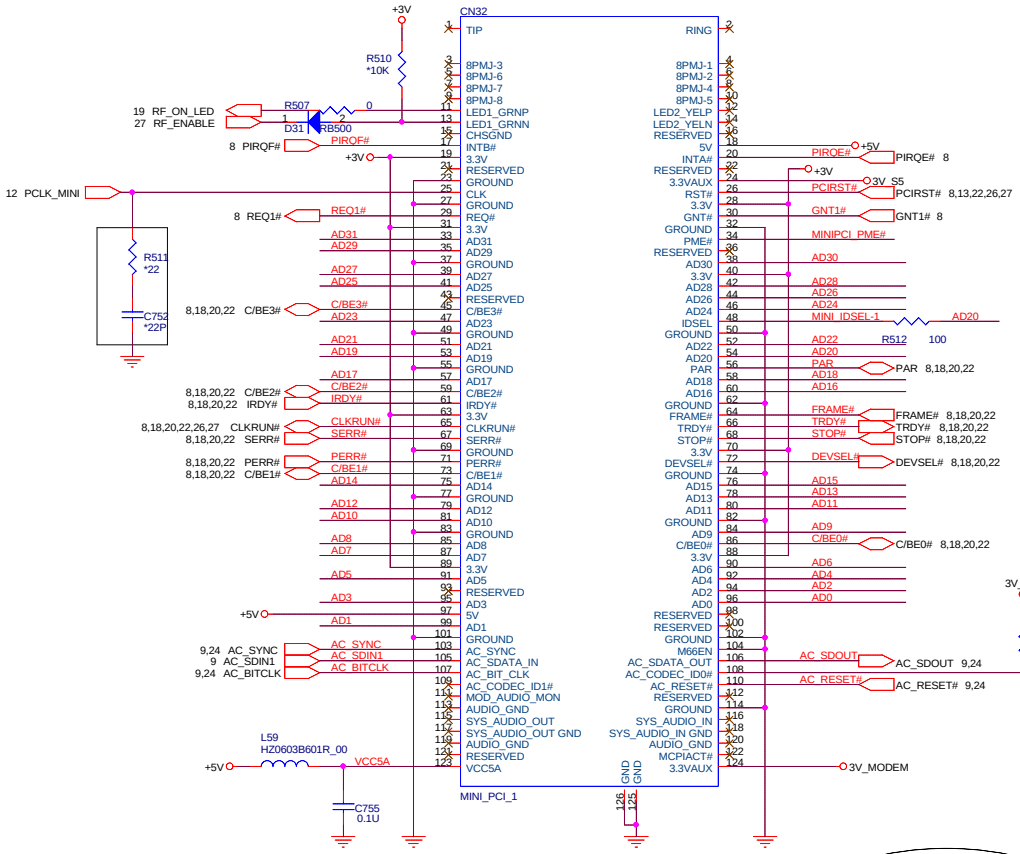
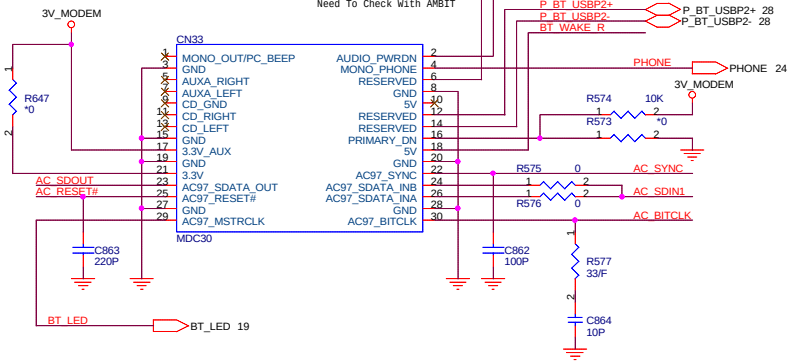
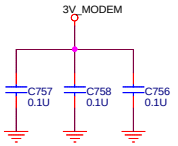
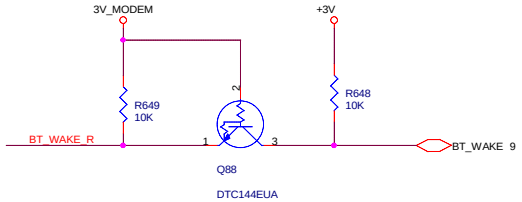
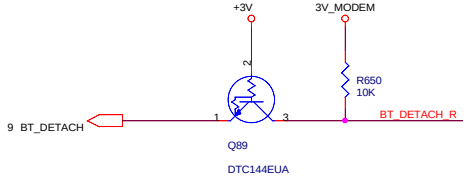
ID Select	: AD20
Interrupt Pin	: PIRQE# , PIRQF#
Request indicates	: REQ1#
Grant indicates	: GNT1#

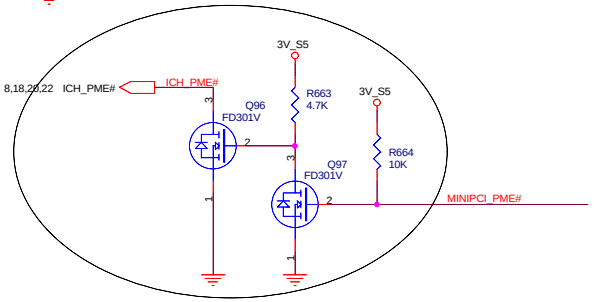


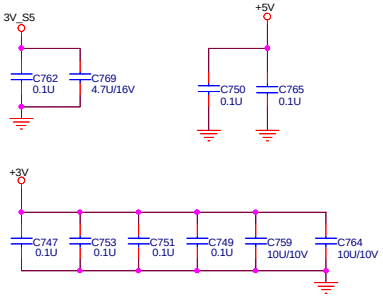












8,18,20,22 AD[0..31] AD[0..31]

QUANTA COMPUTER

Title	MINI-PCI & MDC	
Size: Custom	Document Number: ZI2	Rev: 3B
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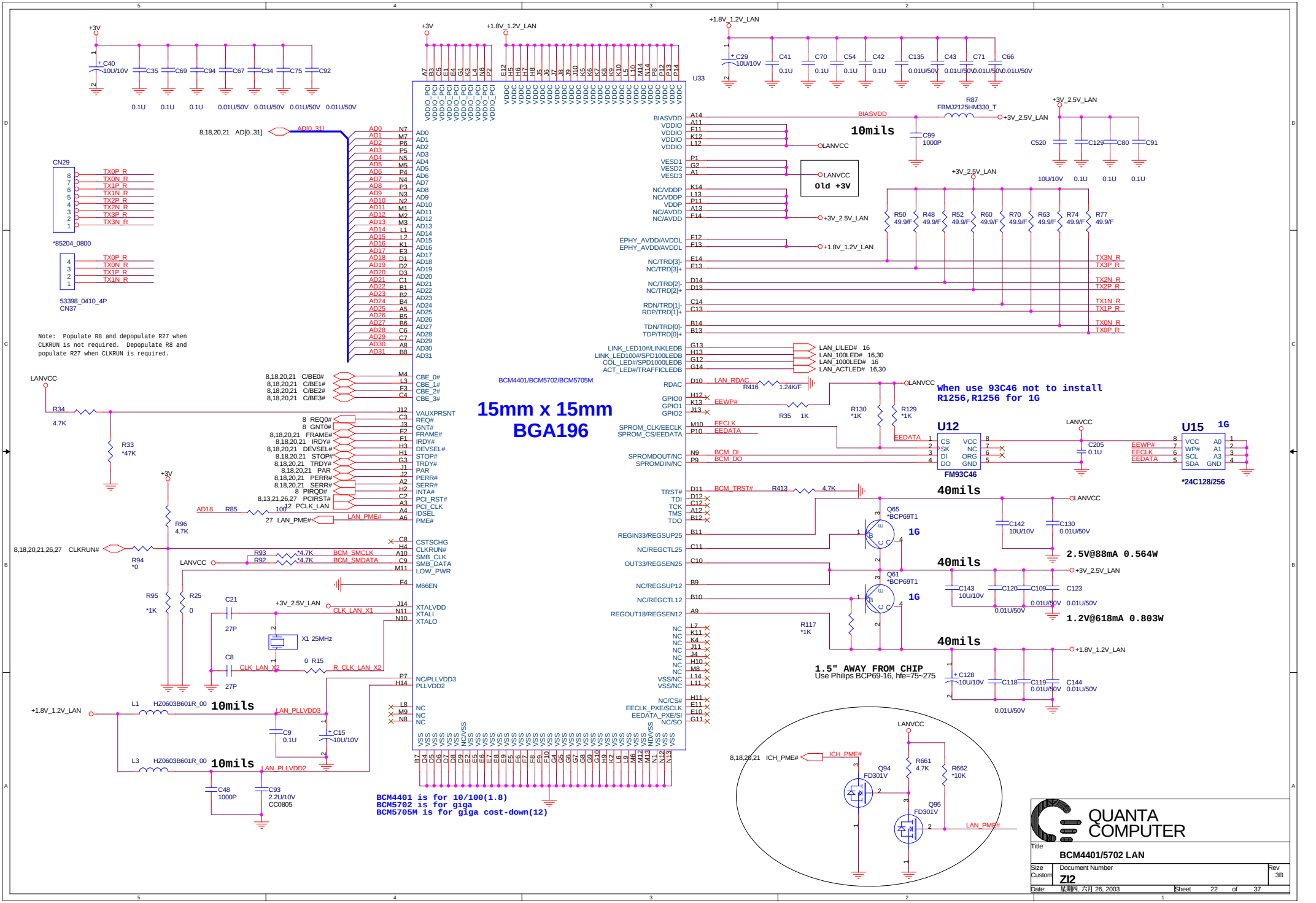
15mm x 15mm
BGA196
BCM4401/BCM5702/BCM5705M
U33
10mils
Old +3V
When use 93C46 not to install
R1256,R1256 for 1G
U12
FM93C46
U15
1G
*24C128/256
40mils
2.5V@88mA 0.564W
1.2V@618mA 0.803W
1.5" AWAY FROM CHIP
Use Philips BCP69-16, hfe=75~275
Note: Populate R8 and depopulate R27 when CLKRUN is not required. Depopulate R8 and populate R27 when CLKRUN is required.
BCM4401 is for 10/100(1.8)
BCM5702 is for giga
BCM5705M is for giga cost-down(12)
QUANTA COMPUTER
BCM4401/5702 LAN
ZI2
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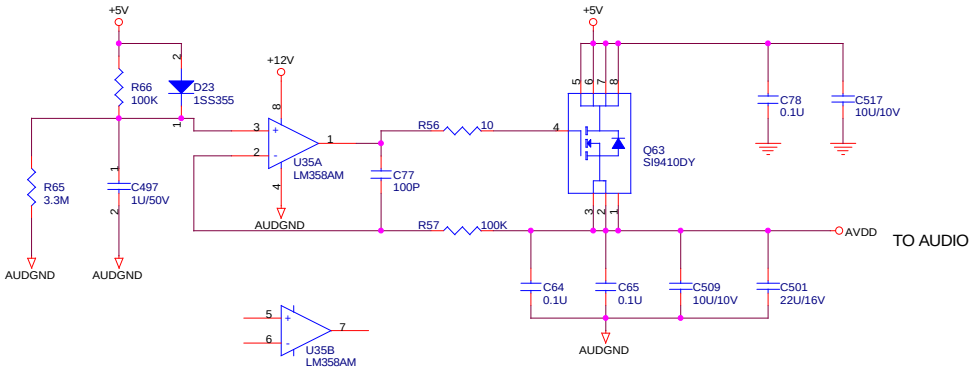
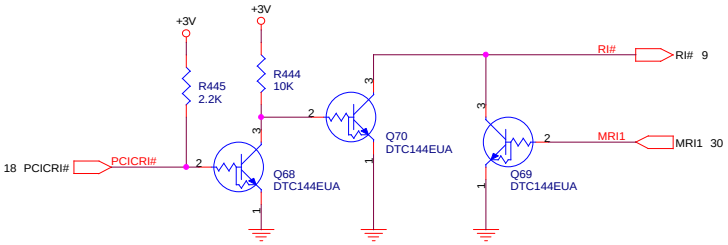
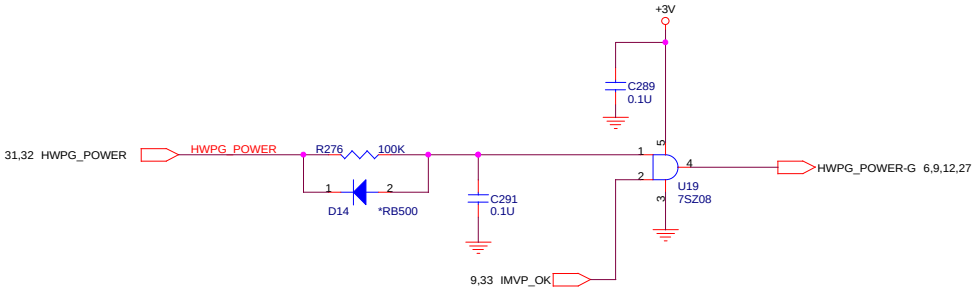
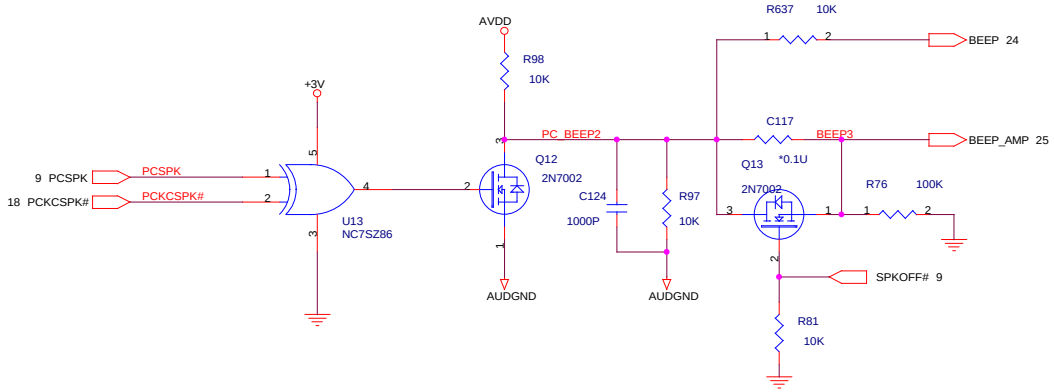
AUDIO POWER









QUANTA COMPUTER

Title	AUDIO POWER & BEEP & FDD SEL	
Size: Custom	Document Number: ZI2	Rev: 3A
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AC97 CODEC

12/02

ALC202 & AD1981 co-design:
For ALC202 (default):
Mount RB, CA = CB = 0.47u
No mount RA & RC
For AD1981:
Mount RA, RB; CA = CB = 4.7K
No mount RB

1A not stuff

SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

Spec Change 10/02

Title	ACP7 CODEC
Size	Custom
Document Number	ZI2
Rev	3B
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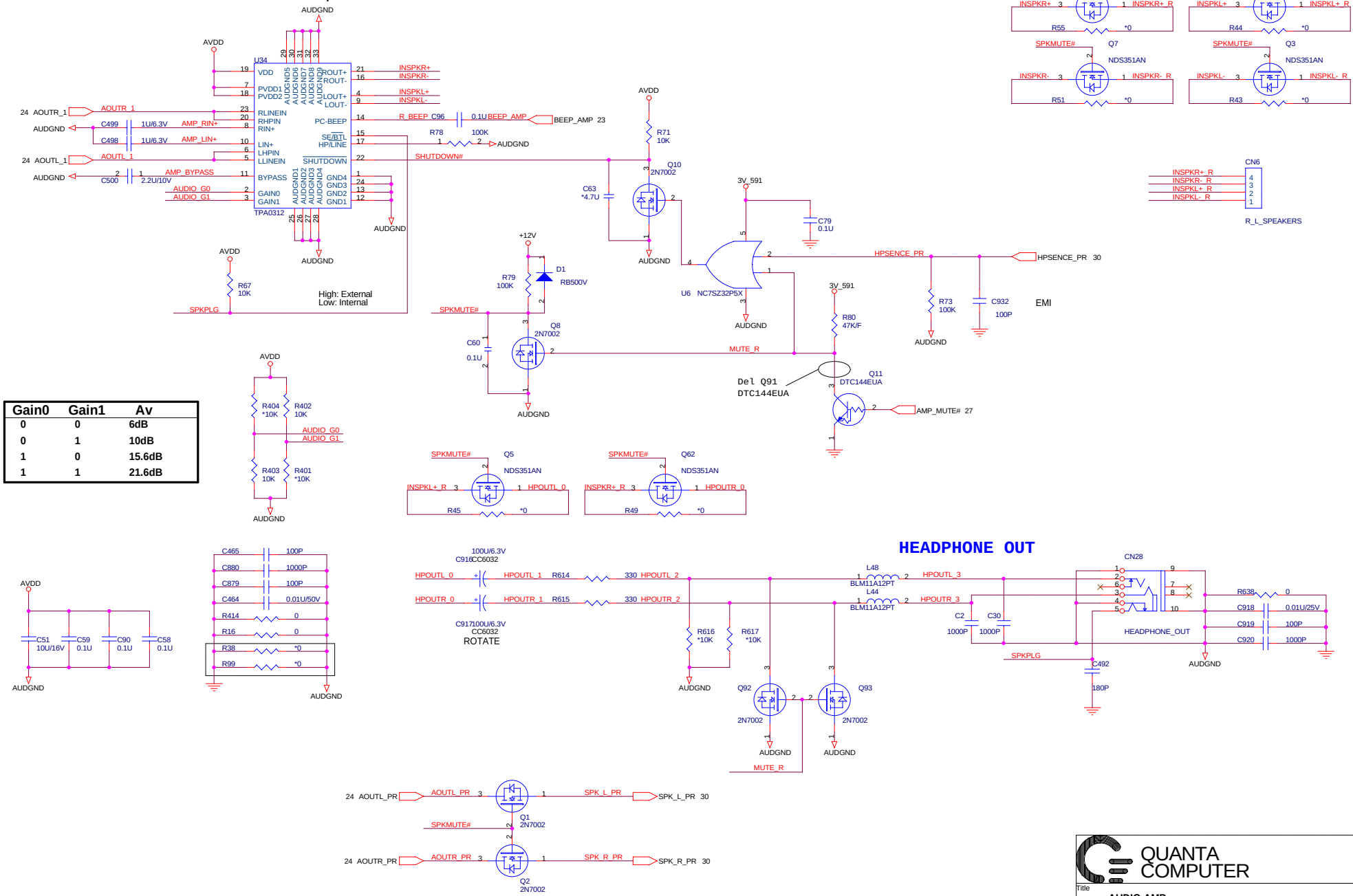
QUANTA COMPUTER

Audio Amplifier



Gain0	Gain1	Av
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

QUANTA COMPUTER

Title	AUDIO AMP	
Size: Custom	Document Number: ZI2	Rev: 3B
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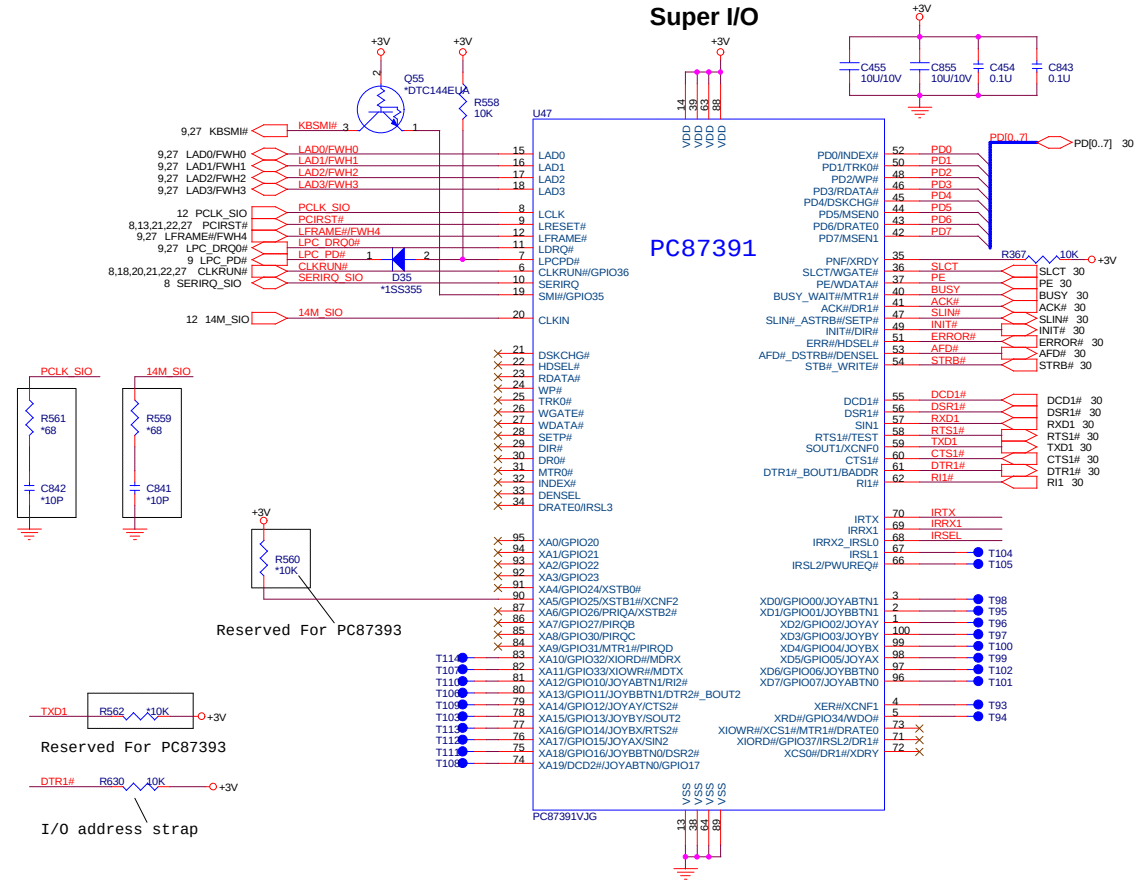
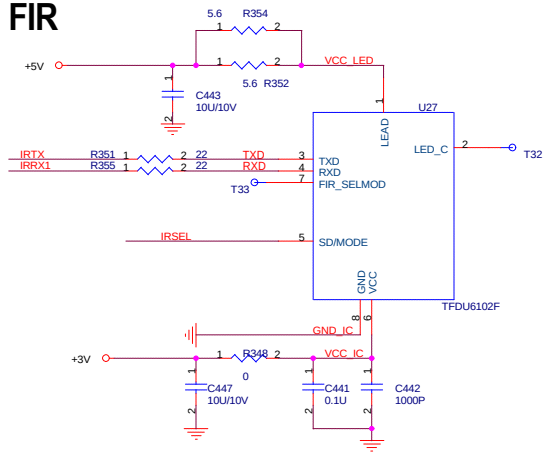
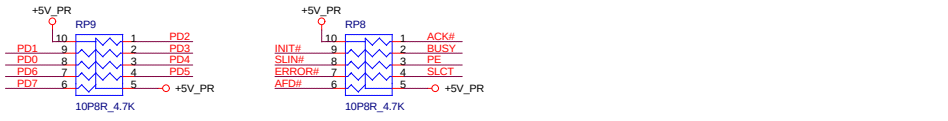
Super I/O

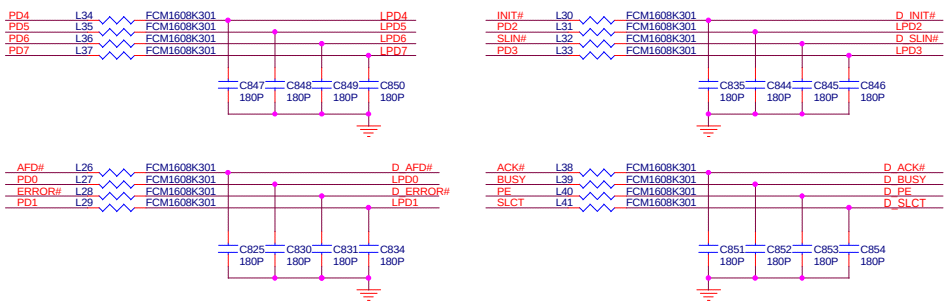


FIR

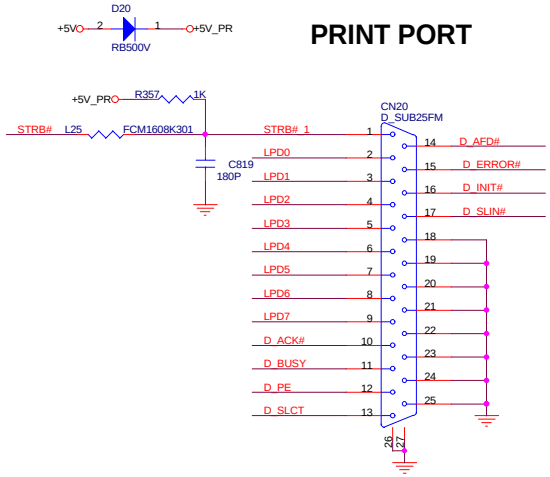


40mil: Power/ GND
35mil: VCC_LED, VCC_IC, GND_IC
10mil: TXD, RXD, IRSEL, IRTX, IRRX1





PRINT PORT

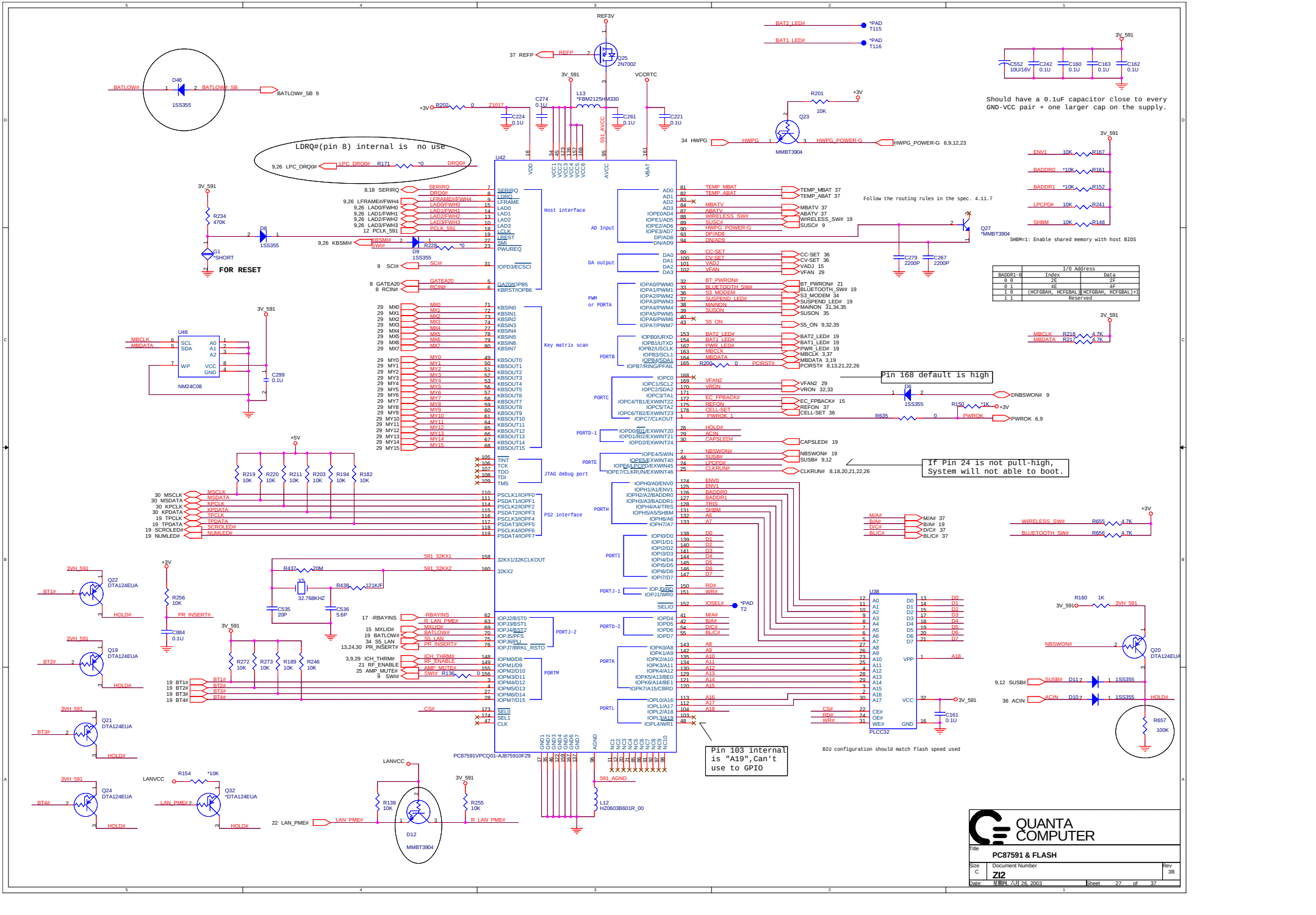


QUANTA COMPUTER

Title: SUPER I/O NS37391

Size	Document Number	Rev
Custom	ZI2	3A

Date: 星期四, 六月 26, 2003 Sheet 26 of 37


LDRQ#(pin 8) internal is no use
FOR RESET
Should have a 0.1uF capacitor close to every GND-VCC pair + one larger cap on the supply.
Follow the routing rules in the spec. 4.11.7
SHBM=1: Enable shared memory with host BIOS
I/O Address
Pin 168 default is high
If Pin 24 is not pull-high, System will not able to boot.
Pin 103 internal is "A19",Can't use to GPIO
BIU configuration should match flash speed used
PC87591VPCQ01-A3875910F29
QUANTA COMPUTER
PC87591 & FLASH
ZI2
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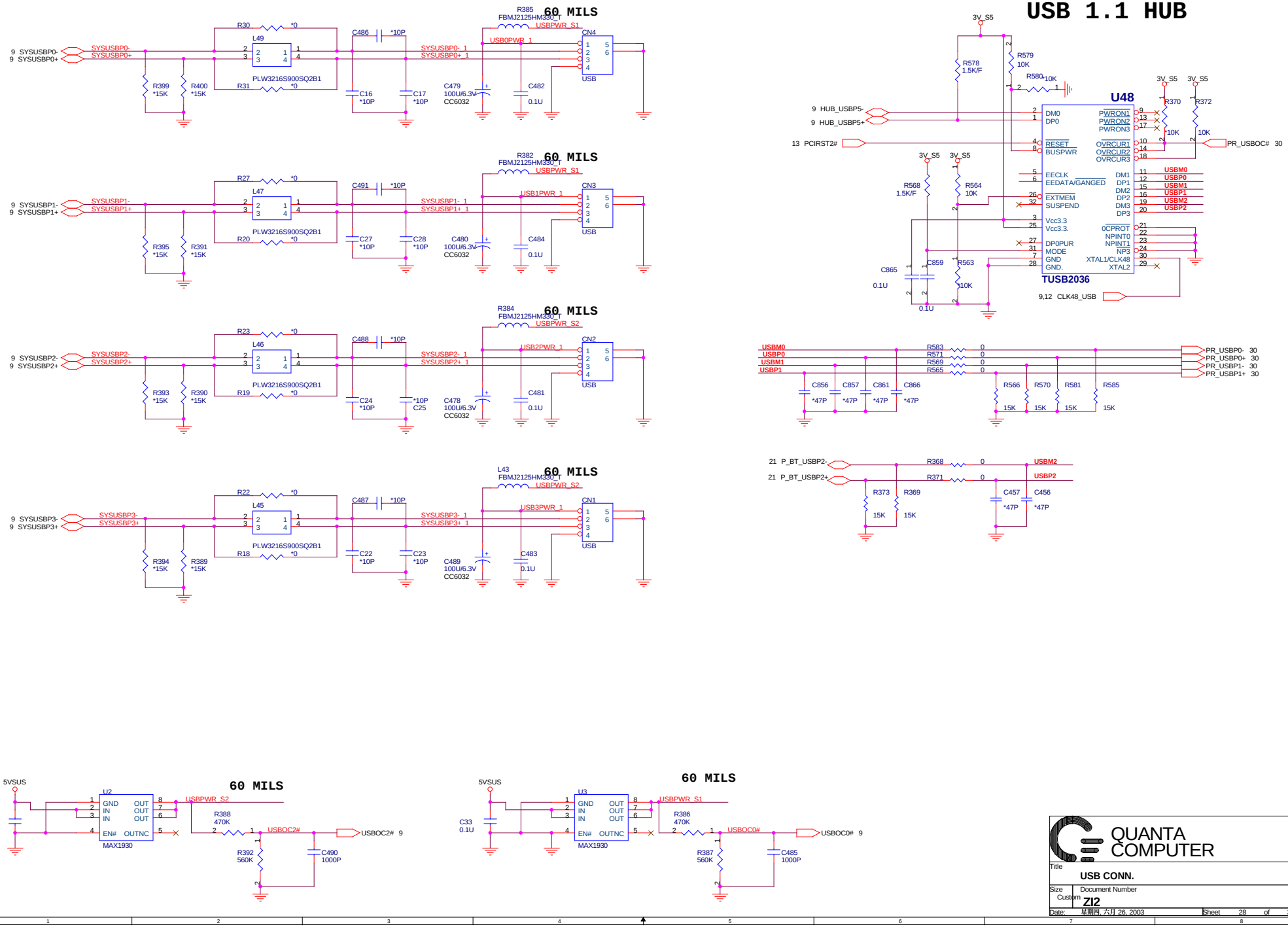
60 MILS
R385 FBMJ2125HM330_T
USBPWR_S1
USB0PWR_1
CN4
USB
R30 *0
L49
PLW3216S900SQ2B1
R31 *0
C486 *10P
C16 *10P
C17 *10P
C479 100U/6.3V CC6032
C482 0.1U
R399 *15K
R400 *15K
9 SYSUSBP0-
9 SYSUSBP0+
SYSUSBP0-
SYSUSBP0+
SYSUSBP0- 1
SYSUSBP0+ 1
60 MILS
R382 FBMJ2125HM330_T
USBPWR_S1
USB1PWR_1
CN3
USB
R27 *0
L47
PLW3216S900SQ2B1
R20 *0
C491 *10P
C27 *10P
C28 *10P
C480 100U/6.3V CC6032
C484 0.1U
R395 *15K
R391 *15K
9 SYSUSBP1-
9 SYSUSBP1+
SYSUSBP1-
SYSUSBP1+
SYSUSBP1- 1
SYSUSBP1+ 1
60 MILS
R384 FBMJ2125HM330_T
USBPWR_S2
USB2PWR_1
CN2
USB
R23 *0
L46
PLW3216S900SQ2B1
R19 *0
C488 *10P
C24 *10P
C25 *10P
C478 100U/6.3V CC6032
C481 0.1U
R393 *15K
R390 *15K
9 SYSUSBP2-
9 SYSUSBP2+
SYSUSBP2-
SYSUSBP2+
SYSUSBP2- 1
SYSUSBP2+ 1
60 MILS
L43 FBMJ2125HM330_T
USBPWR_S2
USB3PWR_1
CN1
USB
R22 *0
L45
PLW3216S900SQ2B1
R18 *0
C487 *10P
C22 *10P
C23 *10P
C489 100U/6.3V CC6032
C483 0.1U
R394 *15K
R389 *15K
9 SYSUSBP3-
9 SYSUSBP3+
SYSUSBP3-
SYSUSBP3+
SYSUSBP3- 1
SYSUSBP3+ 1
USB 1.1 HUB
3V_S5
R578 1.5K/F
R579 10K
R580 10K
U48
TUSB2036
DM0
DP0
RESET
BUSPWR
EECLK
EEDATA/GANGED
EXTMEM
SUSPEND
Vcc3.3
Vcc3.3.
DP0PUR
MODE
GND
GND.
PWRON1
PWRON2
PWRON3
OVRCUR1
OVRCUR2
OVRCUR3
0CPROT
NPINT0
NPINT1
NP3
XTAL1/CLK48
XTAL2
DM1
DP1
DM2
DP2
DM3
DP3
R370 *10K
R372 10K
PR_USBOC# 30
9 HUB_USBP5-
9 HUB_USBP5+
13 PCIRST2#
USBM0
USBP0
USBM1
USBP1
USBM2
USBP2
R568 1.5K/F
R564 10K
R563 10K
C859
C865 0.1U
0.1U
9,12 CLK48_USB
R583 0
R571 0
R569 0
R565 0
C856 *47P
C857 *47P
C861 *47P
C866 *47P
R566 15K
R570 15K
R581 15K
R585 15K
PR_USBP0- 30
PR_USBP0+ 30
PR_USBP1- 30
PR_USBP1+ 30
21 P_BT_USBP2-
21 P_BT_USBP2+
R368 0
R371 0
R373 15K
R369 15K
C457 *47P
C456 *47P
5VSUS
U2
MAX1930
GND
IN
EN#
OUT
OUTNC
C32 0.1U
USBPWR_S2
R388 470K
USBOC2#
USBOC2# 9
R392 560K
C490 1000P
60 MILS
5VSUS
U3
MAX1930
C33 0.1U
USBPWR_S1
R386 470K
USBOC0#
USBOC0# 9
R387 560K
C485 1000P
QUANTA COMPUTER
Title
USB CONN.
Size
Custom
Document Number
ZI2
Rev
3A
Date: 星期四, 六月 26, 2003
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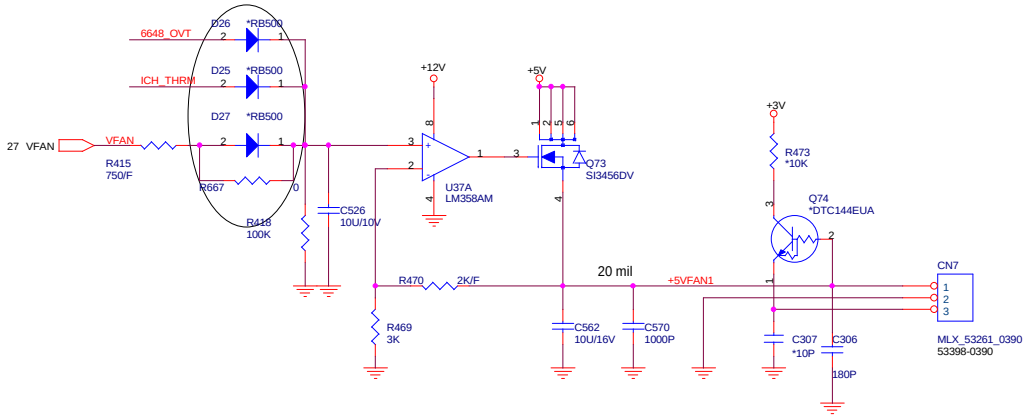
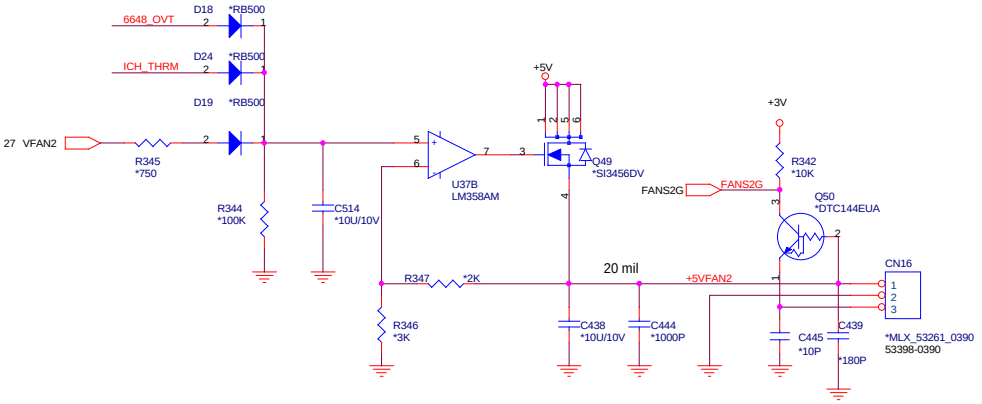
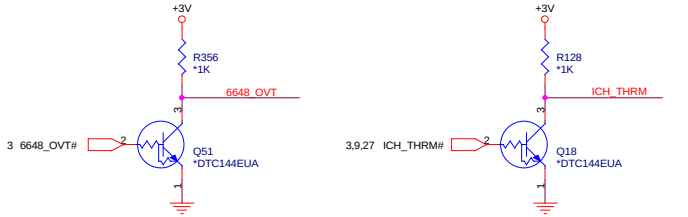
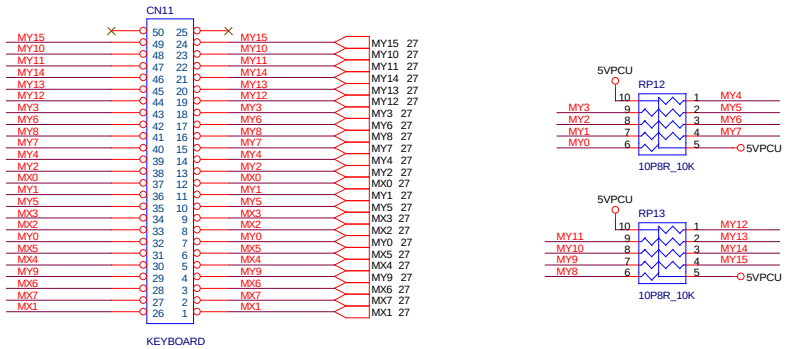


1st FAN OUT CONNECTOR

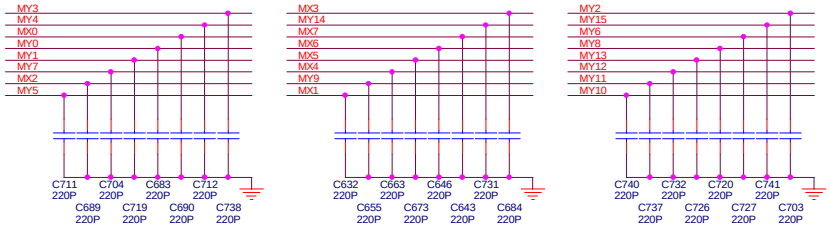


2nd FAN OUT CONNECTOR





KEYB0ARD



QUANTA COMPUTER

Title	K/B , T/P , FAN CON	
Size: Custom	Document Number: ZI2	Rev: 3B
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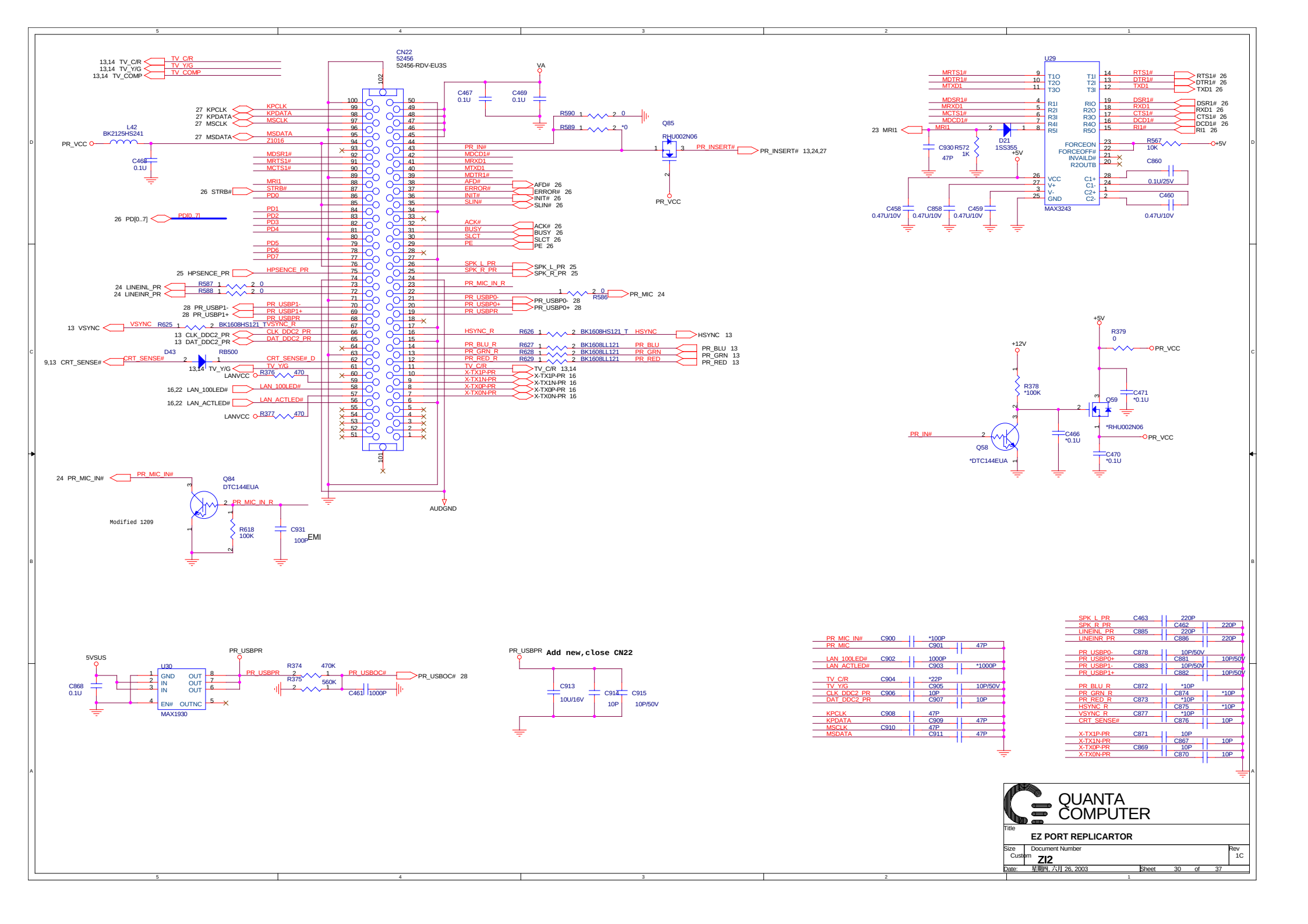

SPK_L_PR	C463	220P					
SPK_R_PR			C462	220P			
LINEINL_PR	C885	220P					
LINEINR_PR			C886	220P			
PR_USBP0-	C878	10P/50V					
PR_USBP0+			C881	10P/50V			
PR_USBP1-	C883	10P/50V					
PR_USBP1+			C882	10P/50V			
PR_BLU_R	C872	*10P					
PR_GRN_R			C874	*10P			
PR_RED_R	C873	*10P					
HSYNC_R			C875	*10P			
VSYNC_R	C877	*10P					
CRT_SENSE#			C876	10P			
X-TX1P-PR	C871	10P					
X-TX1N-PR			C867	10P			
X-TX0P-PR	C869	10P					
X-TX0N-PR			C870	10P			

PR_MIC_IN#	C900	*100P					
PR_MIC			C901	47P			
LAN_100LED#	C902	1000P					
LAN_ACTLED#			C903	*1000P			
TV_C/R	C904	*22P					
TV_Y/G			C905	10P/50V			
CLK_DDC2_PR	C906	10P					
DAT_DDC2_PR			C907	10P			
KPCLK	C908	47P					
KPDATA			C909	47P			
MSCLK	C910	47P					
MSDATA			C911	47P			

QUANTA COMPUTER

Title	EZ PORT REPLICARTOR		
Size: Custom	Document Number: ZI2		Rev: 1C
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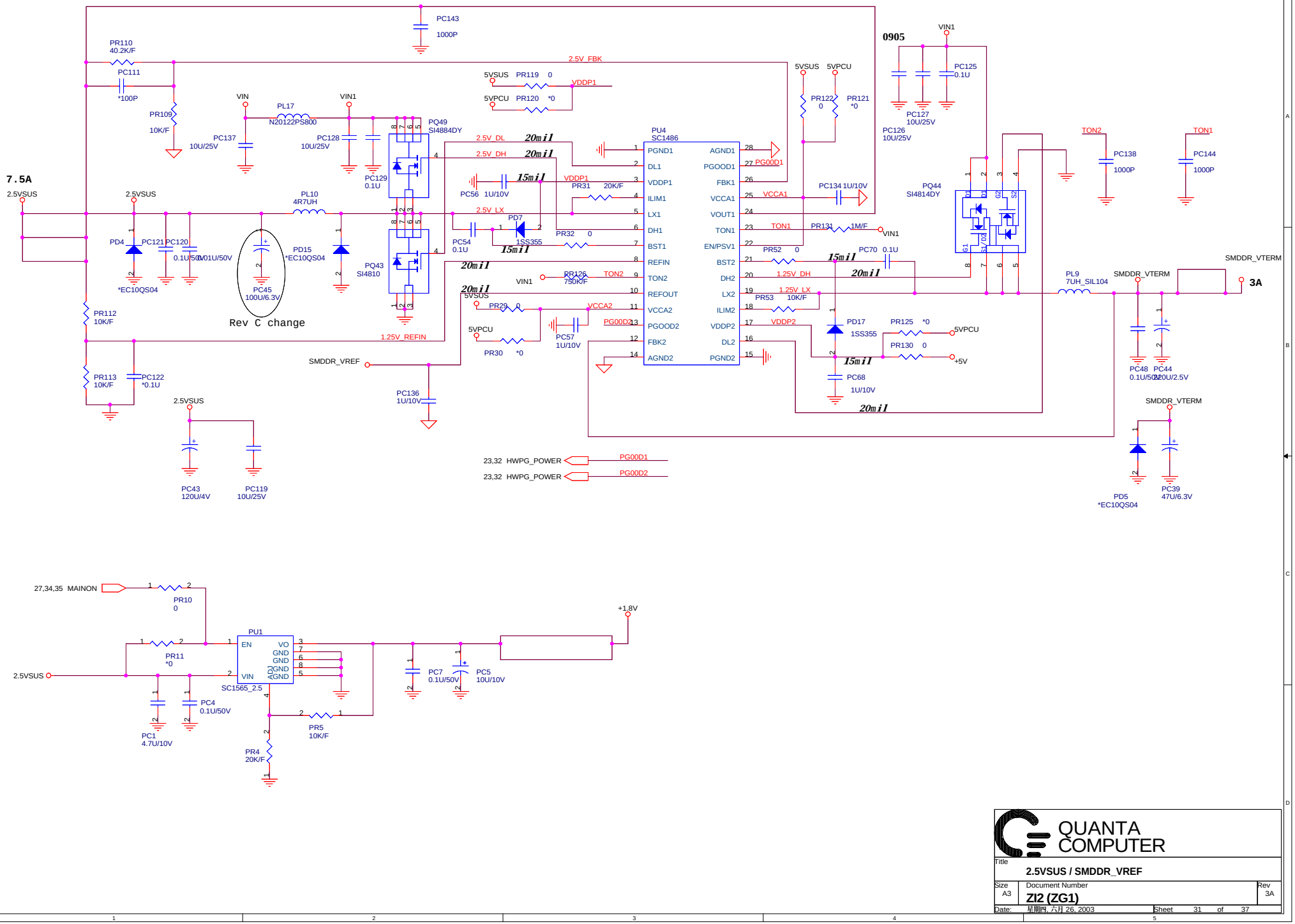

Title	2.5VSUS / SMDDR_VREF
Size	A3
Document Number	ZI2 (ZG1)
Rev	3A
Date:	星期四, 六月 26, 2003
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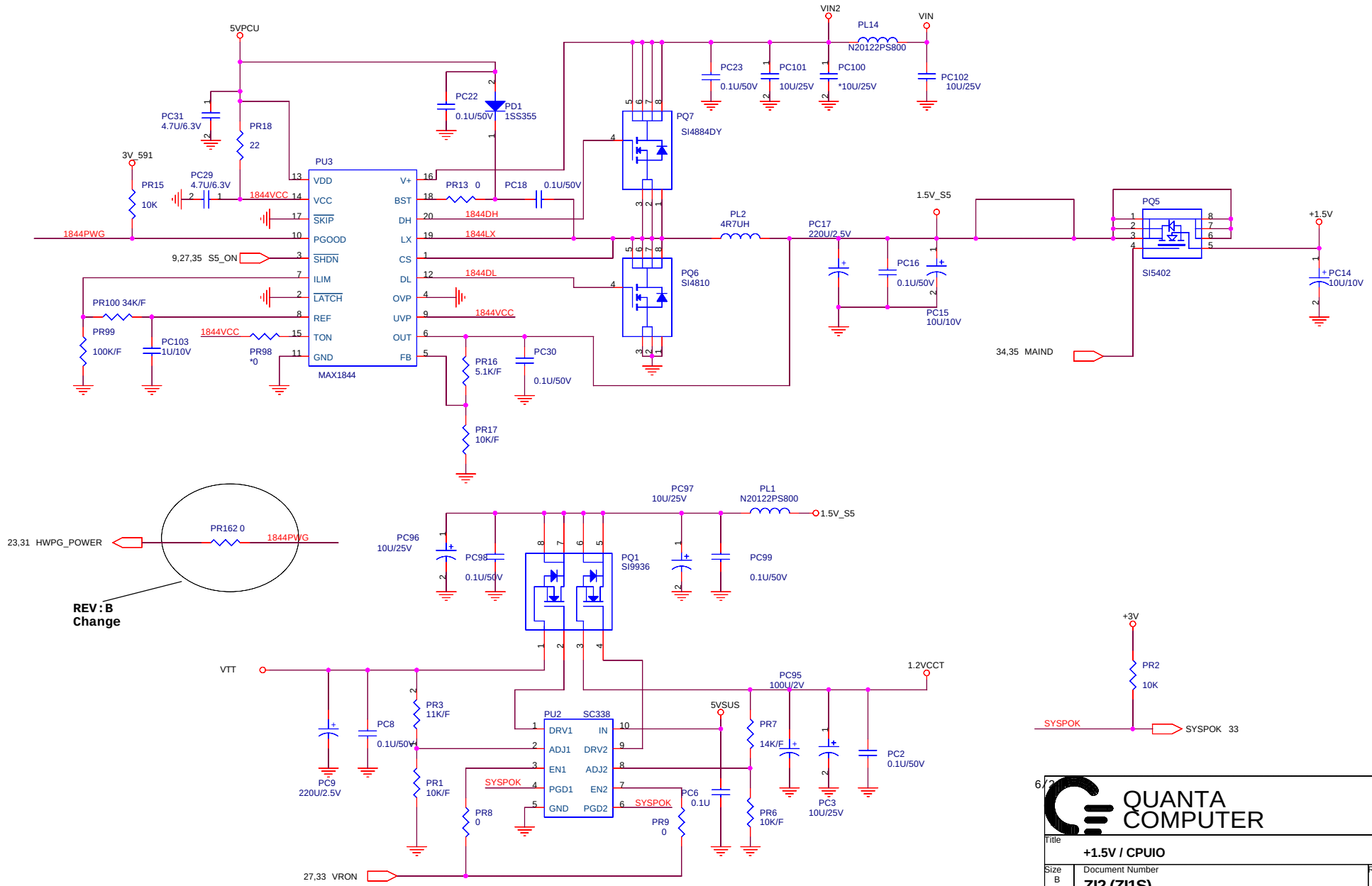
5VPCU
VIN2
VIN
PL14
N20122PS800
PC23
0.1U/50V
PC101
10U/25V
PC100
*10U/25V
PC102
10U/25V
PC22
0.1U/50V
PD1
1SS355
PC31
4.7U/6.3V
PR18
22
3V_591
PR15
10K
PC29
4.7U/6.3V
PU3
MAX1844
VDD
VCC
SKIP
PGOOD
SHDN
ILIM
LATCH
REF
TON
GND
V+
BST
DH
LX
CS
DL
OVP
UVP
OUT
FB
PR13 0
PC18
0.1U/50V
1844VCC
1844DH
1844LX
1844DL
1844PWG
9,27,35 S5_ON
PR100 34K/F
PR99
100K/F
PC103
1U/10V
PR98
*0
PR16
5.1K/F
PC30
0.1U/50V
PR17
10K/F
PQ7
SI4884DY
PQ6
SI4810
PL2
4R7UH
PC17
220U/2.5V
PC16
0.1U/50V
PC15
10U/10V
1.5V_S5
PQ5
SI5402
+1.5V
PC14
10U/10V
34,35 MAIND
23,31 HWPG_POWER
PR162 0
REV : B
Change
PC97
10U/25V
PL1
N20122PS800
PC96
10U/25V
PC98
0.1U/50V
PQ1
SI9936
PC99
0.1U/50V
VTT
PR3
11K/F
PC8
0.1U/50V
PC9
220U/2.5V
PR1
10K/F
PR8
0
PU2
SC338
DRV1
ADJ1
EN1
PGD1
GND
IN
DRV2
ADJ2
EN2
PGD2
SYSPOK
PC6
0.1U
PR9
0
5VSUS
PR7
14K/F
PR6
10K/F
PC95
100U/2V
PC3
10U/25V
PC2
0.1U/50V
1.2VCCT
+3V
PR2
10K
SYSPOK 33
27,33 VRON
QUANTA
COMPUTER
Title
+1.5V / CPUIO
Size
B
Document Number
ZI2 (ZI1S)
Rev
1C
Date:
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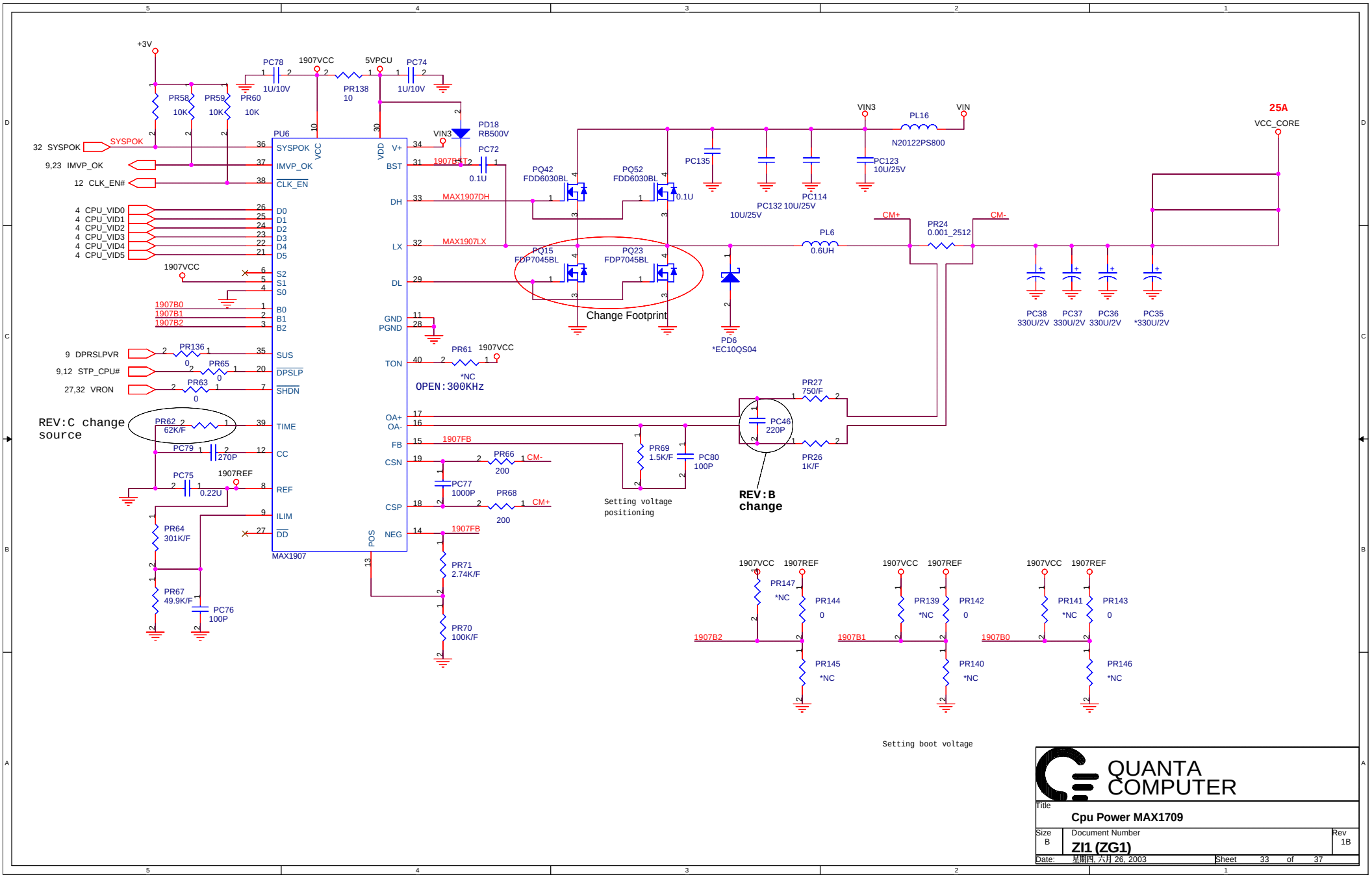
37


+3V
PC78
1907VCC
5VPCU
PC74
1U/10V
PR138
10
1U/10V
PR58
PR59
PR60
10K
10K
10K
PU6
SYSPOK
IMVP_OK
CLK_EN
VCC
VDD
V+
BST
DH
LX
DL
GND
PGND
TON
OA+
OA-
FB
CSN
CSP
NEG
POS
MAX1907
32 SYSPOK
9,23 IMVP_OK
12 CLK_EN#
4 CPU_VID0
4 CPU_VID1
4 CPU_VID2
4 CPU_VID3
4 CPU_VID4
4 CPU_VID5
D0
D1
D2
D3
D4
D5
S2
S1
S0
1907B0
1907B1
1907B2
B0
B1
B2
9 DPRSLPVR
9,12 STP_CPU#
27,32 VRON
SUS
DPSLP
SHDN
PR136
PR65
PR63
REV:C change source
PR62
62K/F
TIME
PC79
270P
CC
PC75
0.22U
1907REF
REF
ILIM
DD
PR64
301K/F
PR67
49.9K/F
PC76
100P
PD18
RB500V
VIN3
PC72
0.1U
1907BST
MAX1907DH
MAX1907LX
PQ42
FDD6030BL
PQ52
FDD6030BL
0.1U
PQ15
FDP7045BL
PQ23
FDP7045BL
Change Footprint
PD6
*EC10QS04
PC135
PC132 10U/25V
PC114
PC123
10U/25V
VIN3
VIN
PL16
N20122PS800
PL6
0.6UH
CM+
CM-
PR24
0.001_2512
25A
VCC_CORE
PC38
330U/2V
PC37
330U/2V
PC36
330U/2V
PC35
*330U/2V
PR61
1907VCC
*NC
OPEN:300KHz
PR27
750/F
PC46
220P
PR26
1K/F
REV:B change
PR69
1.5K/F
PC80
100P
Setting voltage positioning
PR66
200
CM-
PC77
1000P
PR68
200
CM+
1907FB
PR71
2.74K/F
PR70
100K/F
PR147
*NC
PR144
0
PR145
*NC
PR139
*NC
PR142
0
PR140
*NC
PR141
*NC
PR143
0
PR146
*NC
Setting boot voltage
QUANTA COMPUTER
Title
Cpu Power MAX1709
Size B
Document Number
ZI1 (ZG1)
Rev 1B
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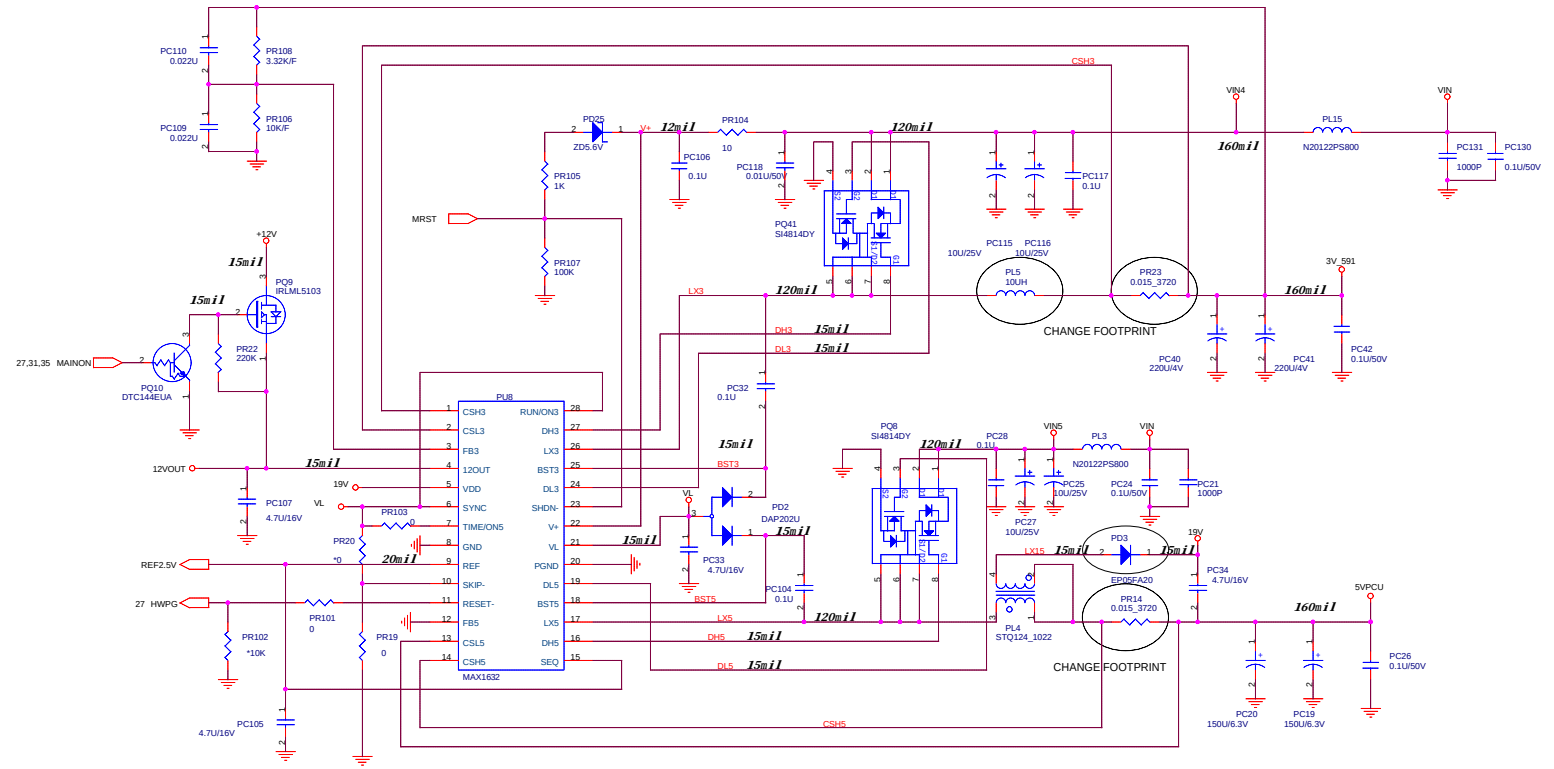
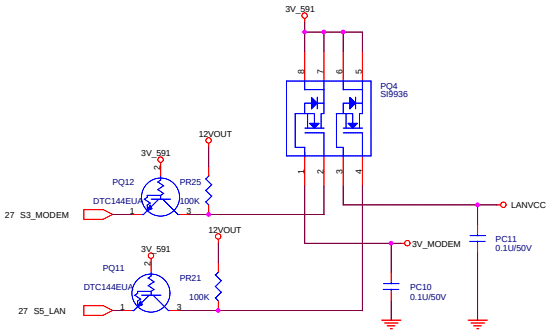
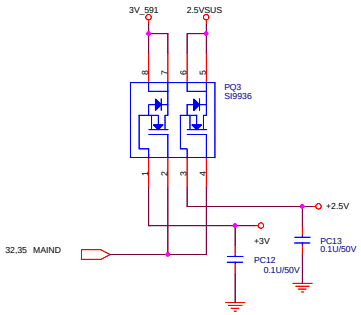
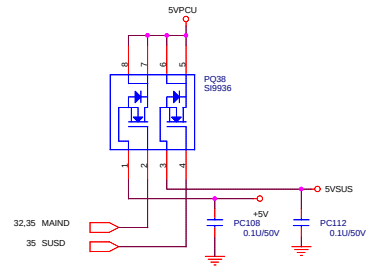


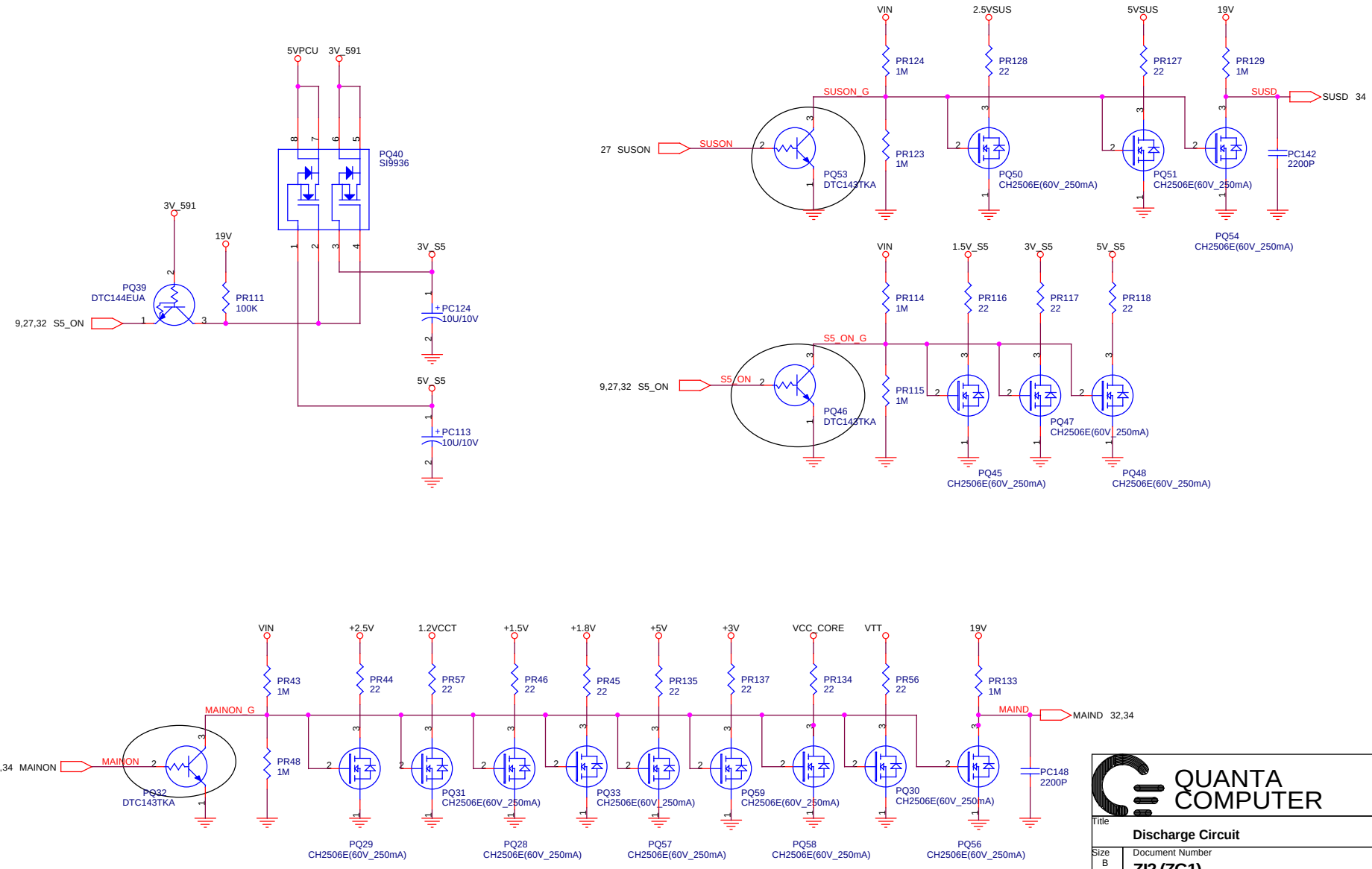






QUANTA COMPUTER

Title	System Power 1632	
Size C	Document Number ZI2 (ZG1)	Rev 1A
Date: 星期四, 六月 26, 2003	Sheet 34 of 37	



QUANTA COMPUTER

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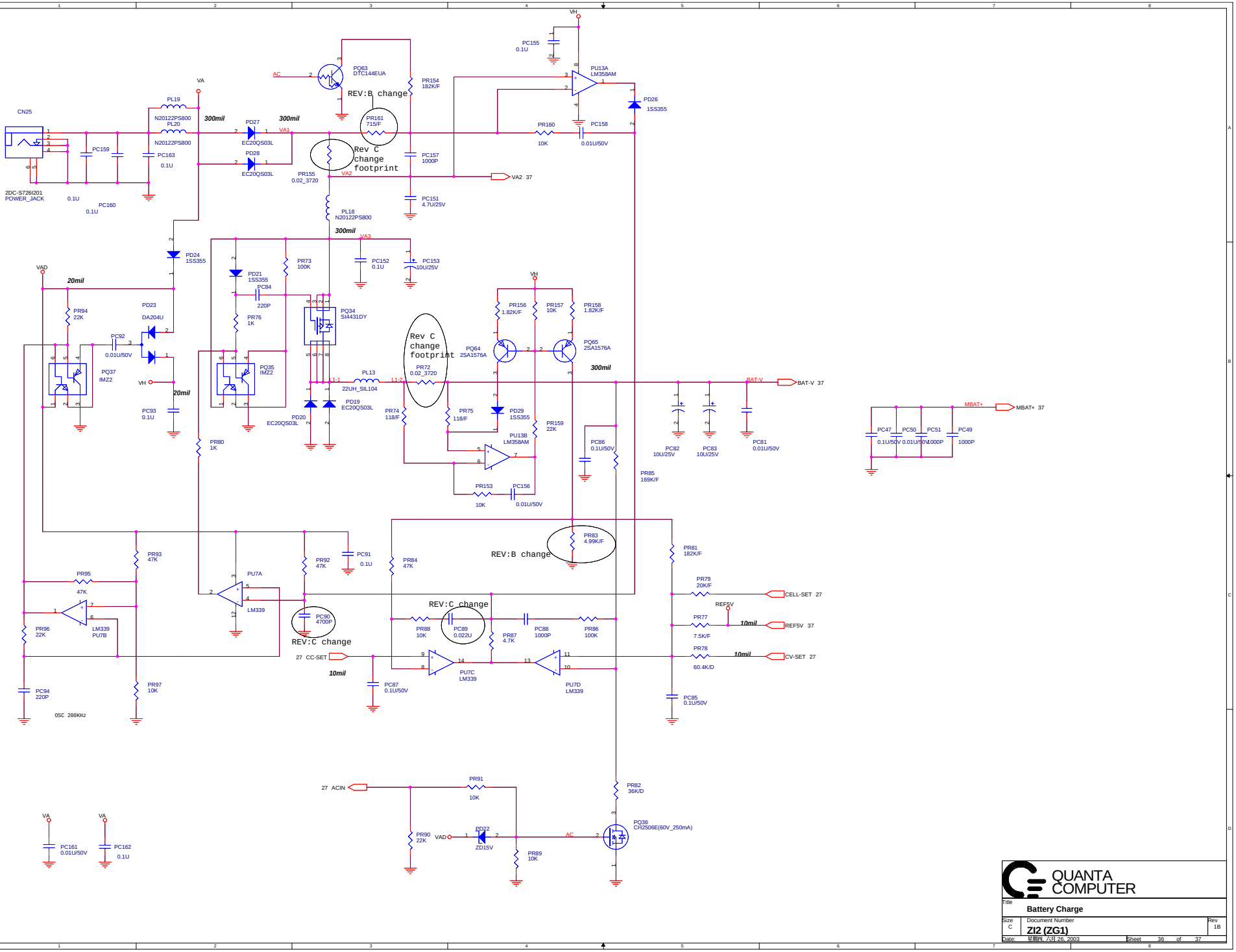
Size B	Document Number **ZI2 (ZG1)**	Rev 3B
Date: 星期四, 六月 26, 2003	Sheet 35 of 37	



QUANTA COMPUTER

Title	Battery Charge	
Size C	Document Number ZI2 (ZG1)	Rev 1B
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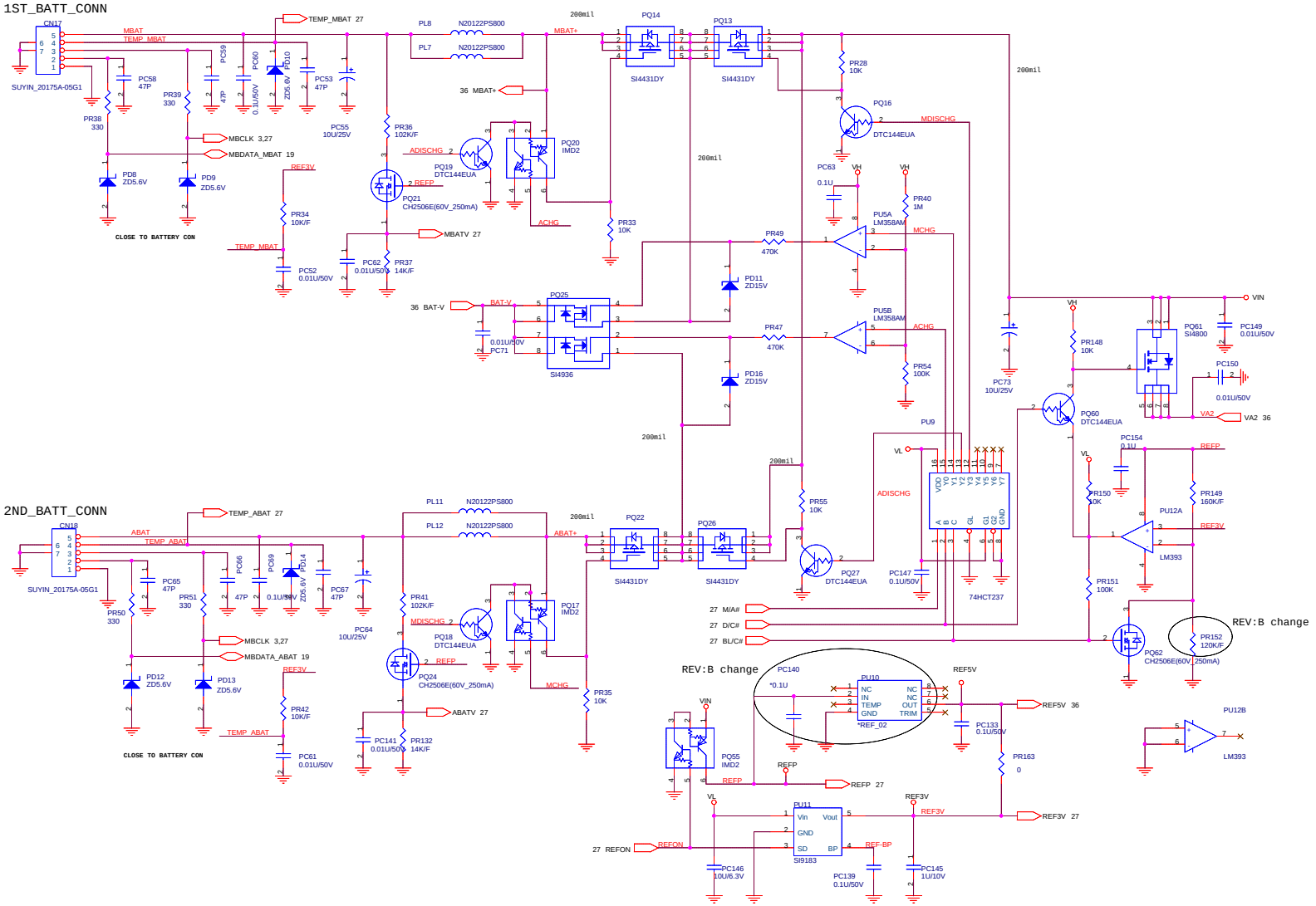

QUANTA COMPUTER

Title	Battery Select	
Size C	Document Number ZI2 (ZG1)	Rev 1A
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