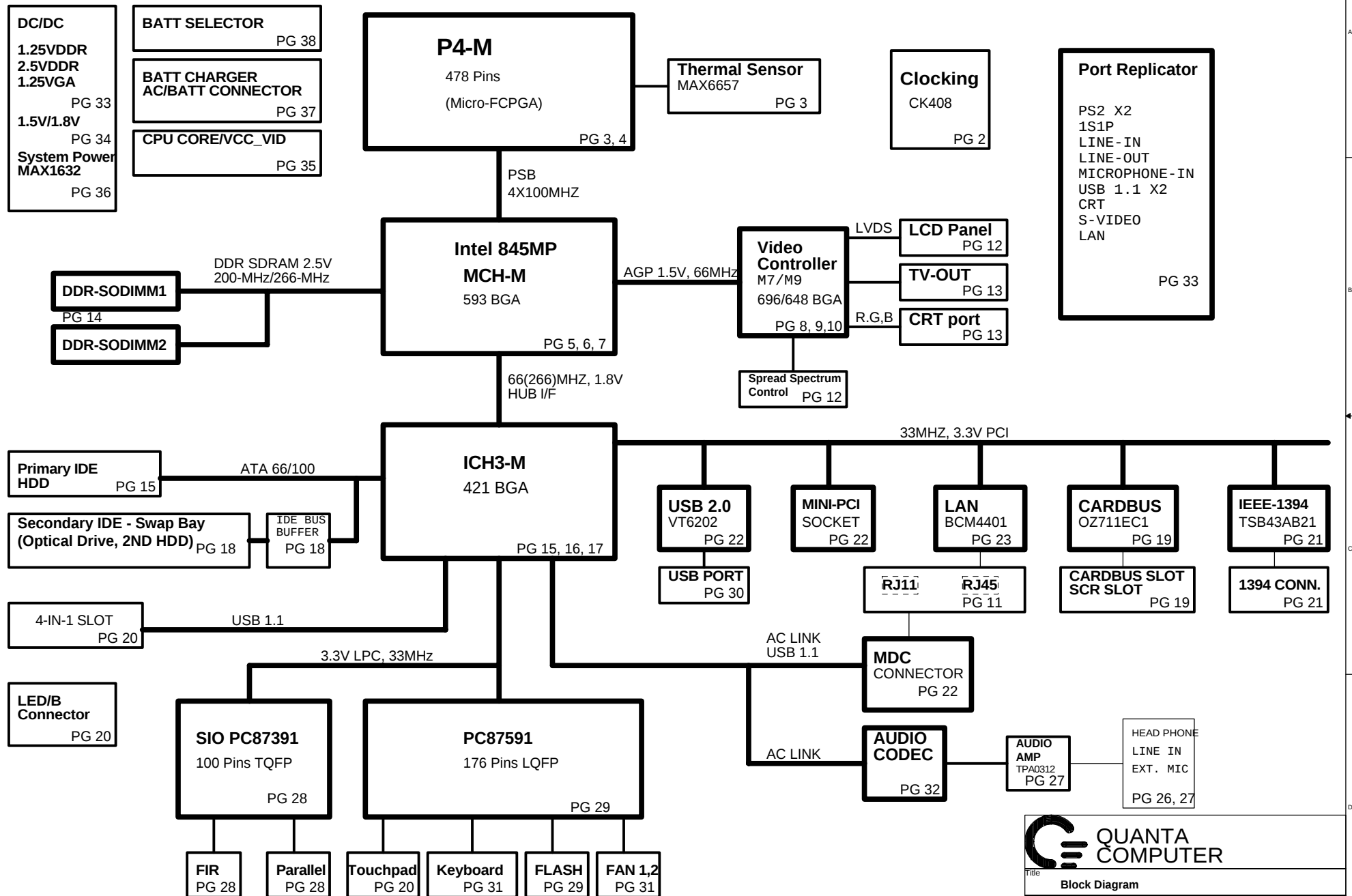
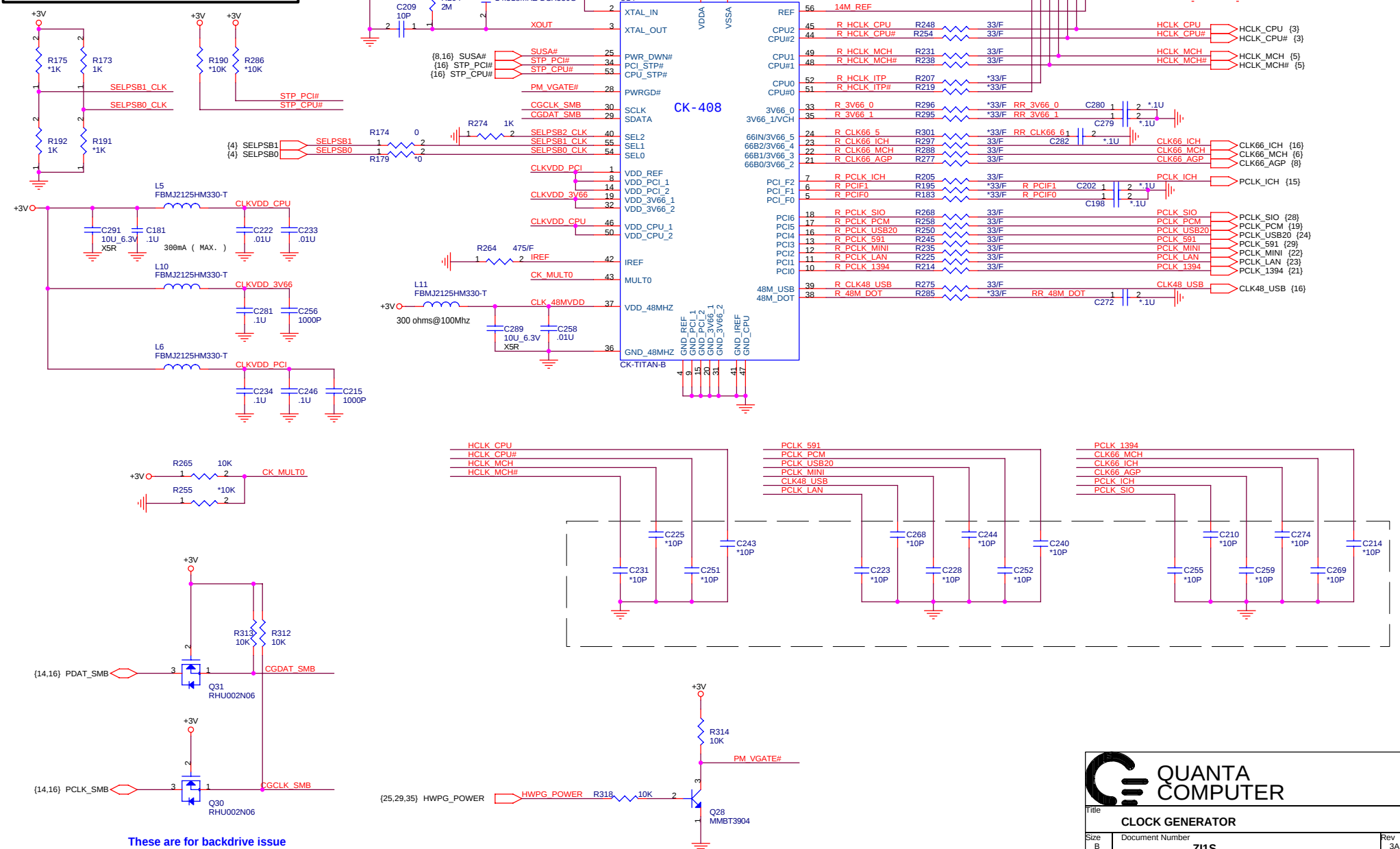
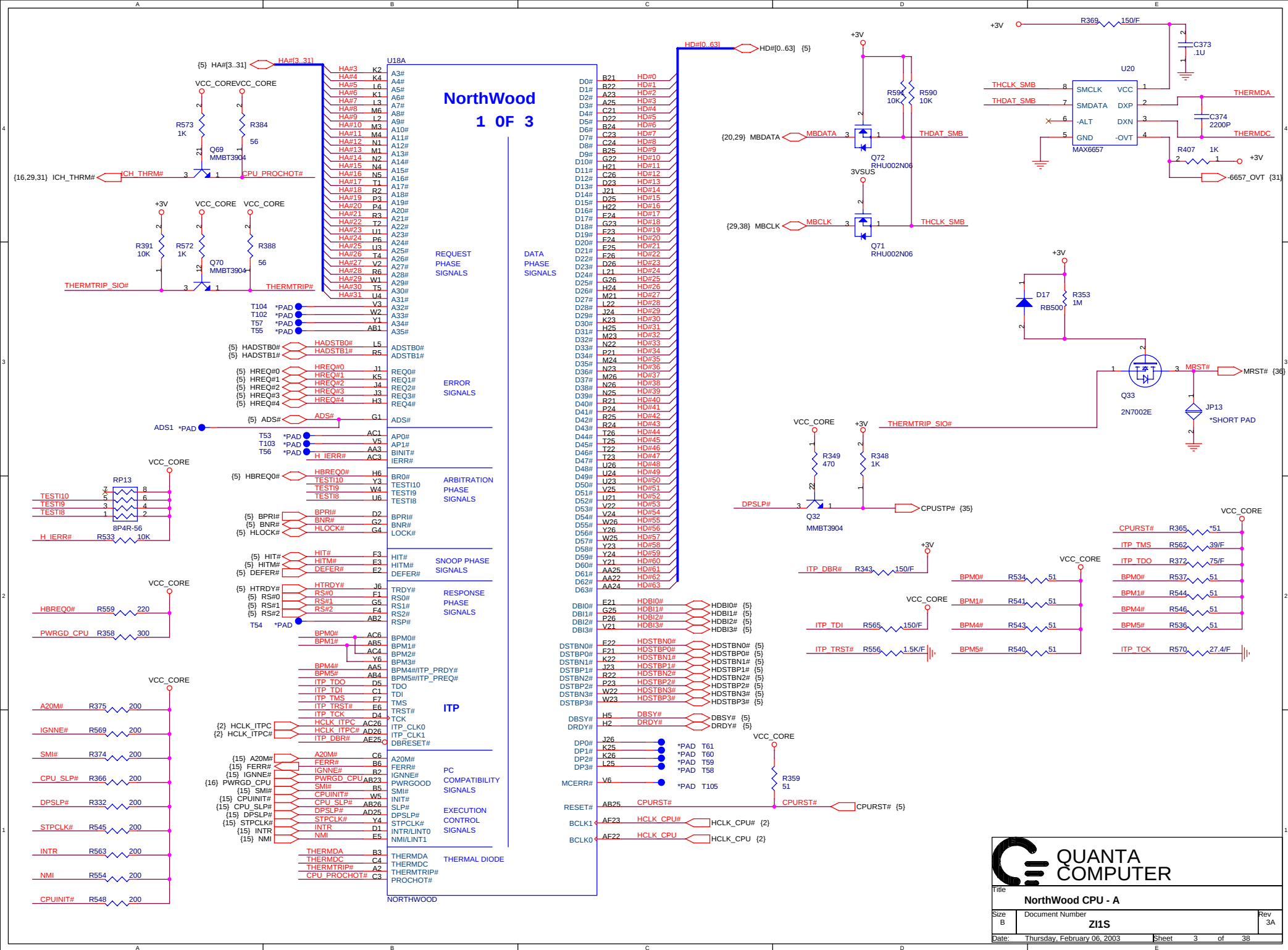


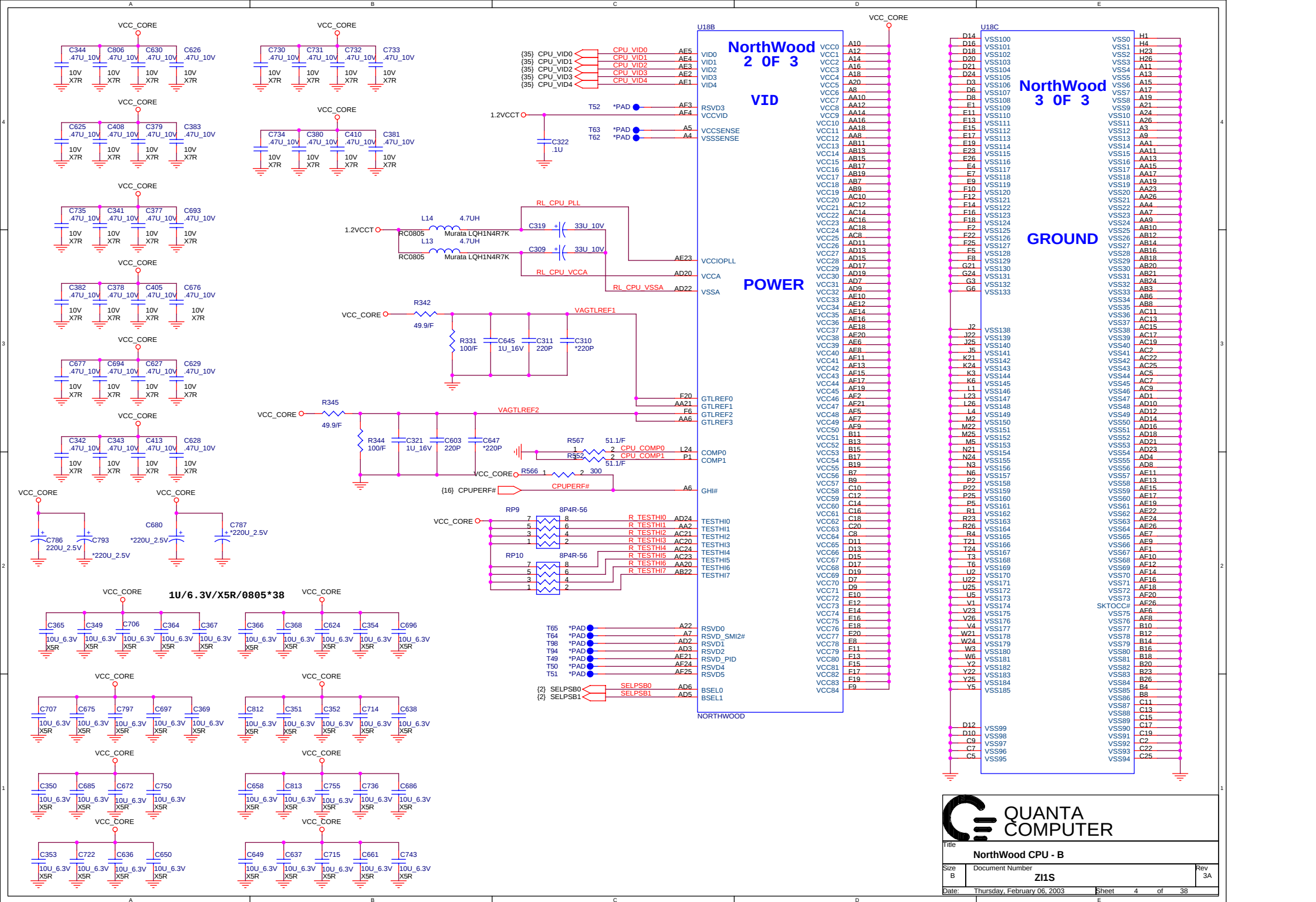
ZI1S BLOCK DIAGRAM

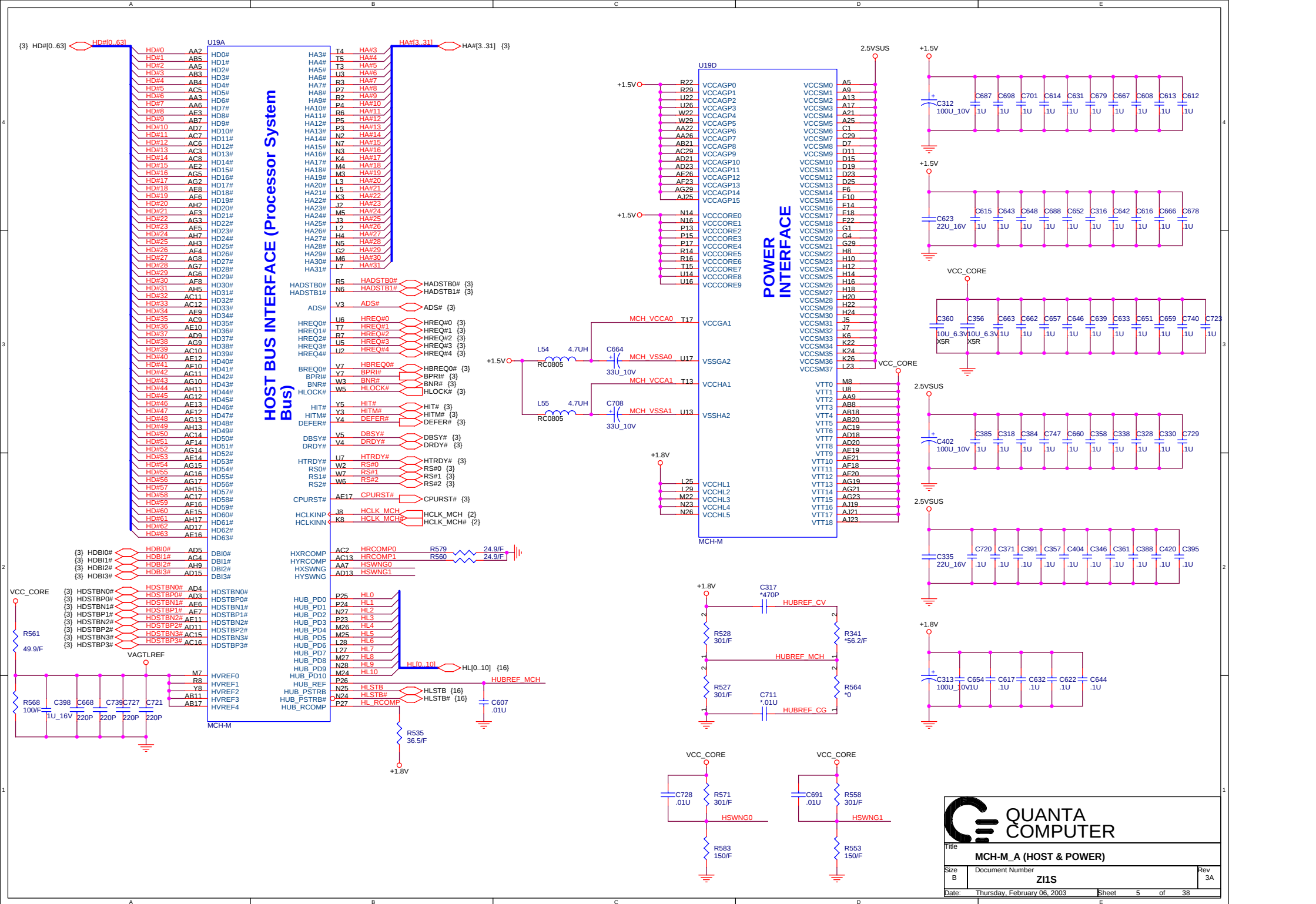


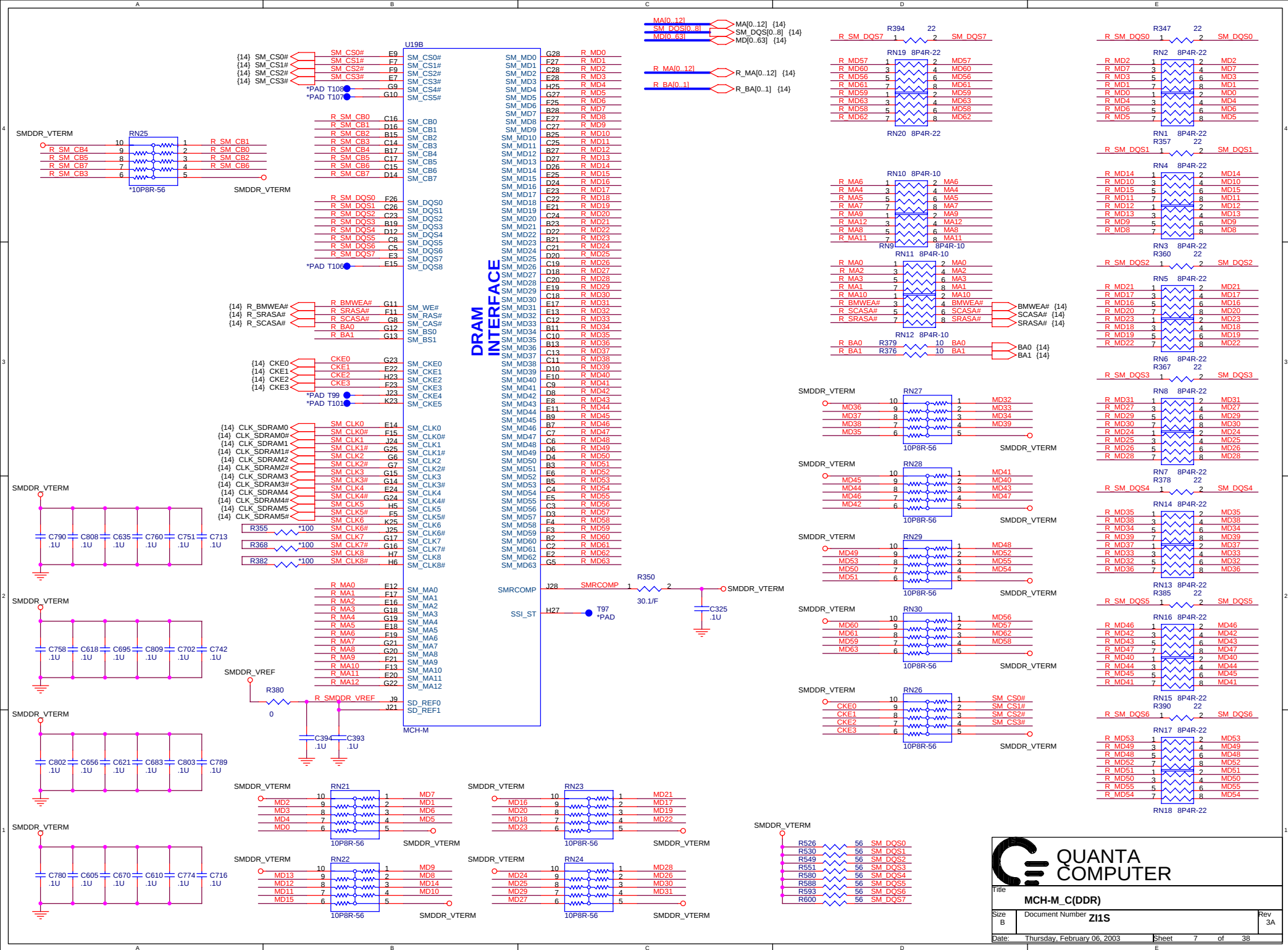
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input

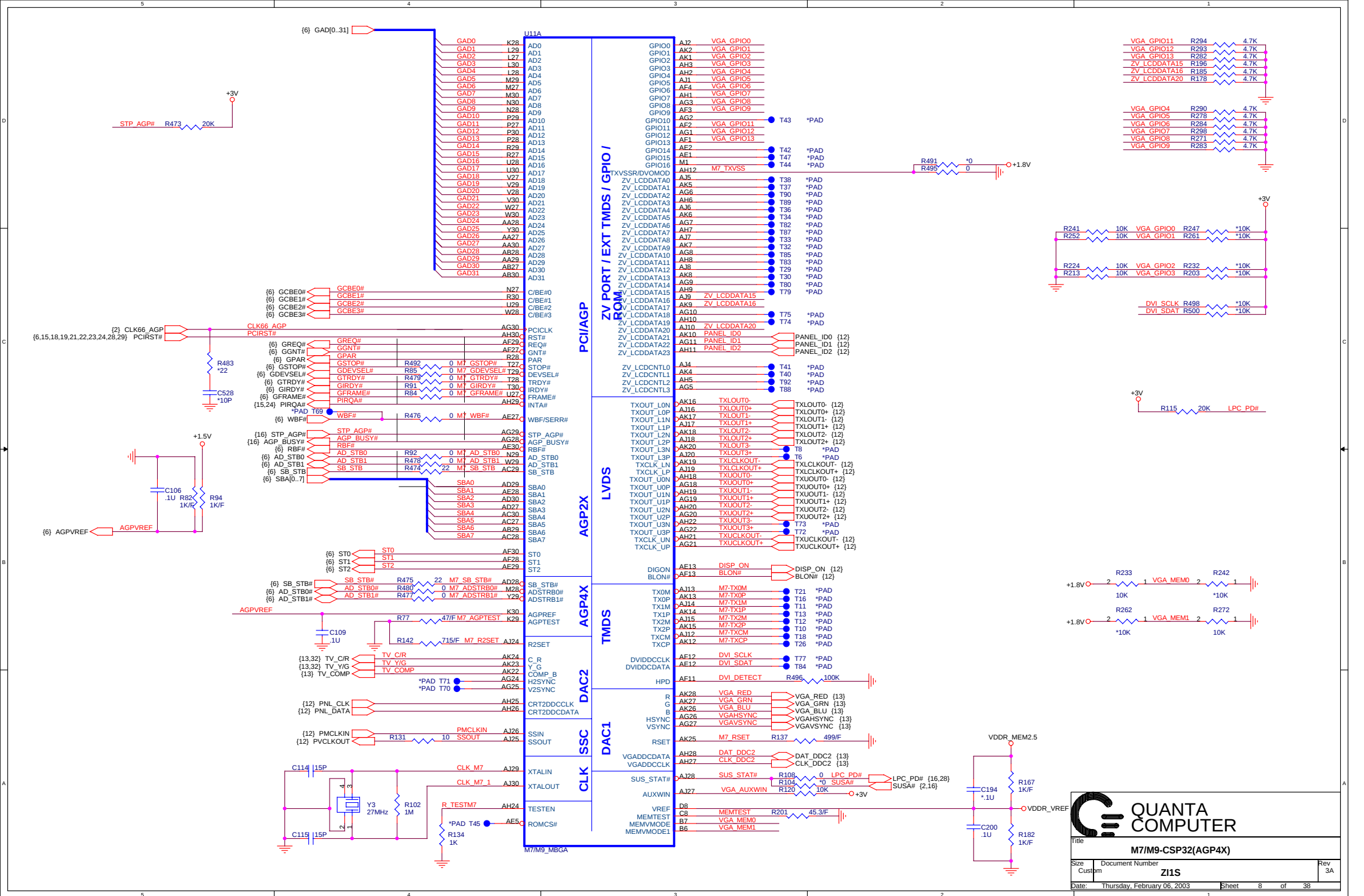


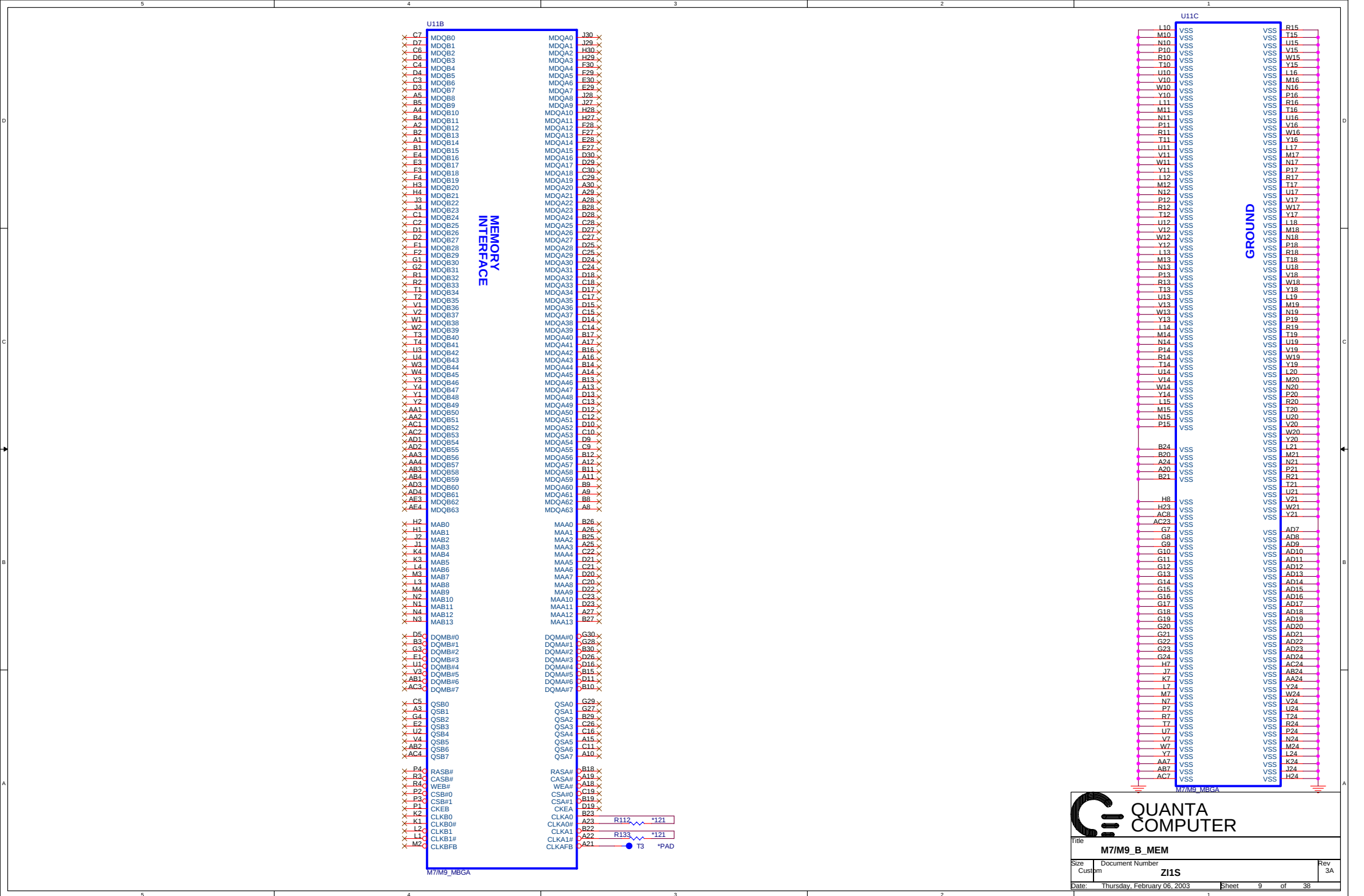


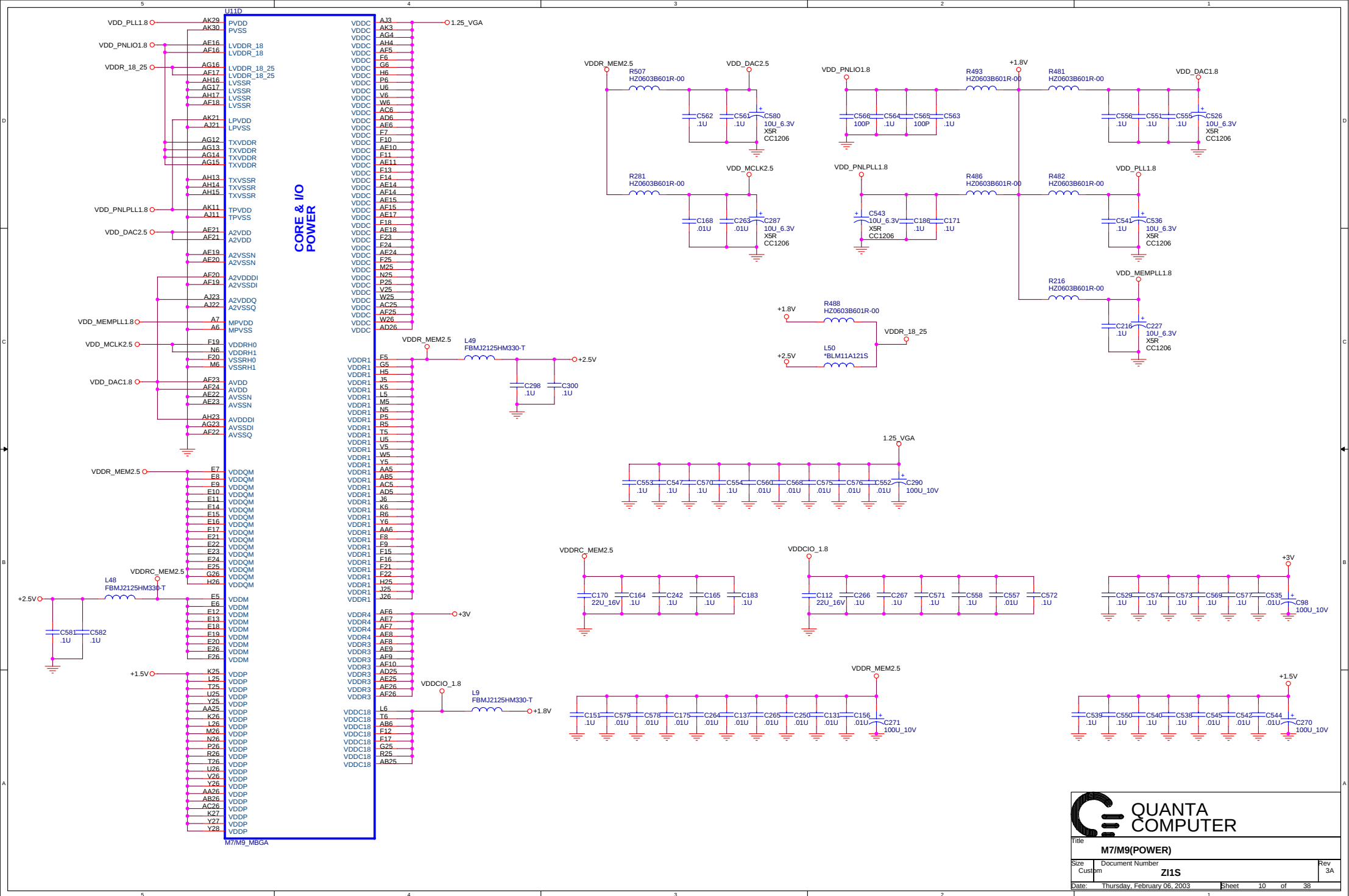


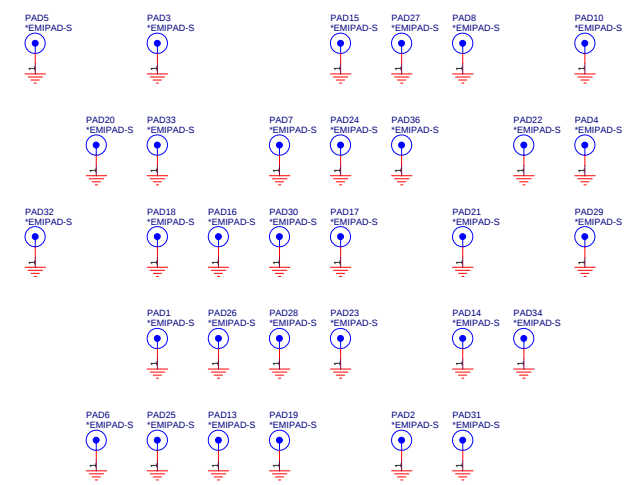
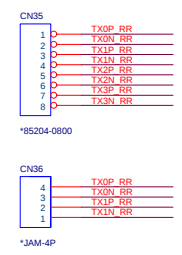
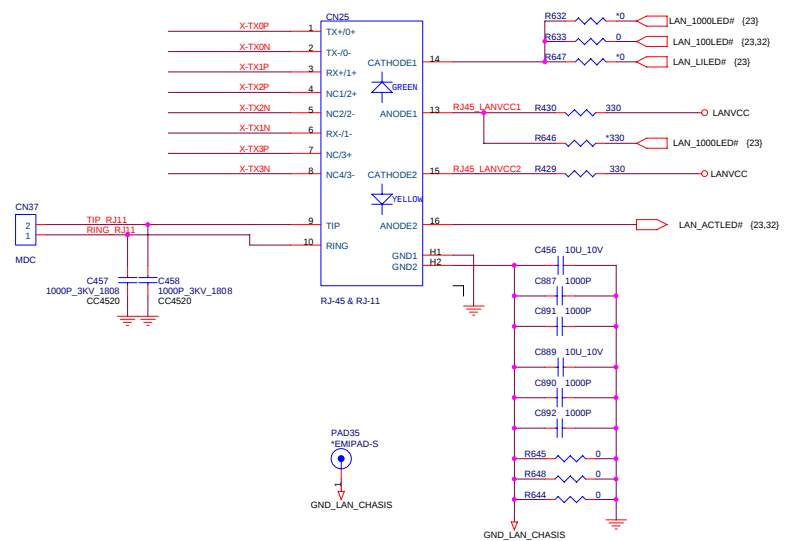
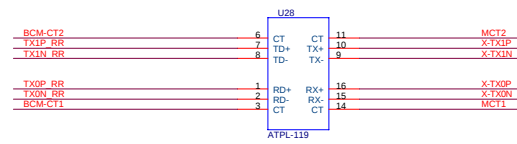
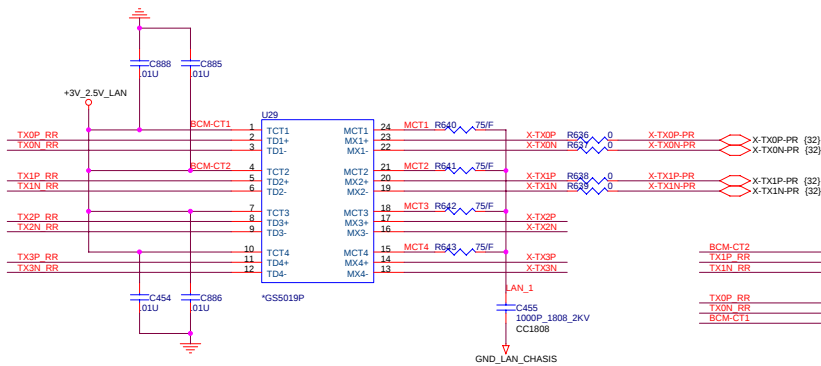
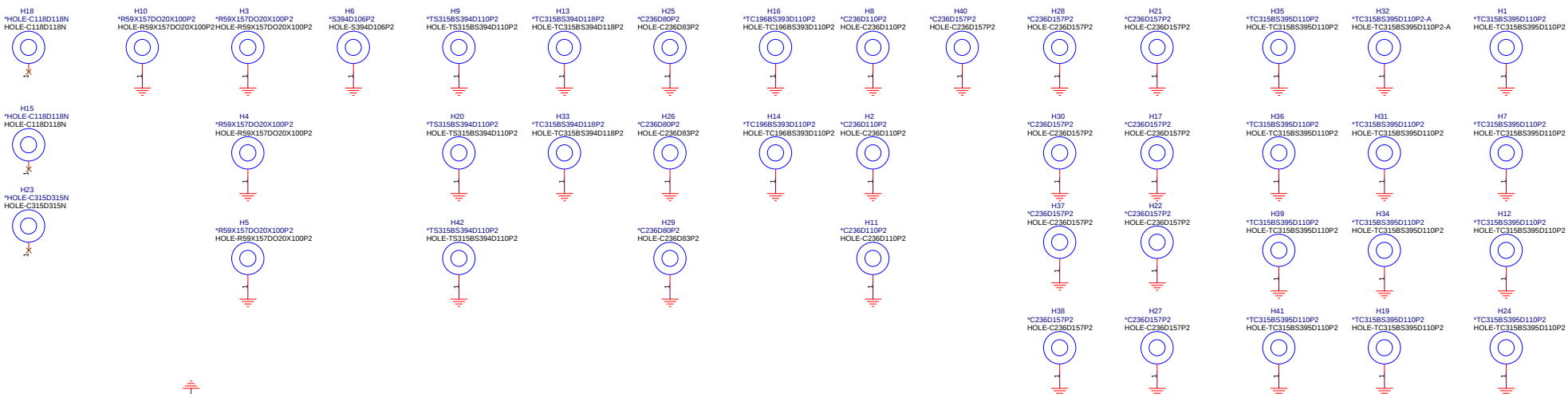


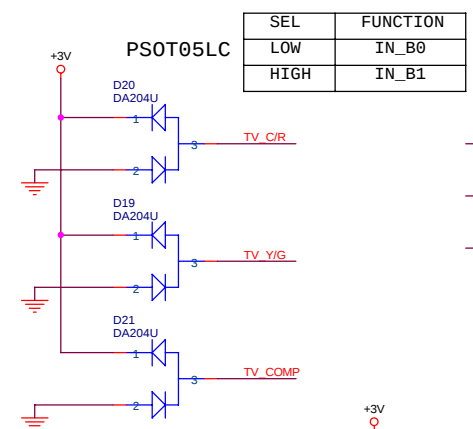
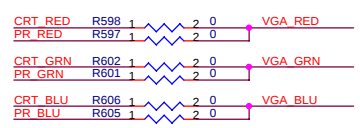
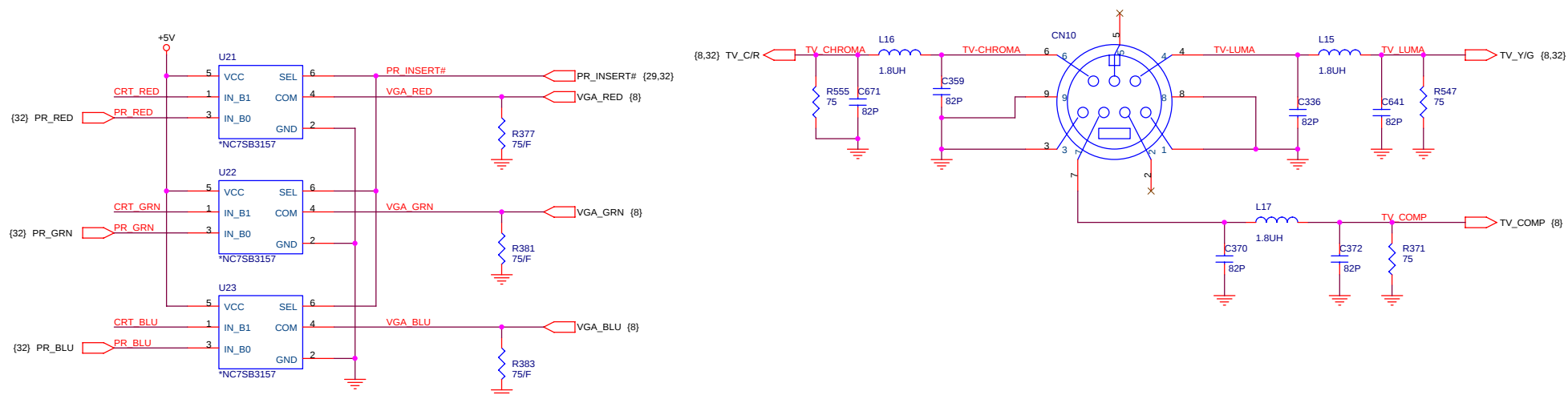




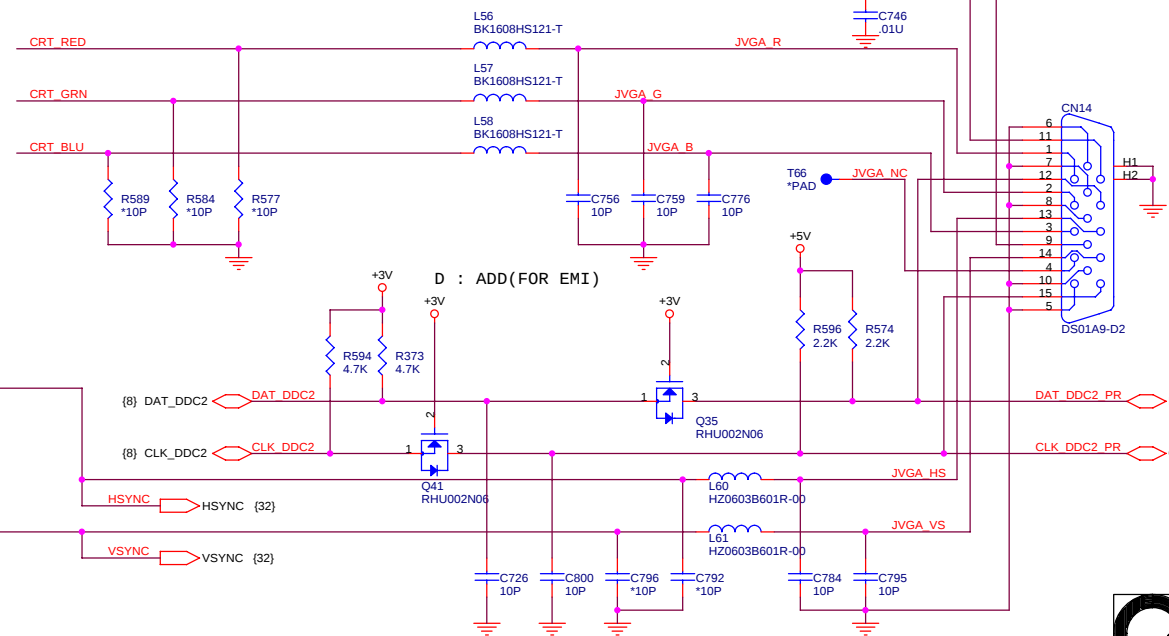








SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



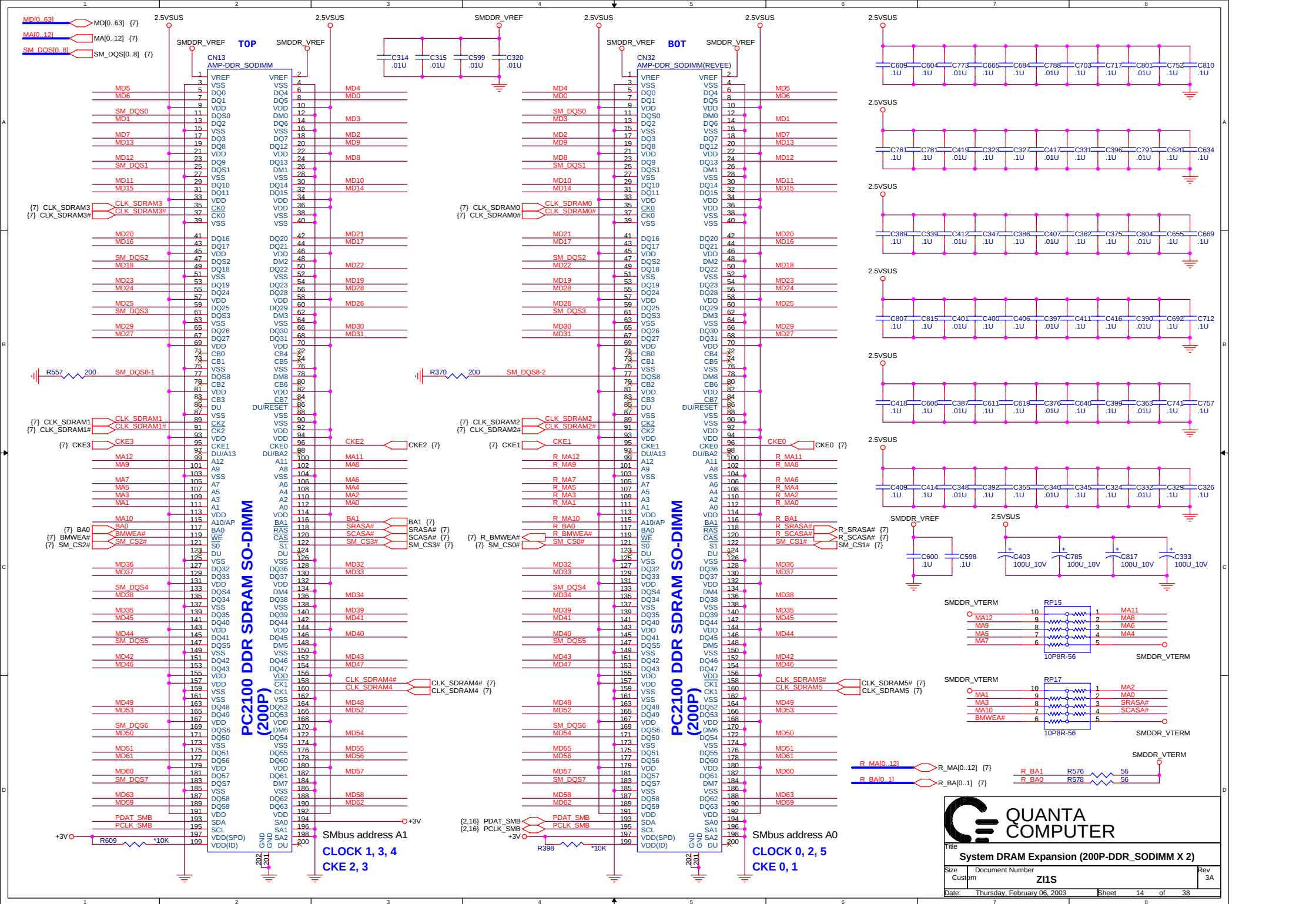
CRT PORT

**QUANTA
COMPUTER**

Title: **CRT & TV-OUT**

Size: Custom Document Number: **Z11S** Rev: 3A

Date: Thursday, February 06, 2003 Sheet: 13 of 38



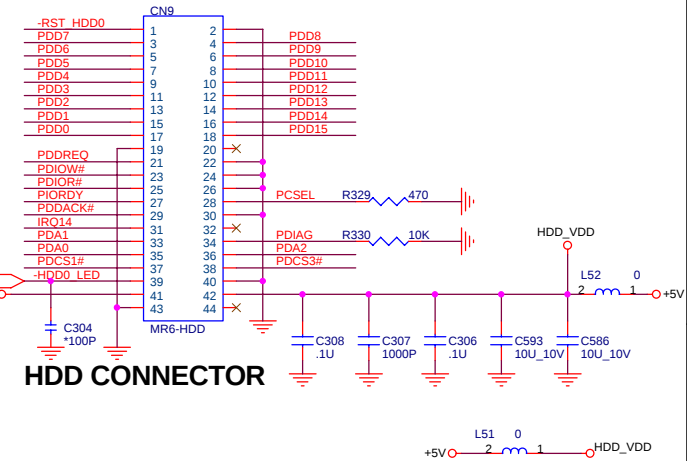
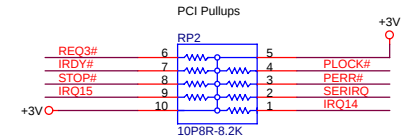
ICH HUB

CPU

PCI

IDE

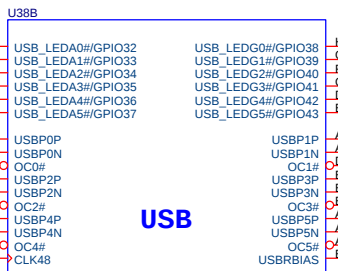
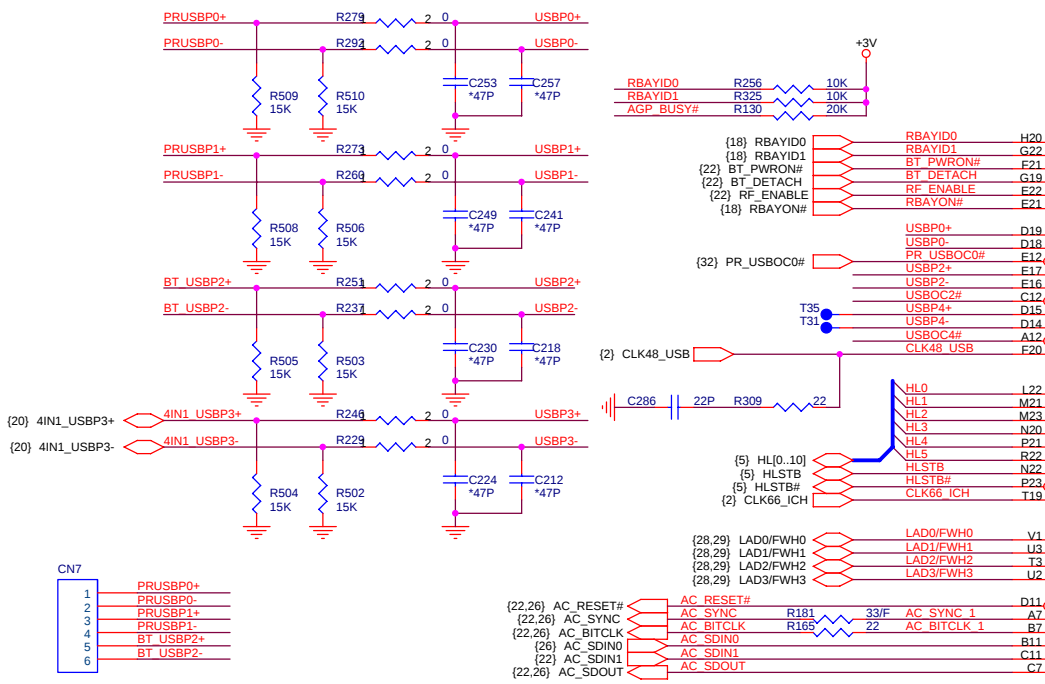
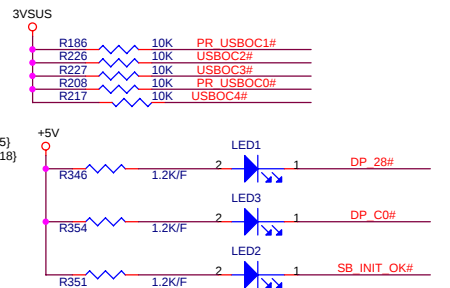
ICH3_G



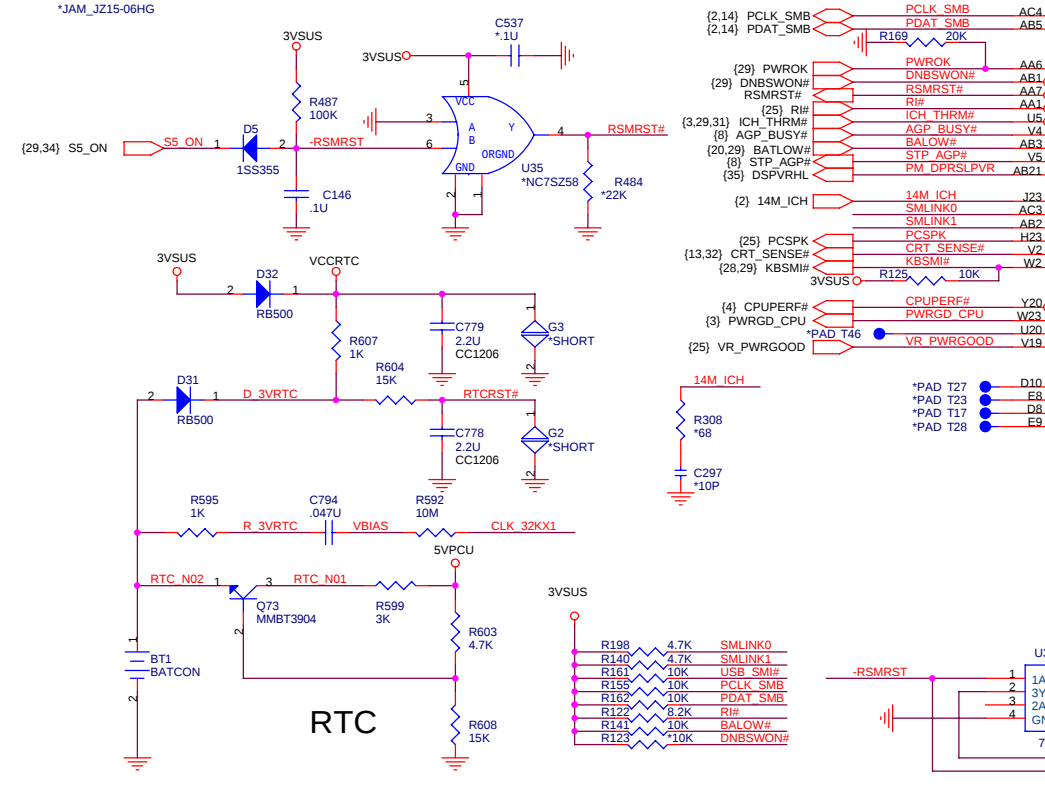
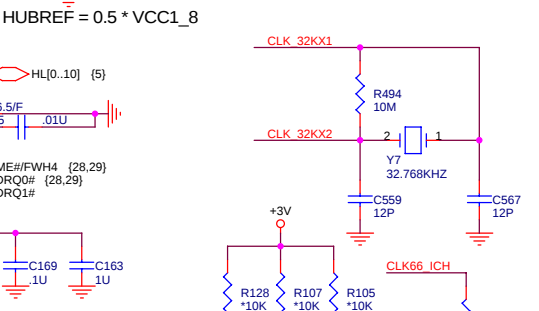
Title			
ICH3-M (CPU,PCI,IDE)			
Size	Document Number	Rev	
Custom	Z11S	3A	
Date:	Thursday, February 06, 2003	Sheet	15 of 38

ICH3_M

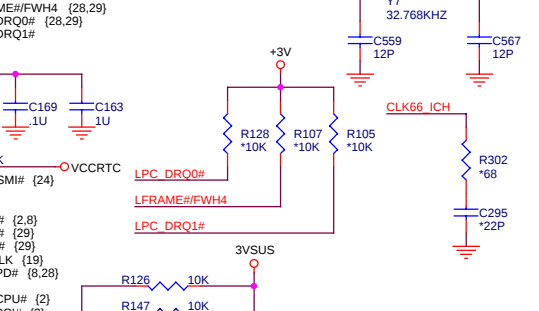
USB 0, 1 to Port Replicator
USB 2 is for Blue Tooth (To MDC)
USB 3 for 4-In-1 Reader



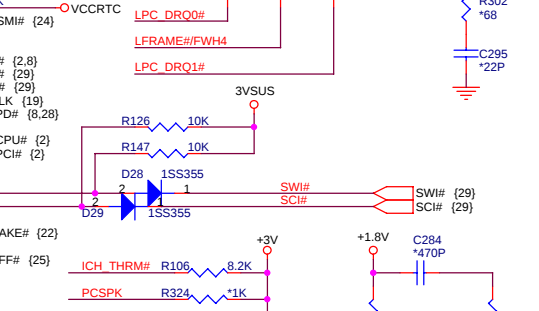
CHANGE TO 18.2 Ohm
HUBREF = 0.5 * VCC1_8



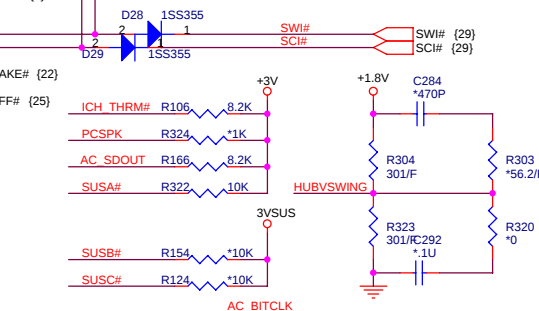
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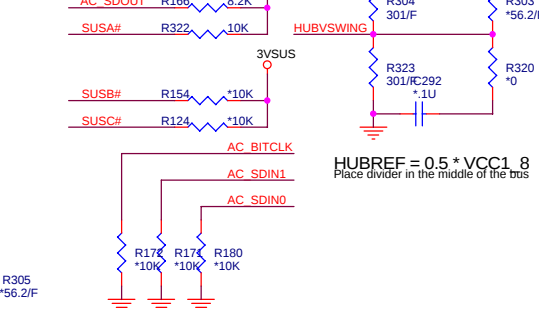
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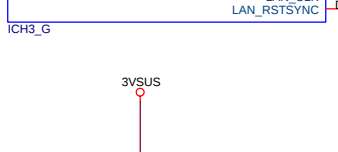
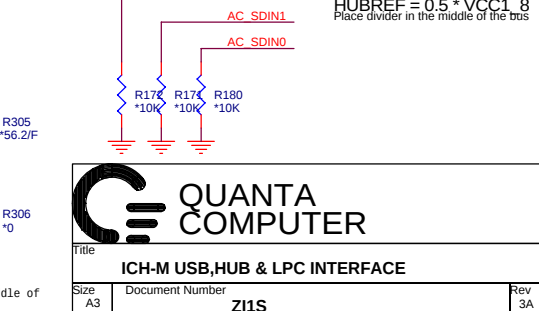
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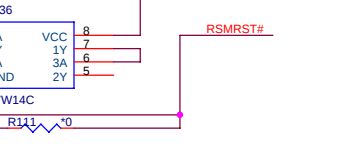
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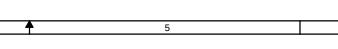
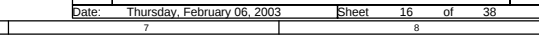
HUBREF = 0.5 * VCC1_8



HUBREF = 0.5 * VCC1_8



HUBREF = 0.5 * VCC1_8

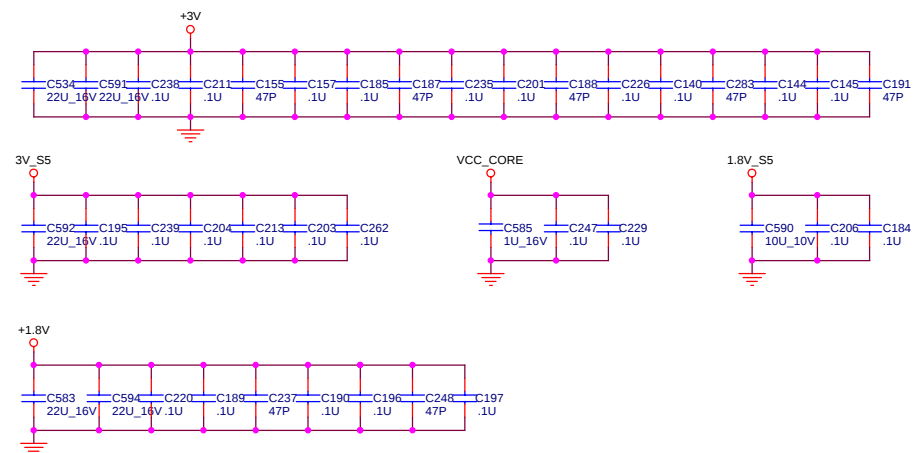
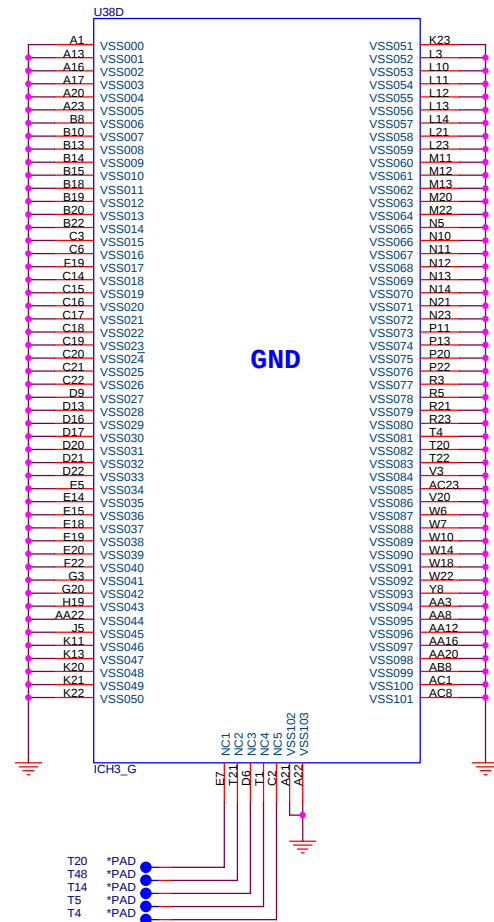
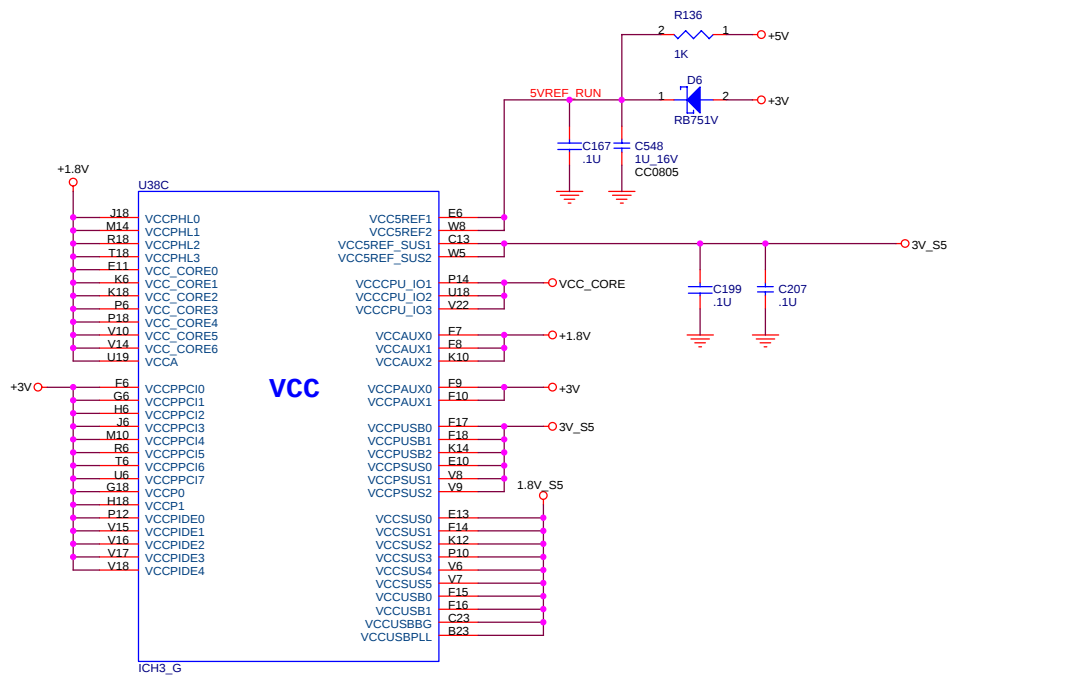


QUANTA COMPUTER

Title: ICH-M USB, HUB & LPC INTERFACE

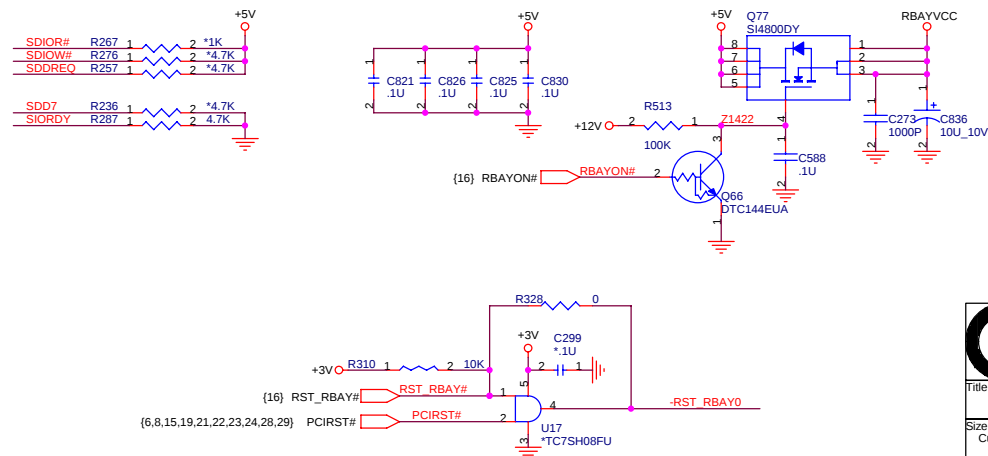
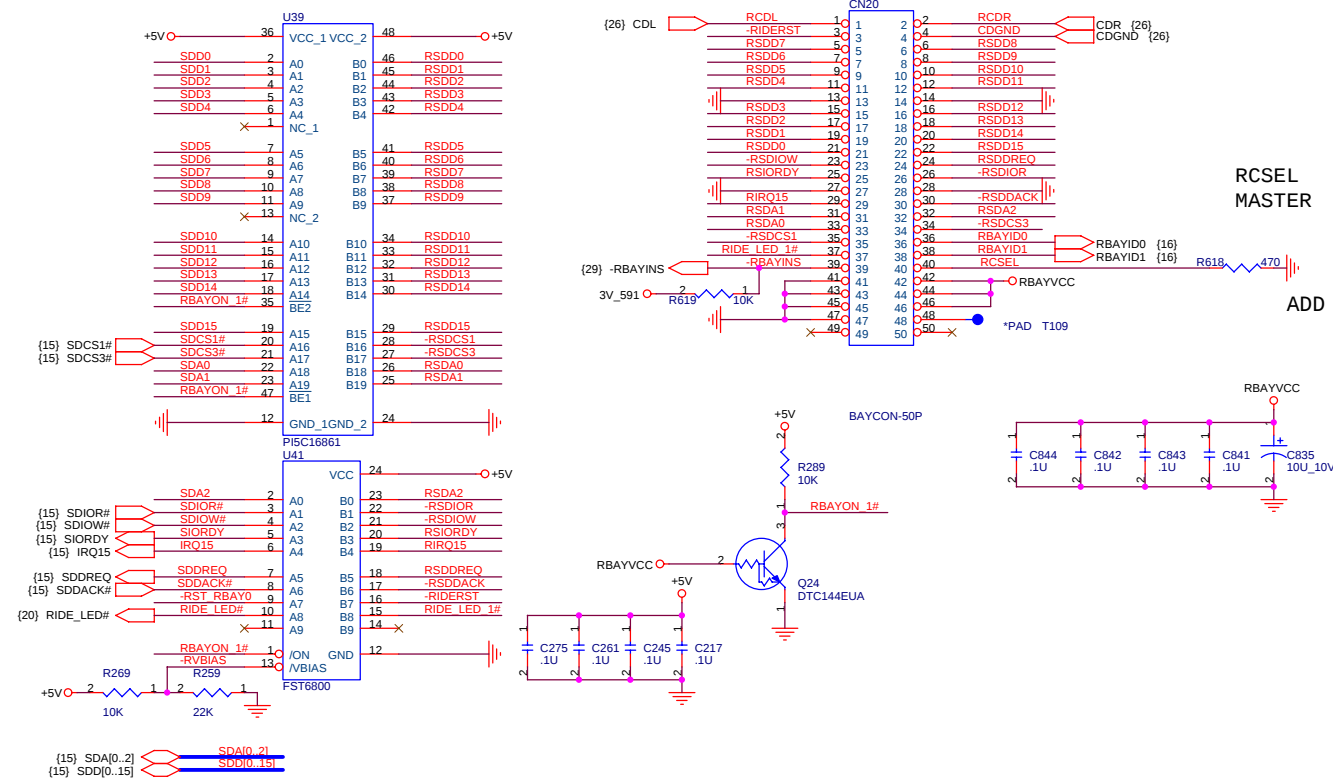
Size: A3 Document Number: Z11S Rev: 3A

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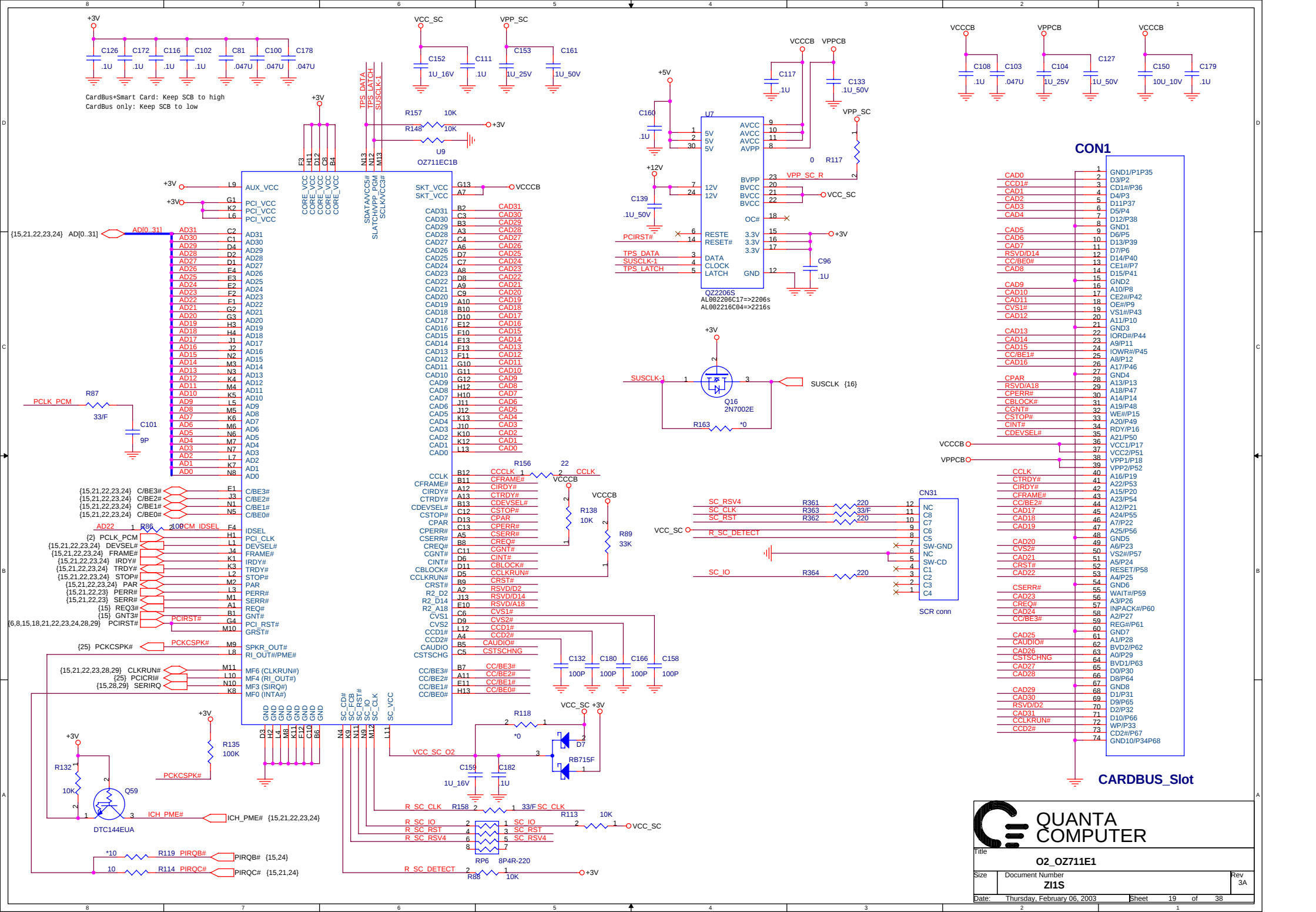


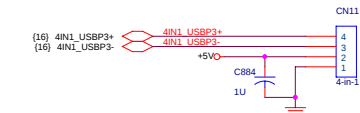
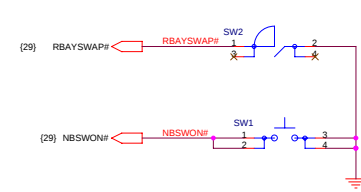
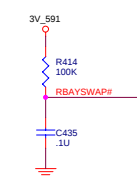
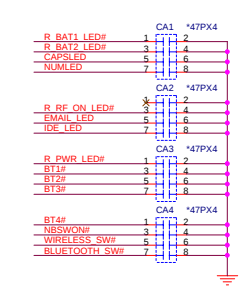
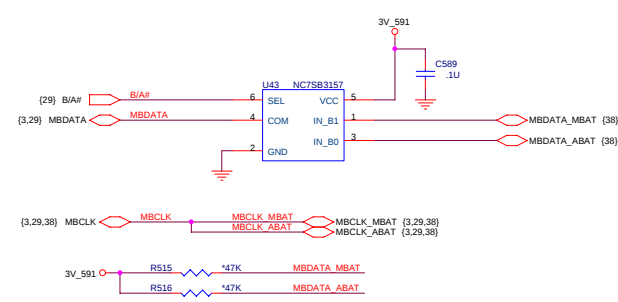
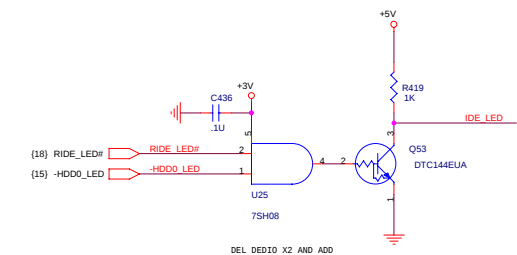
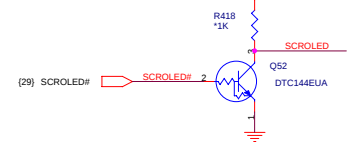
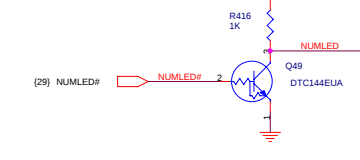
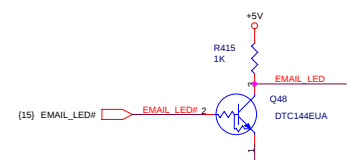
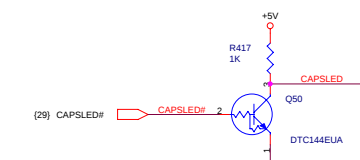
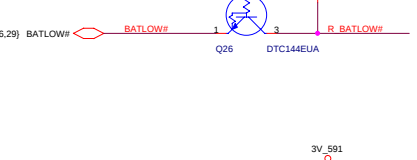
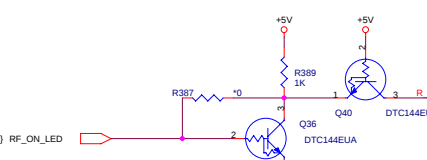
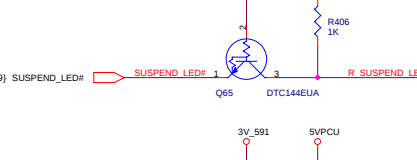
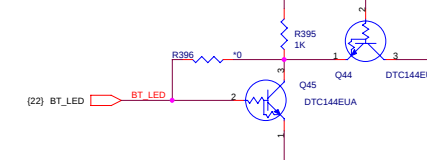
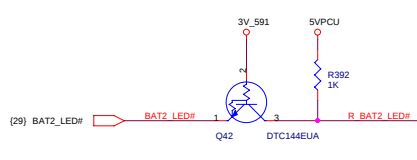
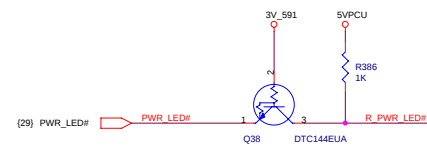
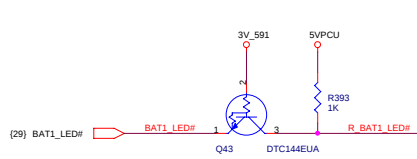
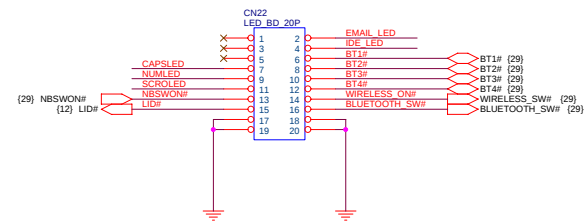
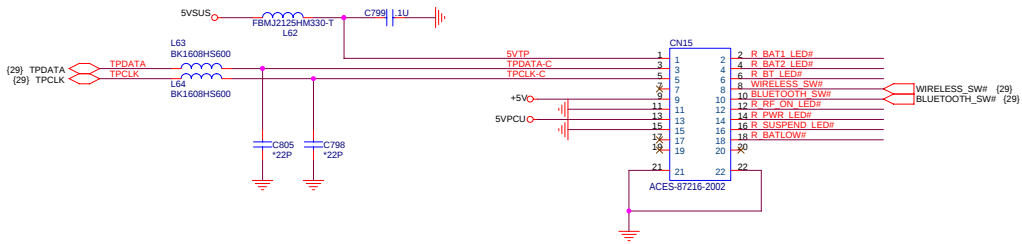
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ICH3-M (POWER&GND)		
Size	Document Number	Rev
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NEED TO CHECK BAY CONNECTOR



Title			
DEVICE BAY			
Size	Document Number	Rev	
Custom	Z1S	3A	
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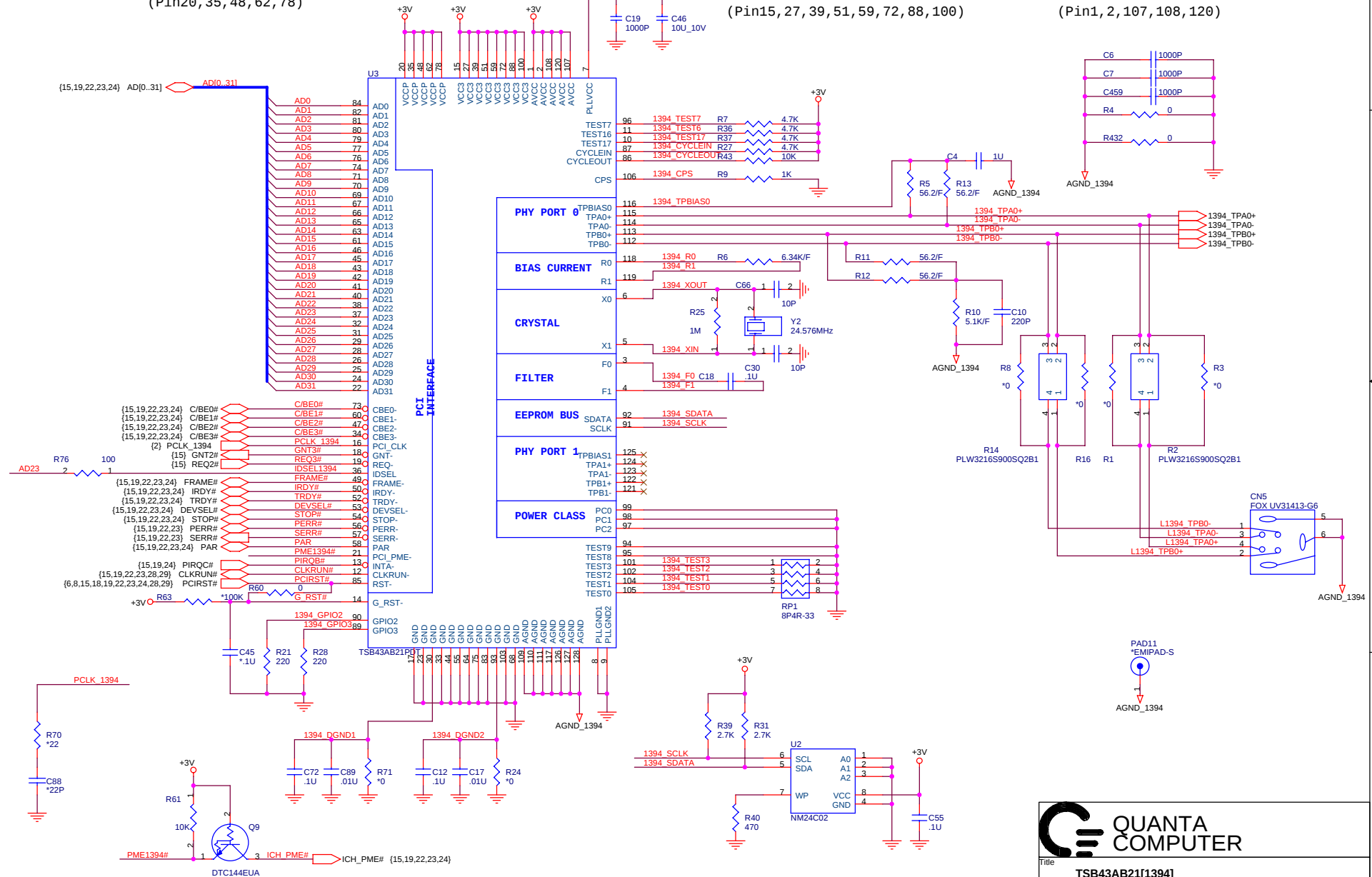


IEEE-1394

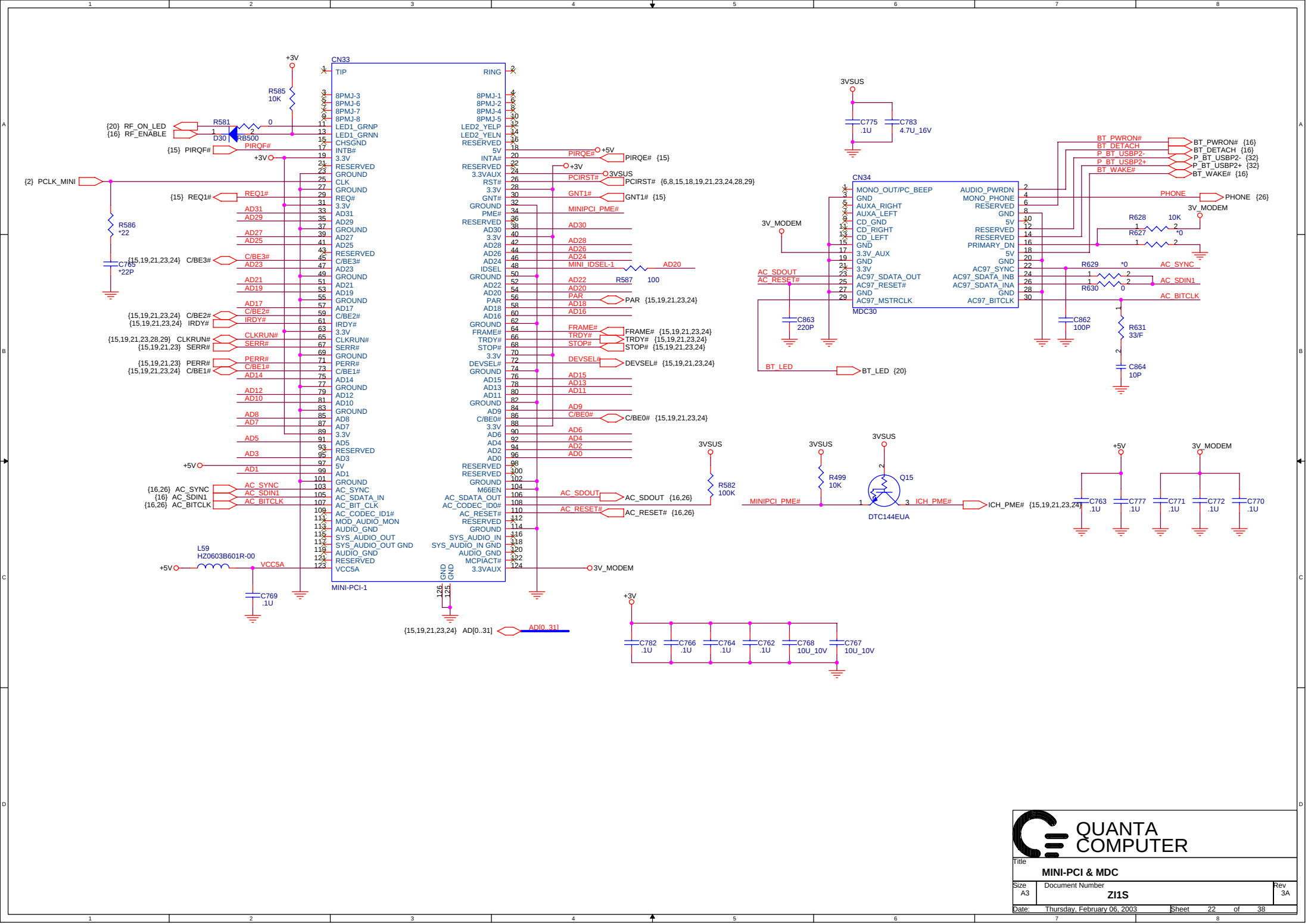
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(Pin20, 35, 48, 62, 78)

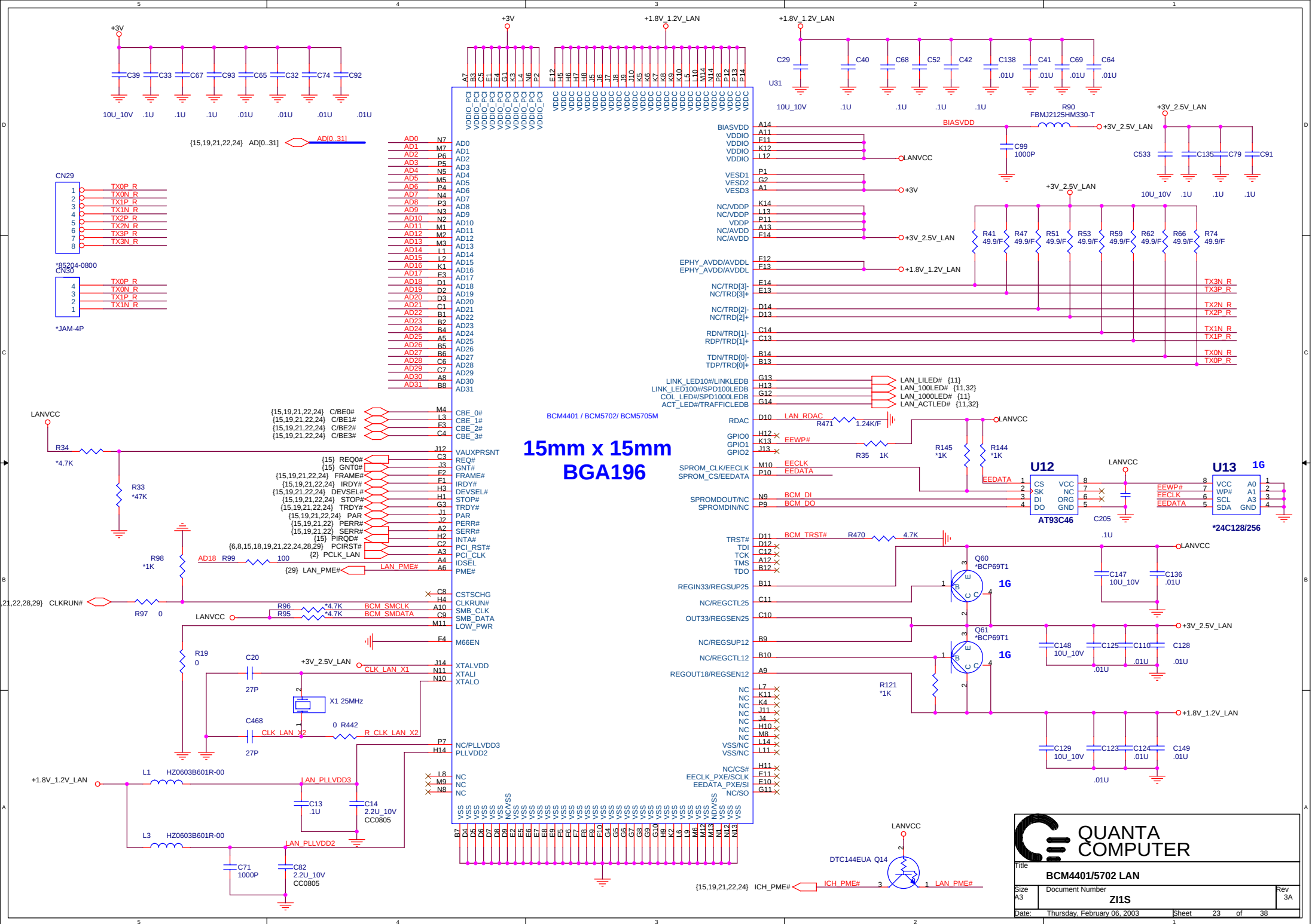
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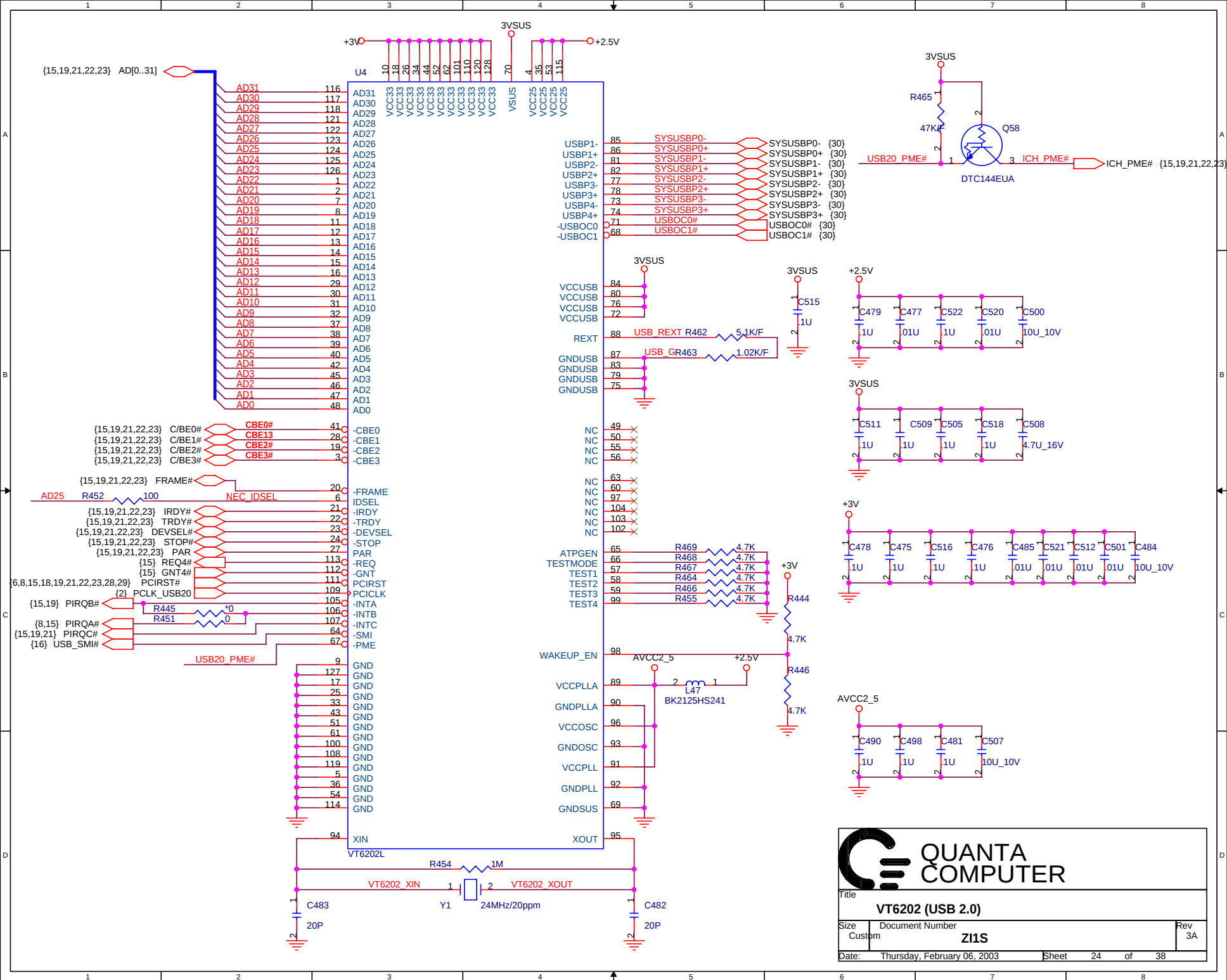
Used for AVCC
(Pin1, 2, 107, 108, 120)

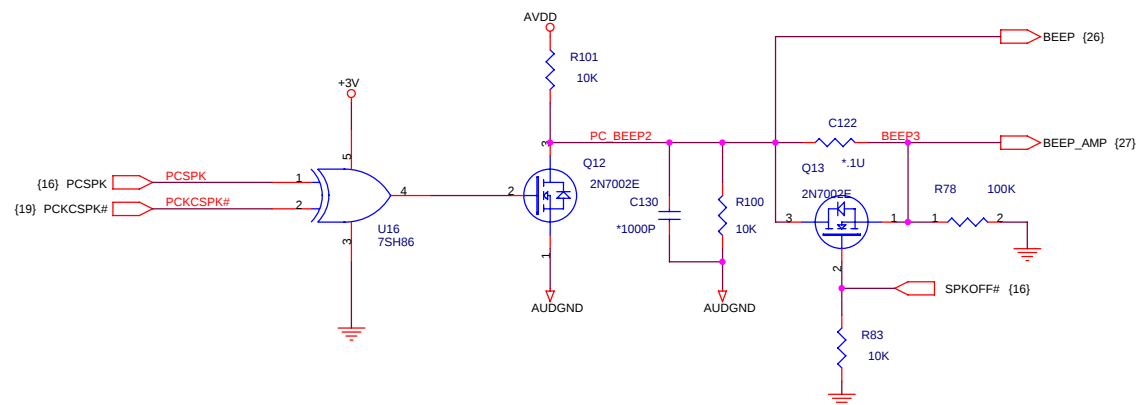
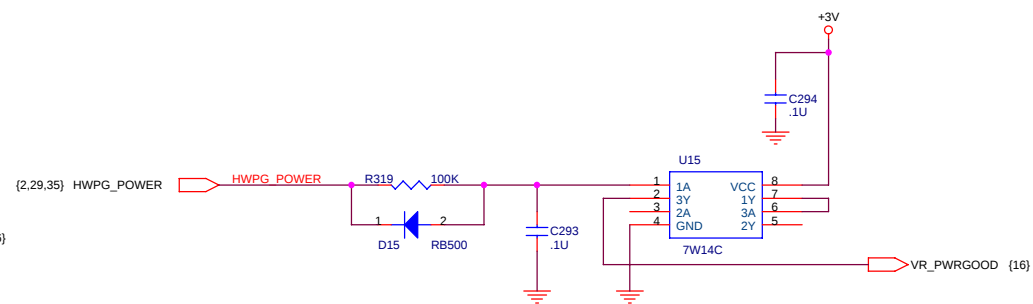
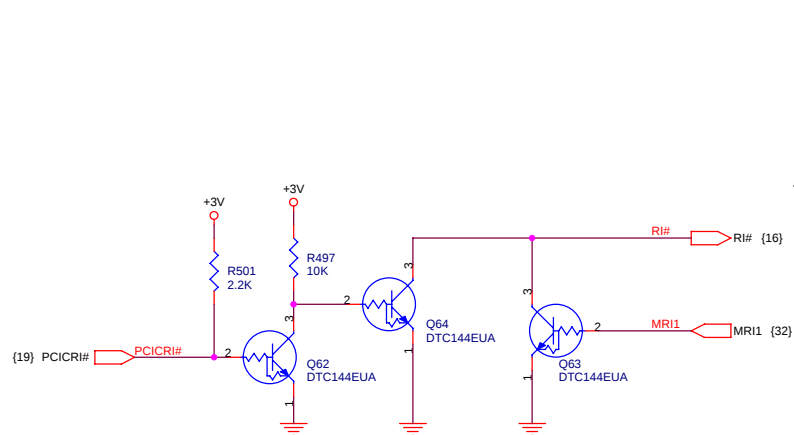
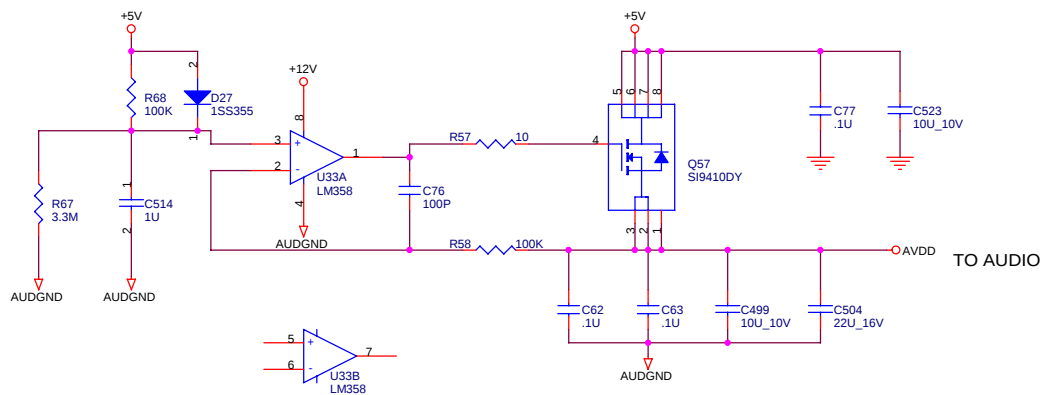


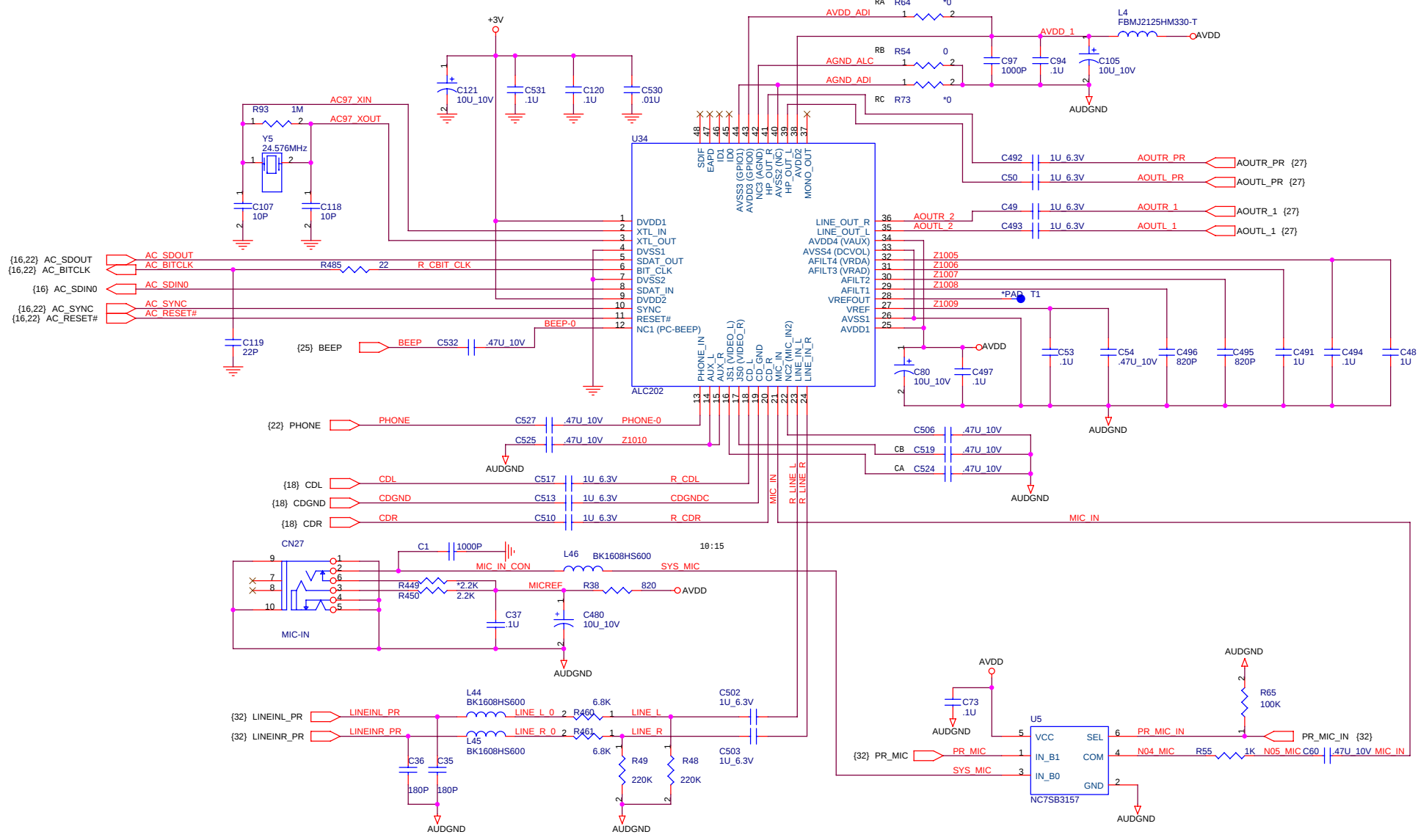
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Size	Document Number	ZI1S
Custom		
Date:	Thursday, February 06, 2003	Sheet 21 of 38
		Rev 3A



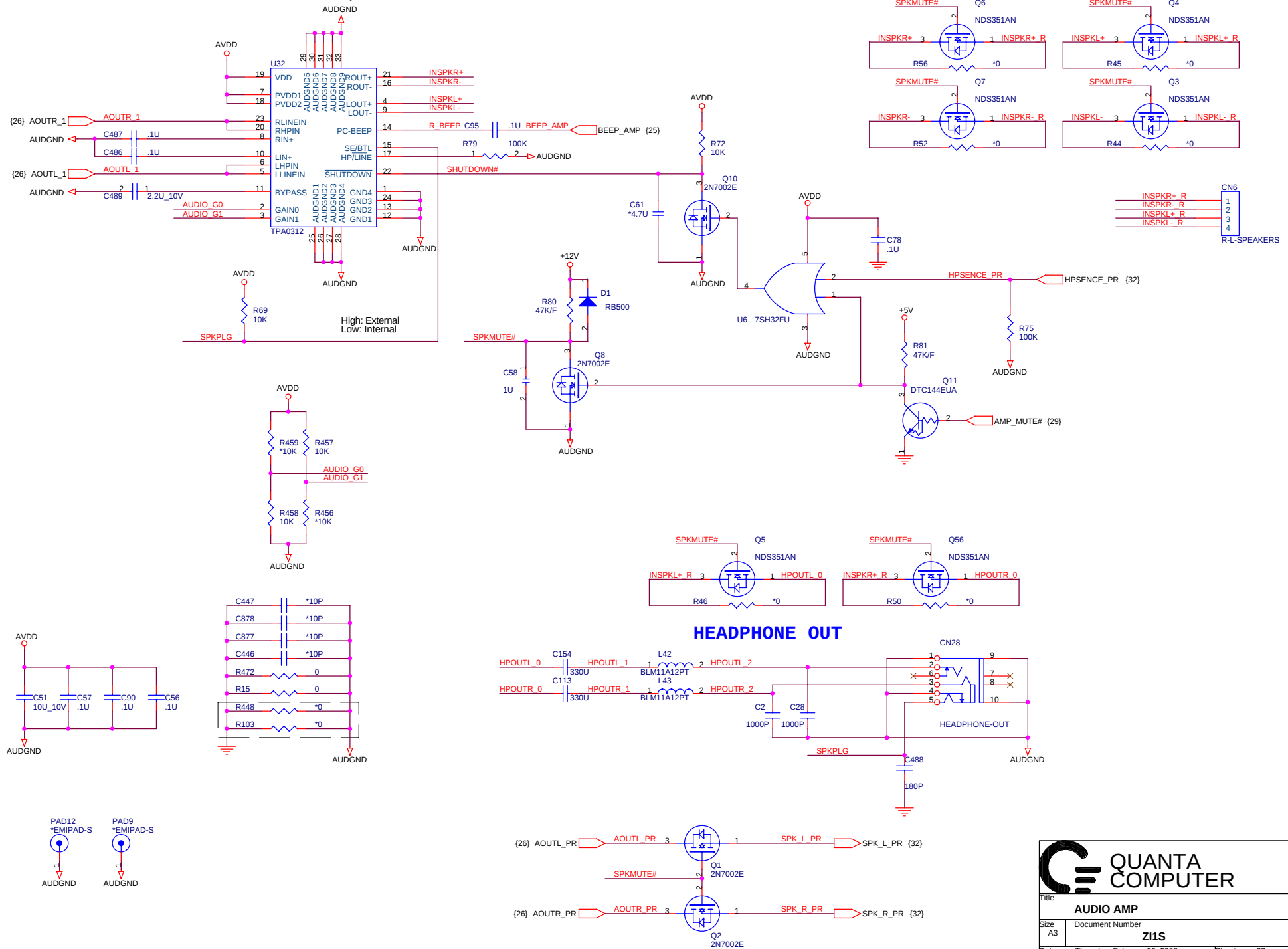




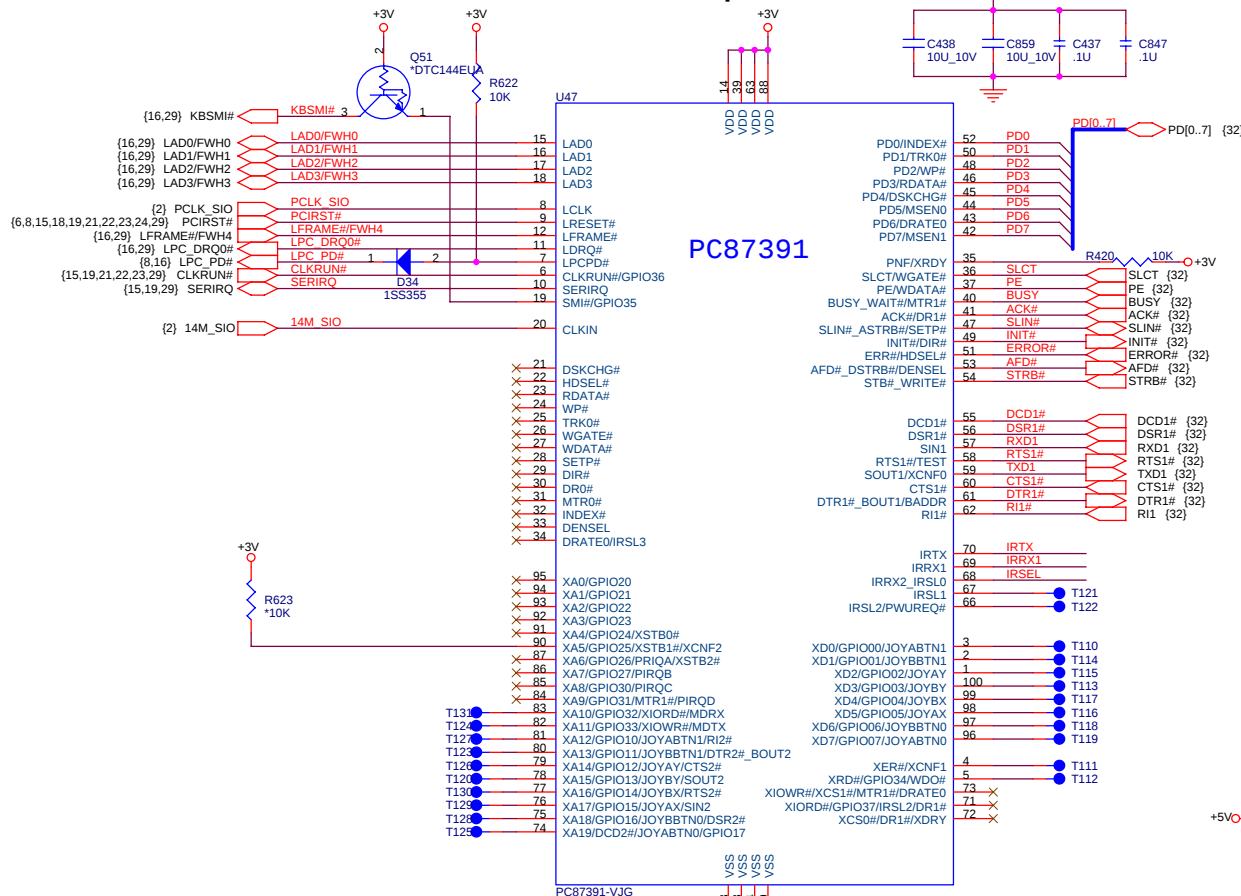




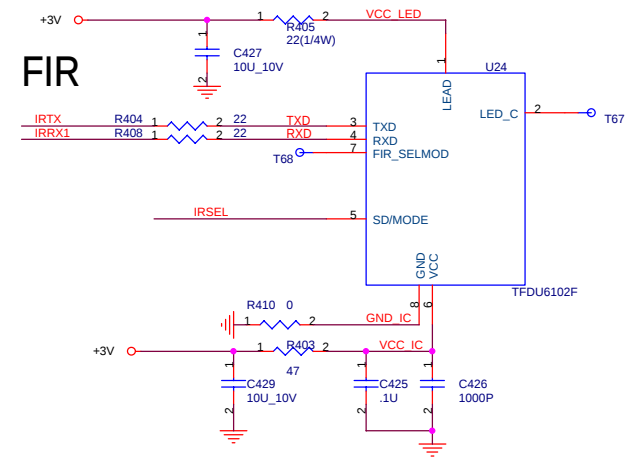
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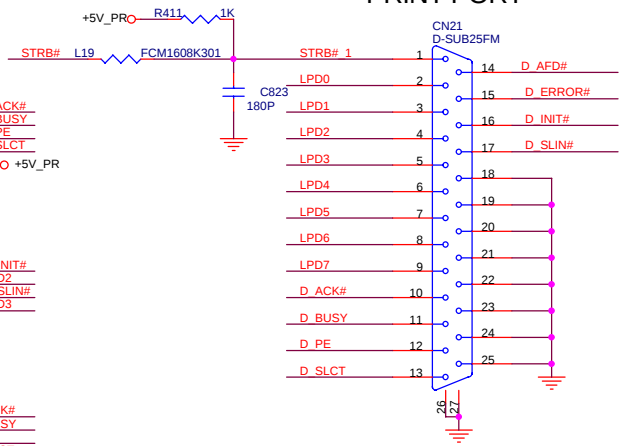
Super I/O



FIR



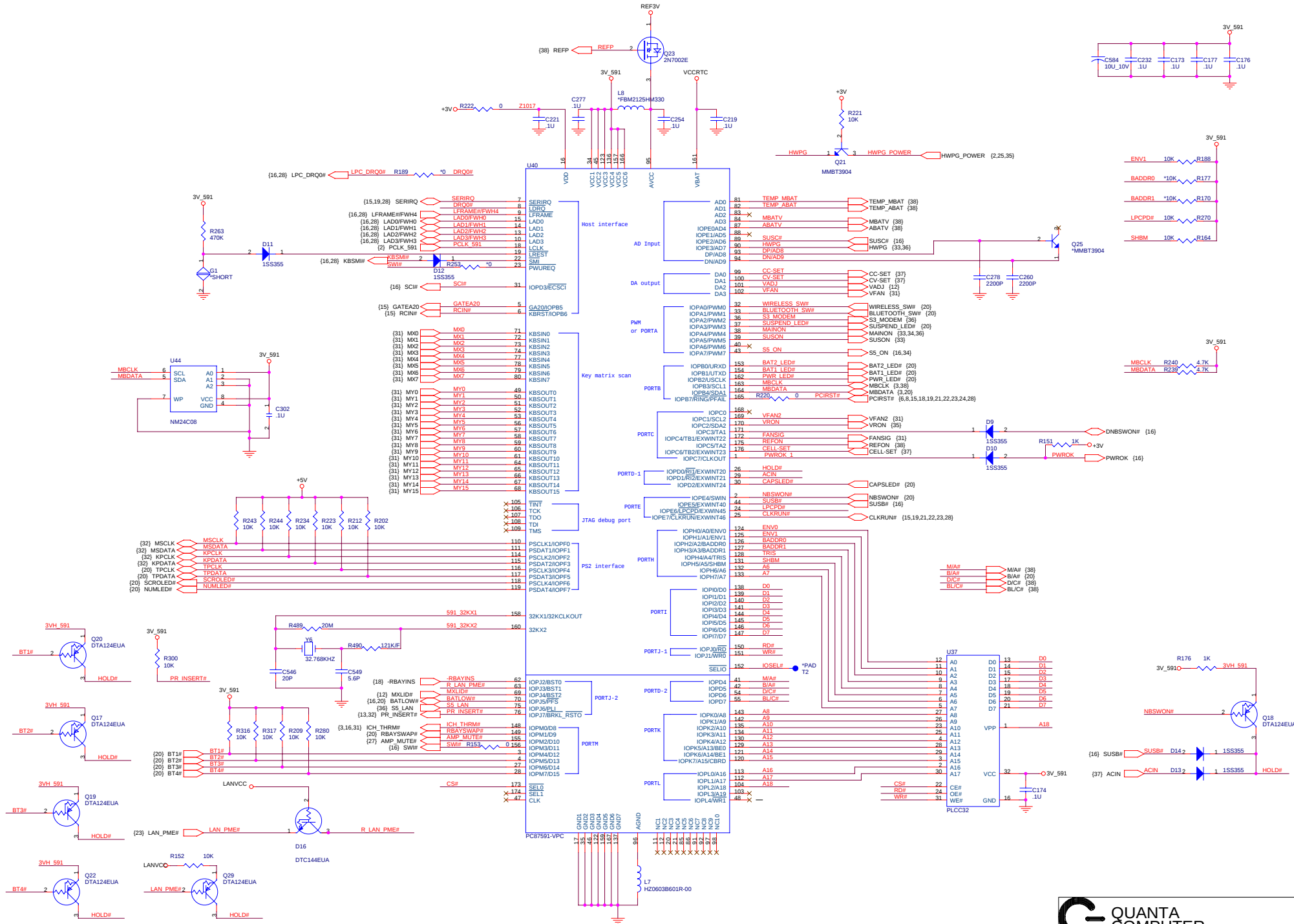
PRINT PORT

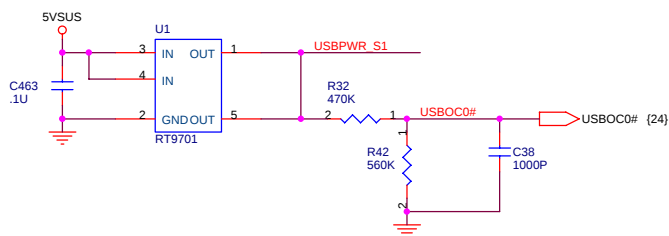
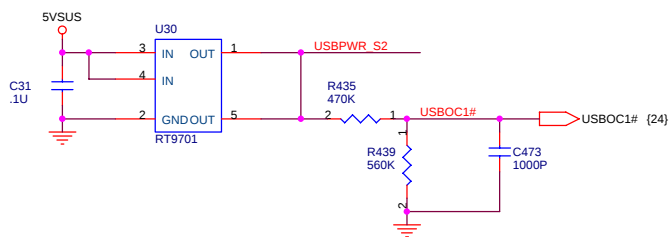
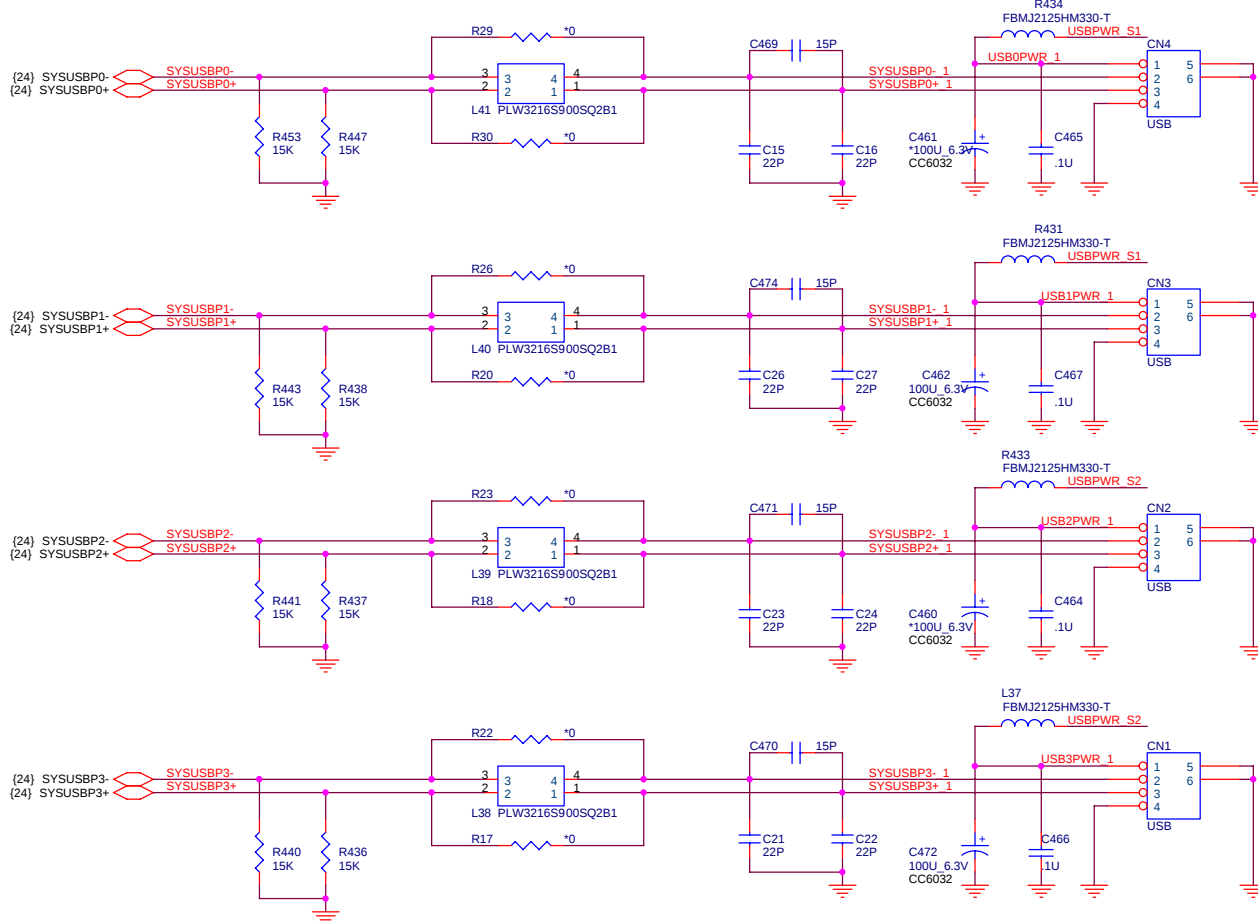


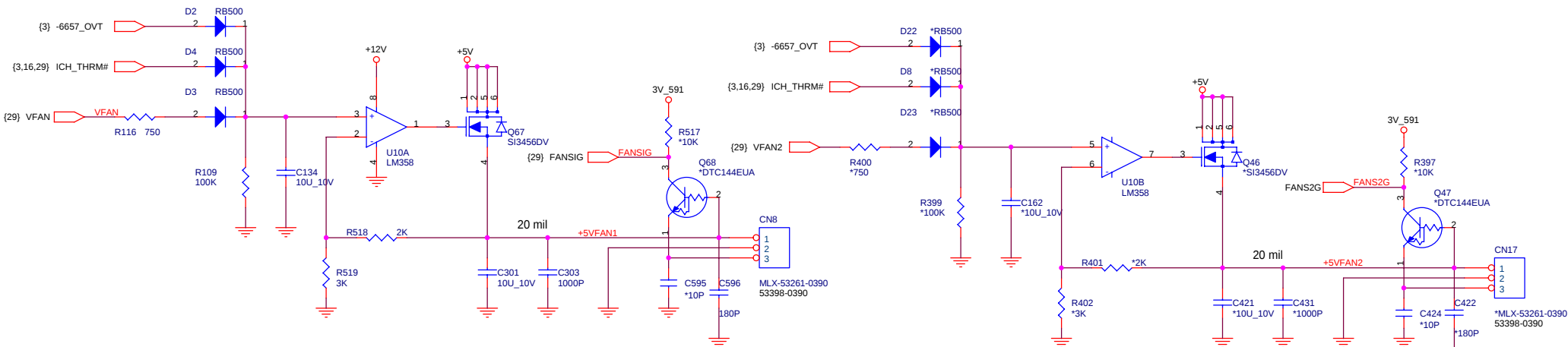


QUANTA COMPUTER

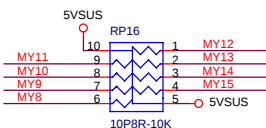
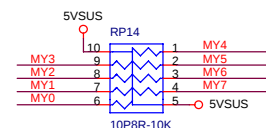
Title SUPER I/O NS37391		
Size A3	Document Number Z11S	Rev 3A
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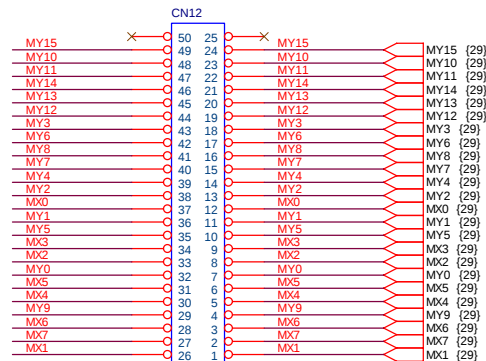




1st FAN OUT CONNECTOR

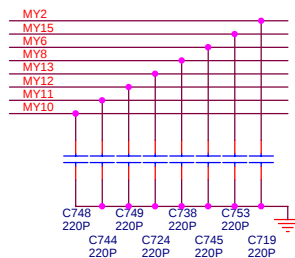
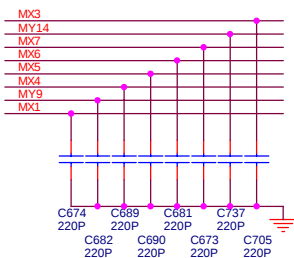
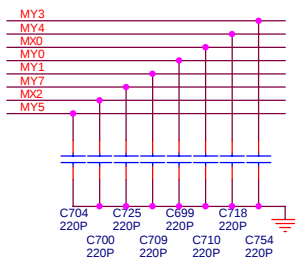


2nd FAN OUT CONNECTOR

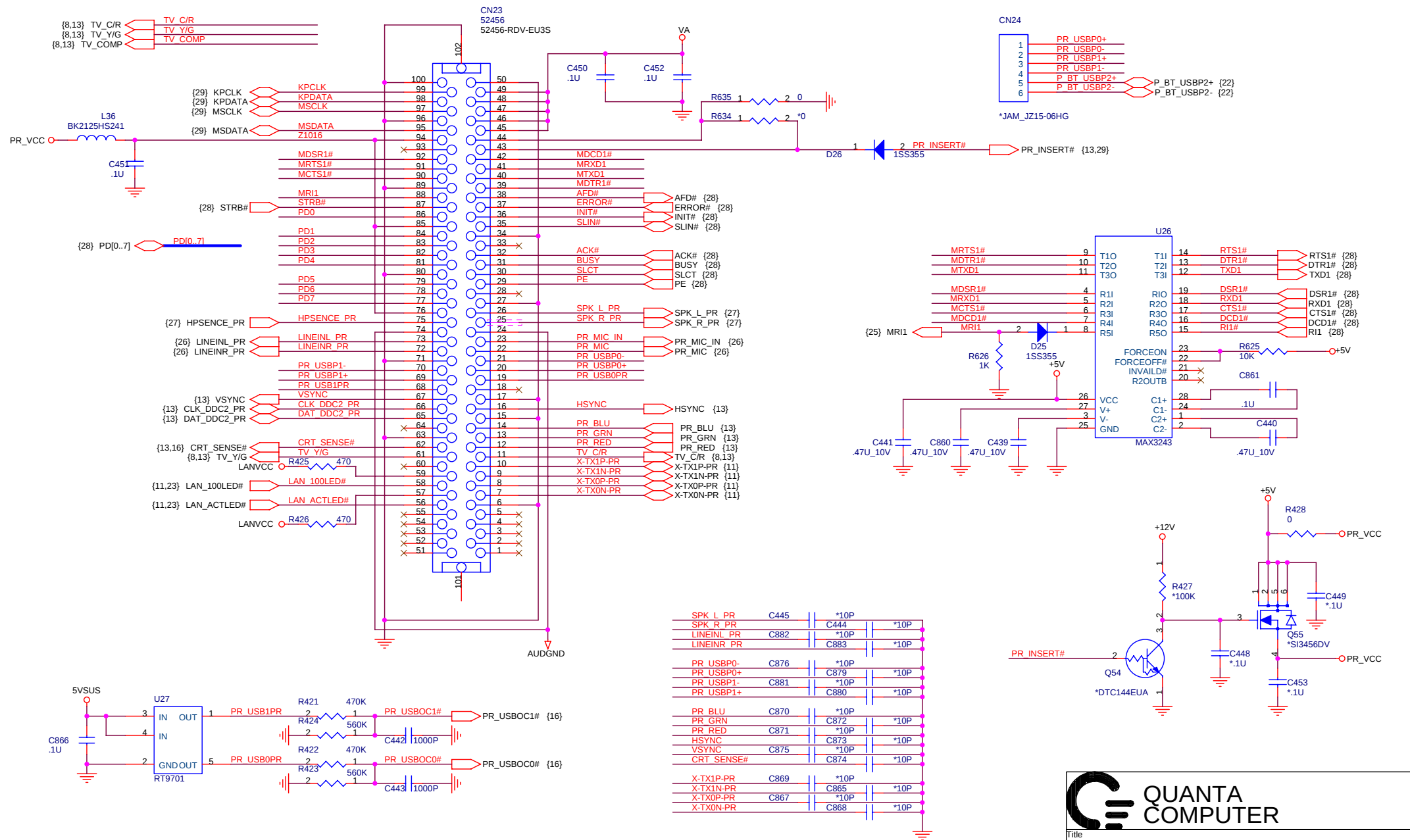


KEYBOARD

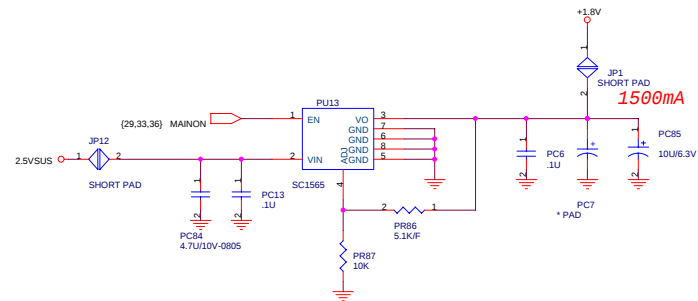
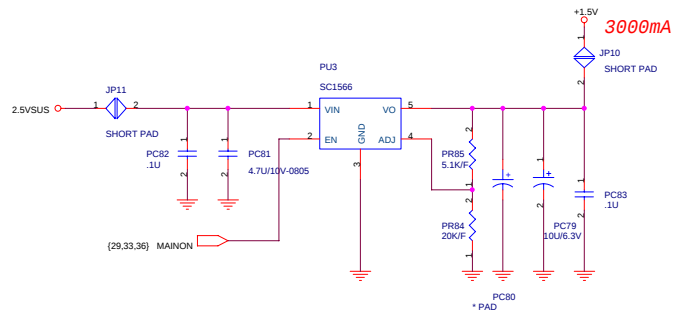
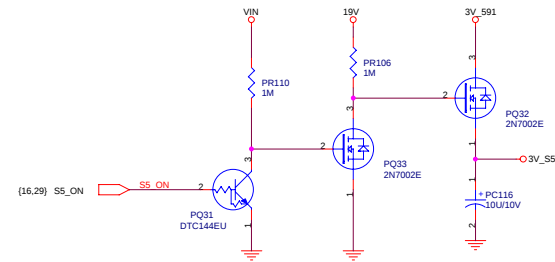
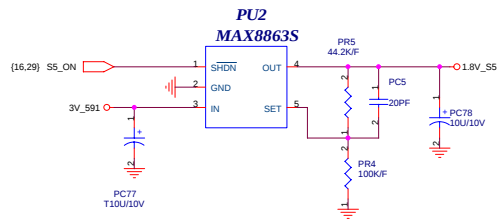
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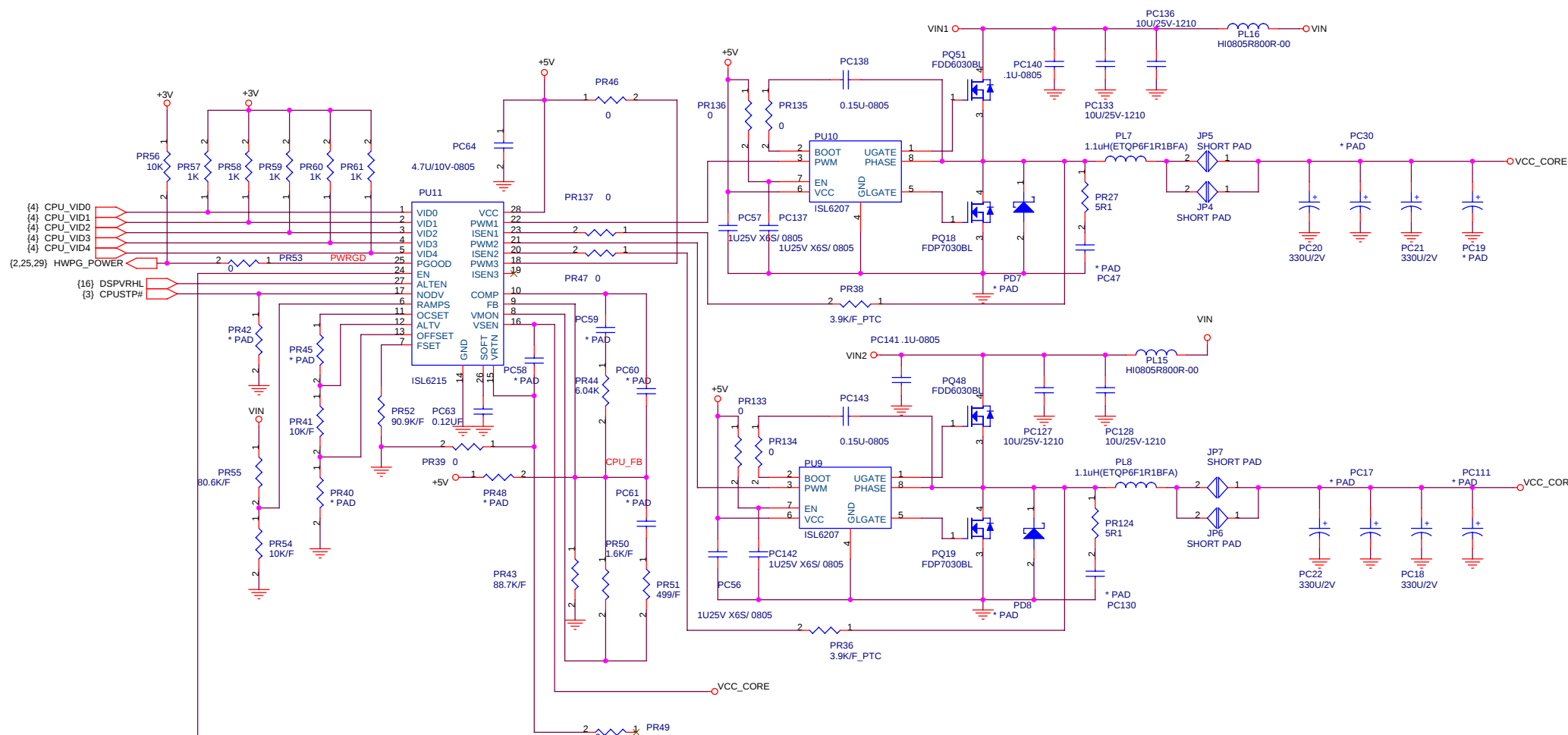


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A3	Z11S	3A
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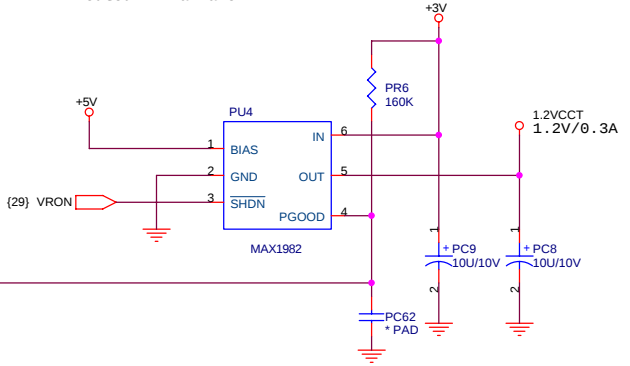


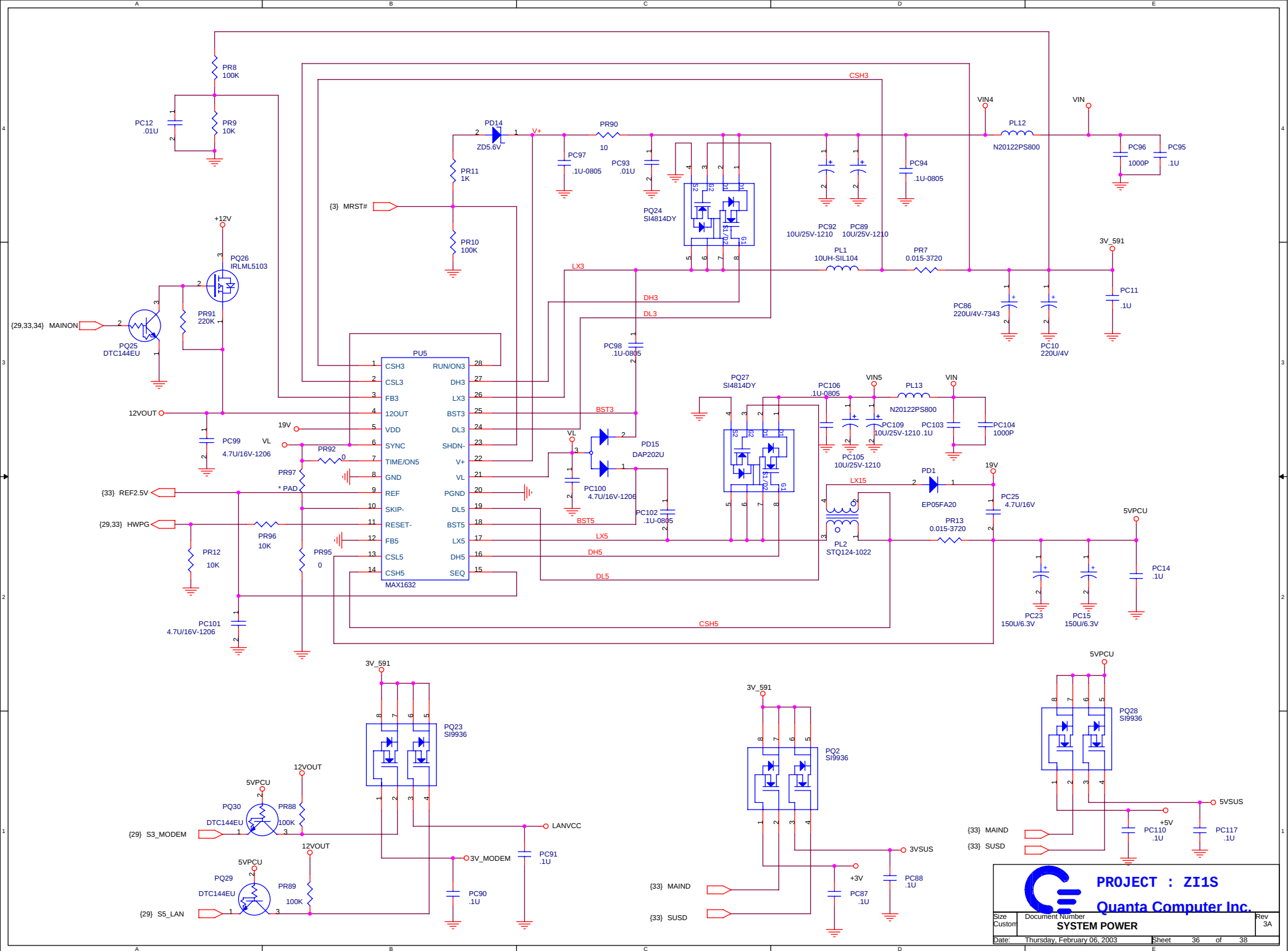
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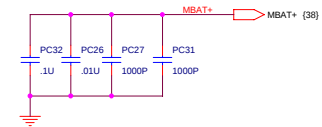




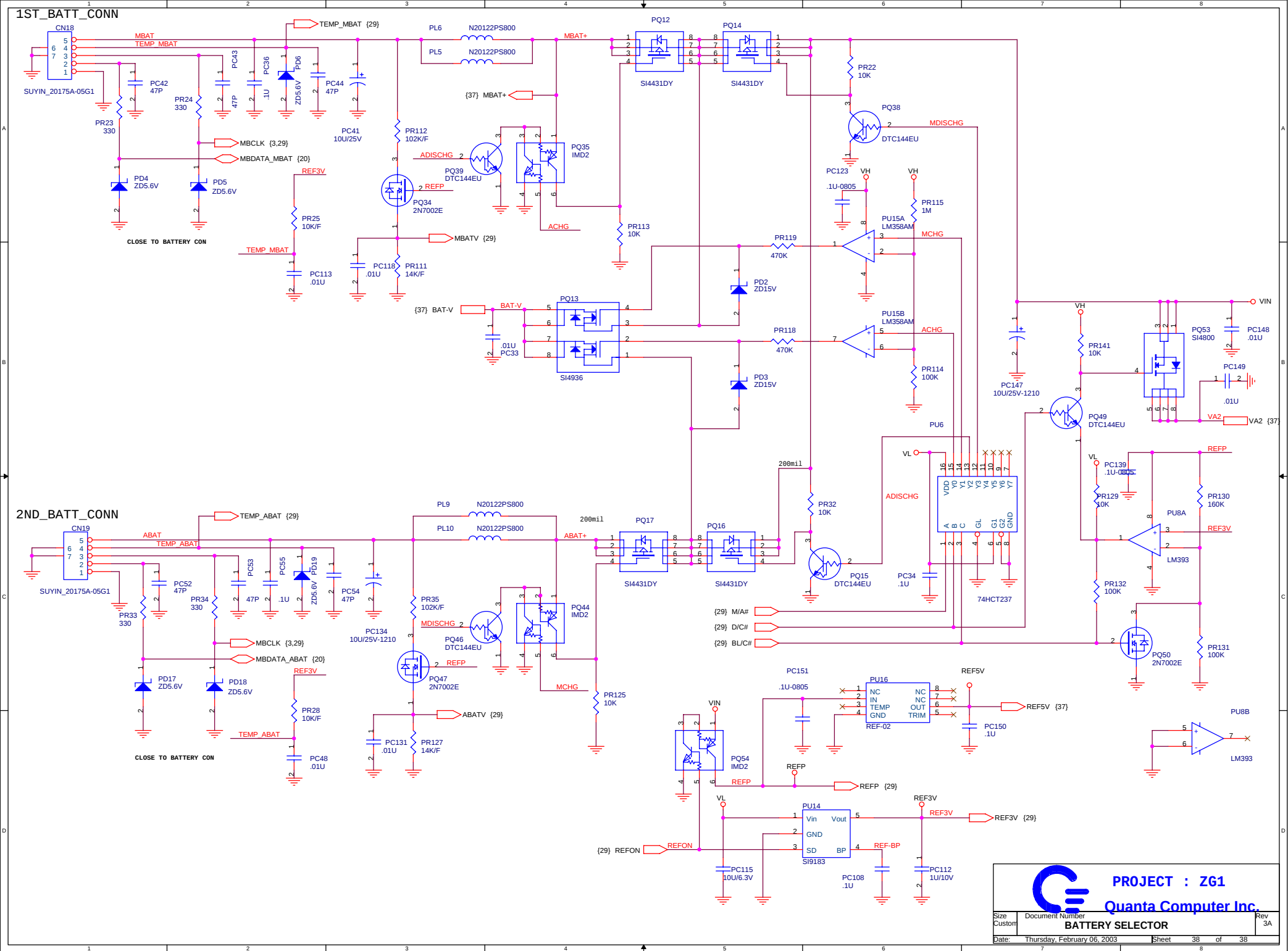
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