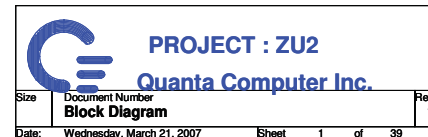
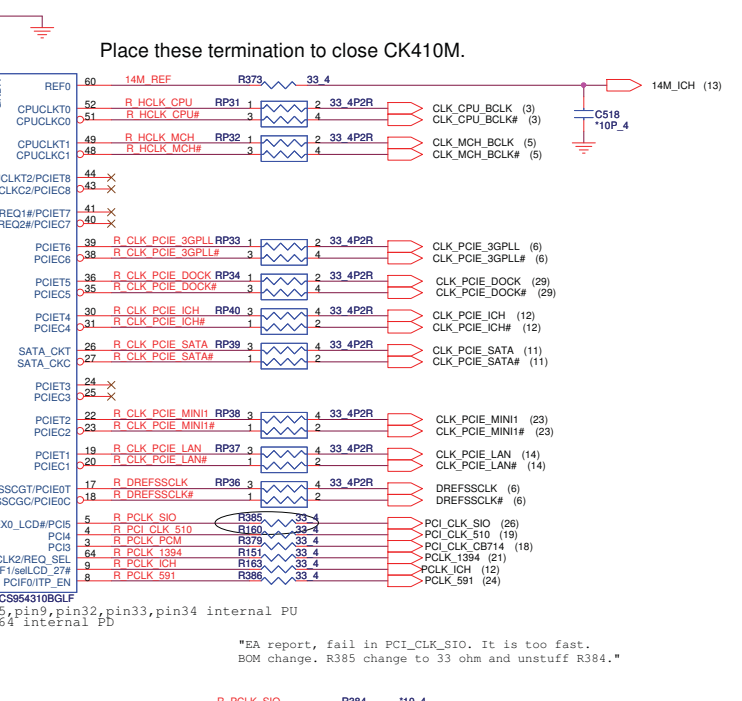
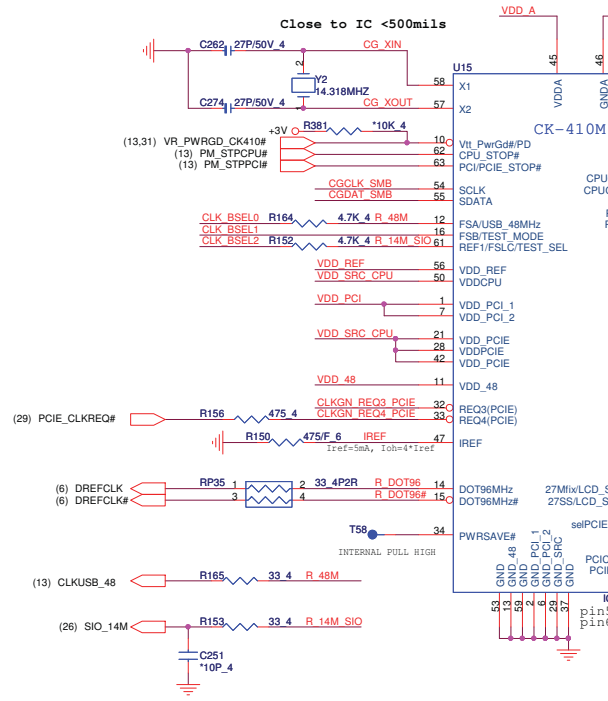
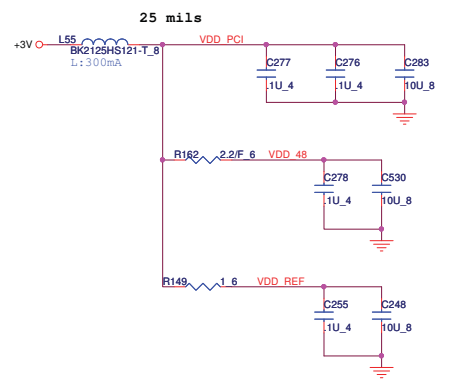
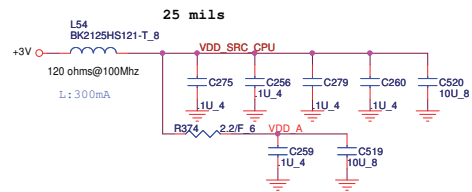
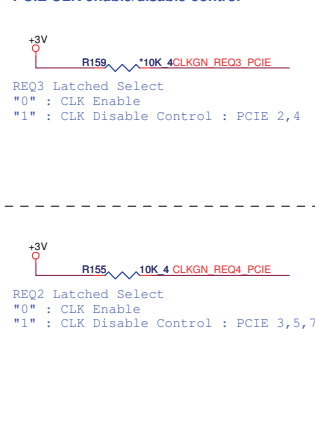


PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts	CLOCK
CB1410	AD17	REQ0# / GNT0#	INTE#	CK410/PCI3
TIAB23	AD25	REQ2# / GNT2#	INTF#	CK410/PCI8
MRS10	AD18	REQ1# / GNT1#	INTG#	CK410/PCI4

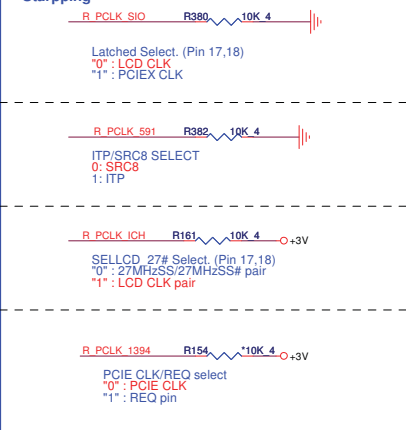




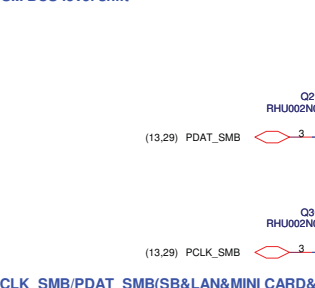
PCIE CLK enable/disable control



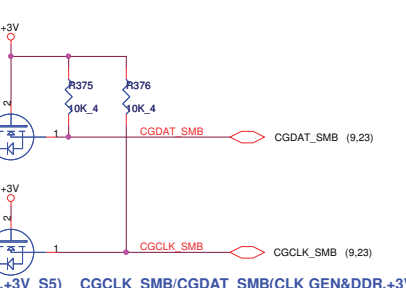
Starpping



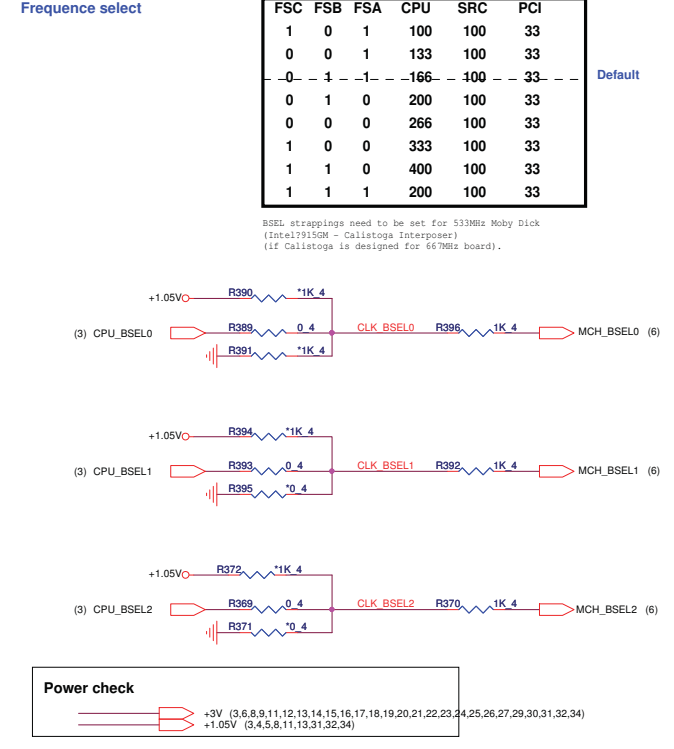
SM BUS level shift



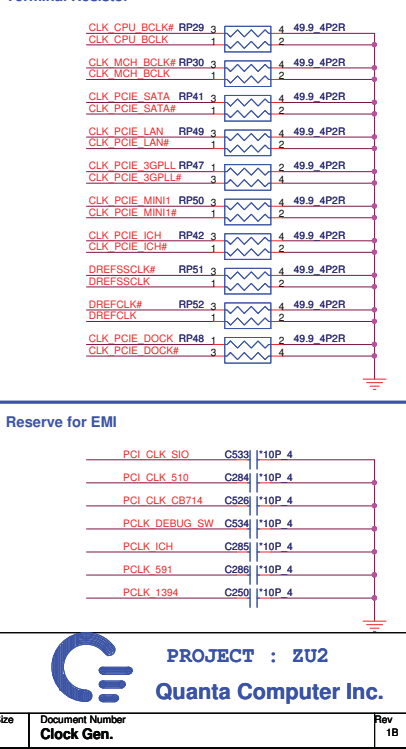
Frequency select

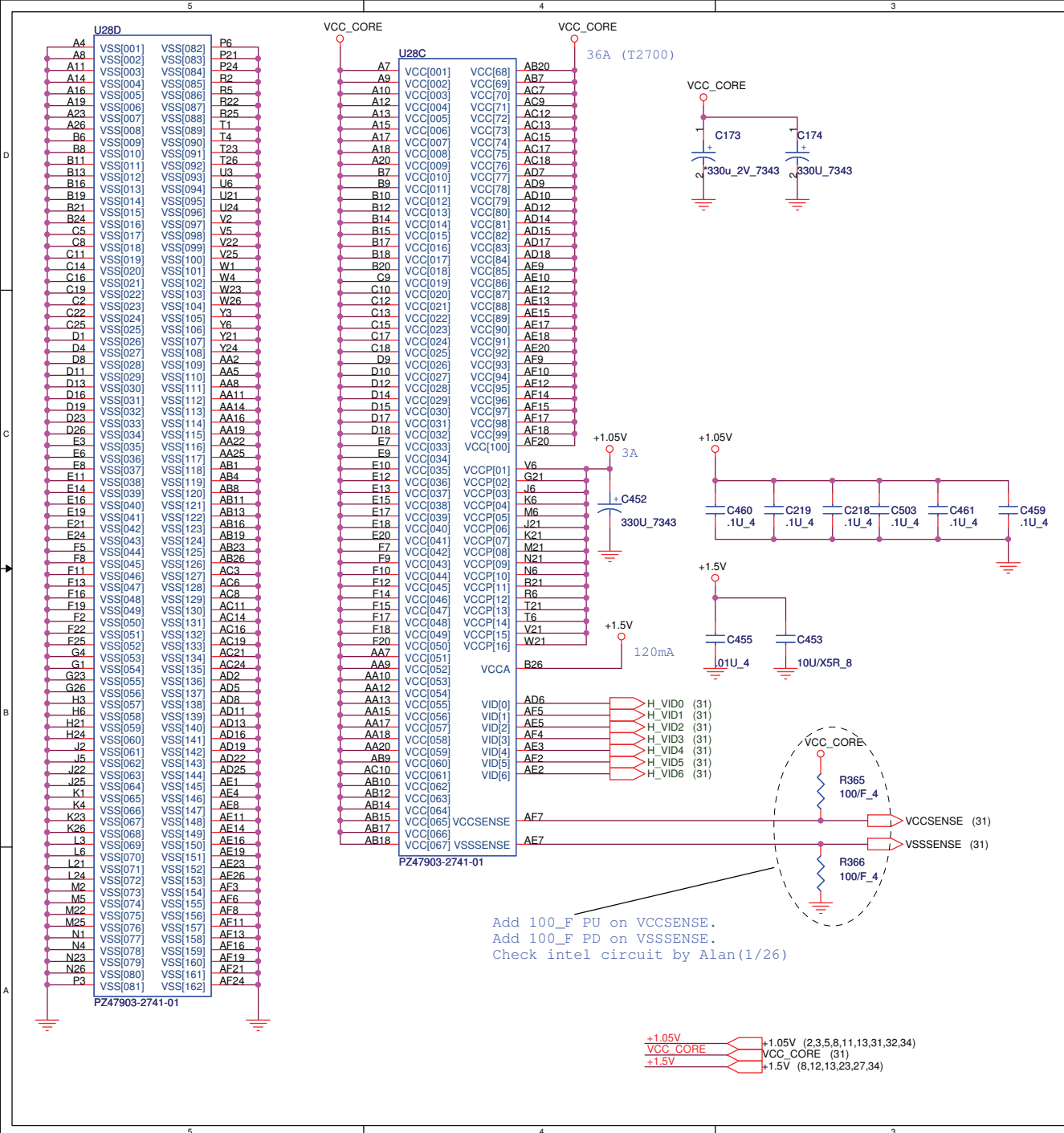


Terminal Resistor

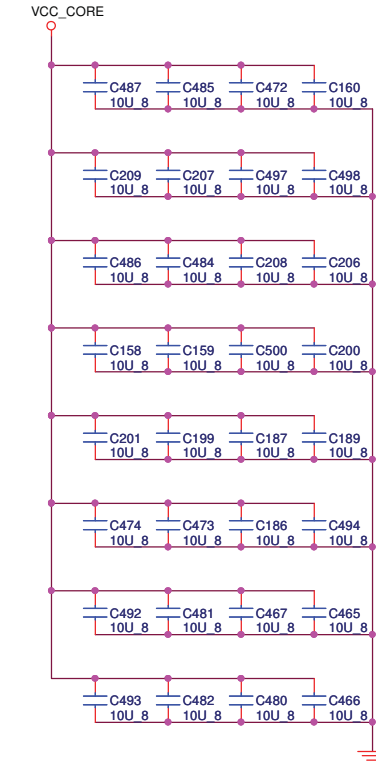
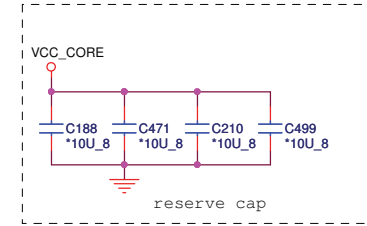


Reserve for EMI



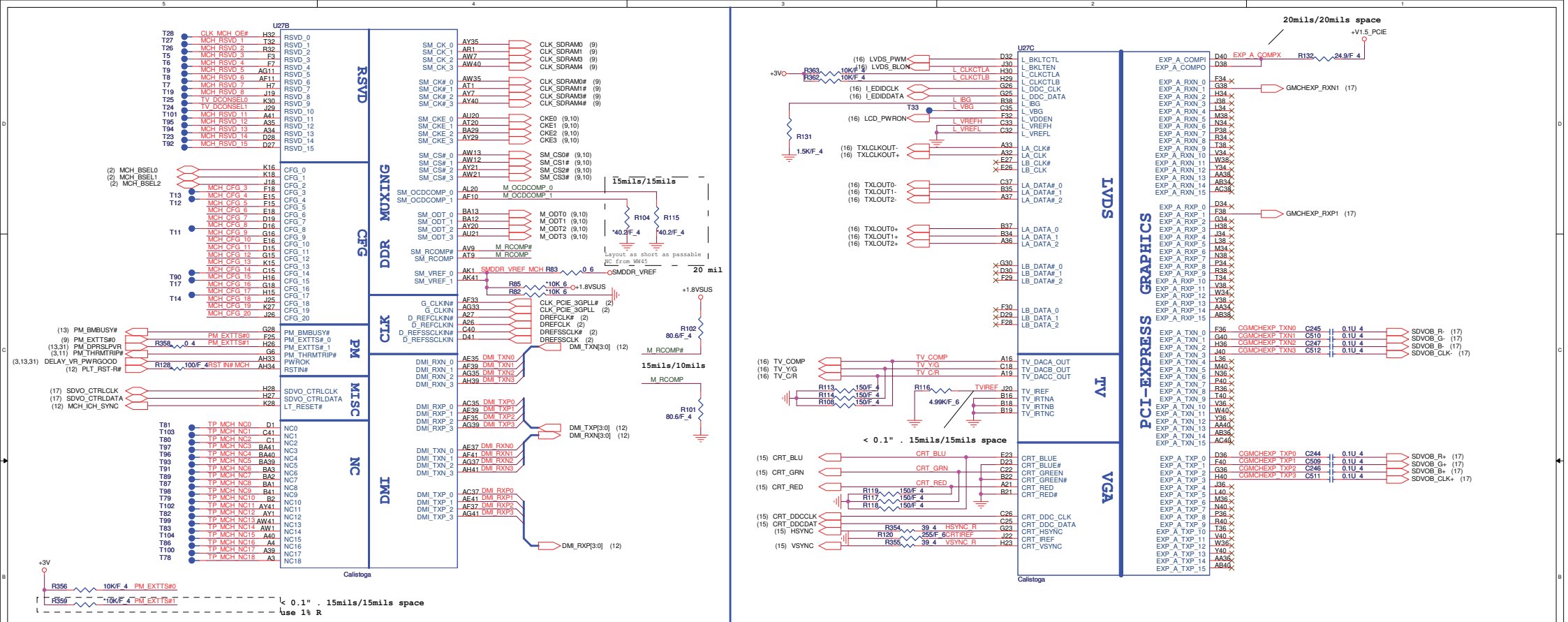


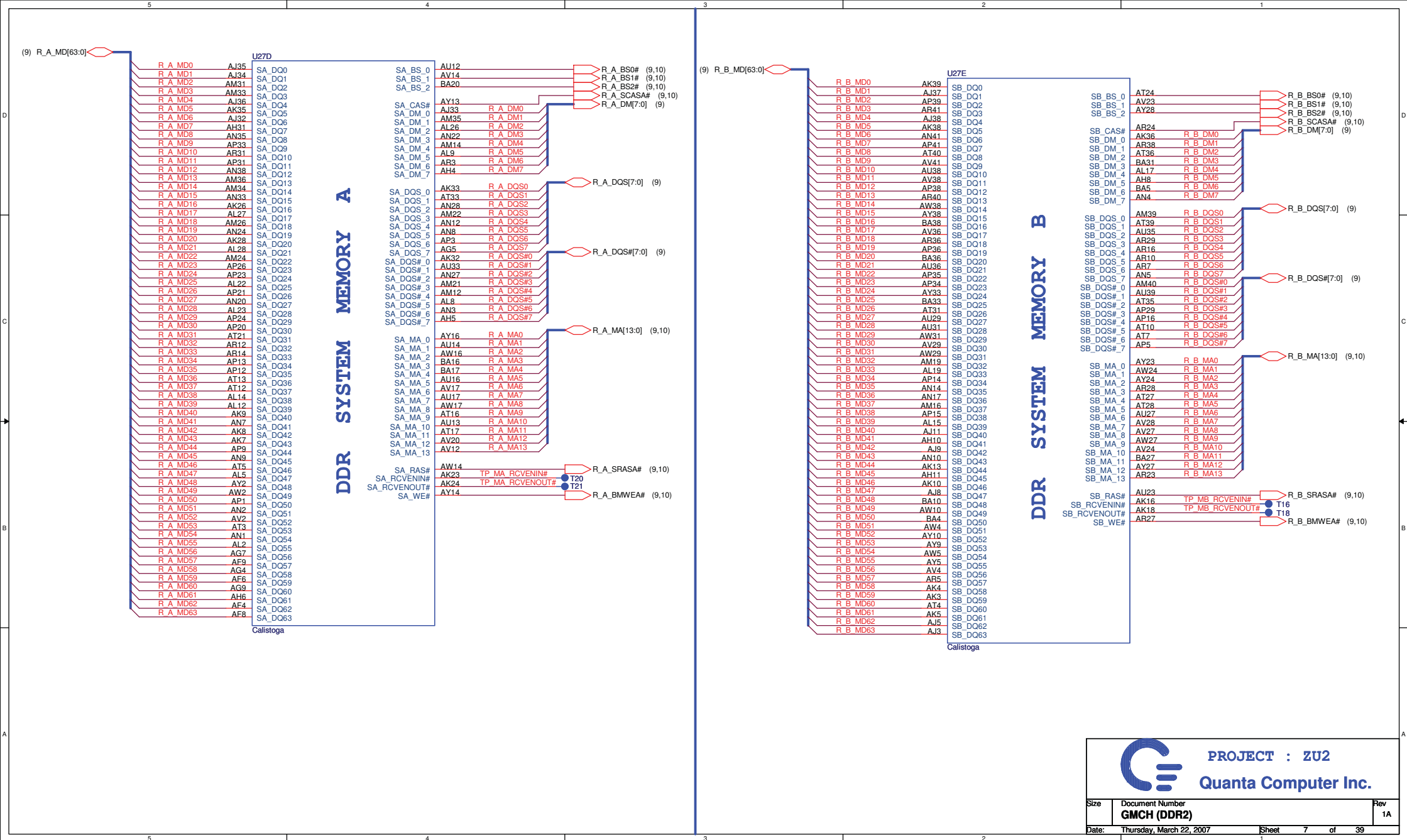
VCC_CORE CAP.



PROJECT : ZU2
Quanta Computer Inc.

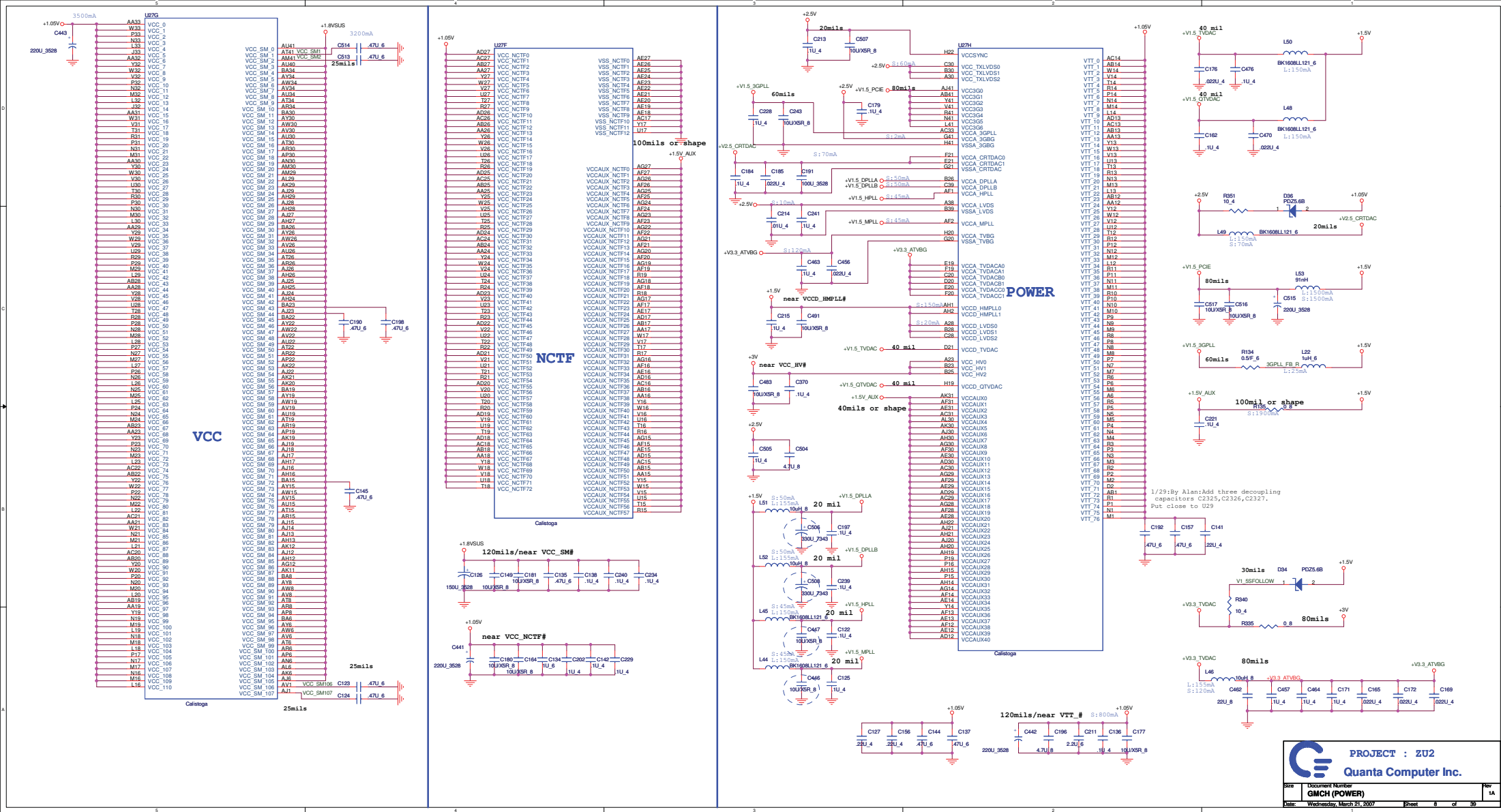
Size	Document Number	Rev
	Yonah/Merom CPU(POWER/GND)-2	1A
Date:	Thursday, March 22, 2007	Sheet 4 of 39

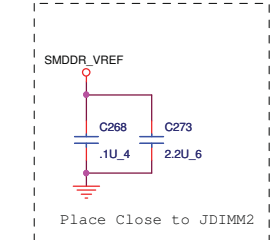
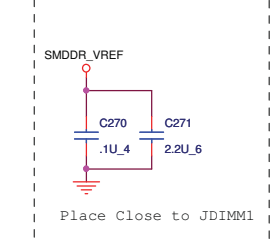
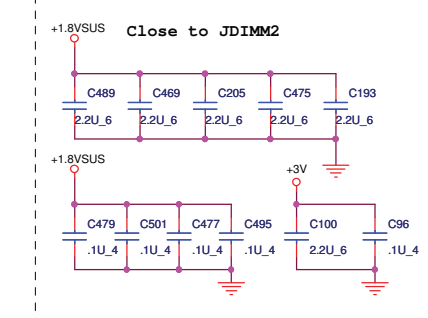
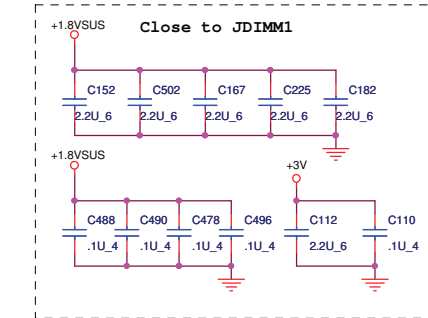
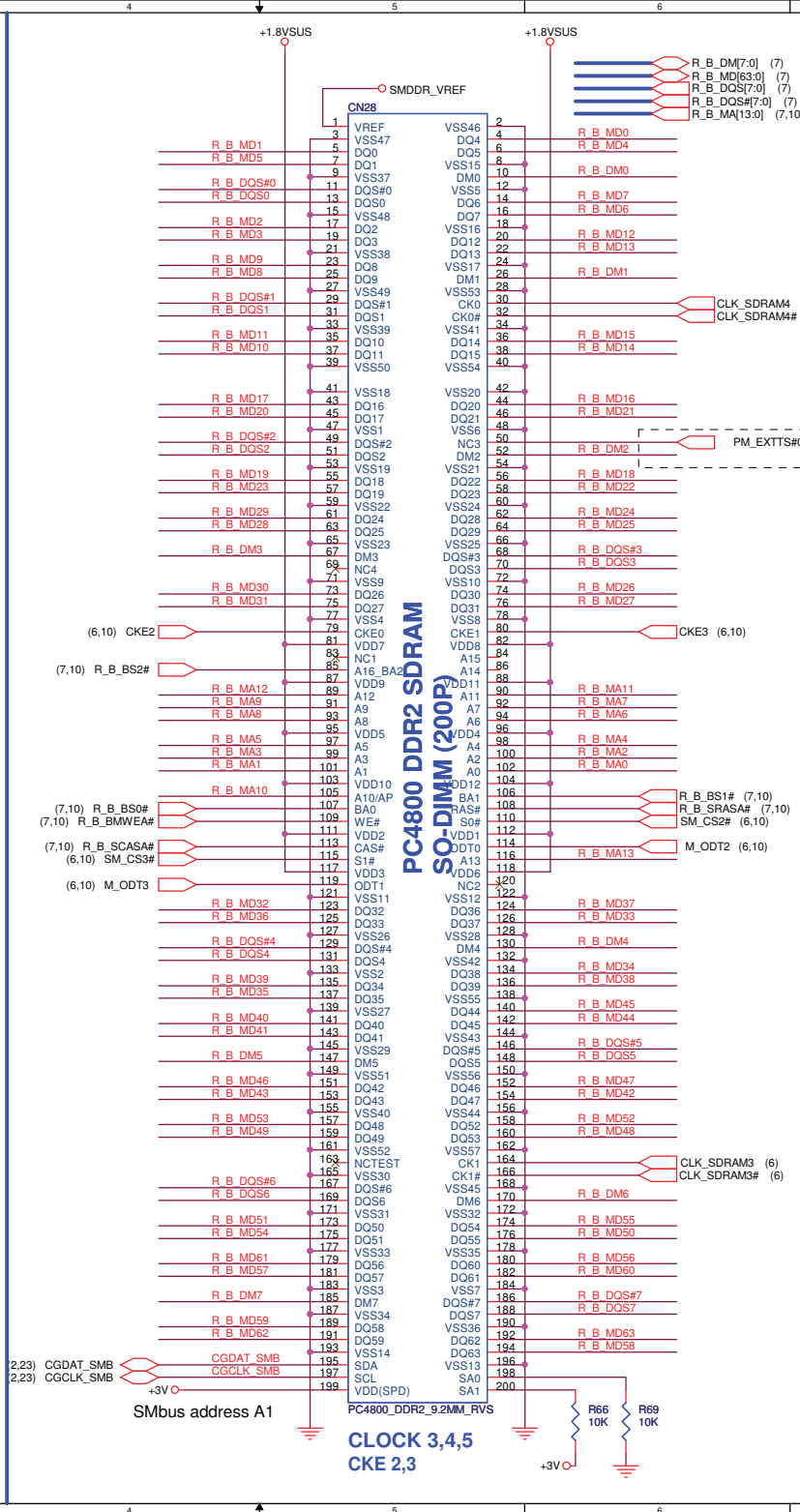
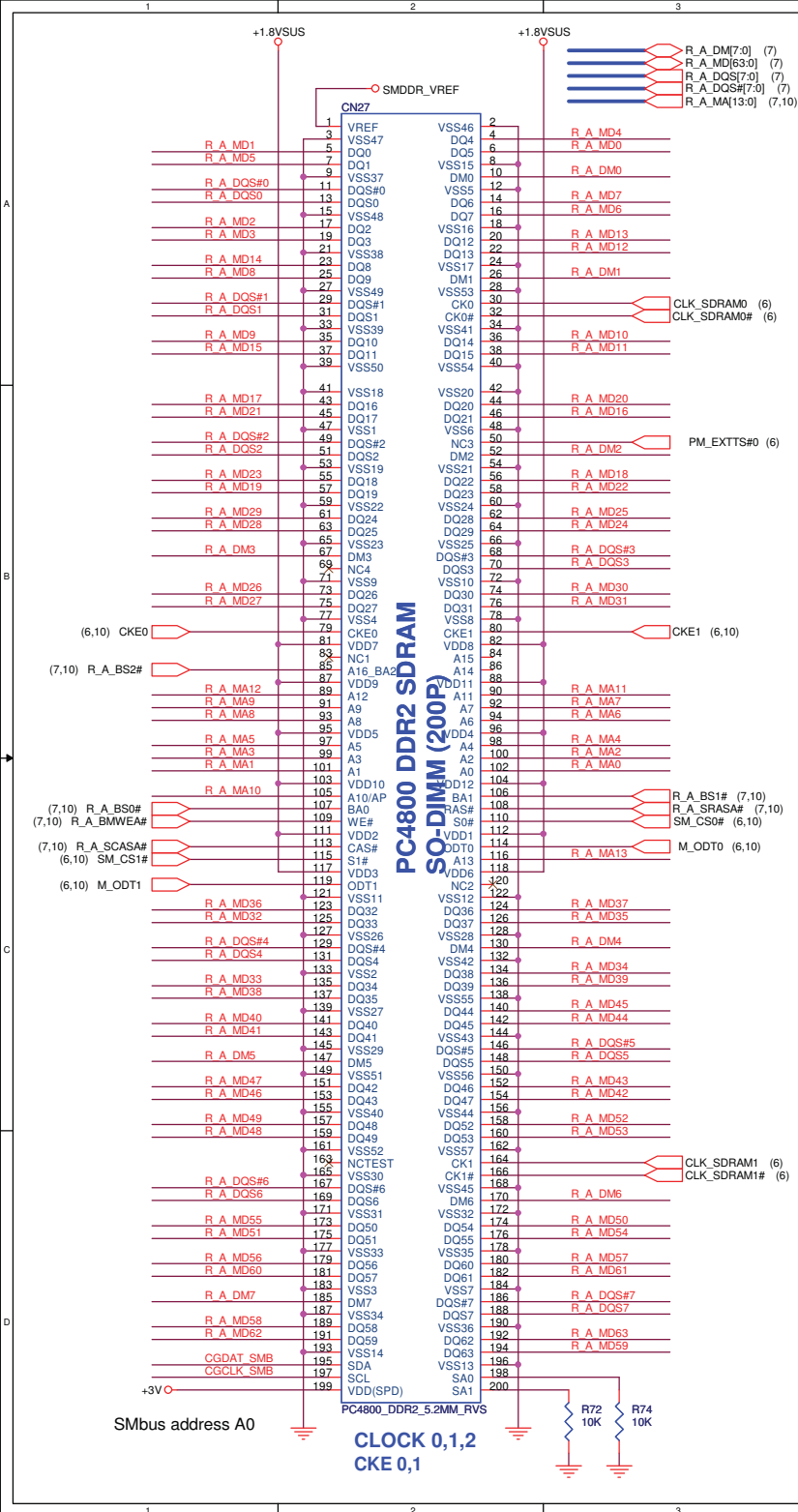




PROJECT : ZU2
Quanta Computer Inc.

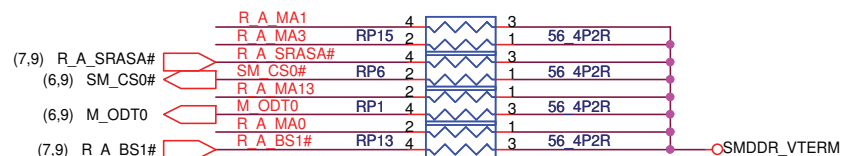
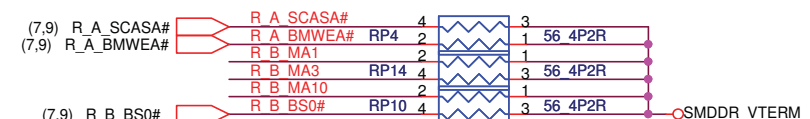
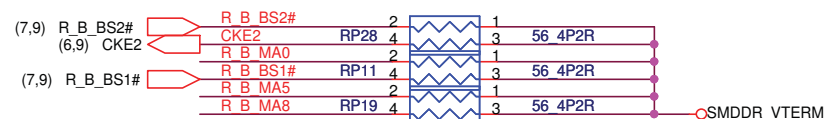
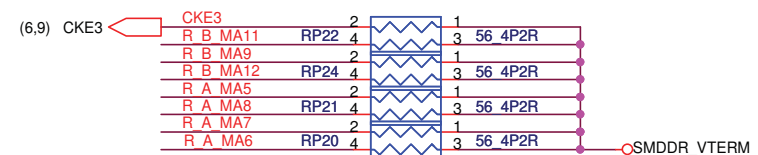
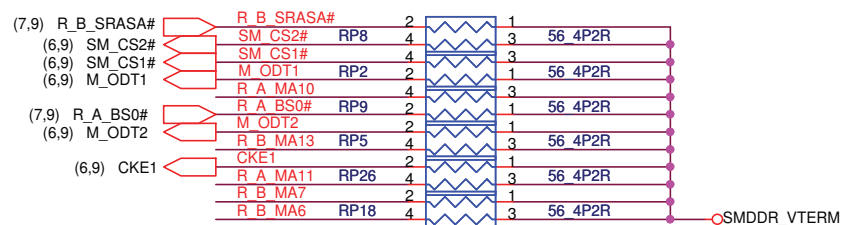
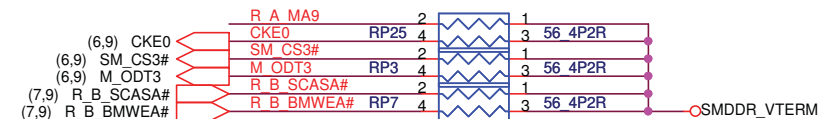
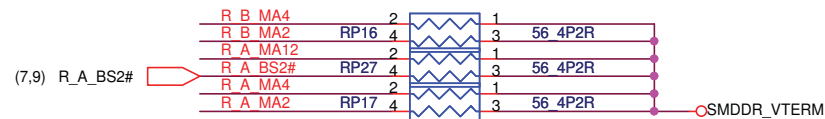
Size	Document Number GMCH (DDR2)	Rev 1A
Date:	Thursdav, March 22, 2007	Sheet 7 of 39





Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM

Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM

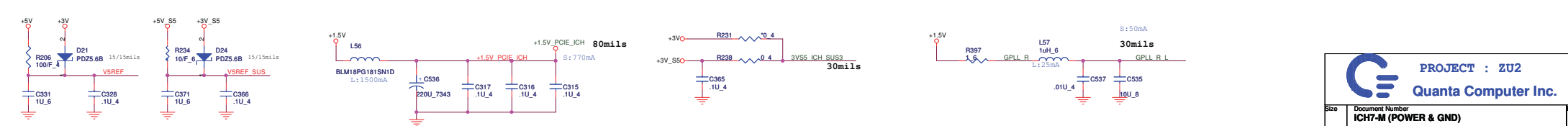
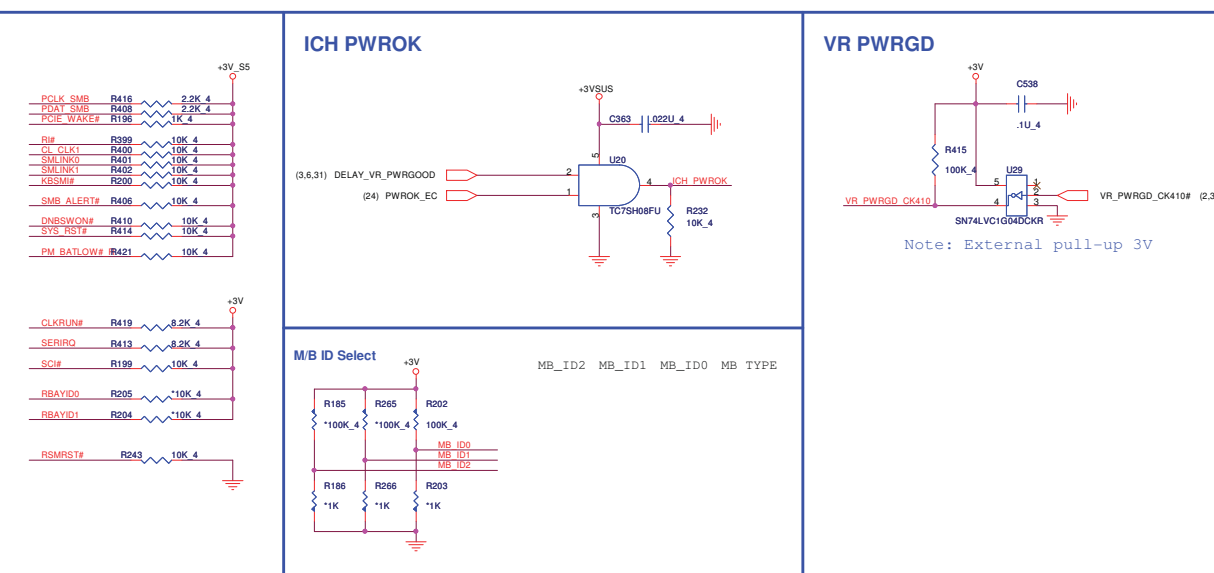
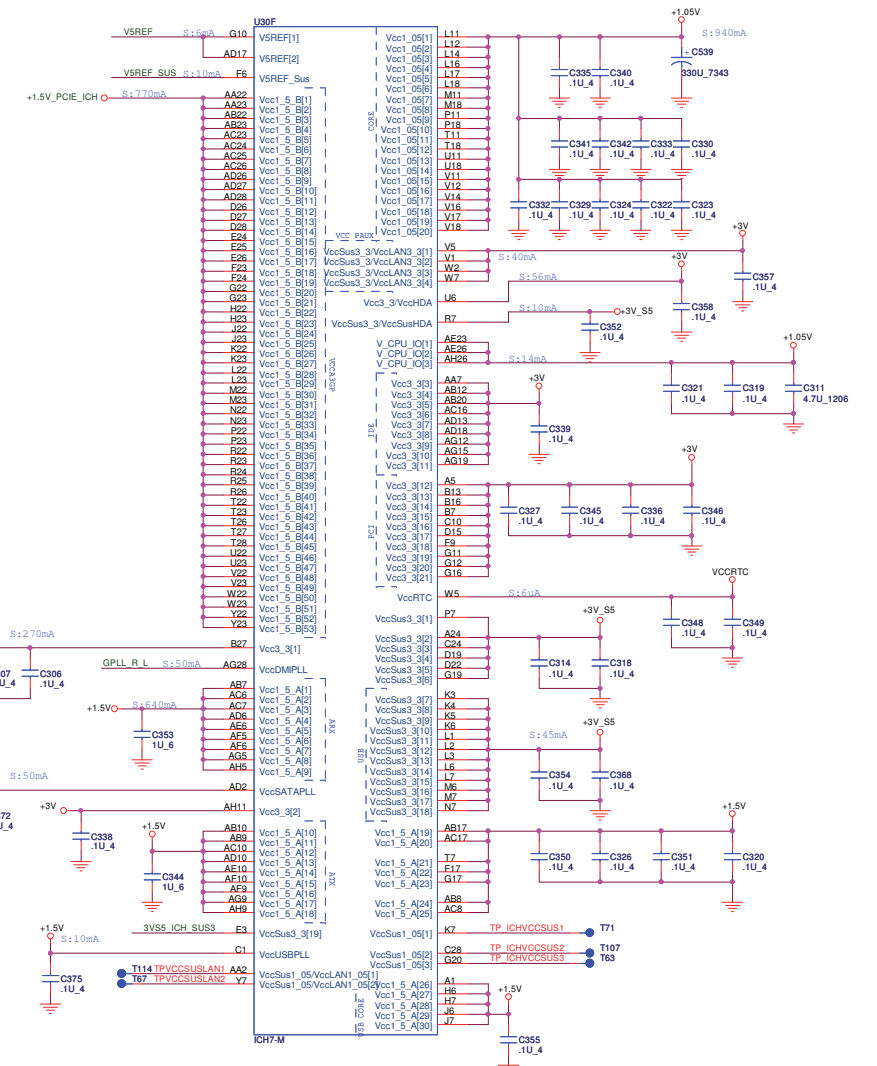


PROJECT : ZU2
Quanta Computer Inc.

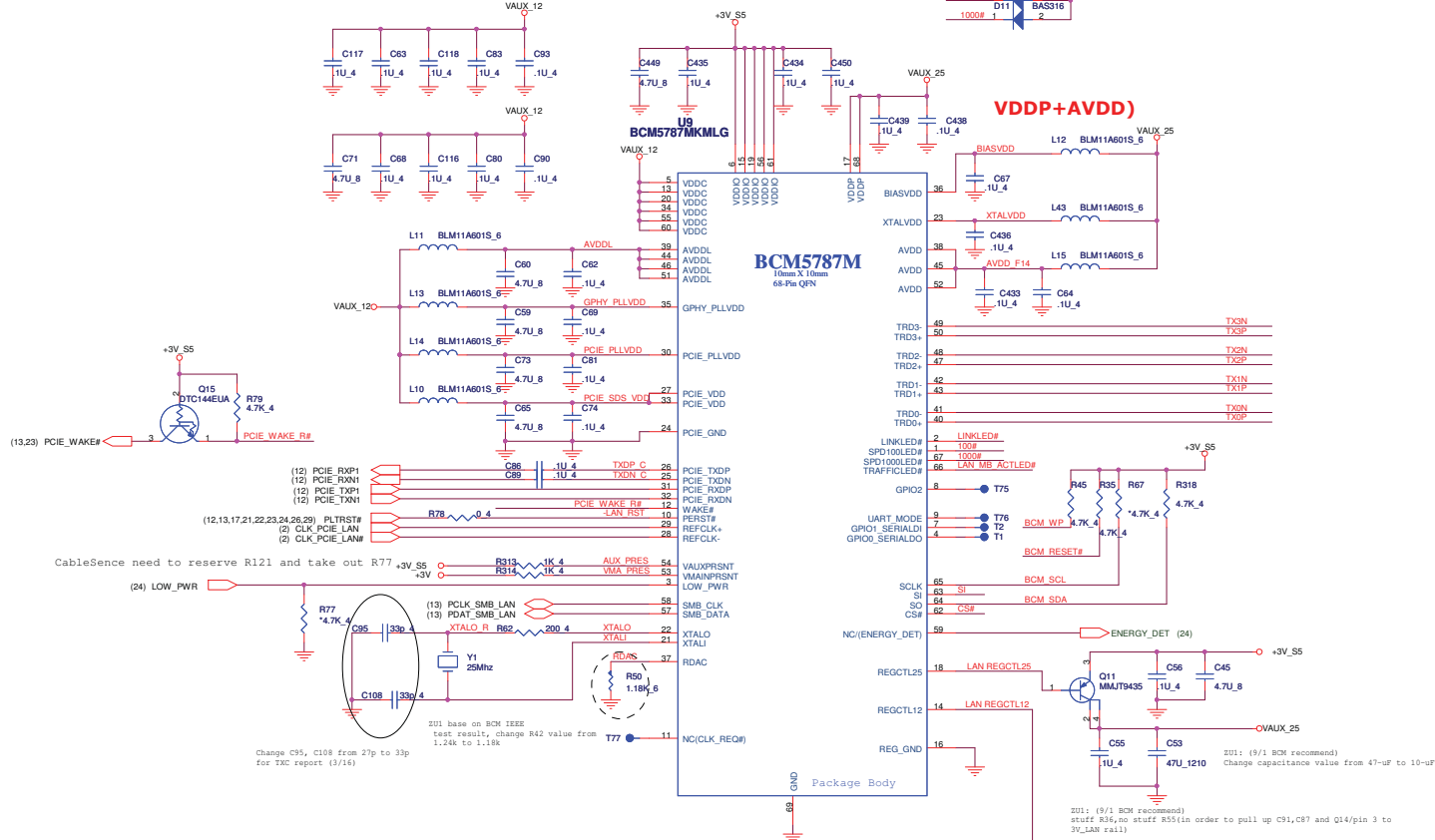
Size	Document Number DDR2 RES. ARRAY
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Rev	
1A	

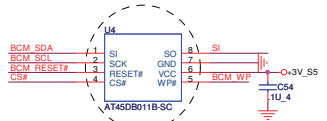
Date:	Friday, March 23, 2007	Sheet	10	of	39
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Giga LAN BCM5787M

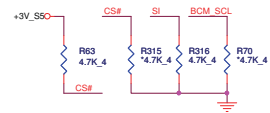


Flash ROM/EEPROM

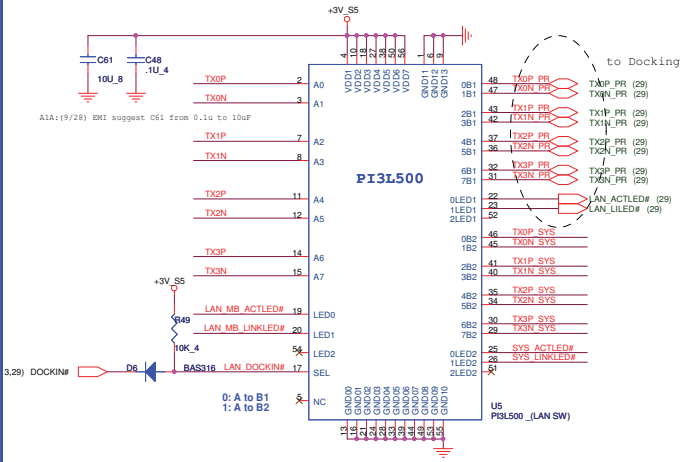


EEPROM Strapping

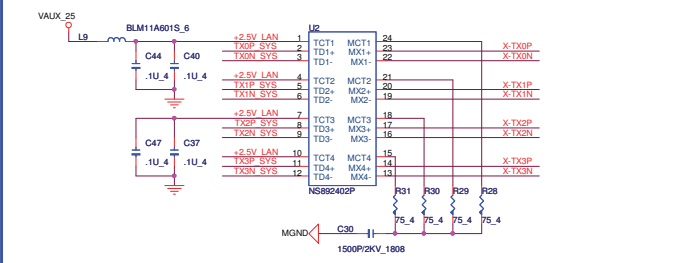
	SO	SI	CS#	SCLK
24c64	1	1	0	1
AT45DB011B	1	0	1	1



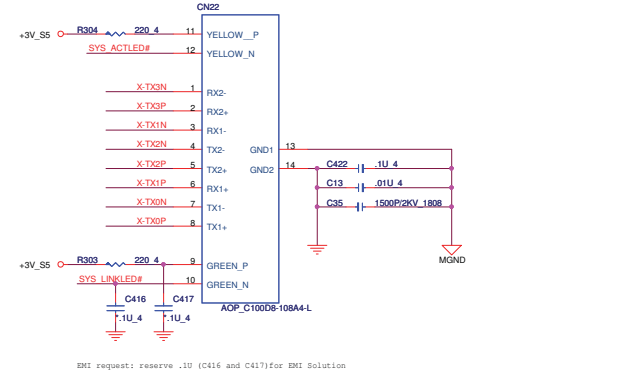
LAN Switch



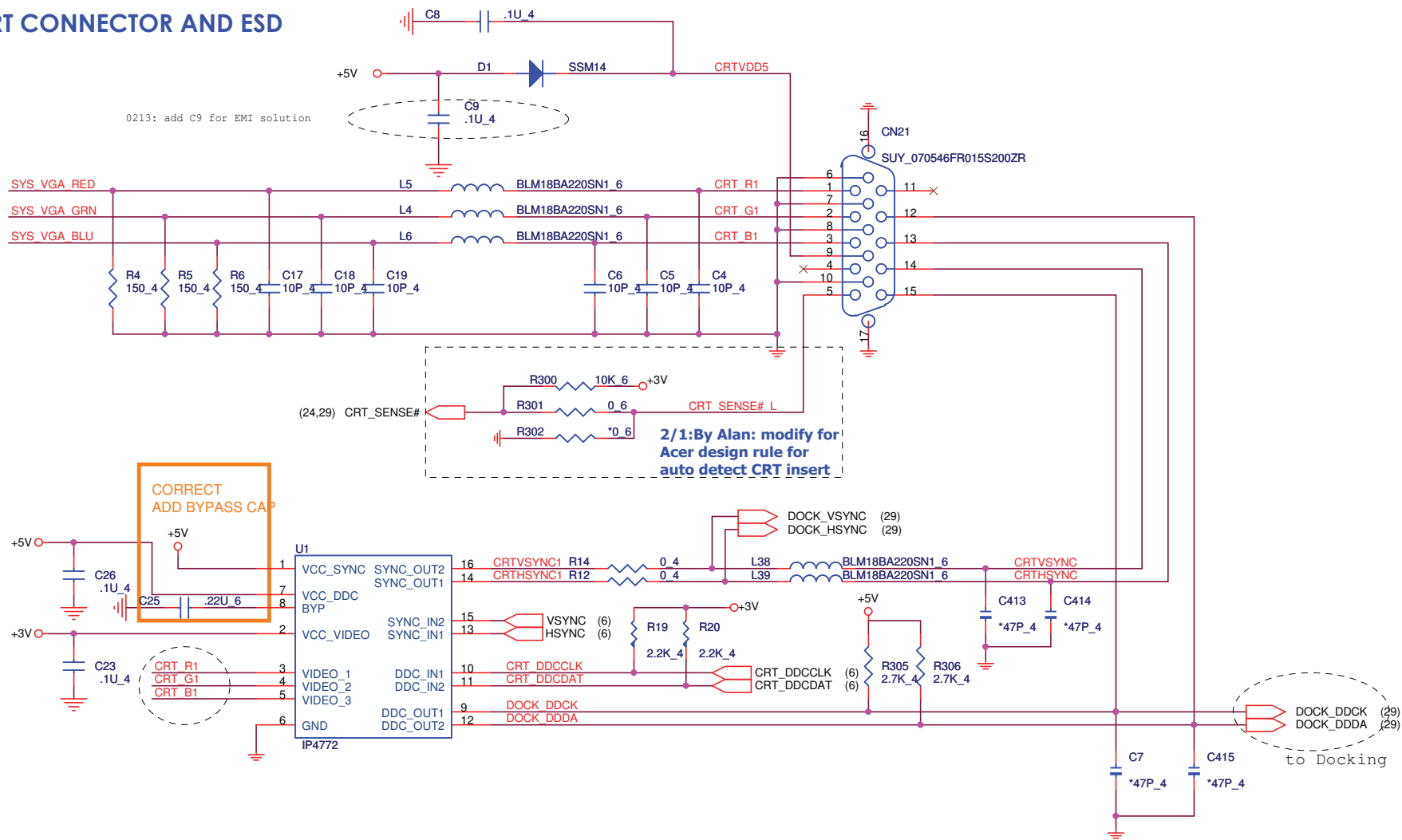
LAN Transformer



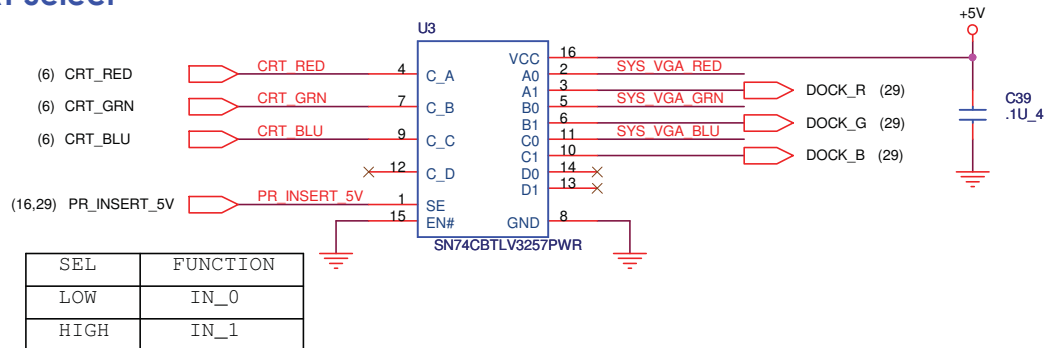
RJ45



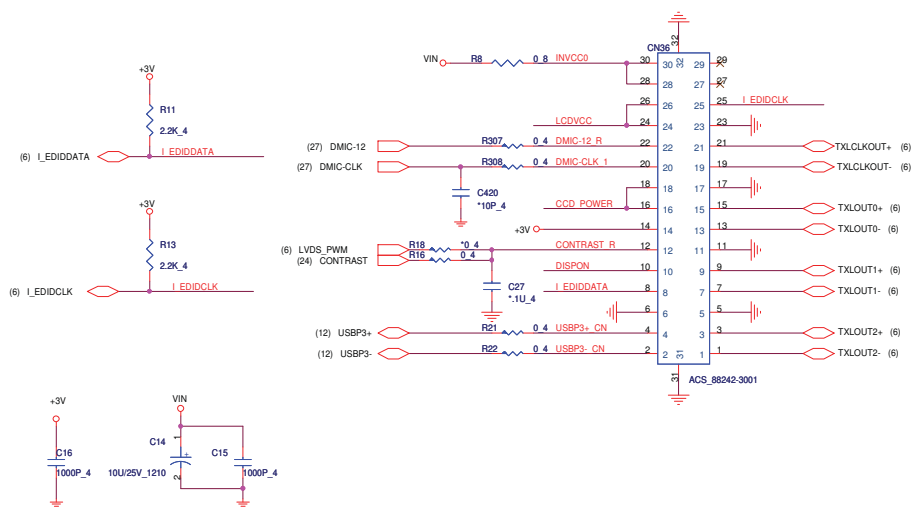
CRT CONNECTOR AND ESD



CRT Select



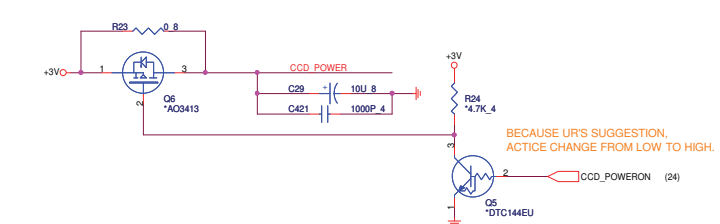
LVDS



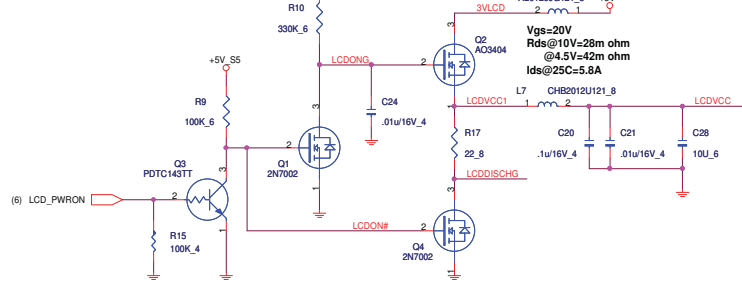
ZU1:(12/28) EMI request: reserve L-C footprint for debug use (R52,C650)

(3/19)Change MB LCD connector pin define(CN3) and LCD cable pin define to cover production line issue
(Inverter short with signal to burn system)->ZRI issue
(1)pin 27,29->NC
(2)pin 8->INT_LVDS_EDIDDATA

CAMERA MODULE

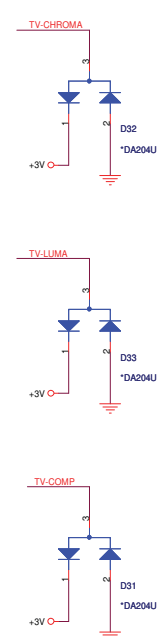
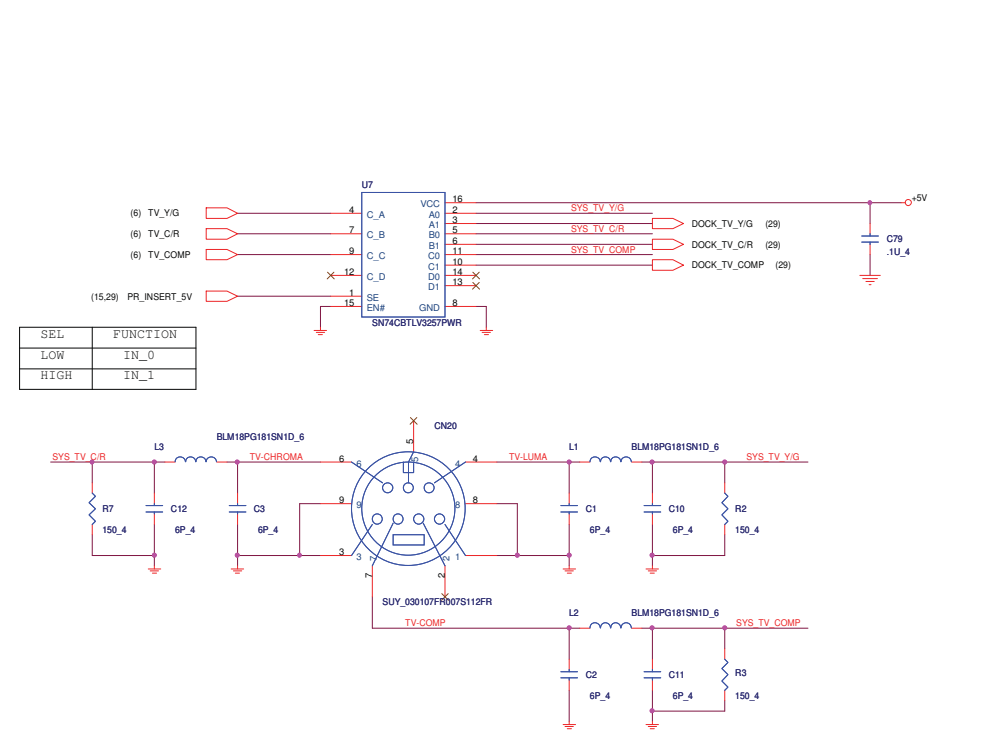


LCD Power

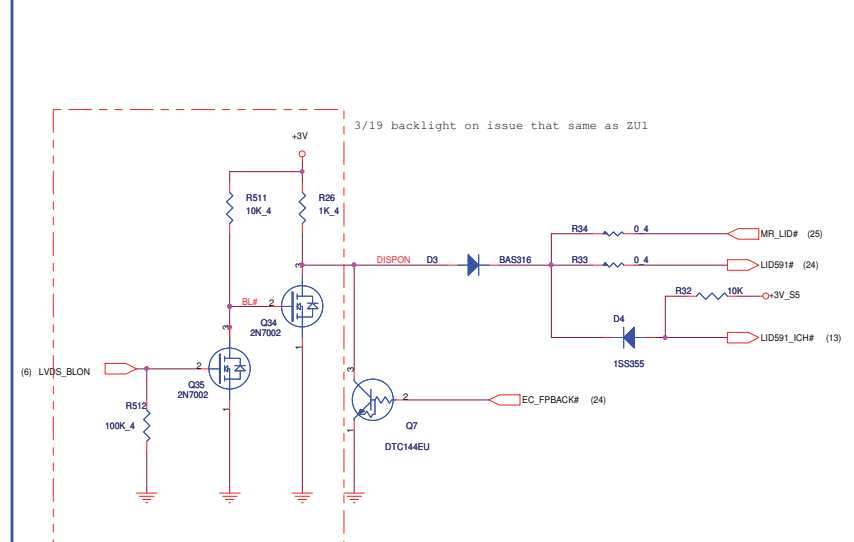


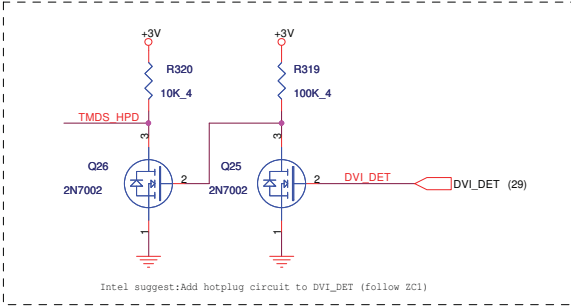
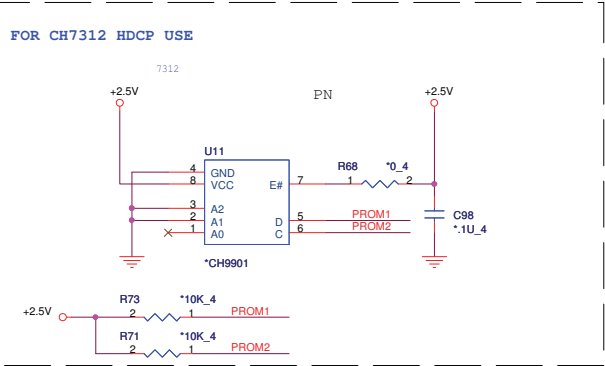
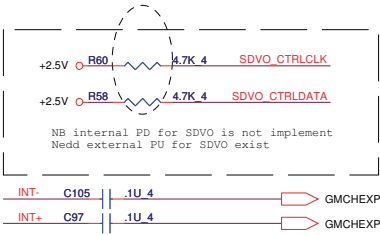
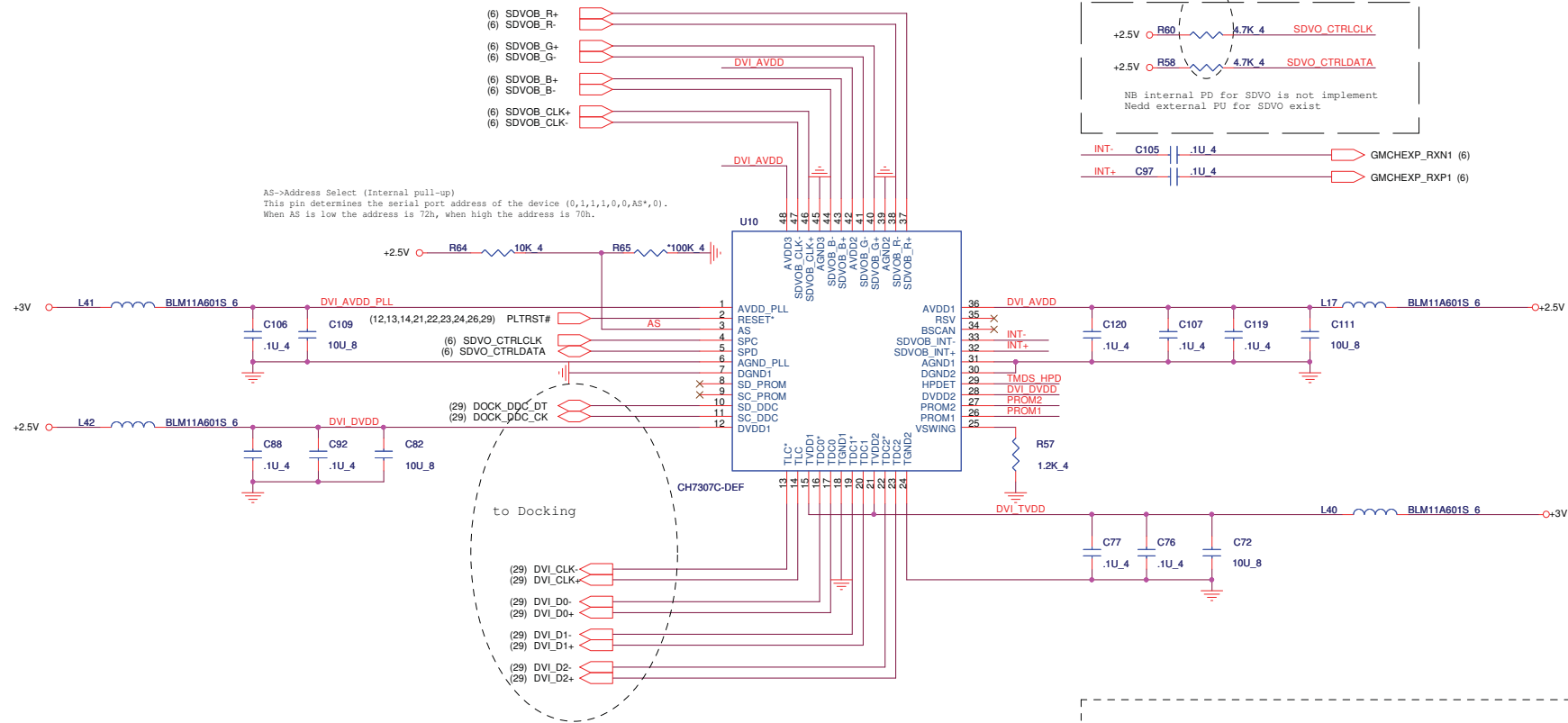
1/29:By Alan: AAT4280 fail on power ON rising time and falling time. EA.Additionally, some LCD panels will have garbage. follow ZRI circuit.

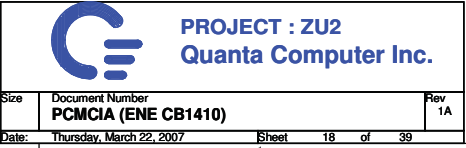
TV Out (SVHS) MiniDIN 7-pin



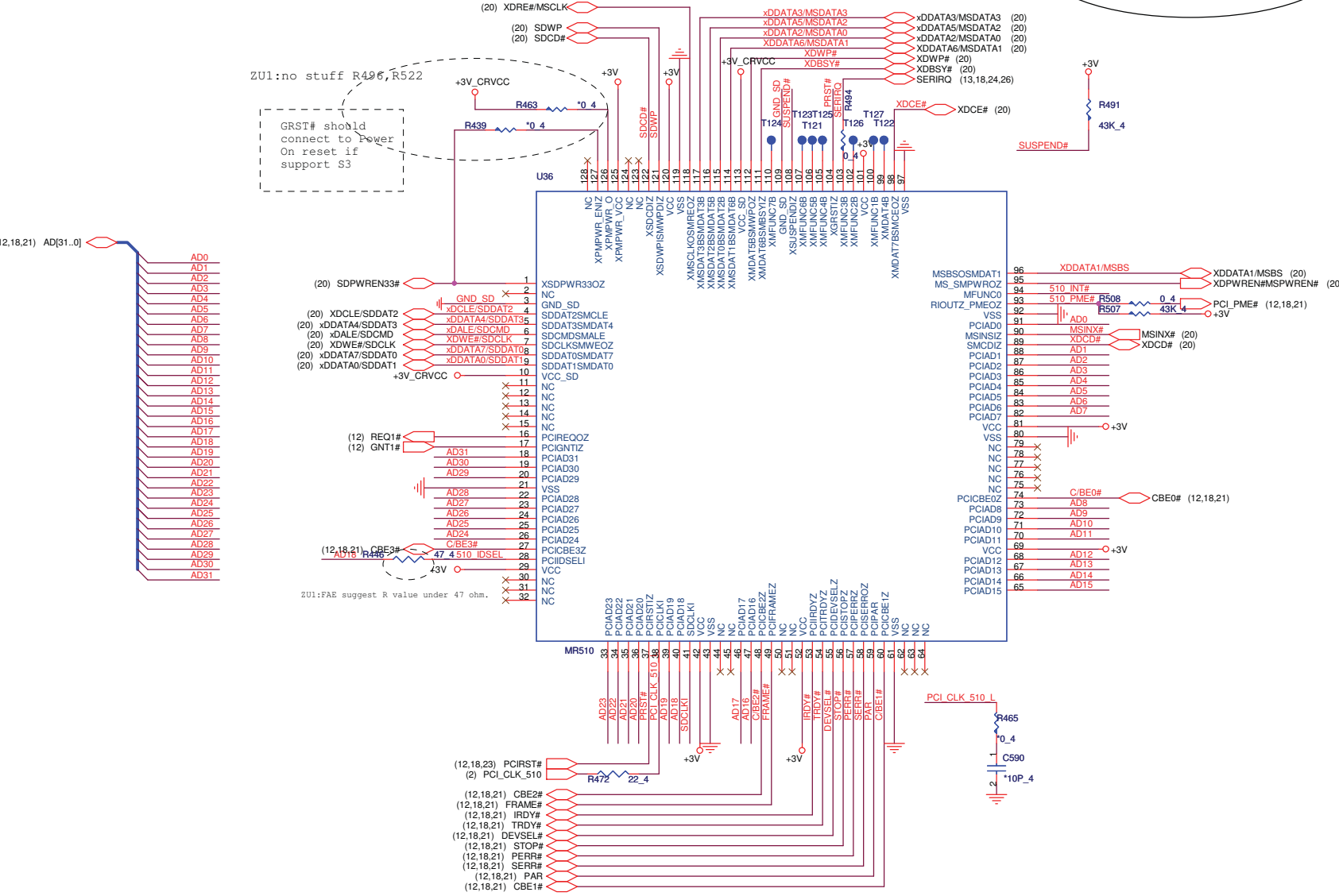
MR Sensor



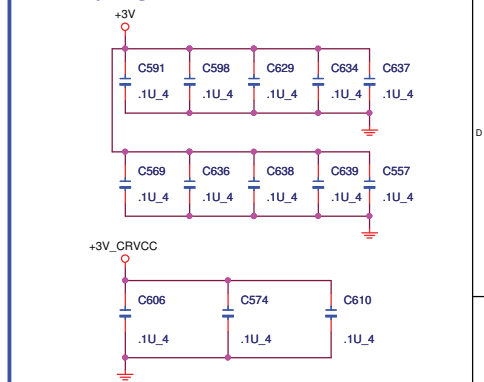




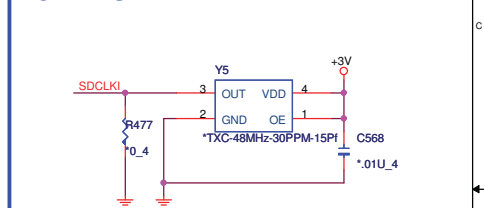
ID Select : AD18
 Interrupt Pin : INTG#
 Request Indicate : REQ1#
 Grant Indicate : GNT1#



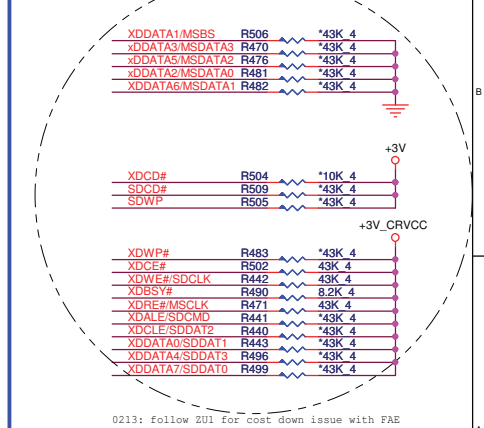
Decoupling CAP.



48MHz CLK



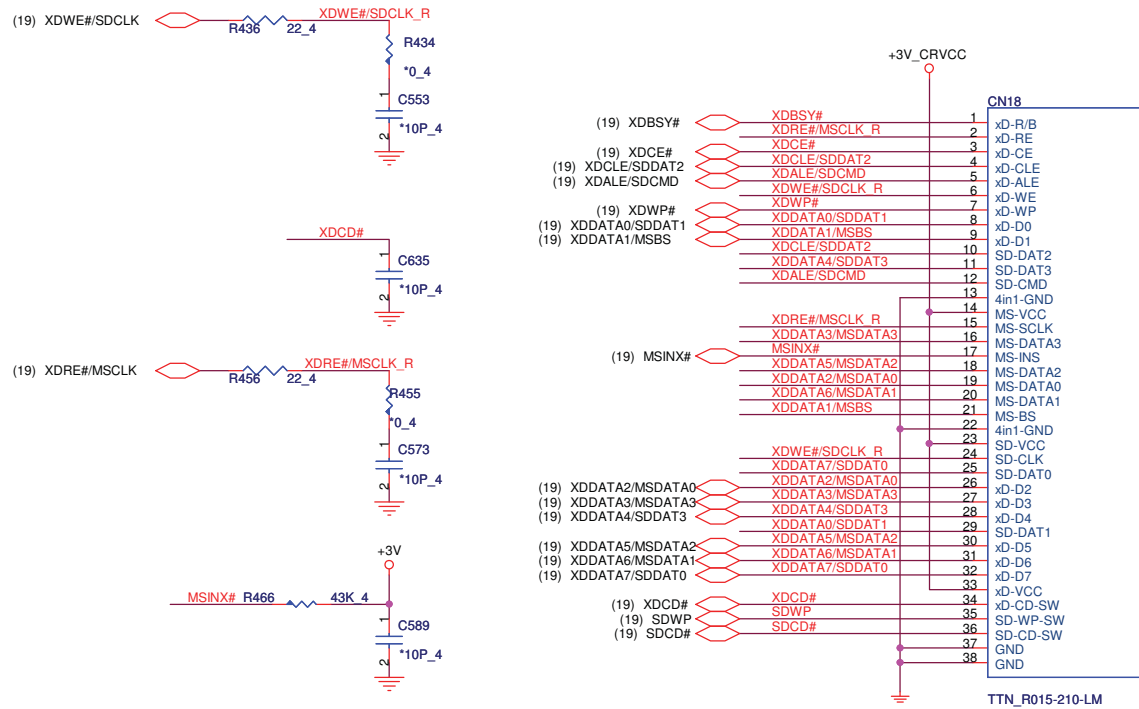
PU/PD



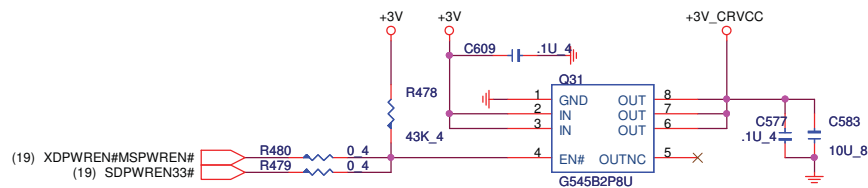
PROJECT : ZU2
Quanta Computer Inc.

Size	Document Number	Flew 18
	Card Reader (MR510)	
Date:	Wednesday, March 28, 2007	Sheet 19 of 39

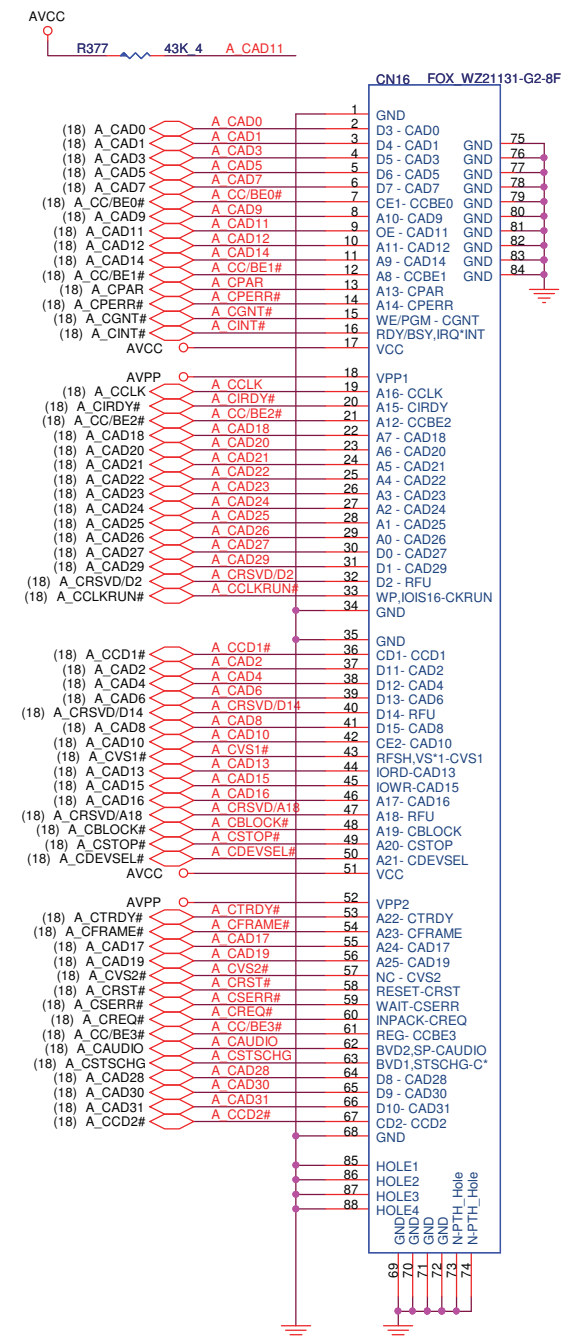
CardReader



CardReader Power switch

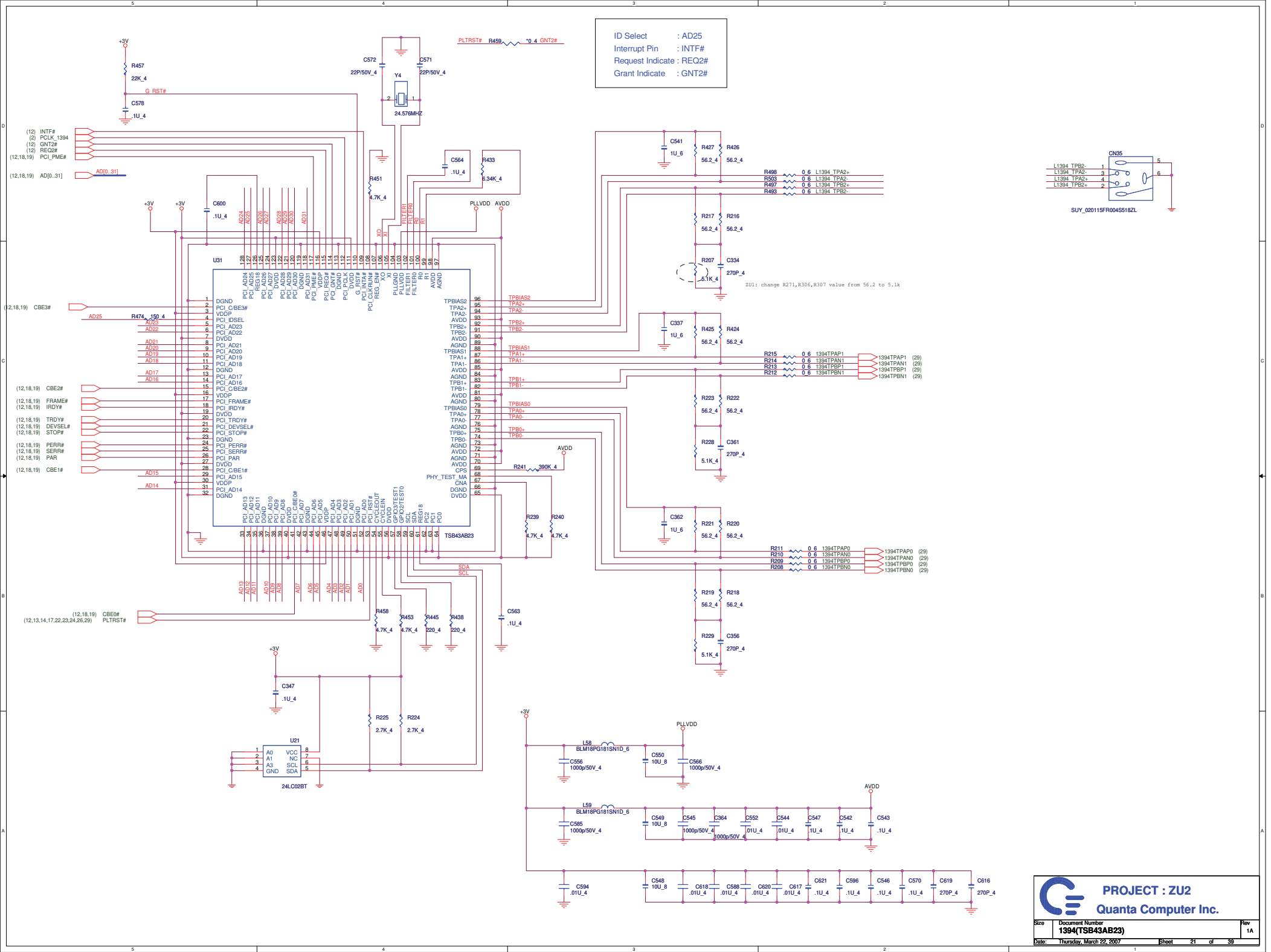


PCMCIA

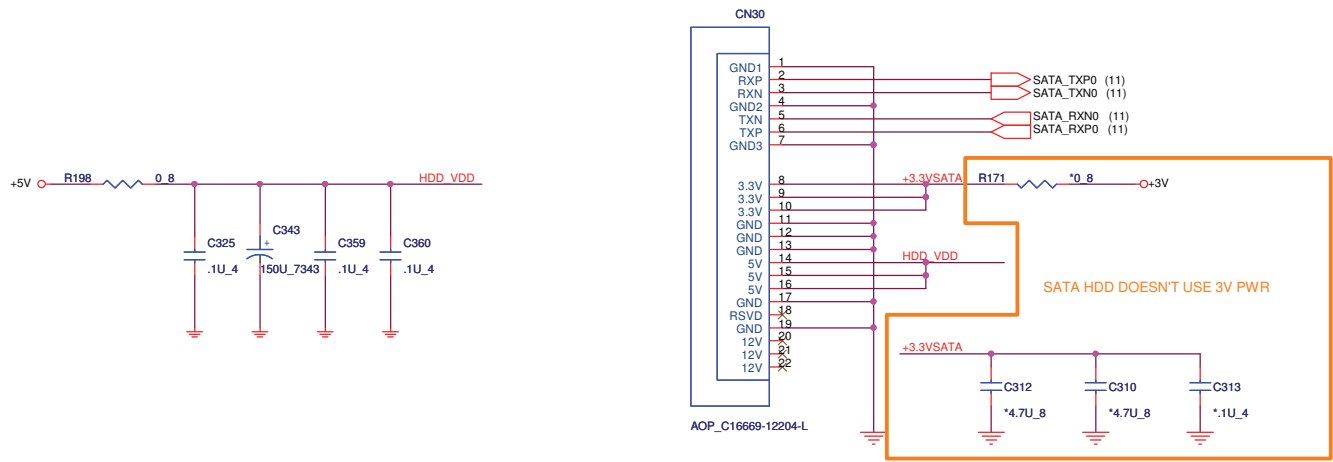


PROJECT : ZU2
Quanta Computer Inc.

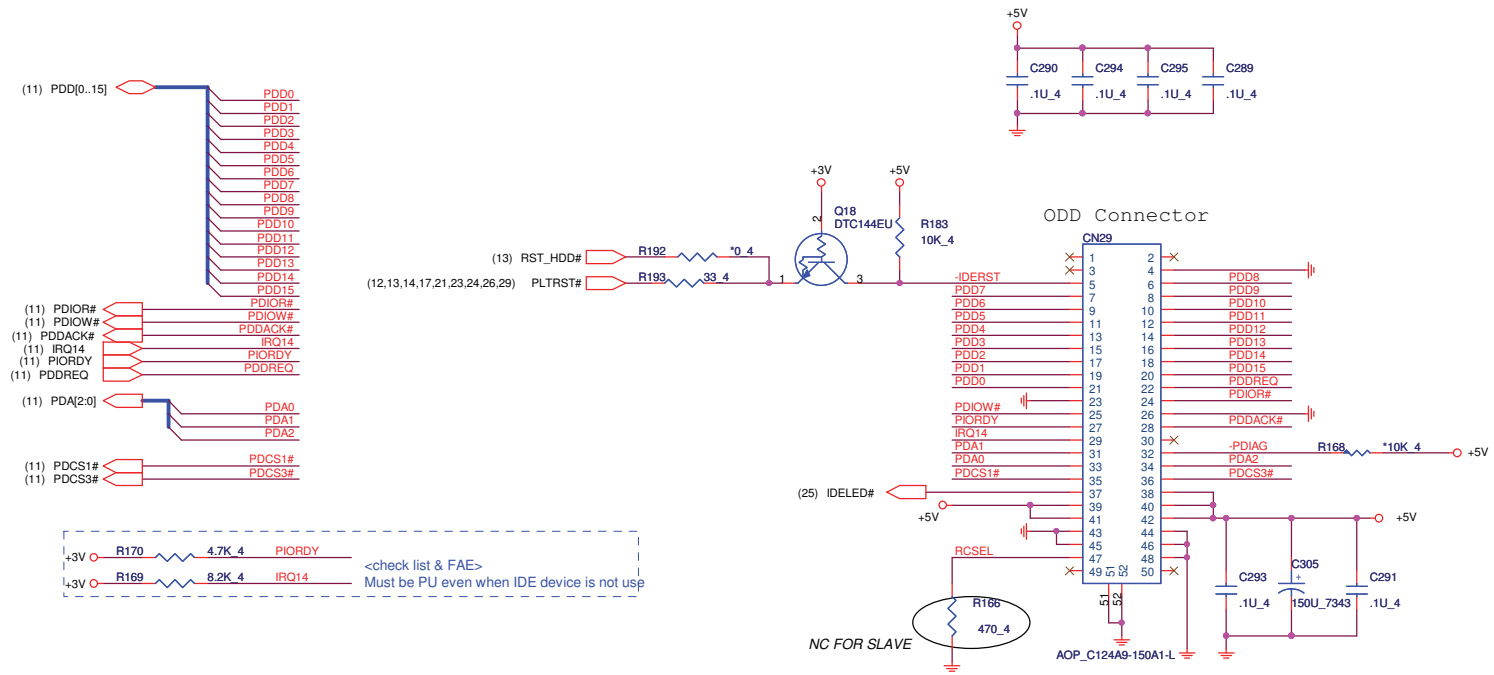
Size	Document Number CARD Reader & PCMCIA SLOT	Rev 1A
Date:	Thursday, March 22, 2007	Sheet 20 of 39



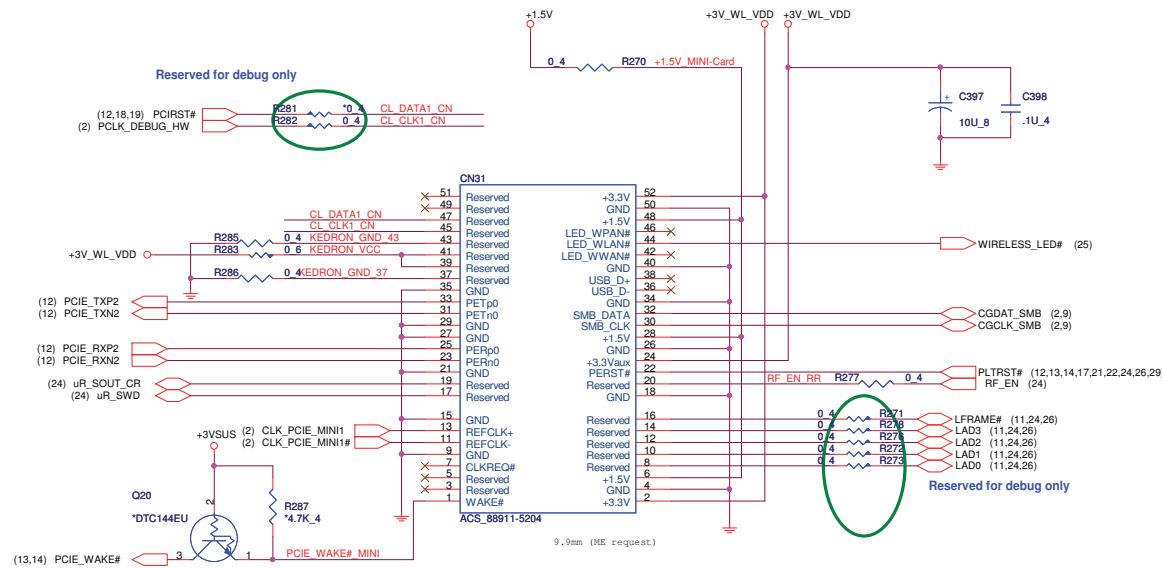
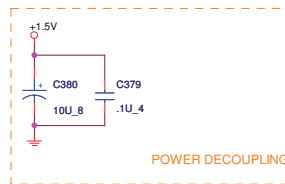
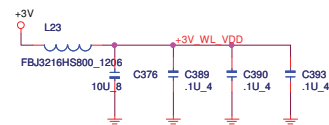
SATA HDD



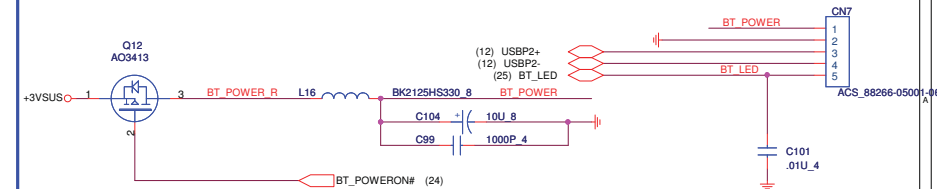
PATA ODD



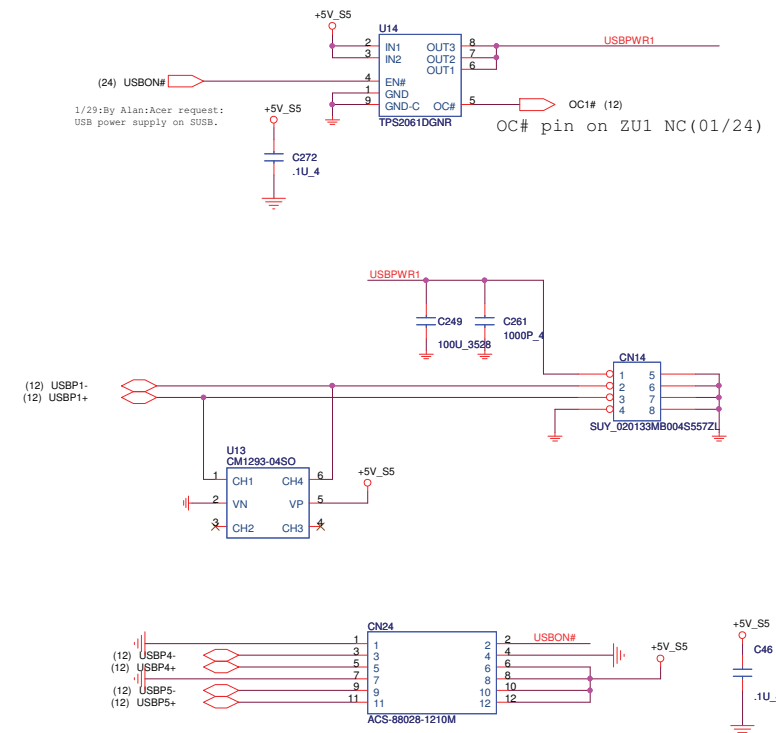
MINI-Card



BLUETOOTH MODULE CONNECTOR



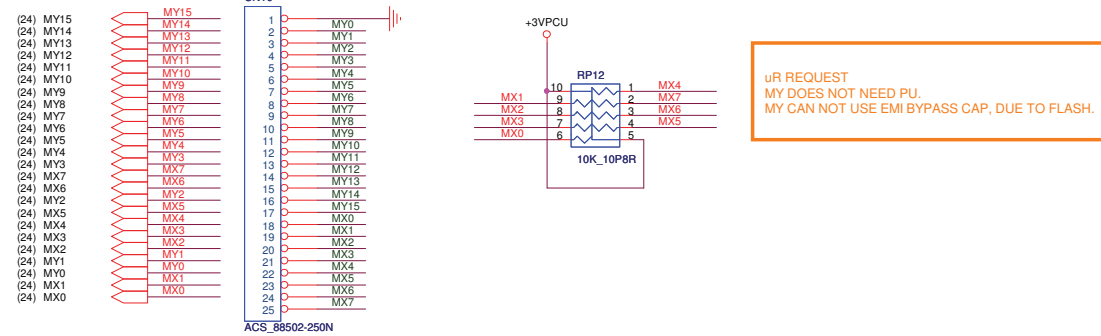
System USB



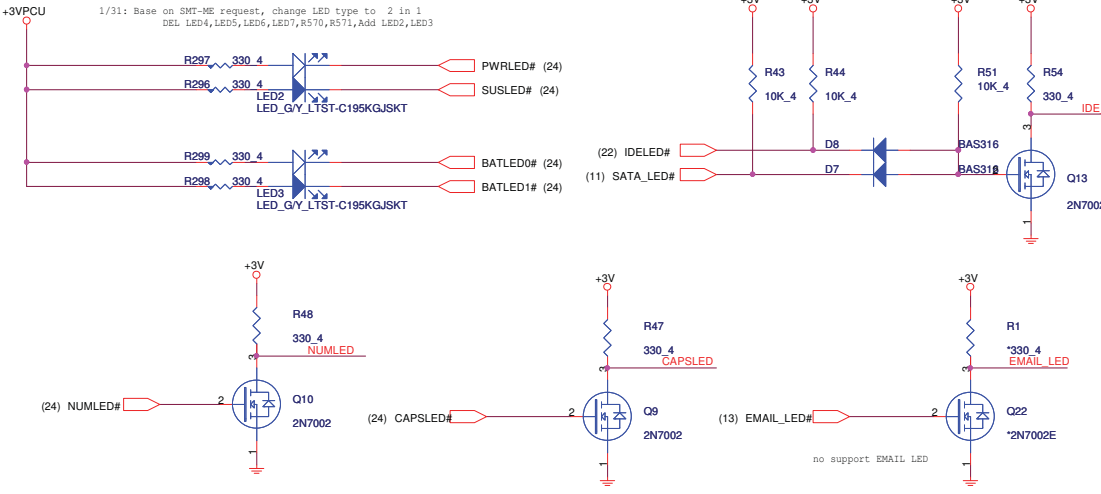
PROJECT : ZU2
Quanta Computer Inc.

Size	Document Number Mini card/USB/Bluetooth	Rev 1B
Date:	Wednesday, March 28, 2007	Sheet 23 of 39

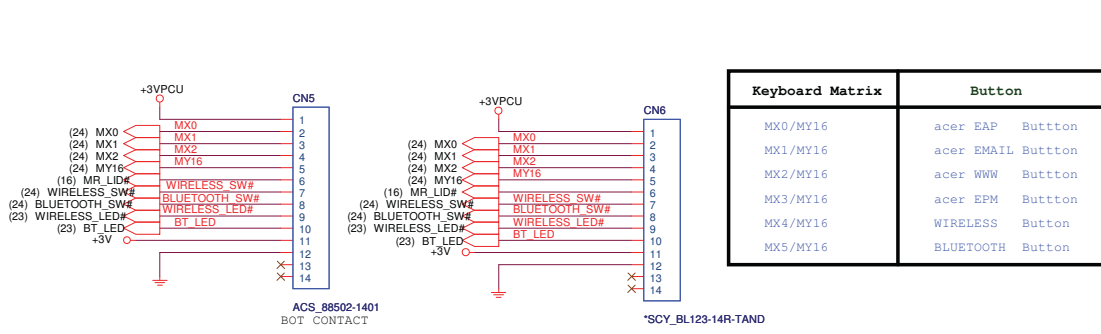
INT K/B



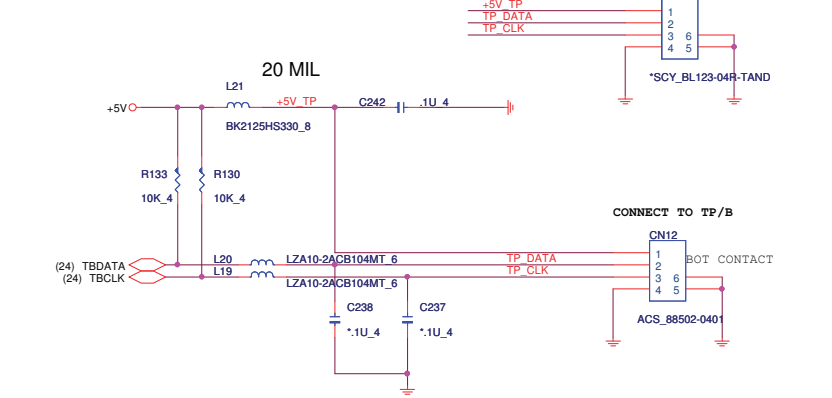
LED



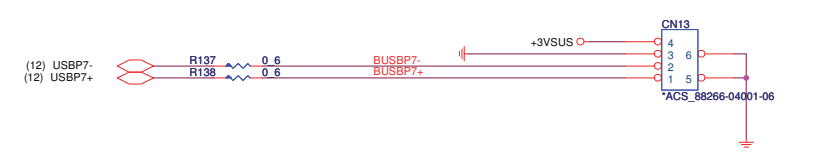
Function Board



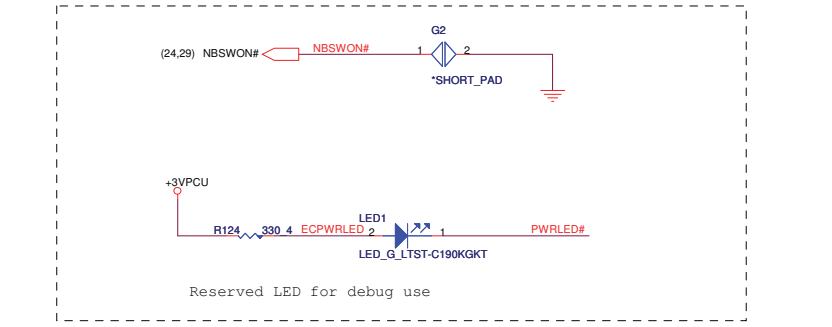
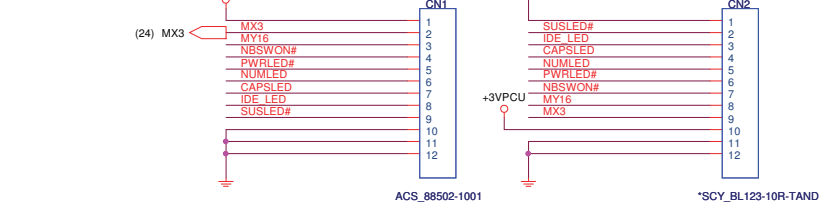
TOUCH PAD

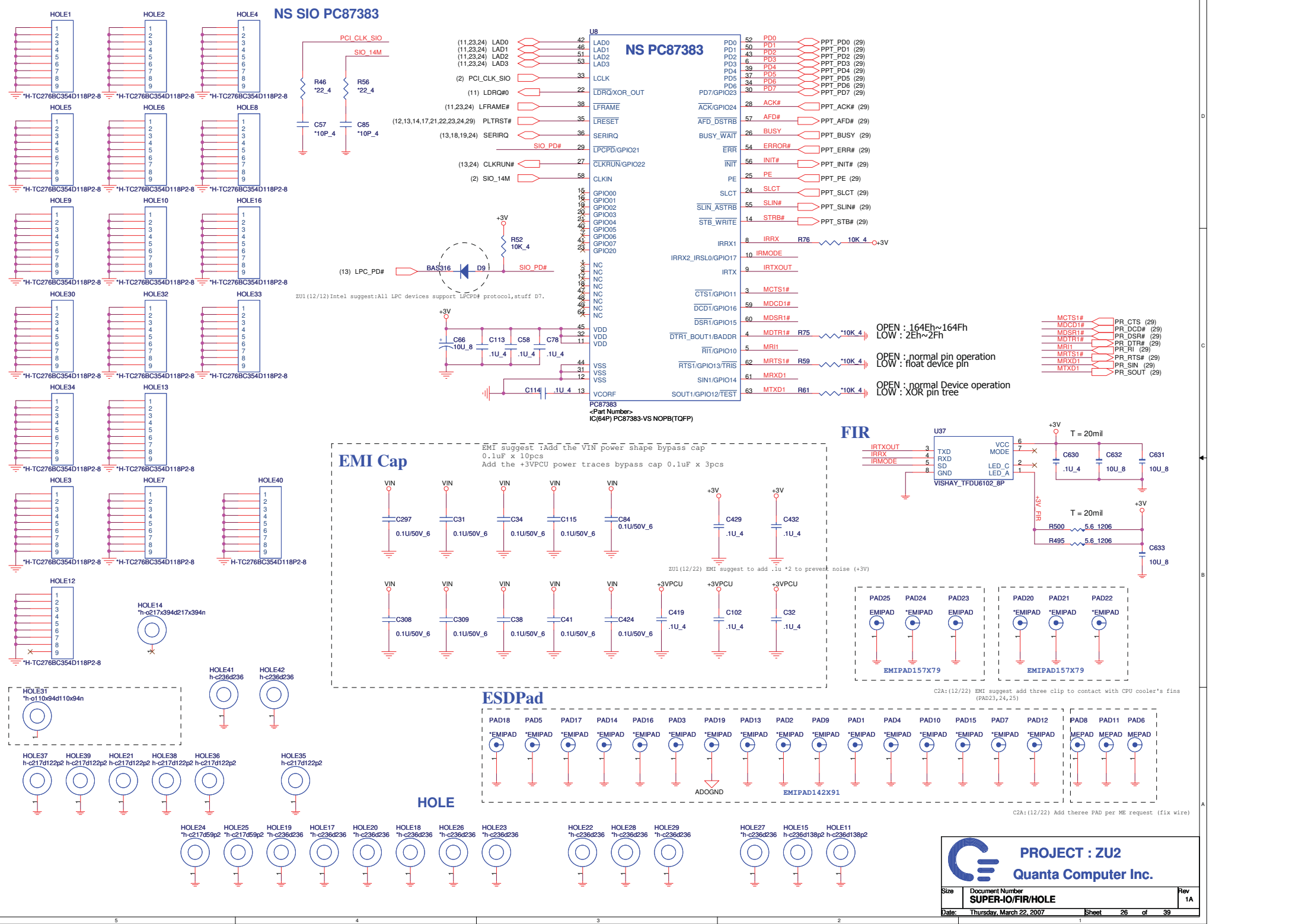


Finger Printer

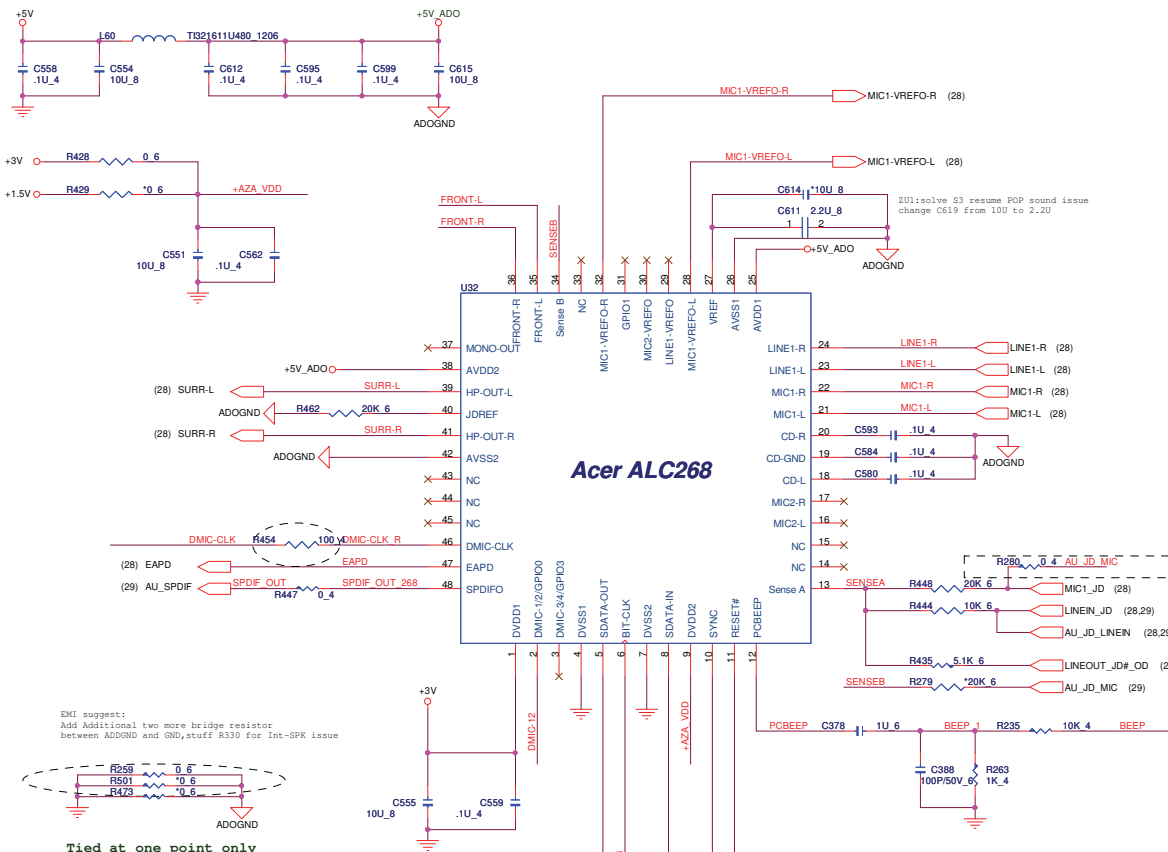


LED Board

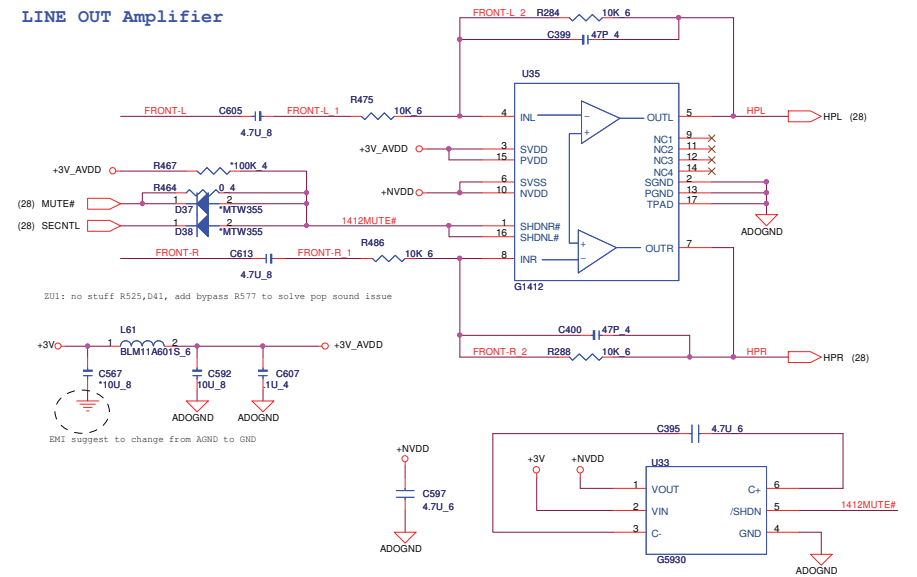




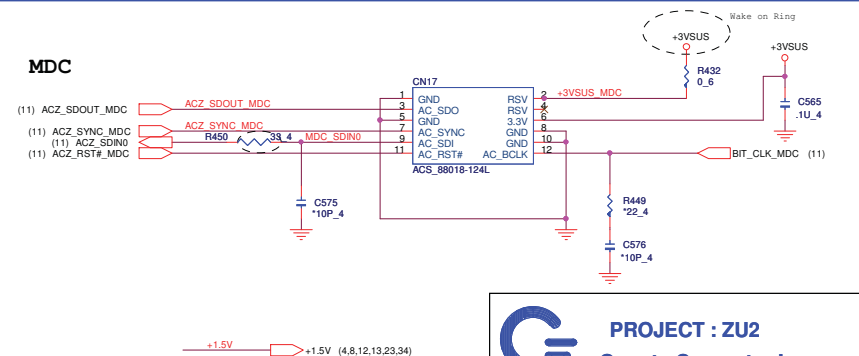
CODEC (ALC268)



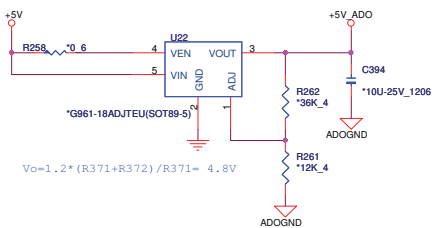
LINE OUT Amplifier



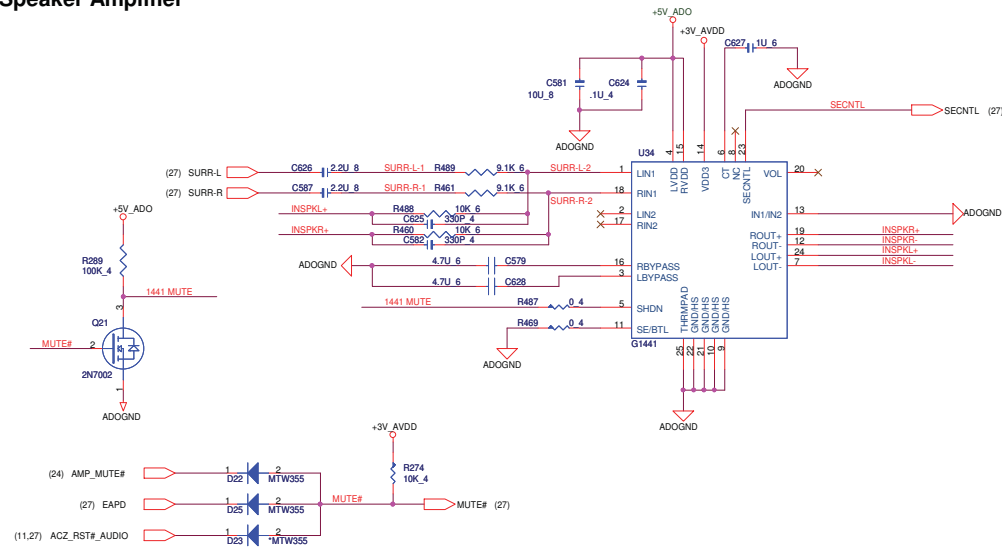
MDC



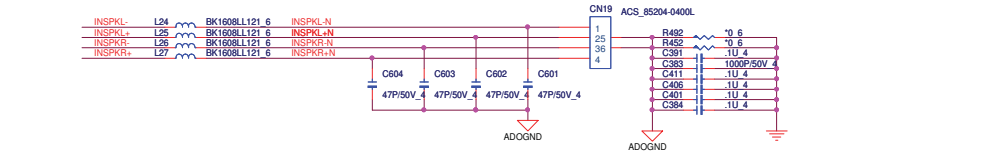
Reserve Audio power



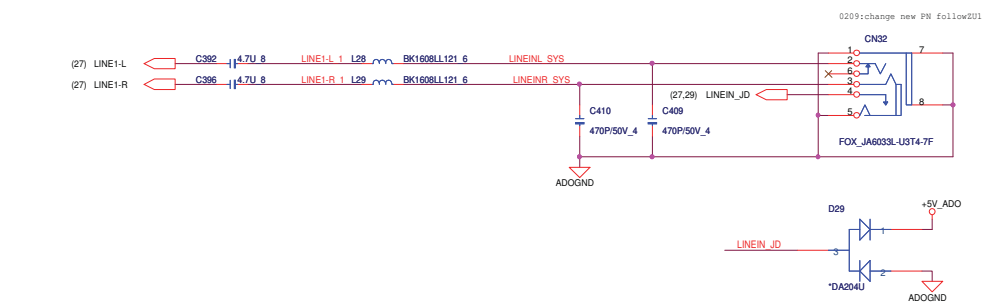
Speaker Amplifier



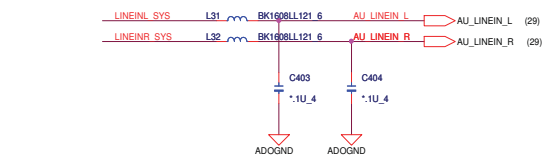
SPEAKER



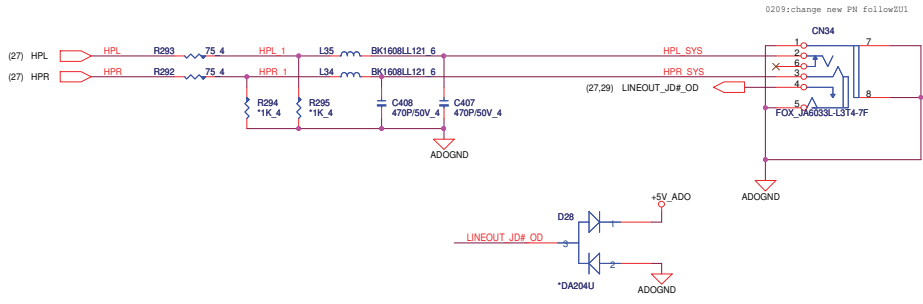
SYSTEM LINE IN



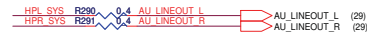
Docking LINE IN



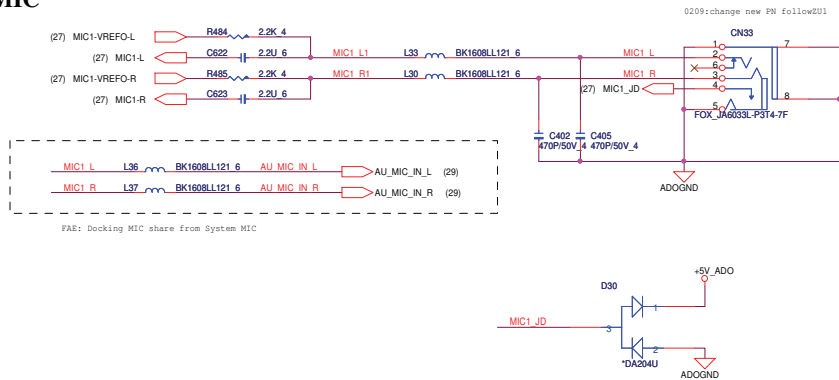
SYSTEM LINE OUT

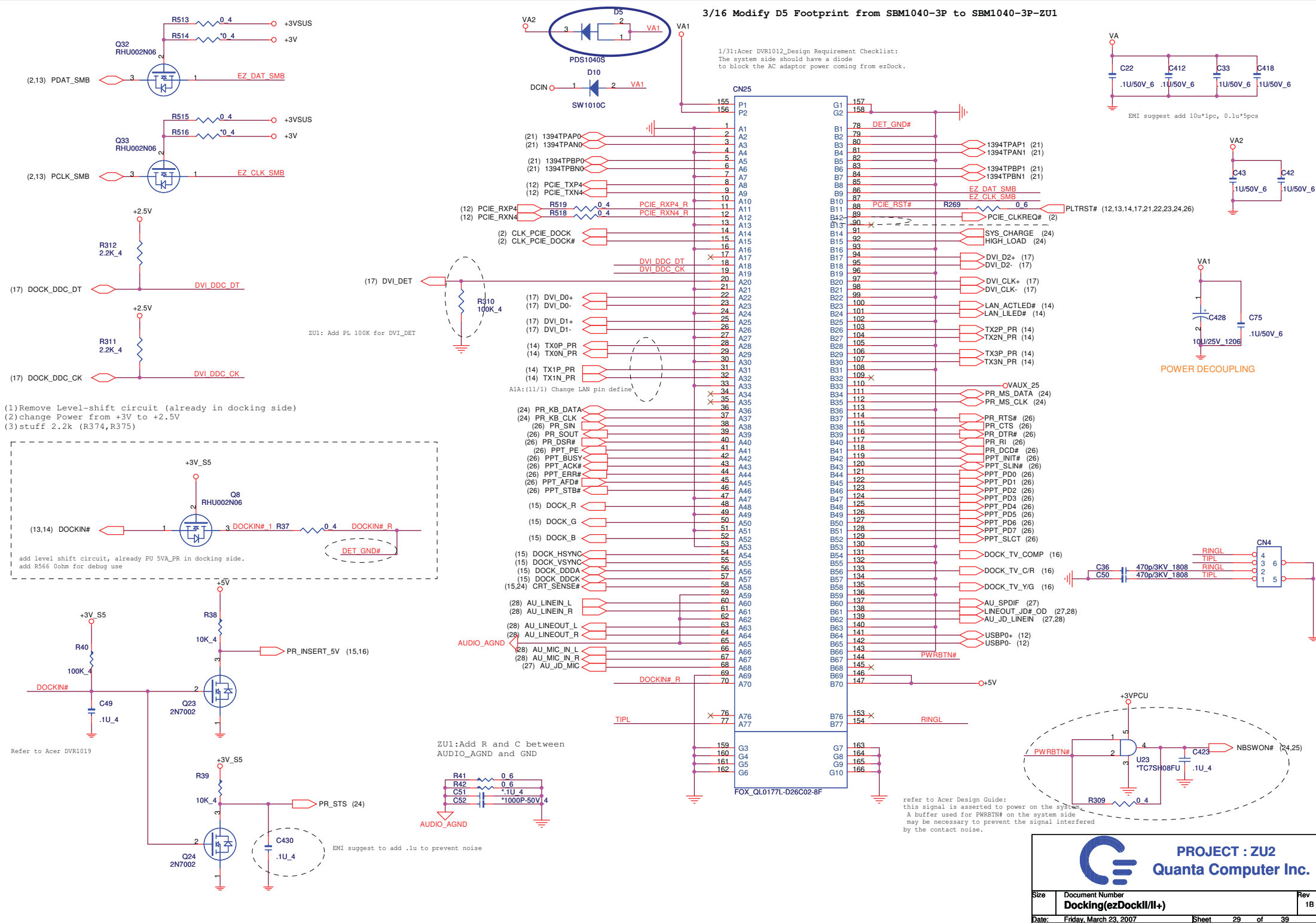


Docking LINE OUT/SPDIF



SYSTEM MIC





3/16 Modify D5 Footprint from SBM1040-3P to SBM1040-3P-ZU1

1/31:Acer DVR1012_Design Requirement Checklist:
The system side should have a diode to block the AC adaptor power coming from ezDock.

EMI suggest add 10u*1pc, 0.1u*5pcs

POWER DECOUPLING

refer to Acer Design Guide:
this signal is asserted to power on the system.
A buffer used for PWRBTN# on the system side
may be necessary to prevent the signal interfered
by the contact noise.



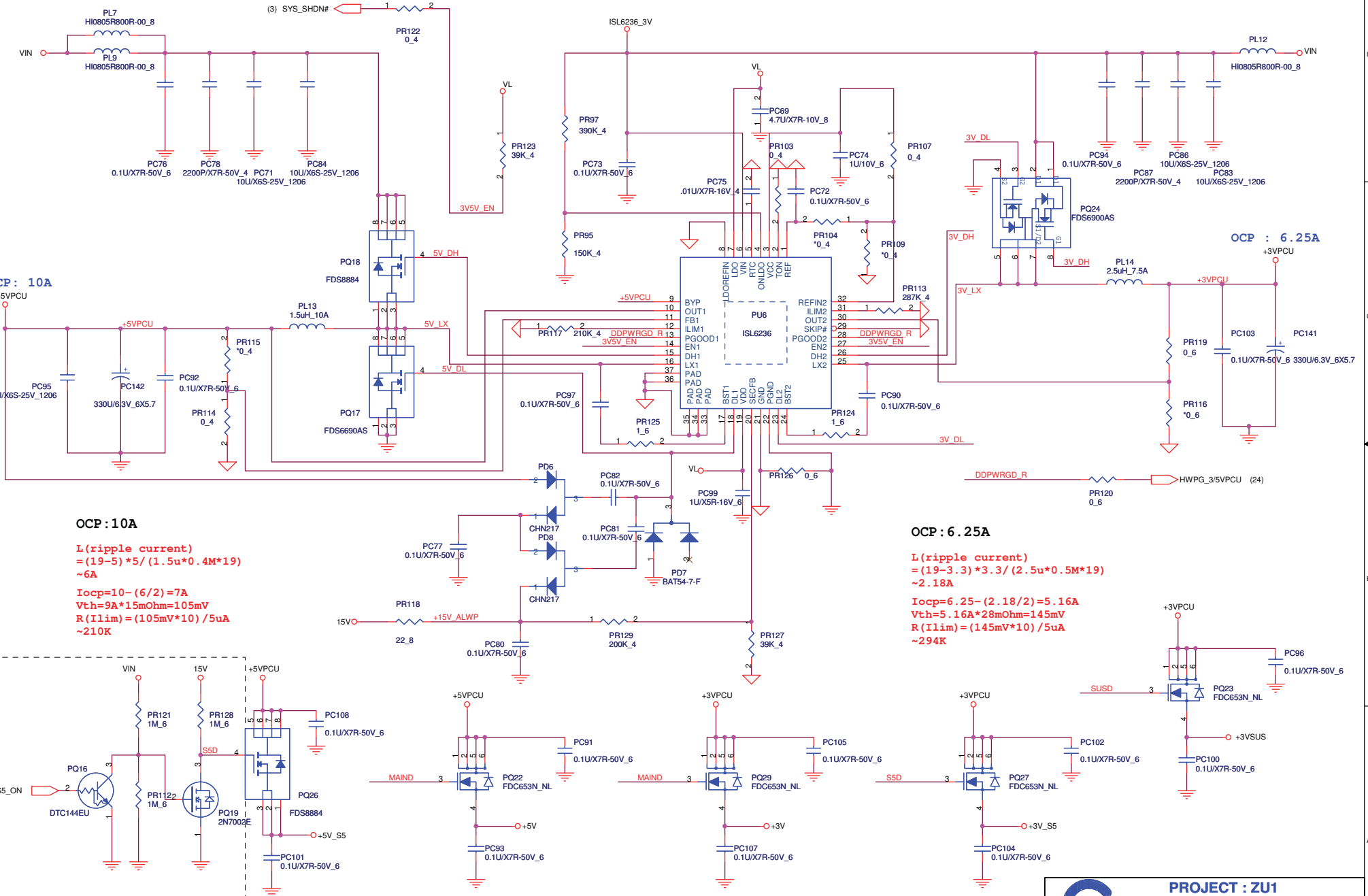
PROJECT : ZU2
Quanta Computer Inc.

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	Docking(ezDockII/II+)	1B
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MAIND (34)

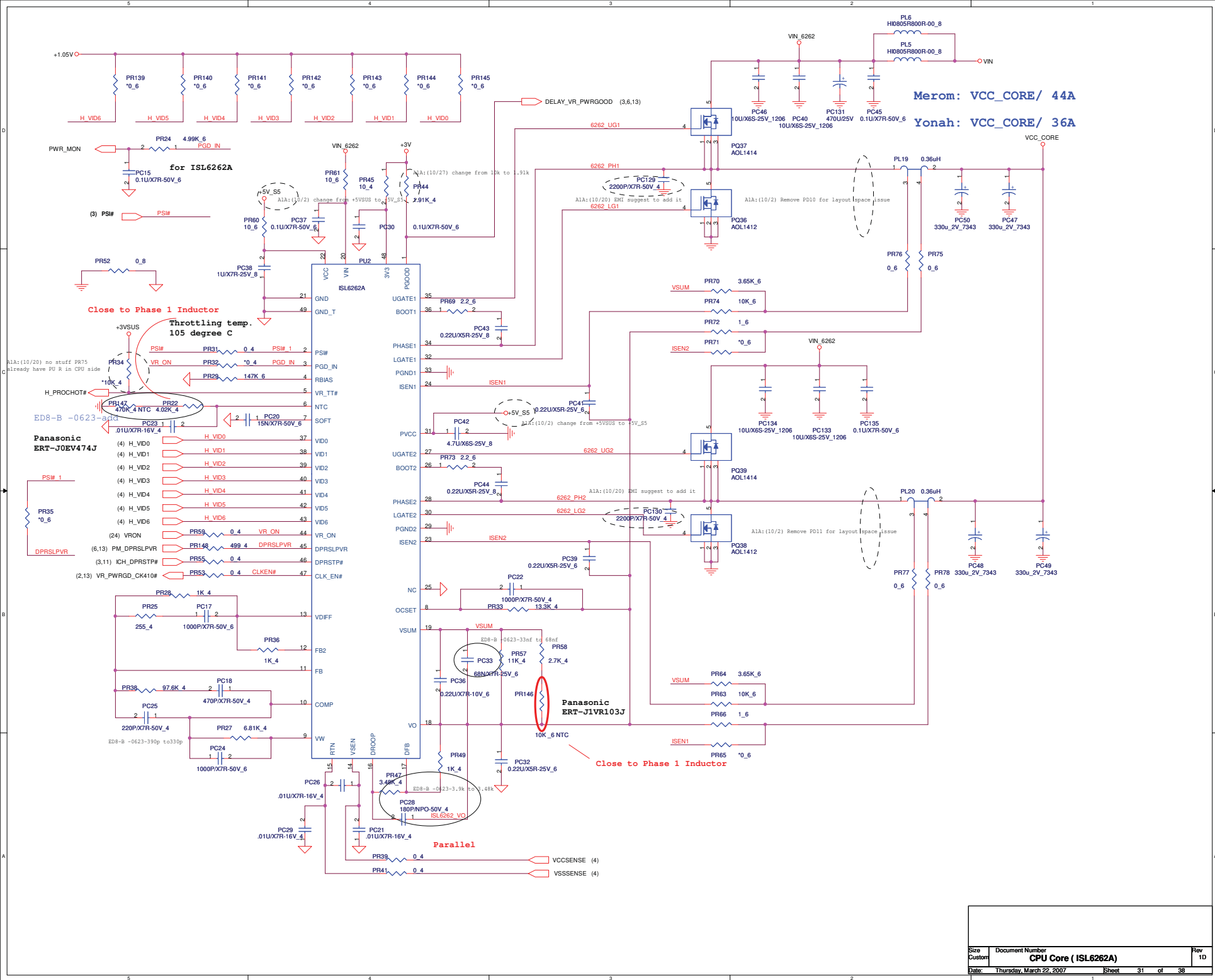
SUSD (34)

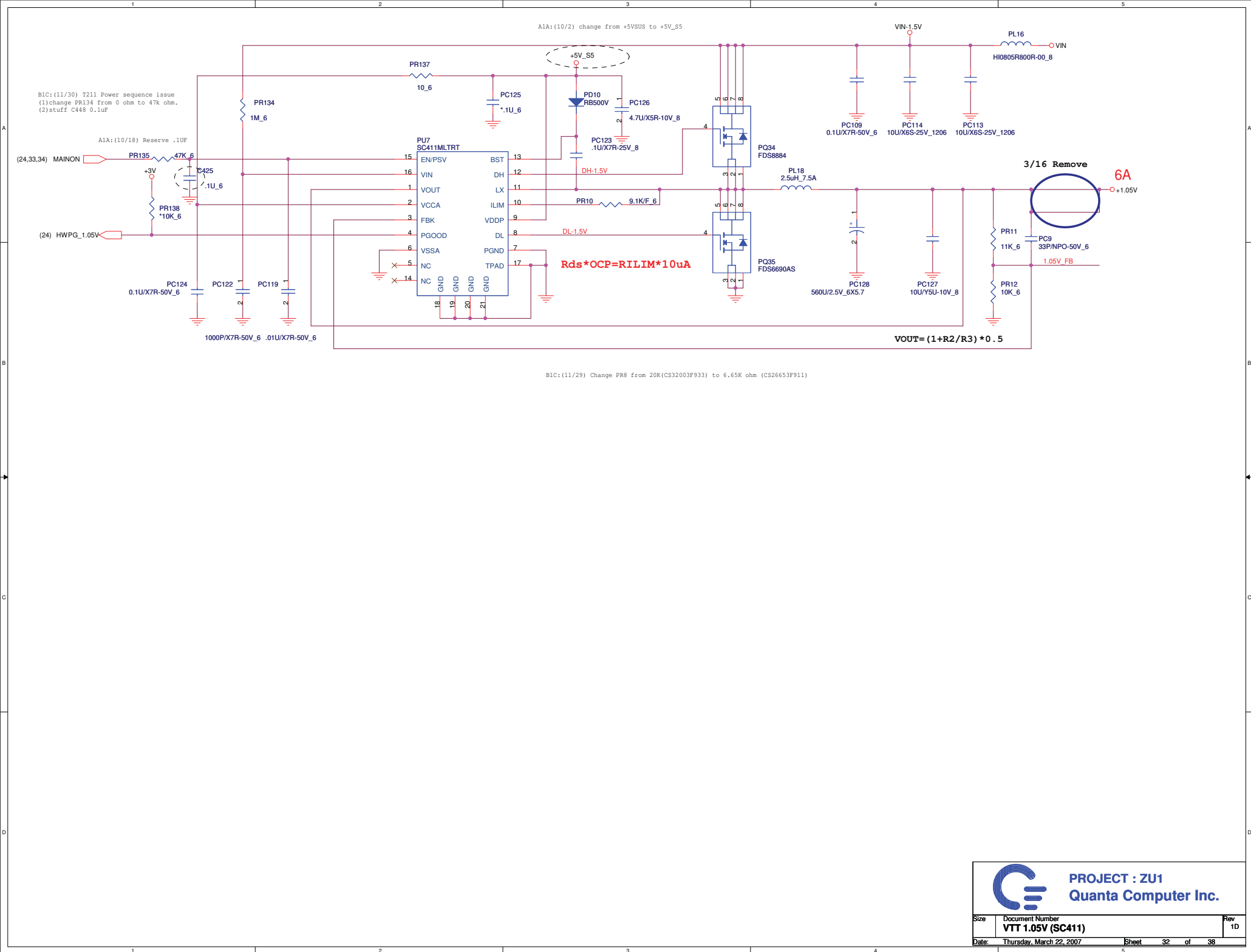
(3) SYS_SHDN#



C2A: (12/10) change S5_ON control circuit

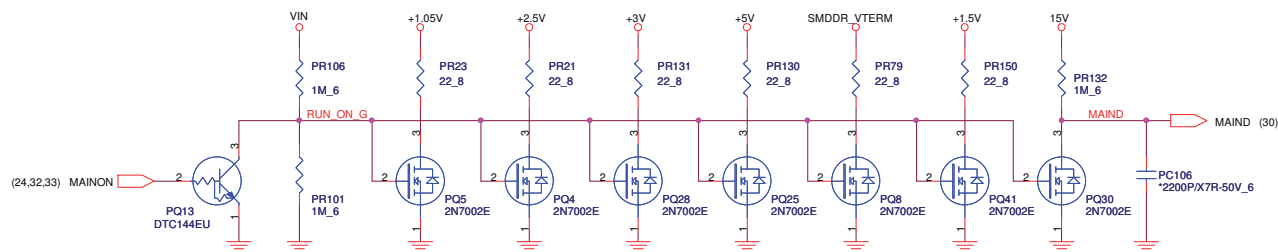
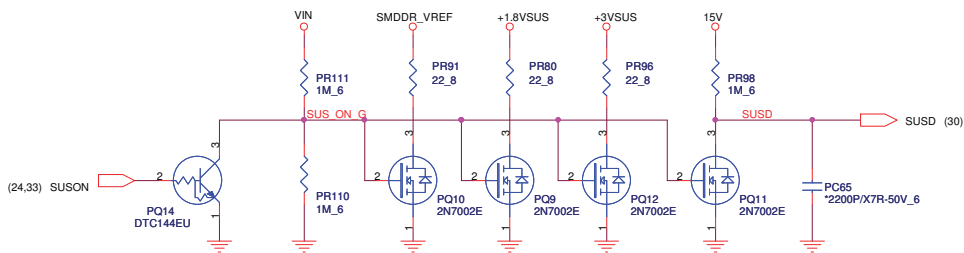
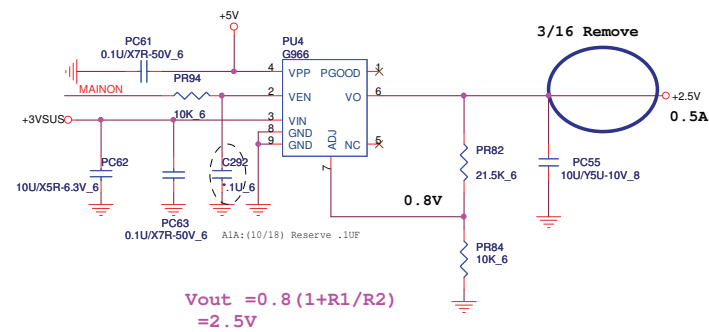
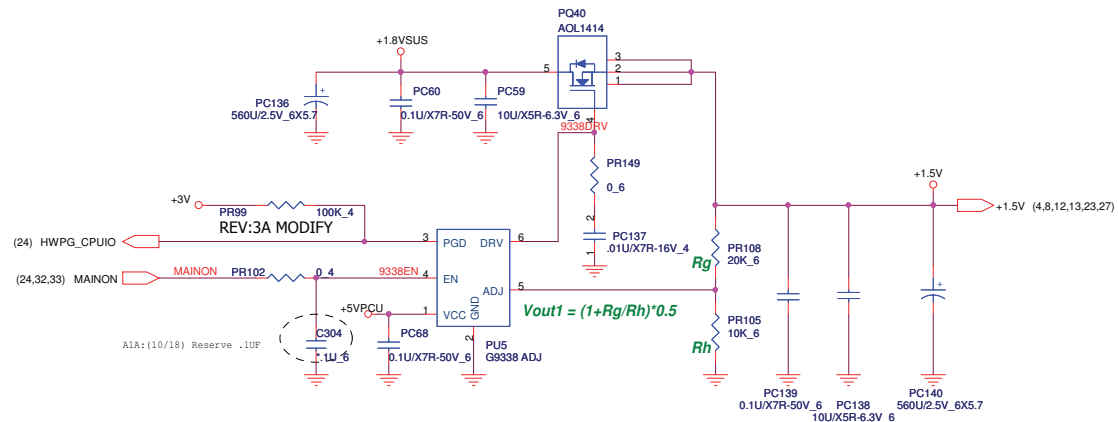
B1C: (11/29) Change PQ26 from FDS6690AS (BAM66900022) to FDS8884 (BAM88840006)





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3/16 Add fuse

