

Dunlin LA-2601 Schematics Document

Intel Dothan / Alviso GM(PM) / DDR-2 / ICH6-M

(NV43/44M)

2005 / 03 / 14

Rev:1.0

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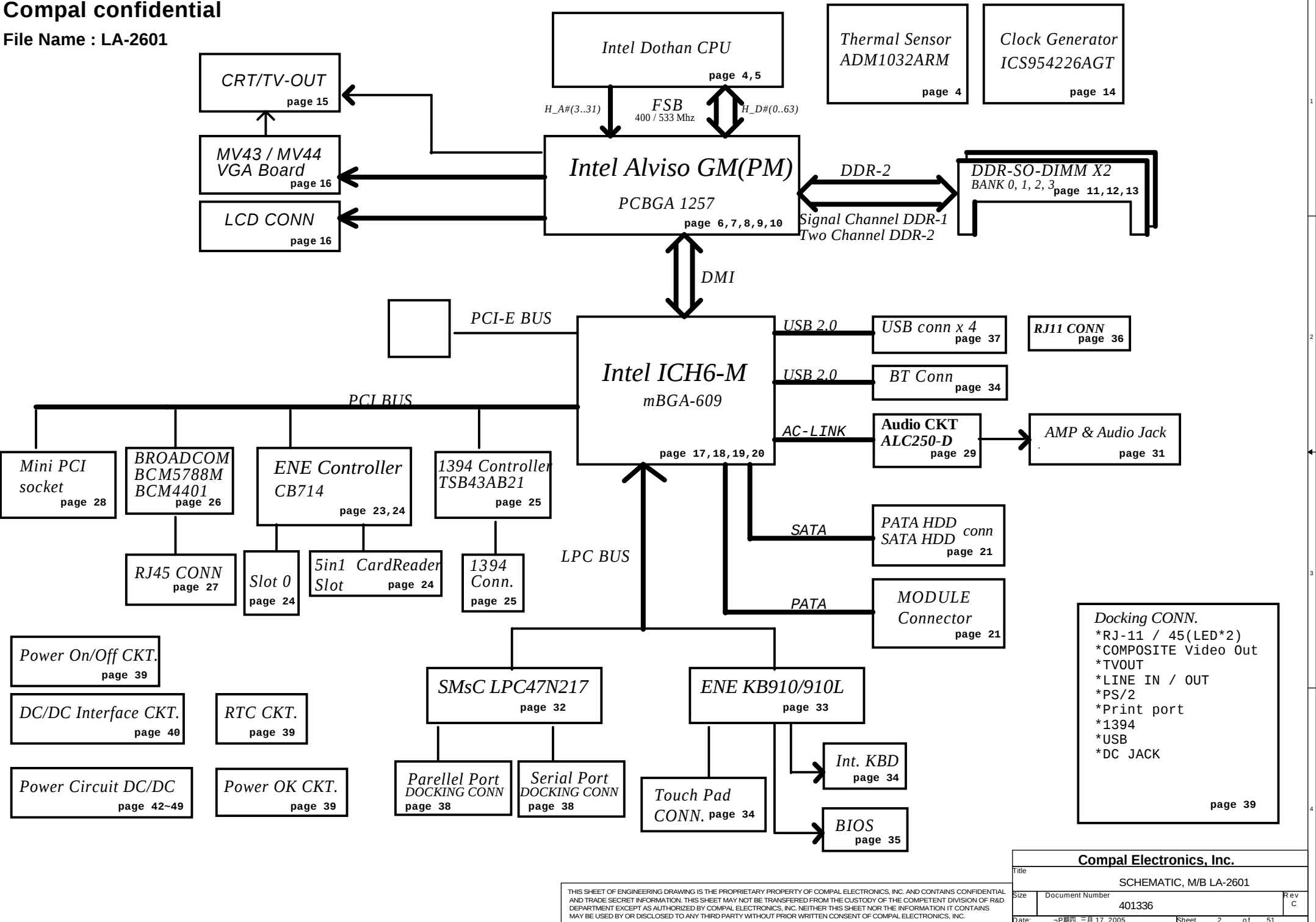
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File Name : LA-2601



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Voltage Rails

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.05VS	1.05V switched power rail	ON	OFF	OFF
+DDRVTT	1.25V switched power rail for DDR terminator	ON	OFF	OFF
+1.5VALW	1.5V always on power rail	ON	ON	ON*
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+DDRVC	2.5V power rail for DDR	ON	ON	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+5VMD	5V switched power rail for Module Bay	ON	OFF	OFF
+12VALW	12V always on power rail	ON	ON	ON*
+RTCV	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD20	2	PIRQA/PIRQB
1394	AD16	0	PIRQE
SD	AD20	2	PIRQA/PIRQB
Mini-PCI	AD18	1	PIRQG/PIRQH
LAN	AD17	3	PIRQF

EC SM Bus1 address

Device	Address	Device	Address
Smart Battery	0001 011X b	ADM1032	1001 110X b
EEPROM(24C16/02)	1010 000X b	2nd Battery	1001 011X b
(24C04)	1011 000Xb		

ICH6M SM Bus address

Device	Address
Clock Generator (ICS 952623)	1101 001Xb
DDR DIMM0	1010 000Xb
DDR DIMM2	1010 010Xb

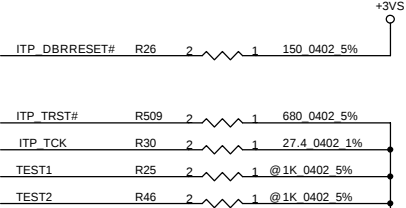
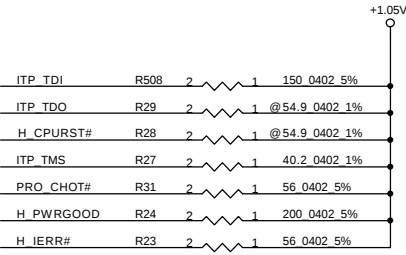
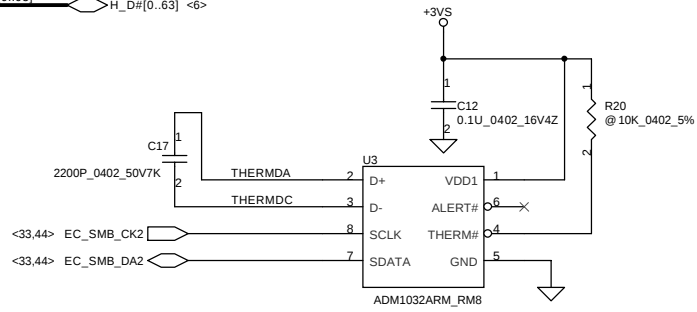
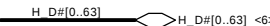
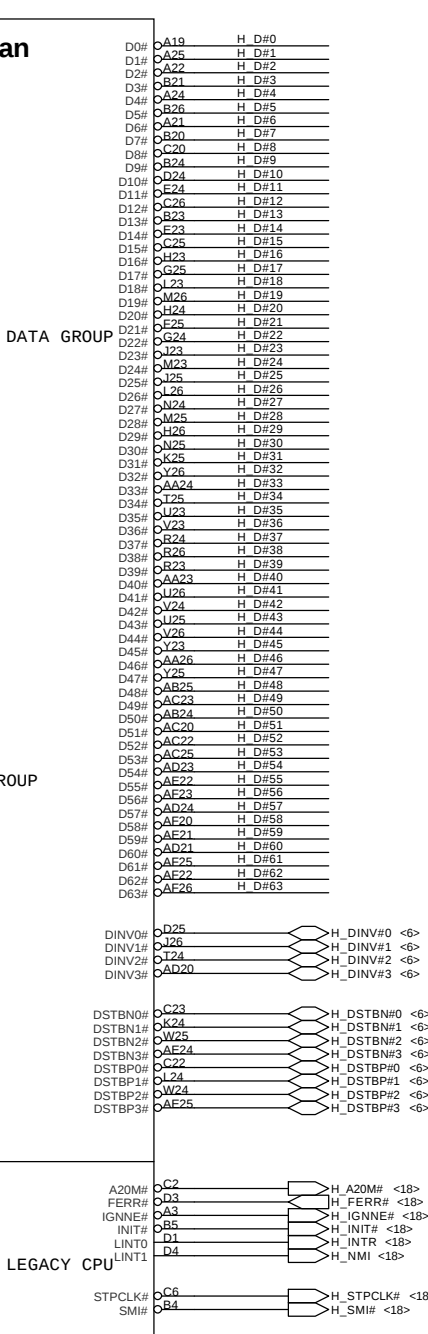
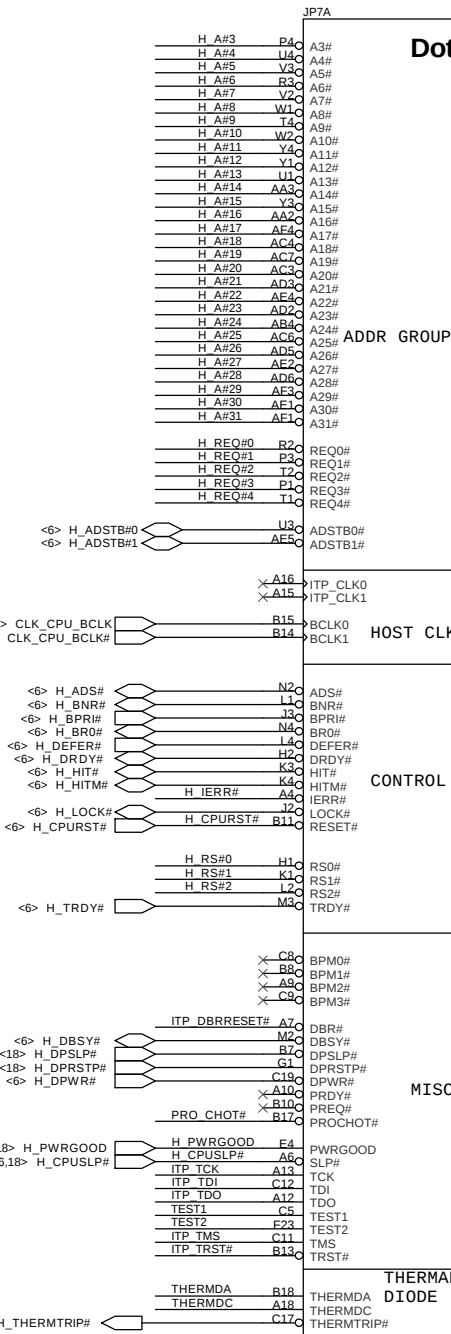
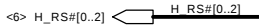
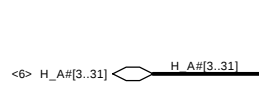
STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra / Rc	100K +/- 5%			
Board ID	Rb / Rd	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

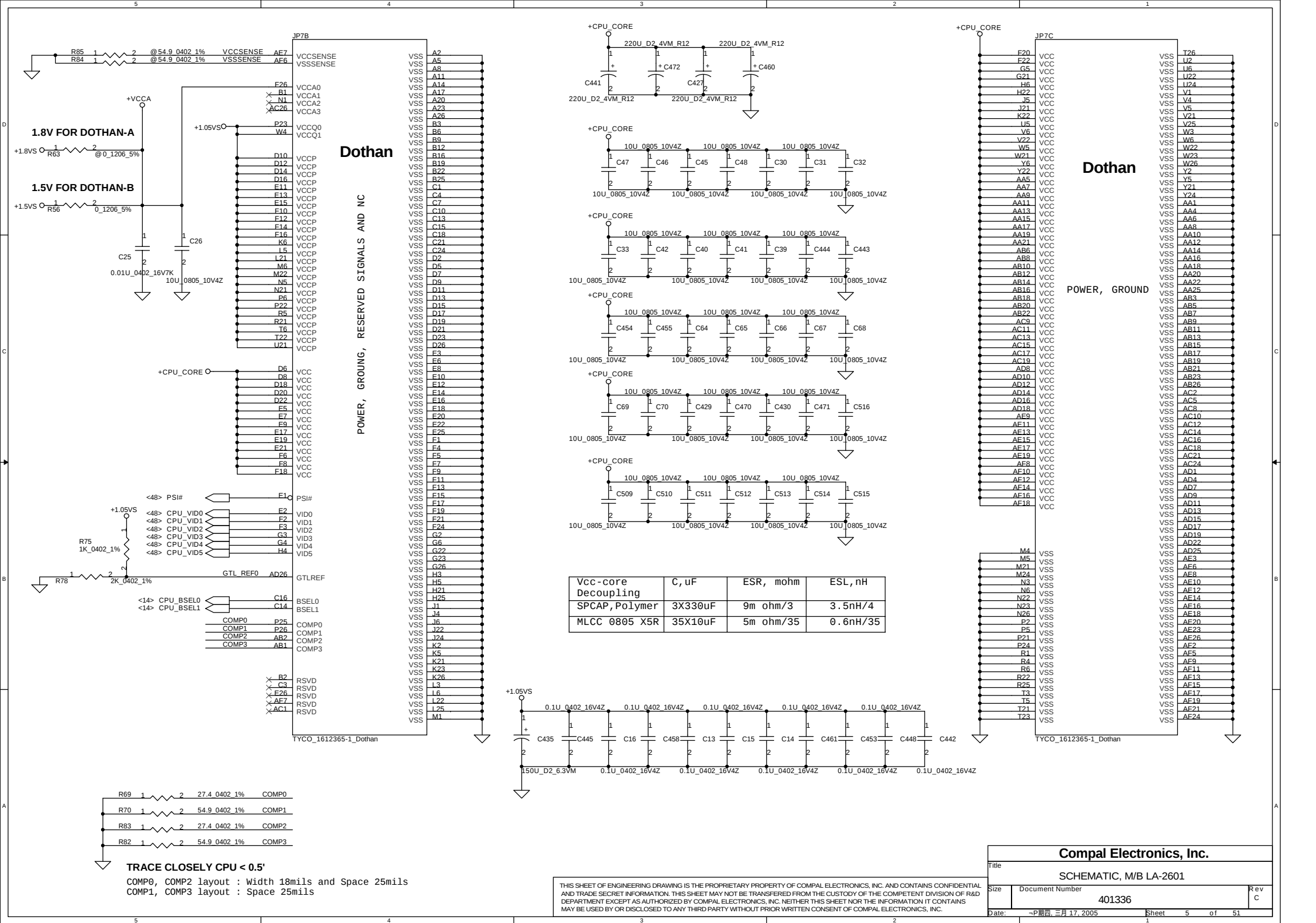
Board ID	PCB Revision
0	0.1
1	
2	
3	
4	
5	
6	
7	

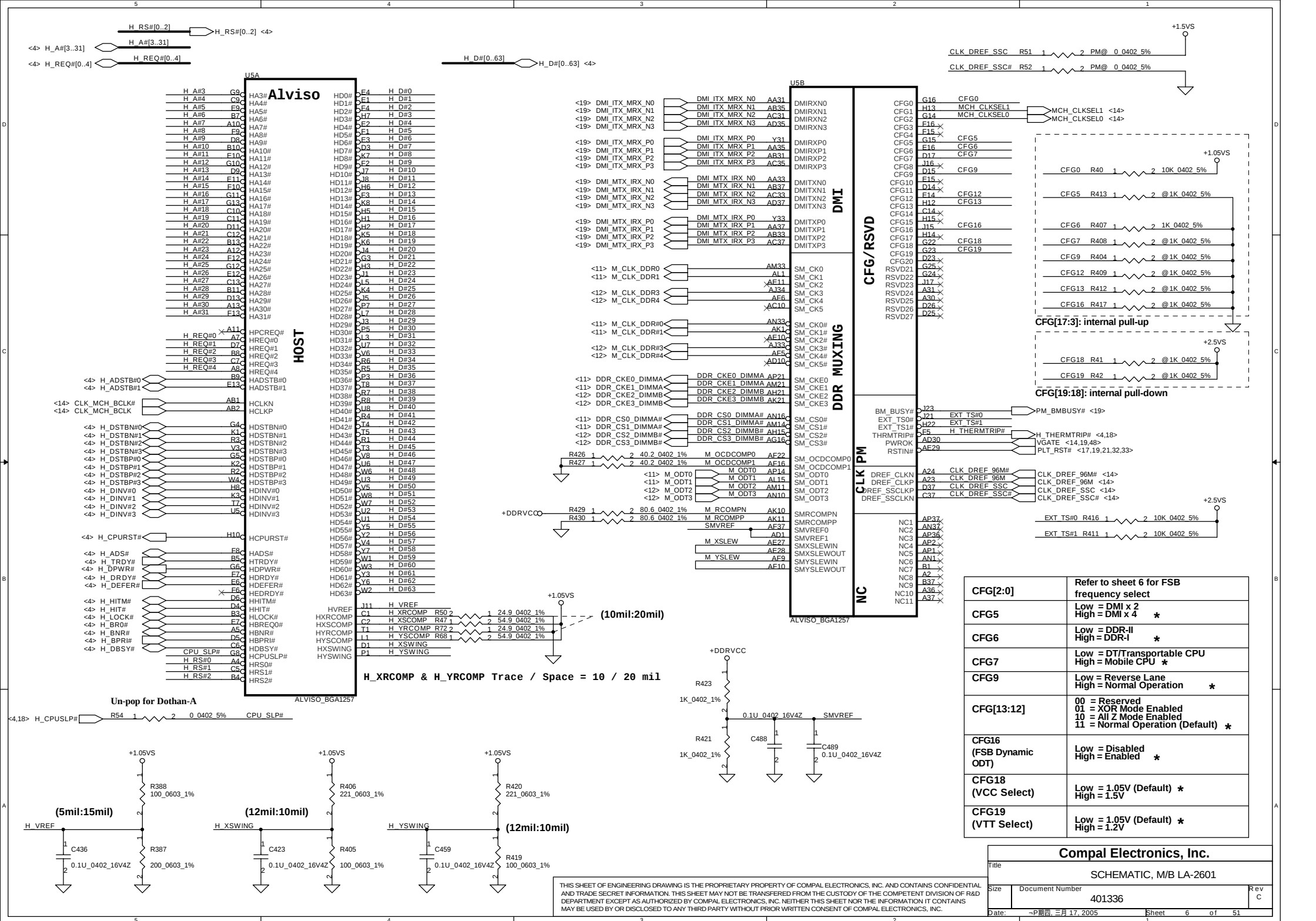


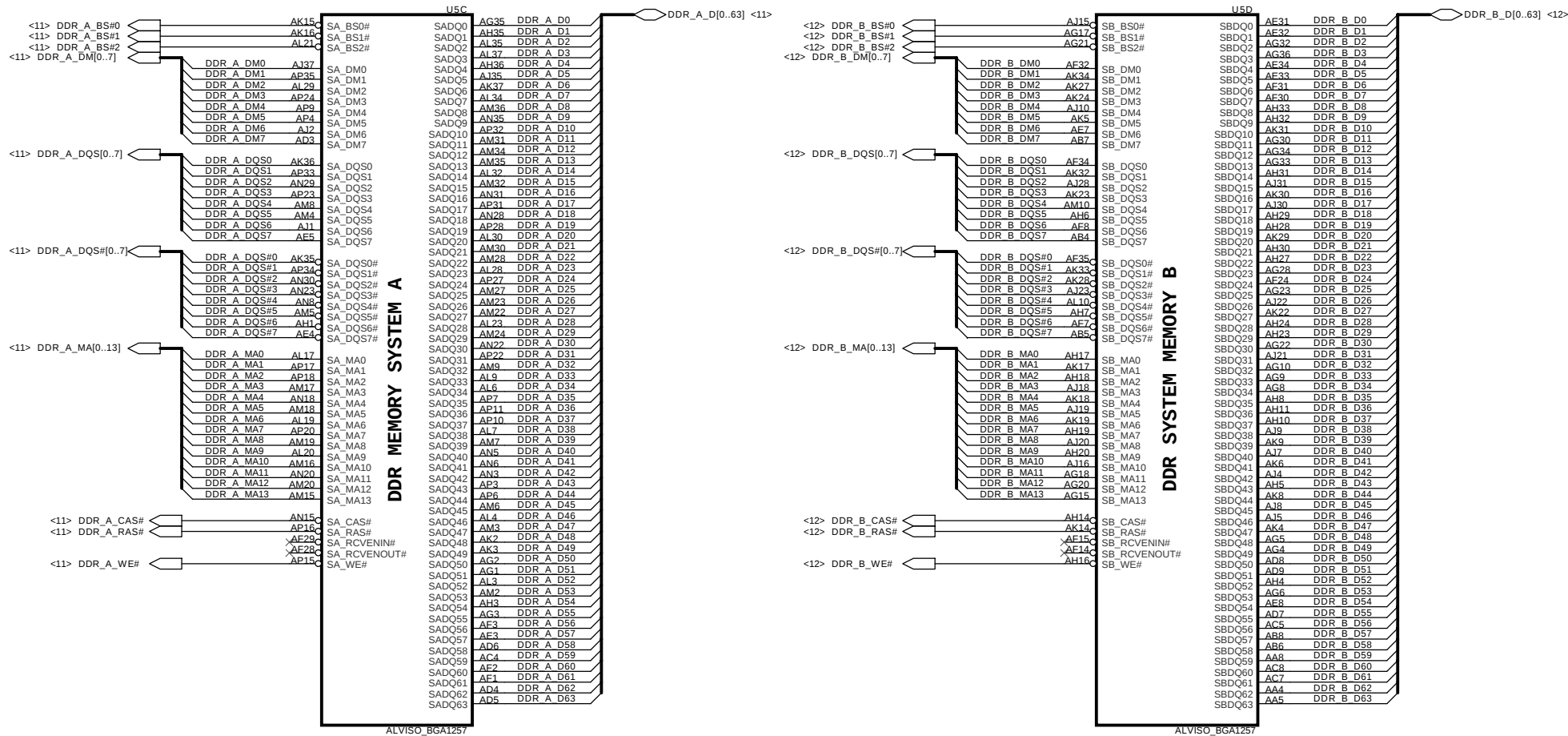
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THERMDA & THERMDC Trace / Space = 10 / 10 mil

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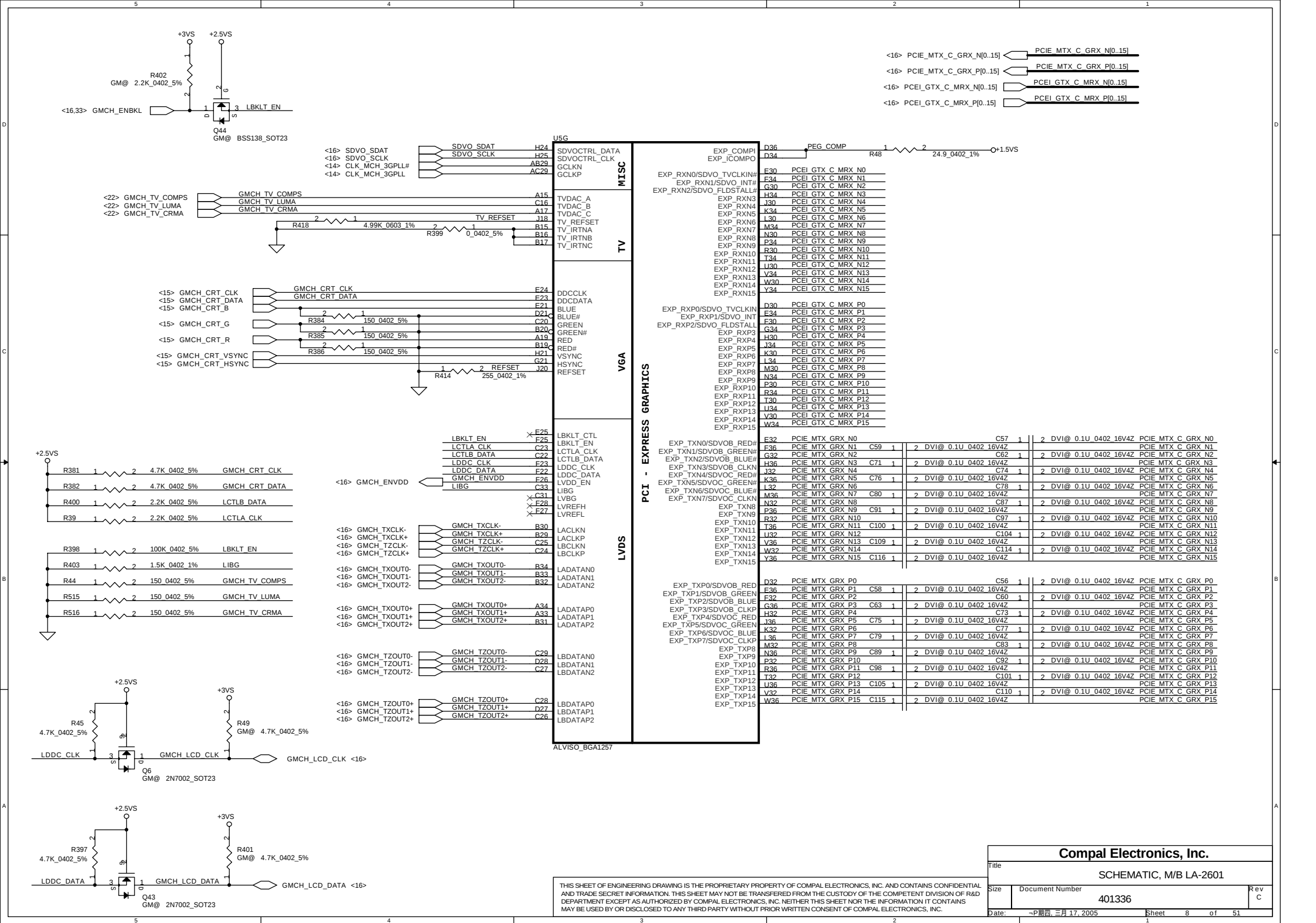


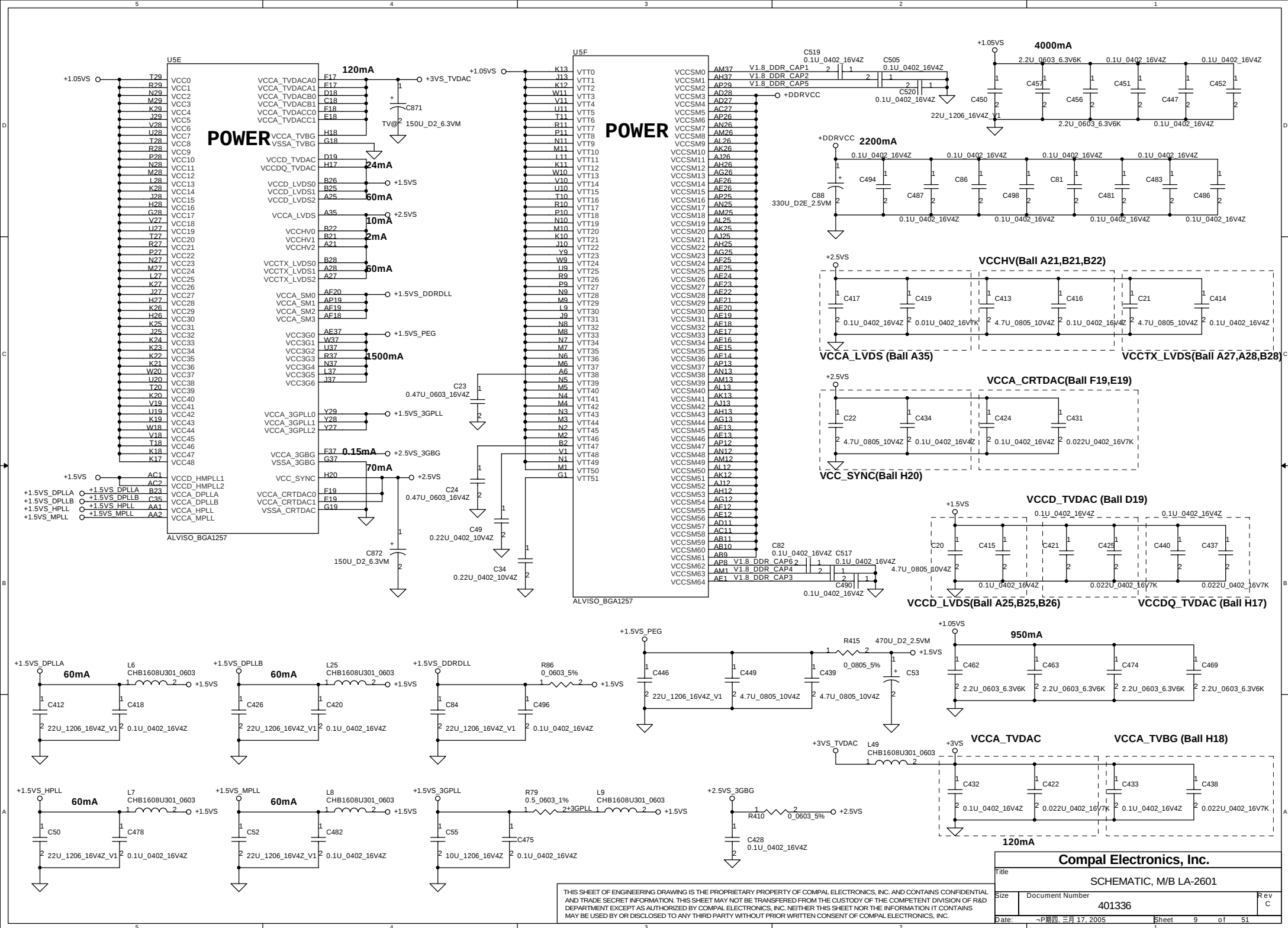


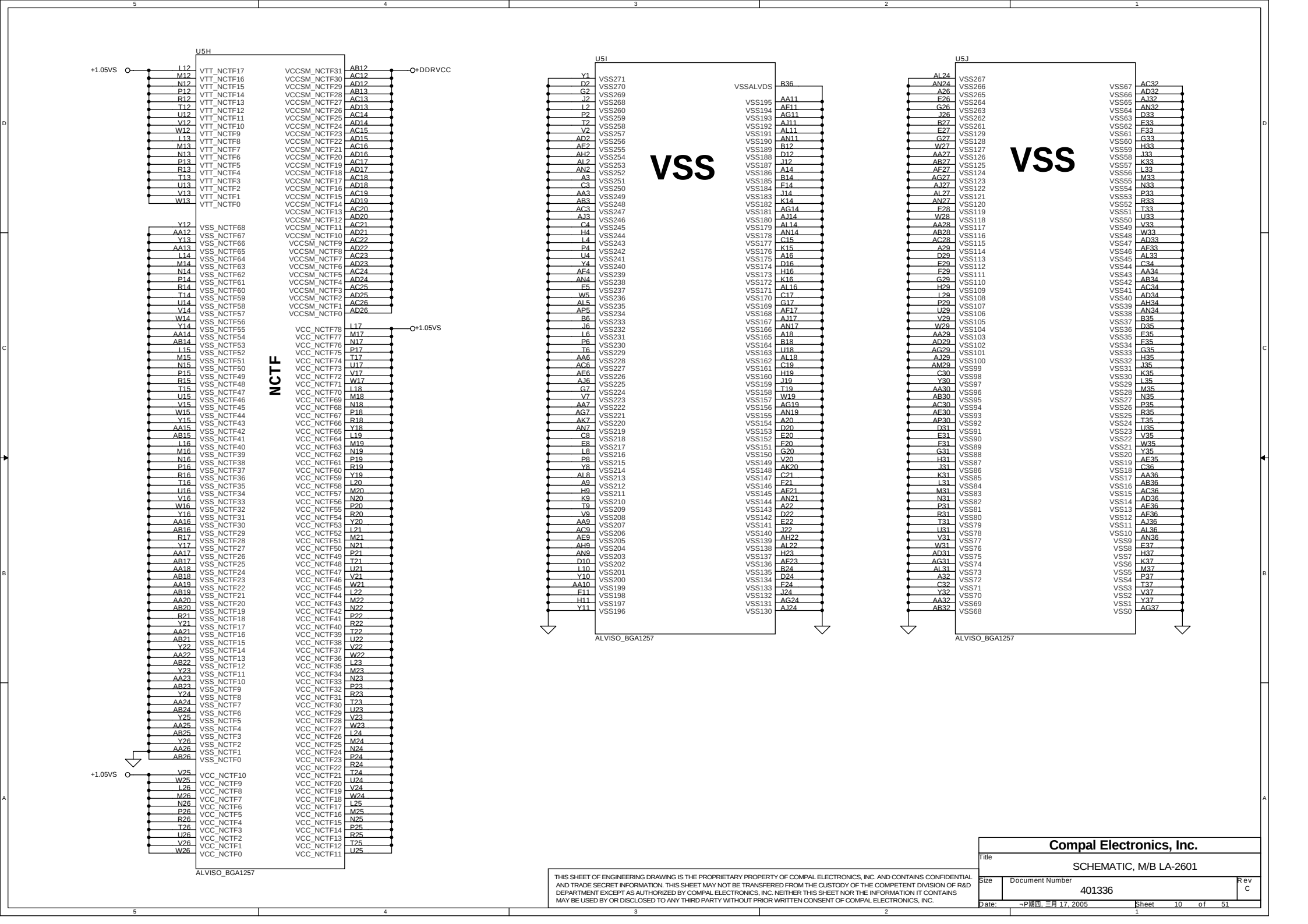


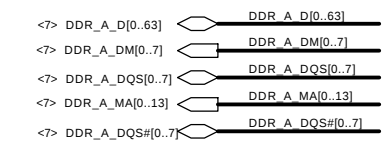
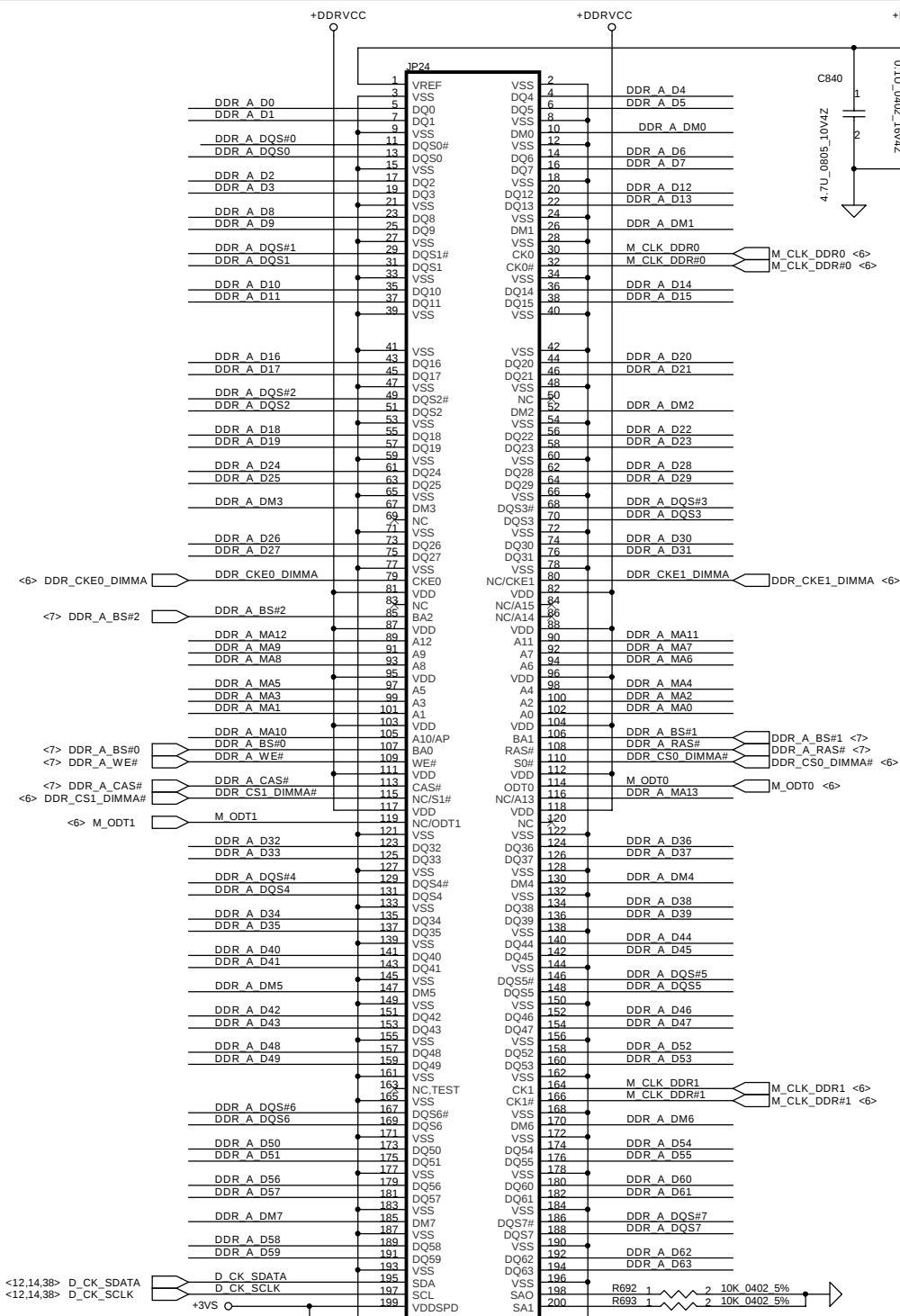
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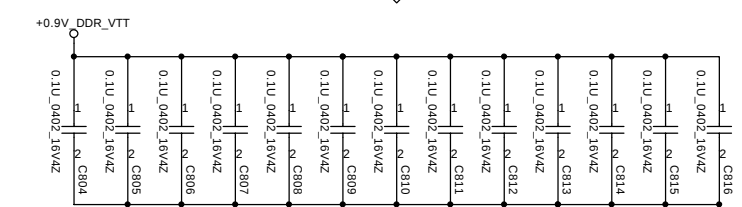
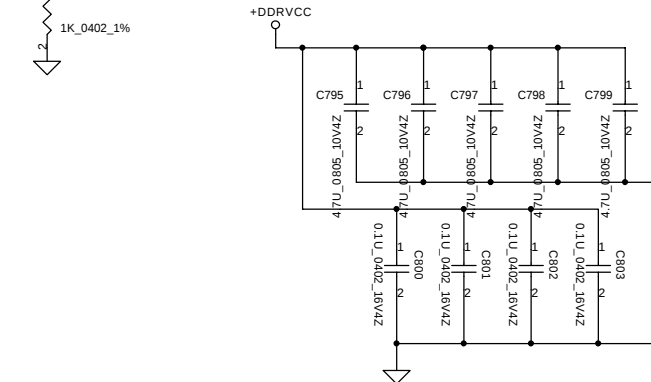




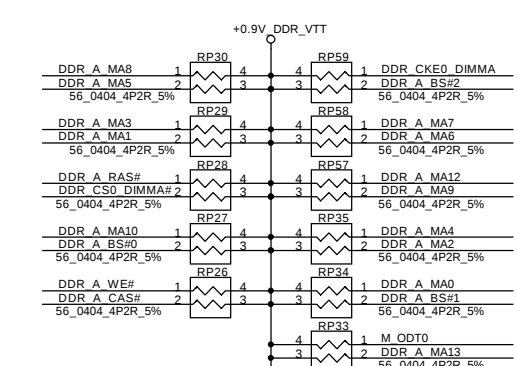




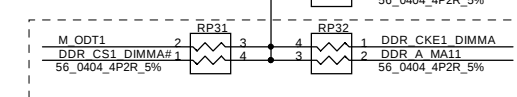
Layout Note:
Place near DIMM



Layout Note:
Place one cap close to every 2 pullup
resistors terminated to +0.9V_DDR_VTT



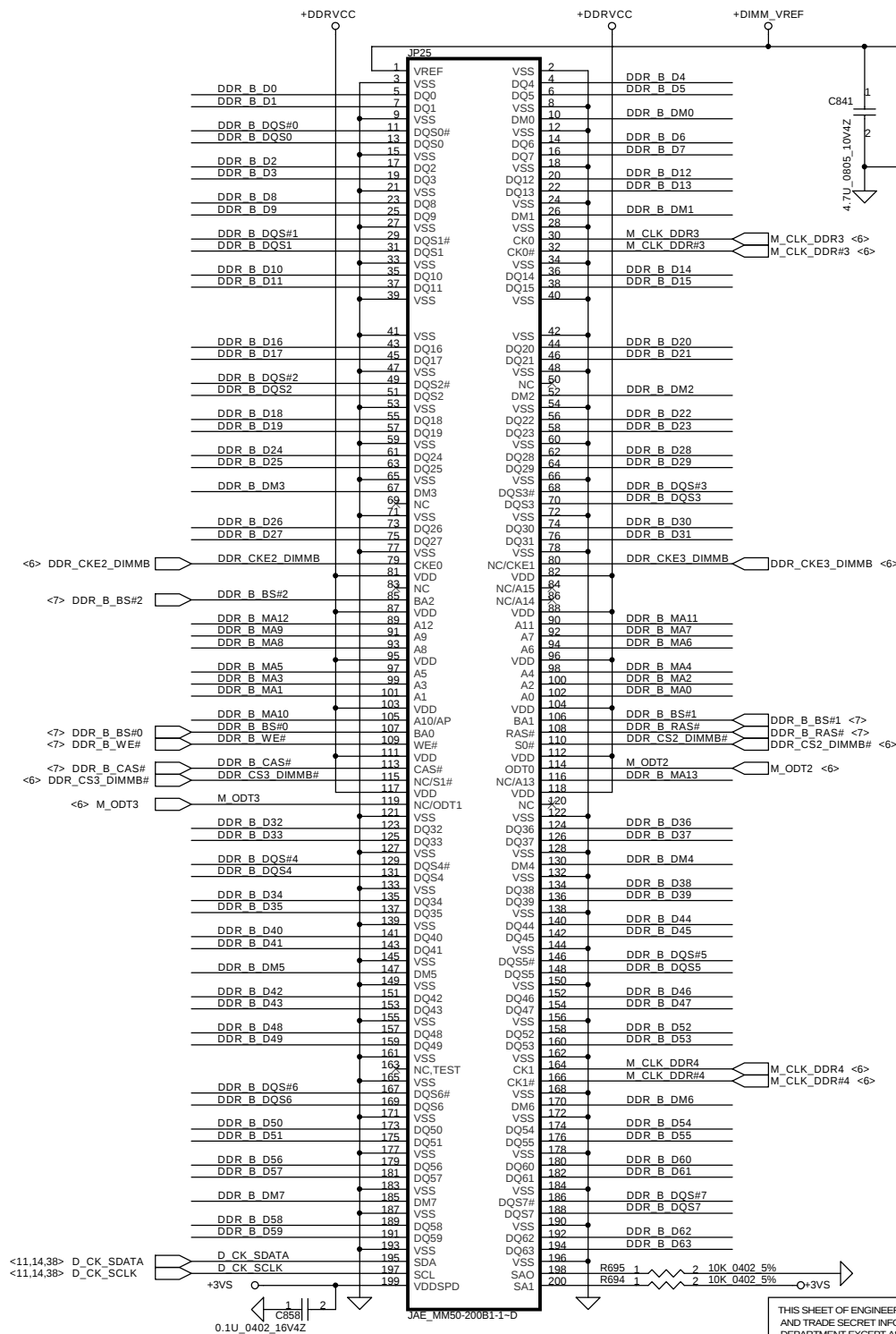
Layout Note:
Place these resistor
closely DIMM0,all
trace length<750 mil



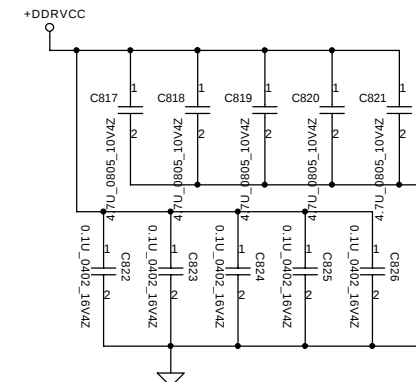
Layout Note:
Place these resistor
closely DIMM0,all
trace length
Max=1.3"

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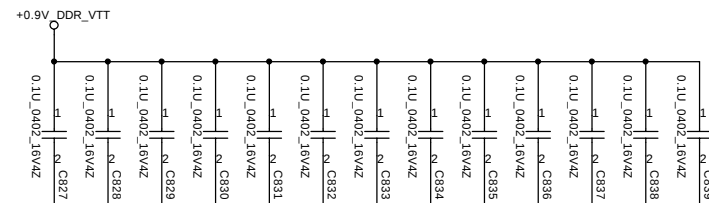
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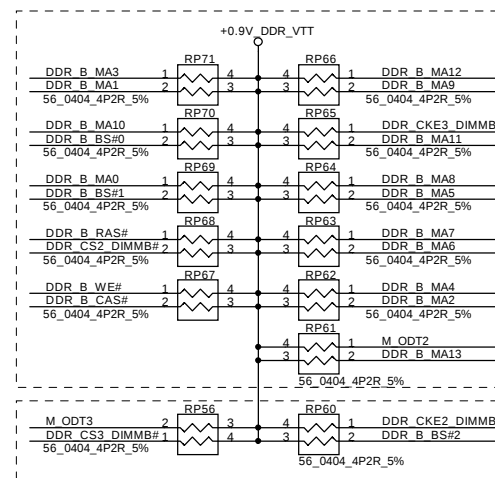
<7> DDR_B_D[0..63]  DDR B D[0..63]
 <7> DDR_B_DM[0..7]  DDR B DM[0..7]
 <7> DDR_B_DQS[0..7]  DDR B DQS[0..7]
 <7> DDR_B_MA[0..13]  DDR B MA[0..13]
 <7> DDR_B_DQS#[0..7]  DDR B DQS#[0..7]



Layout Note:
Place near DIMM



Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9V_DDR_VTT



Layout Note:
Place these resistor closely DIMM0,all trace length<750 mil

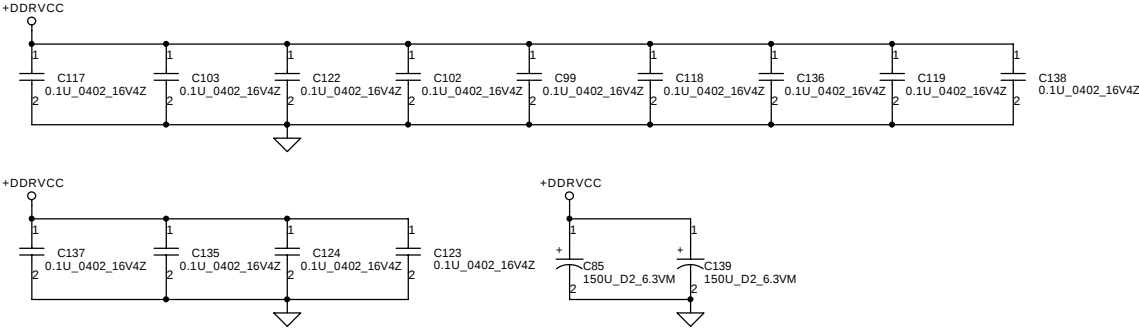
Layout Note:
Place these resistor closely DIMM0,all trace length Max=1.3"

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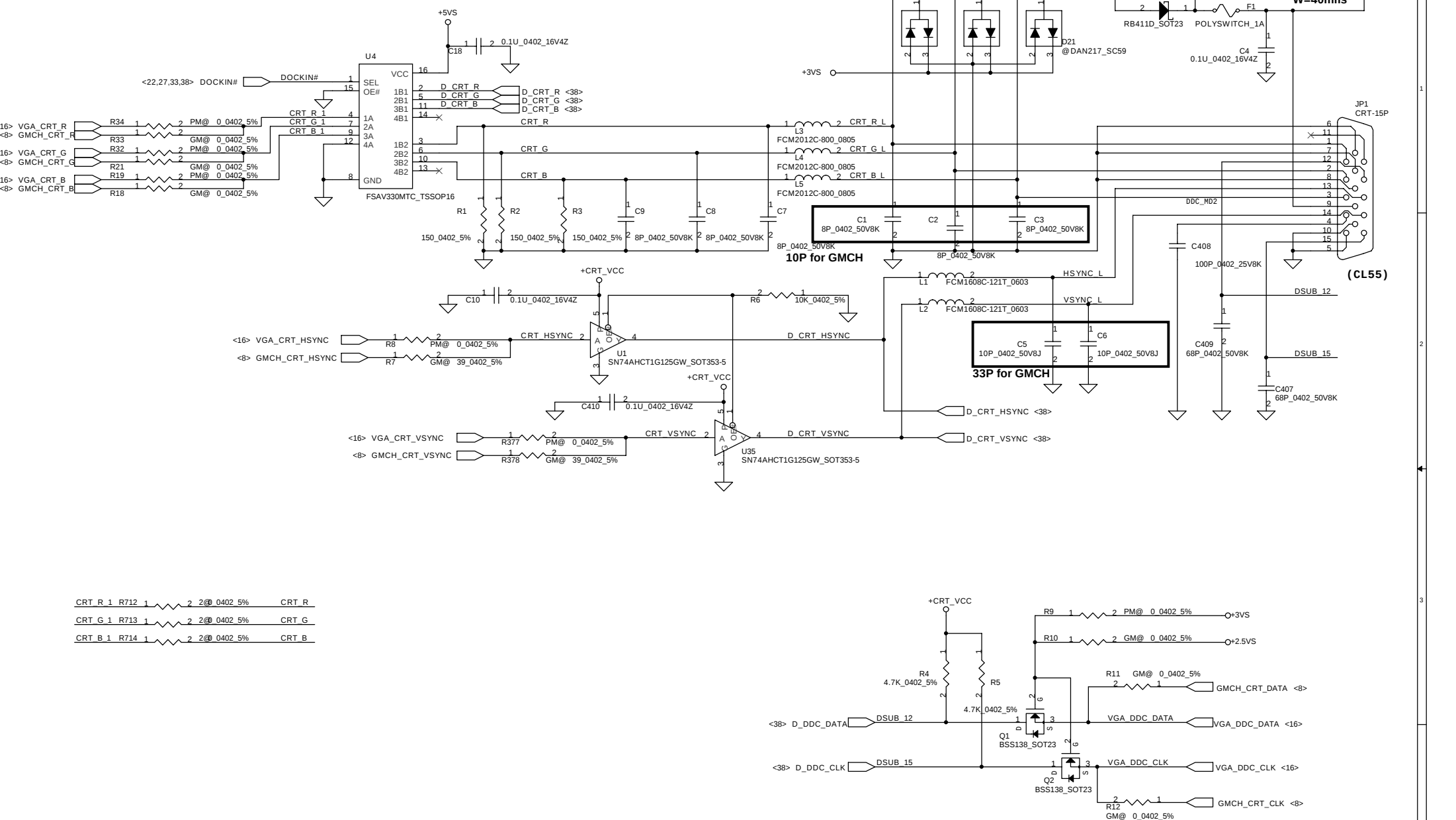
Layout note :
Distribute as close as possible
to DDR-SODIMM.



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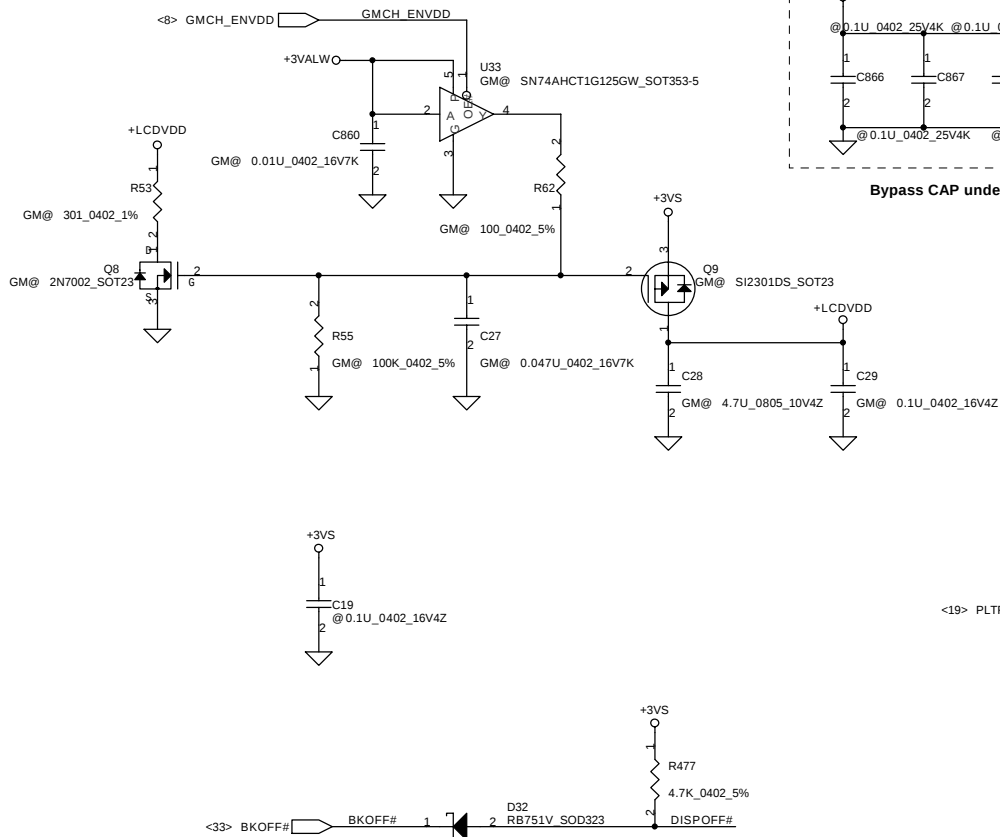
CRT Connector



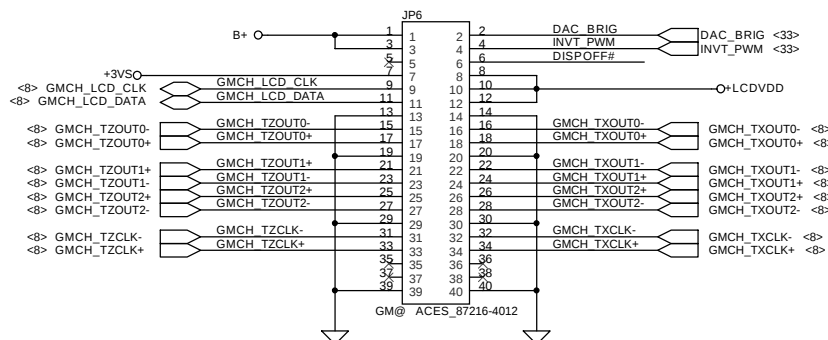
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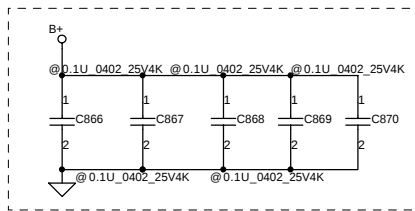
LCD POWER CIRCUIT



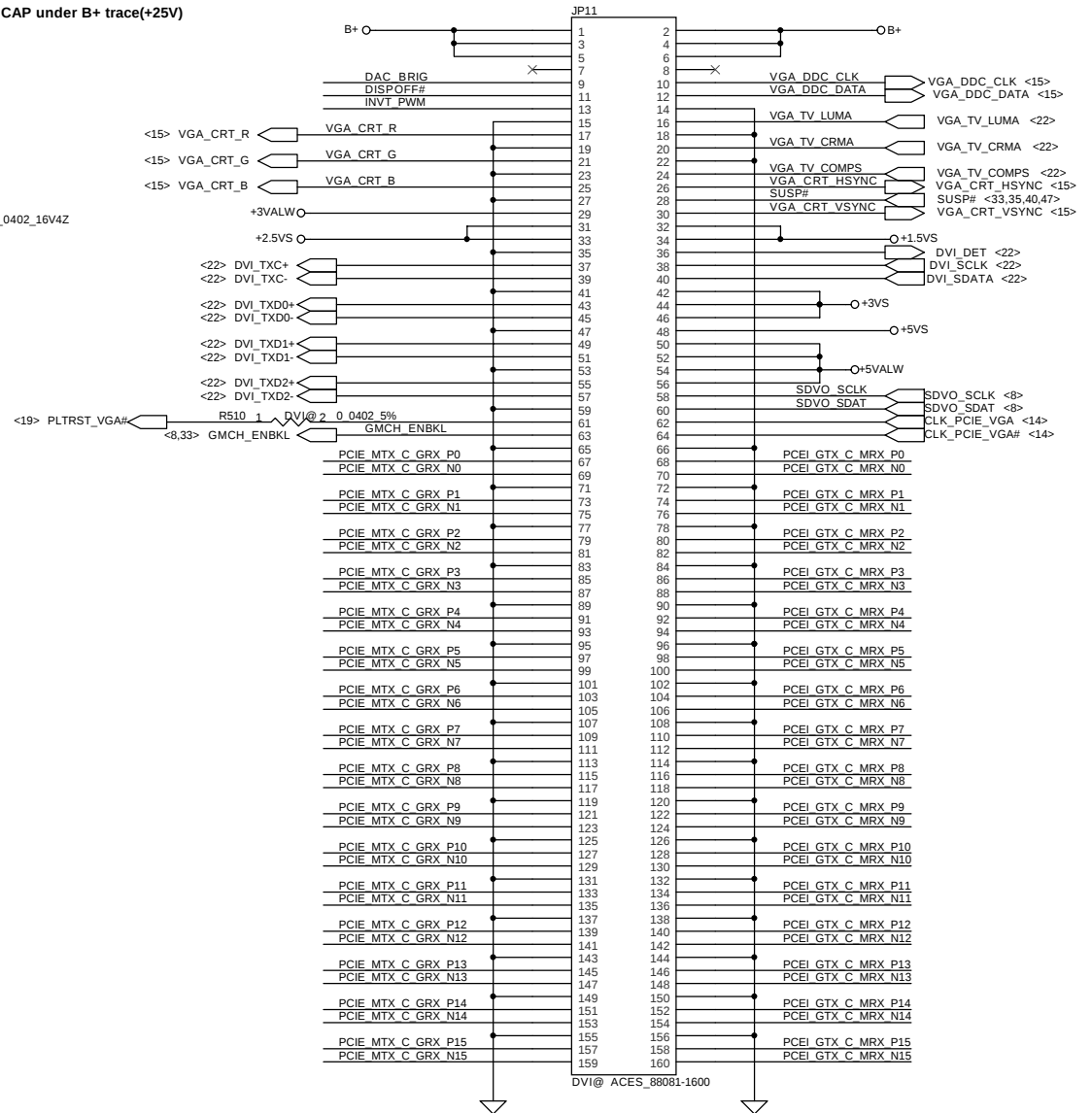
LCD/PANEL BD. Conn.



Bypass CAP under B+ trace(+25V)



VGA BOARD Conn.



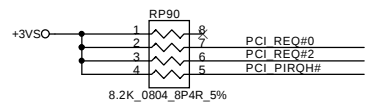
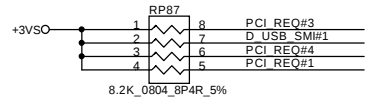
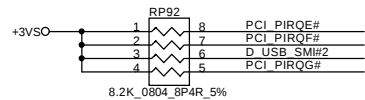
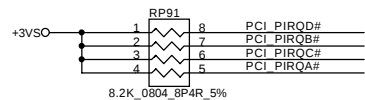
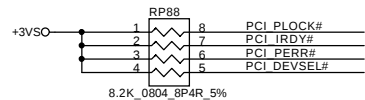
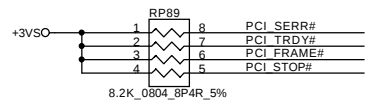
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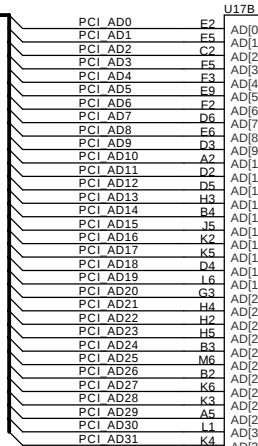


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<23,25,26,28> PCI_FRAME#

<23> PCI_PIRQA#

<23> PCI_PIRQB#



PCI_FRAME# J3

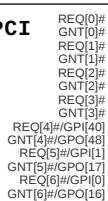
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PCI_PIRQB# I2

PCI_PIRQC# M1

PCI_PIRQD# I3

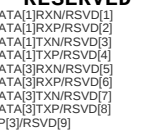
PCI



Interrupt I/F

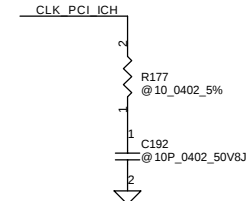
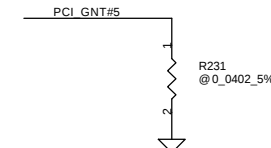


RESERVED



ICH6_BGA609

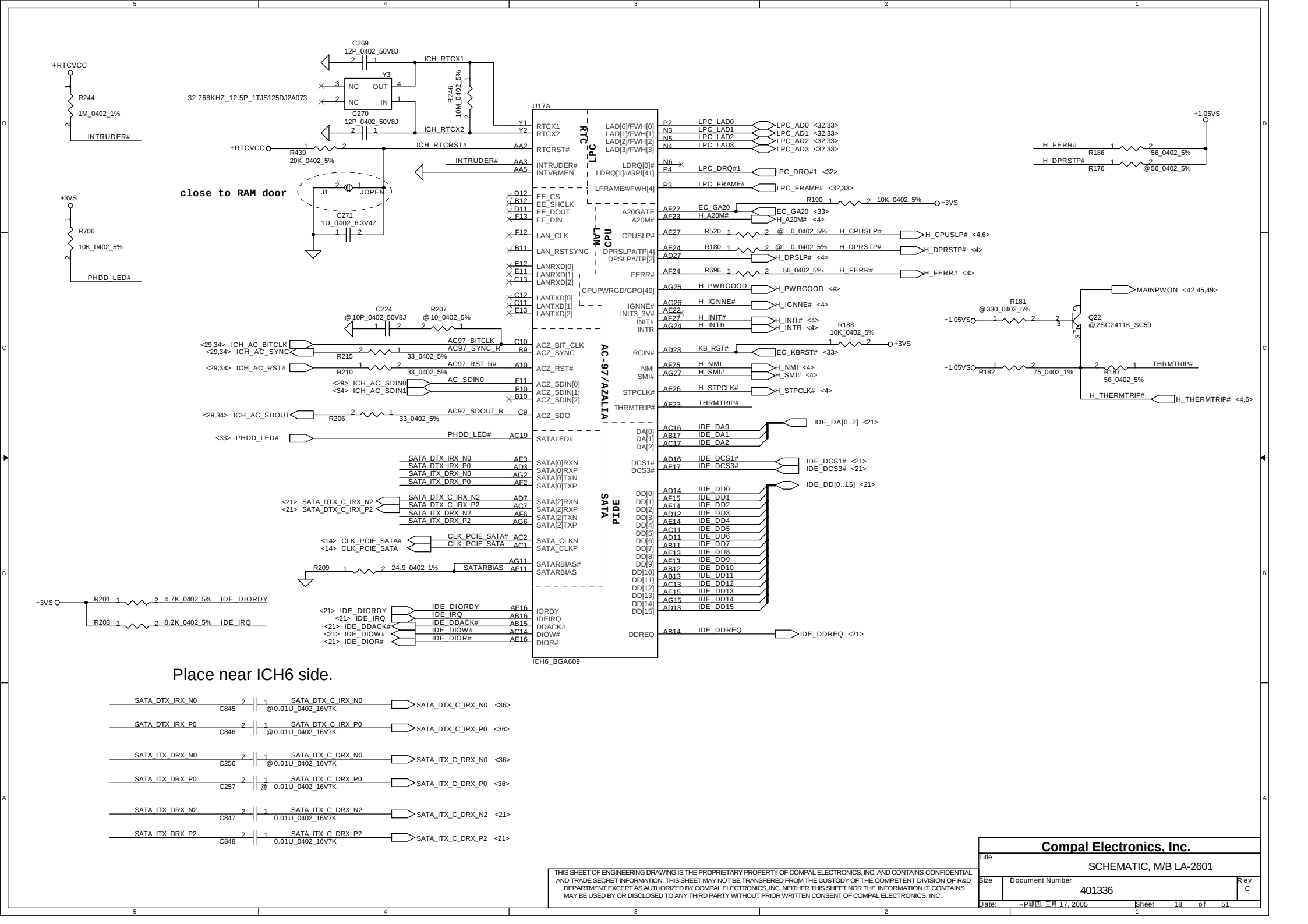
Internal Pull-up.
Sample high destination is LPC.

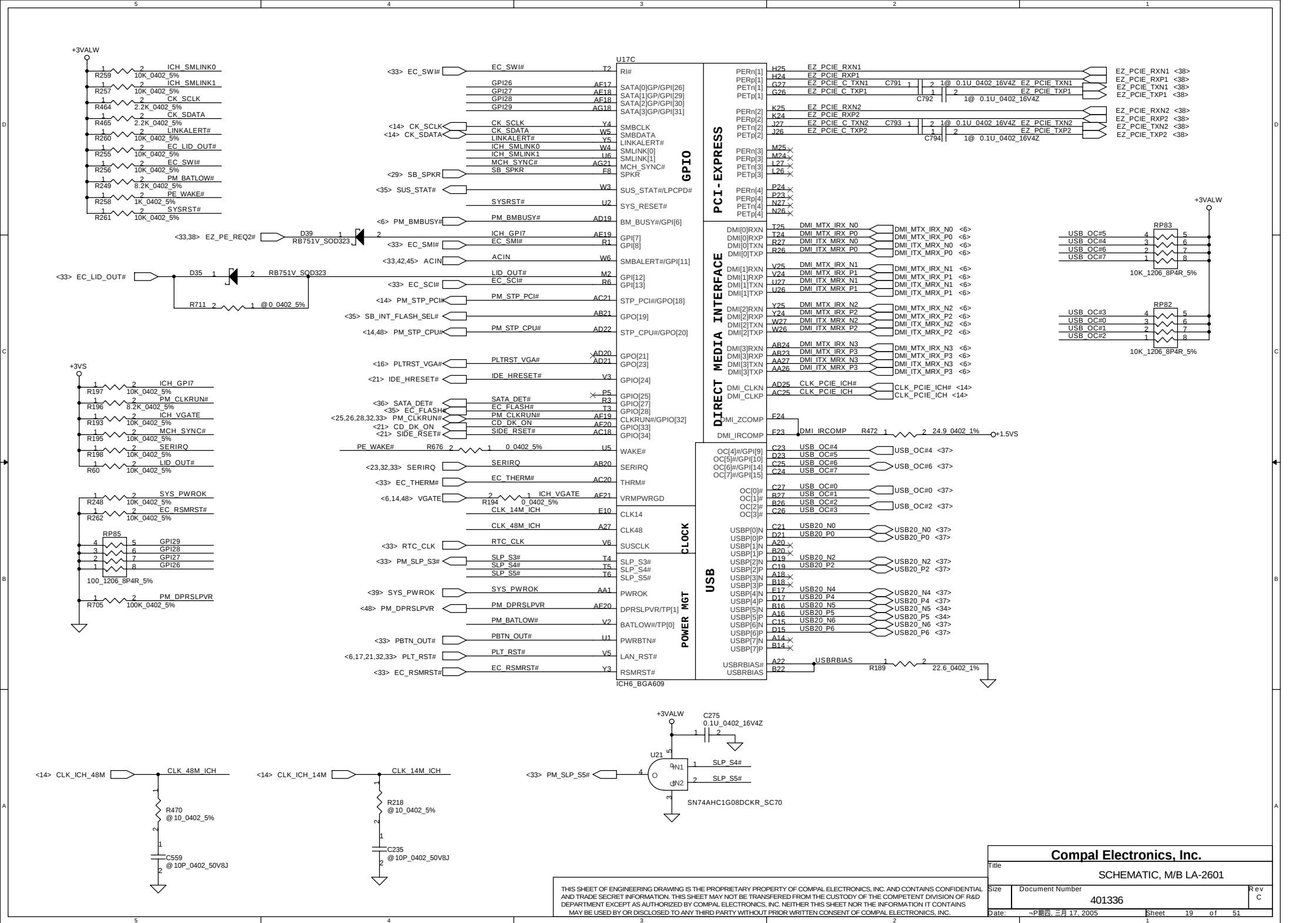


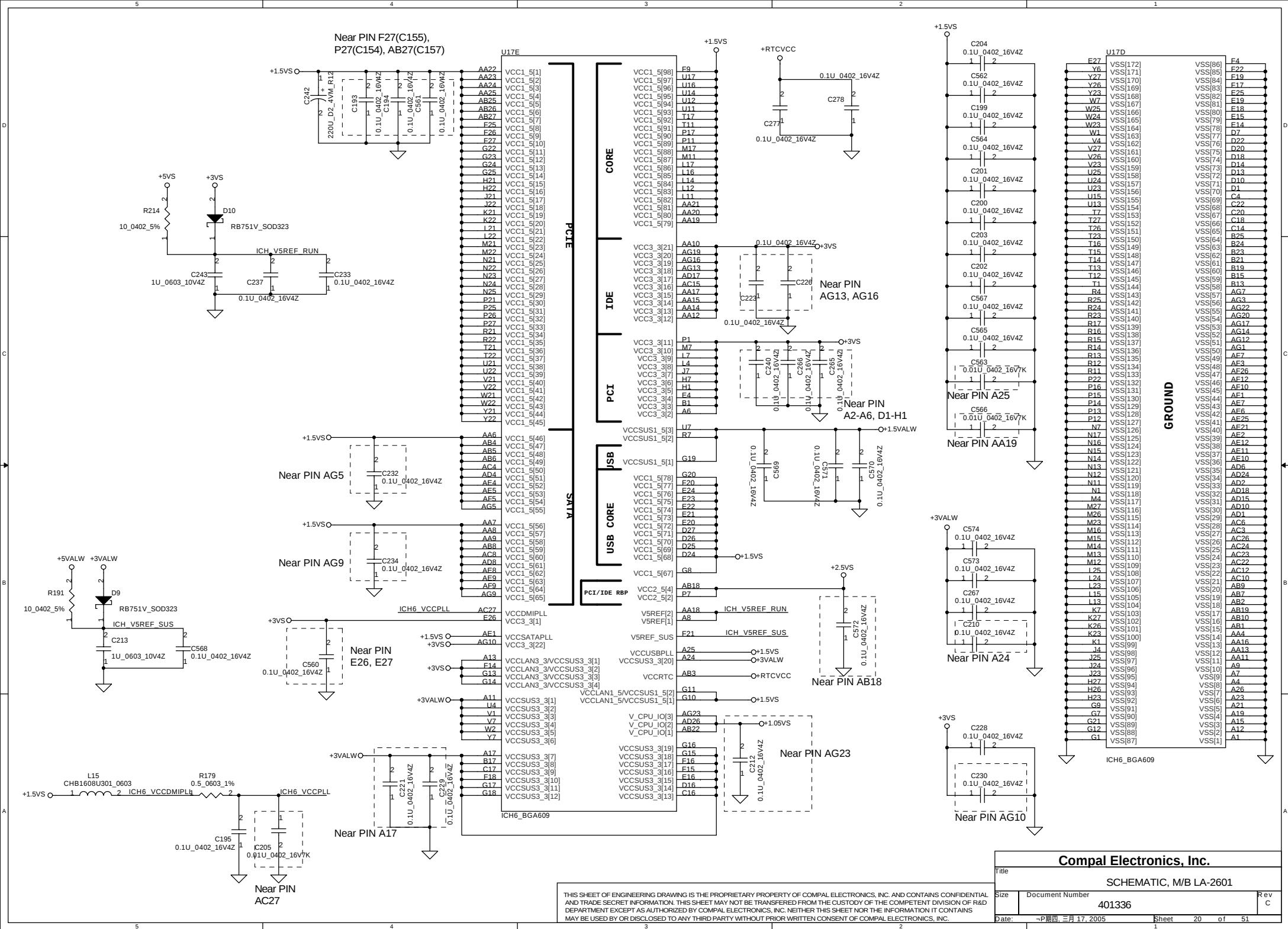
Compal Electronics, Inc.

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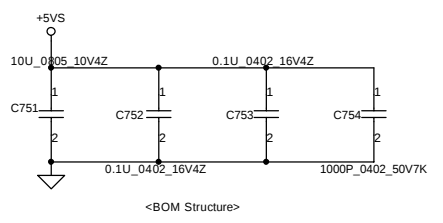
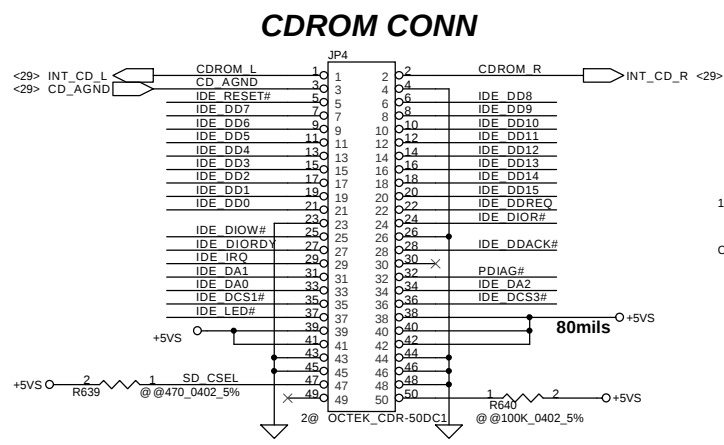
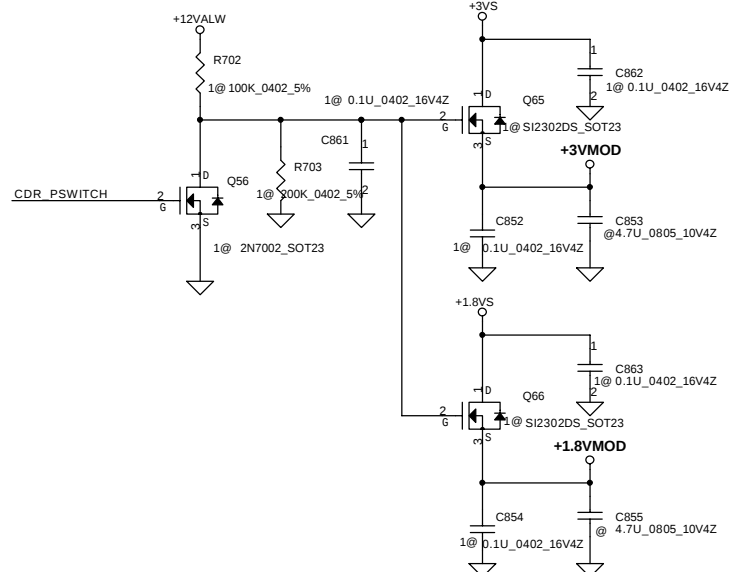
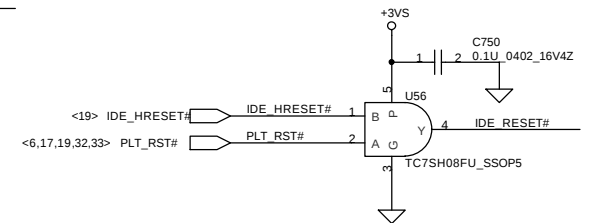
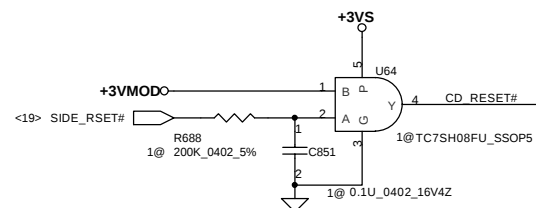
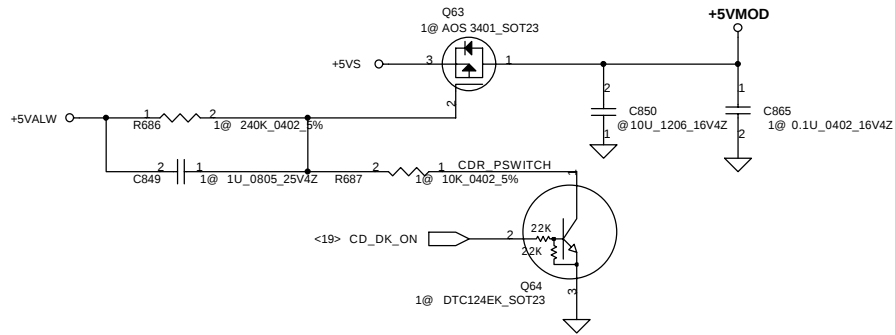
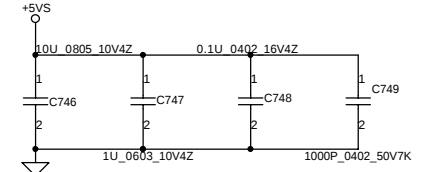
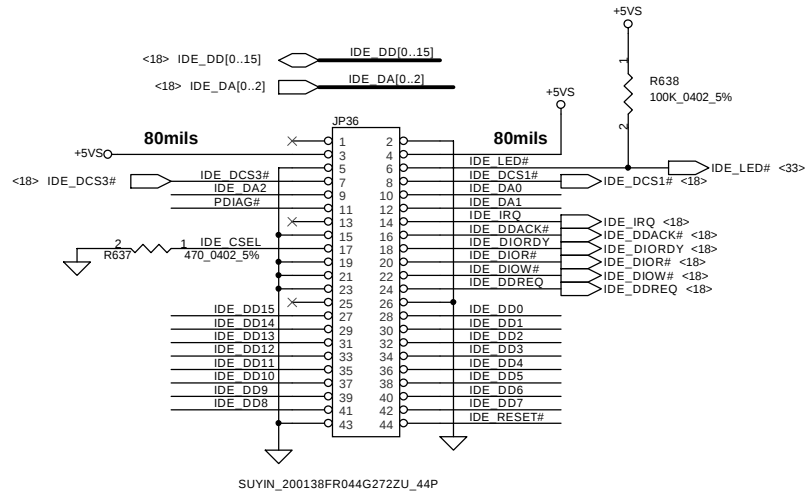
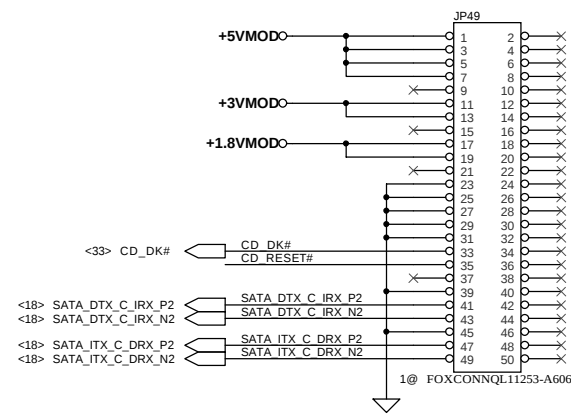
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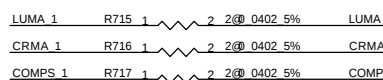
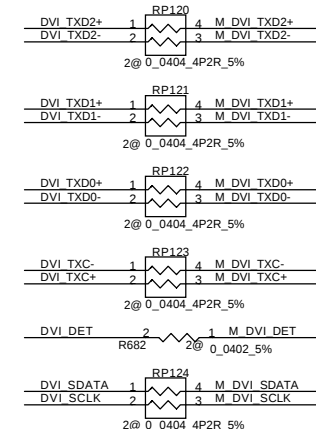
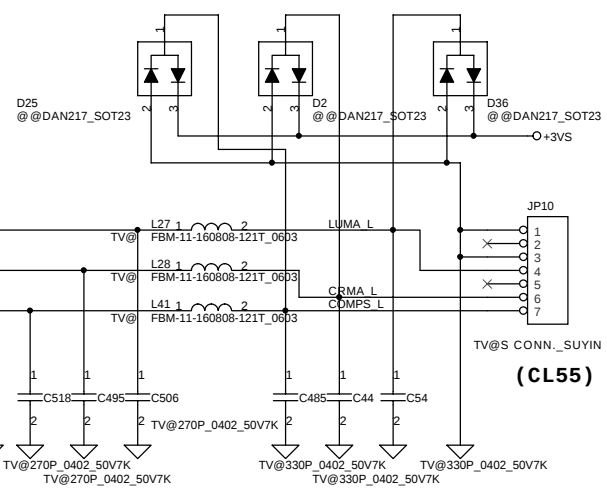
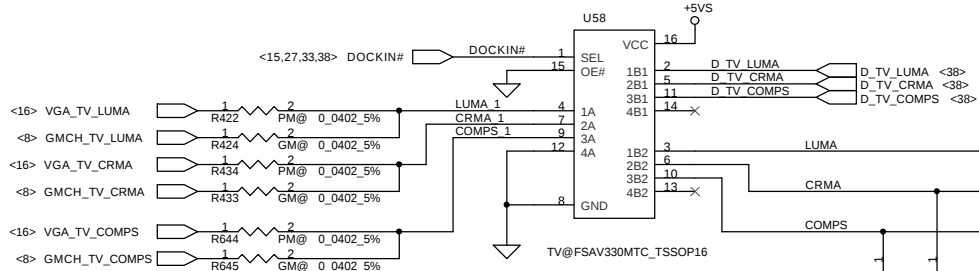
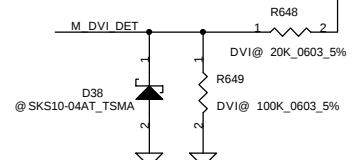
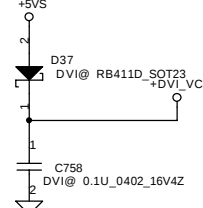
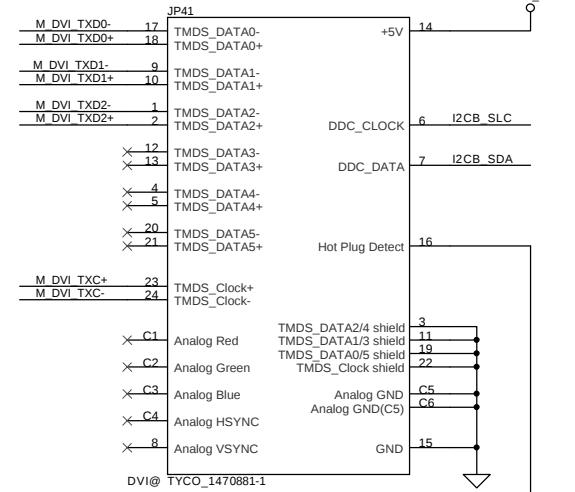
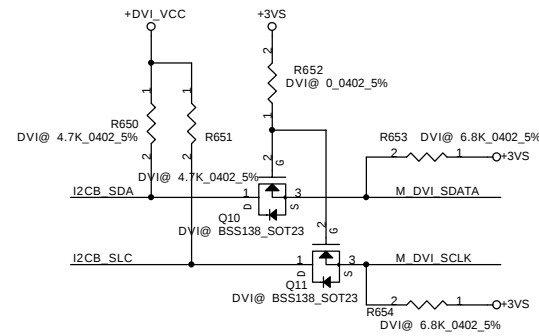
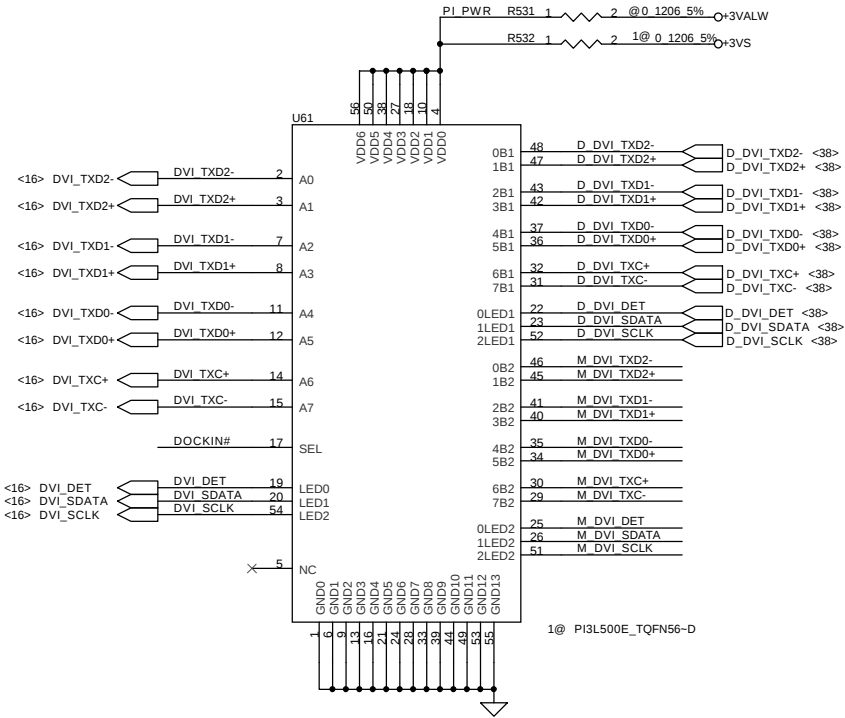
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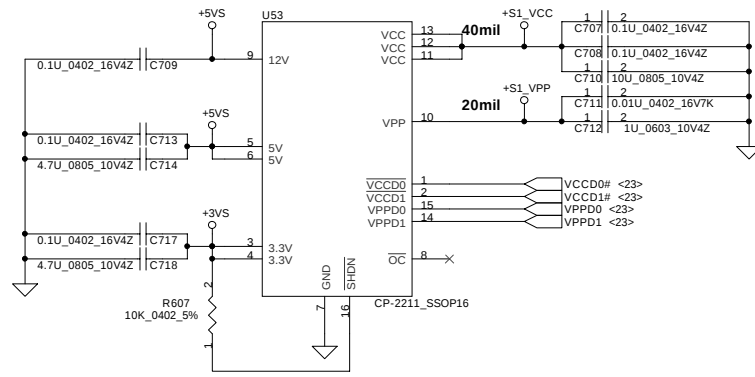
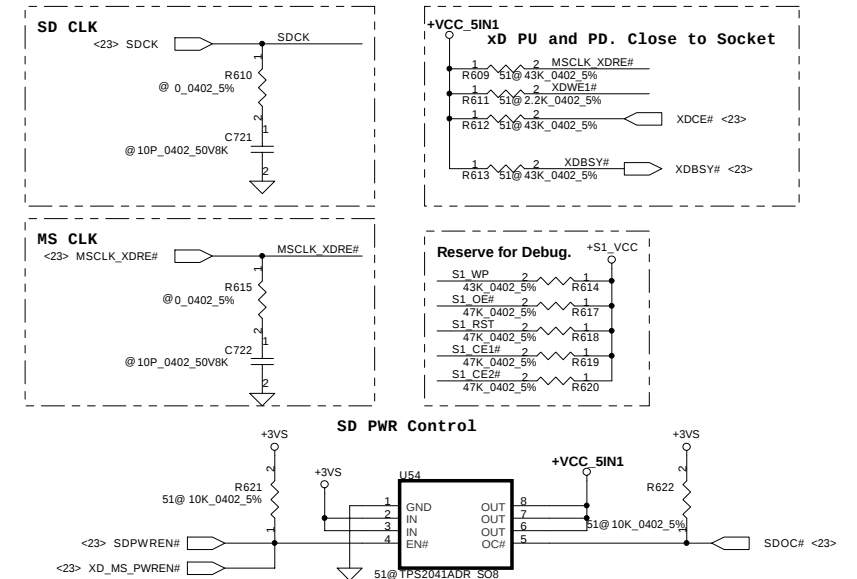
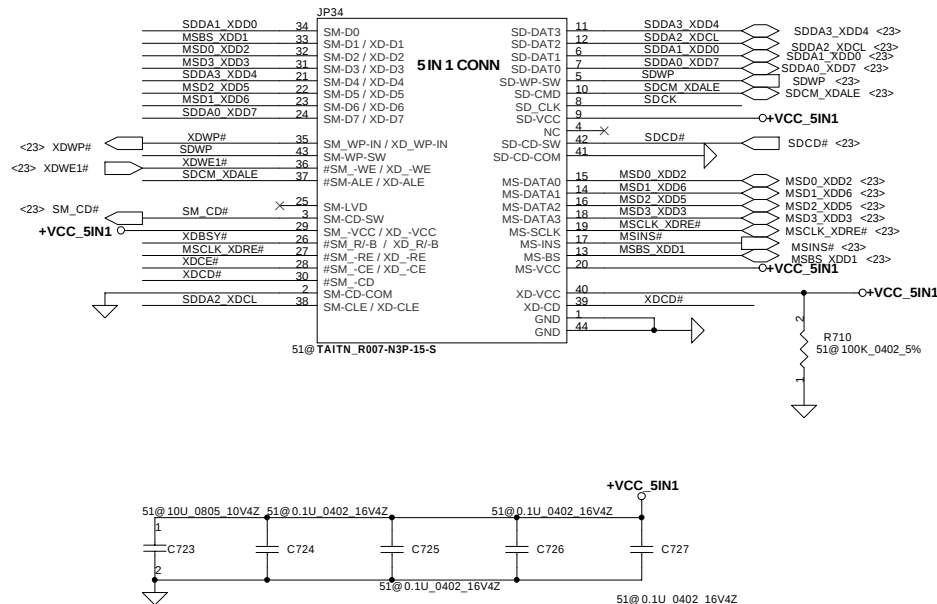
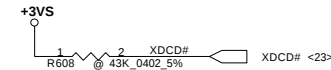
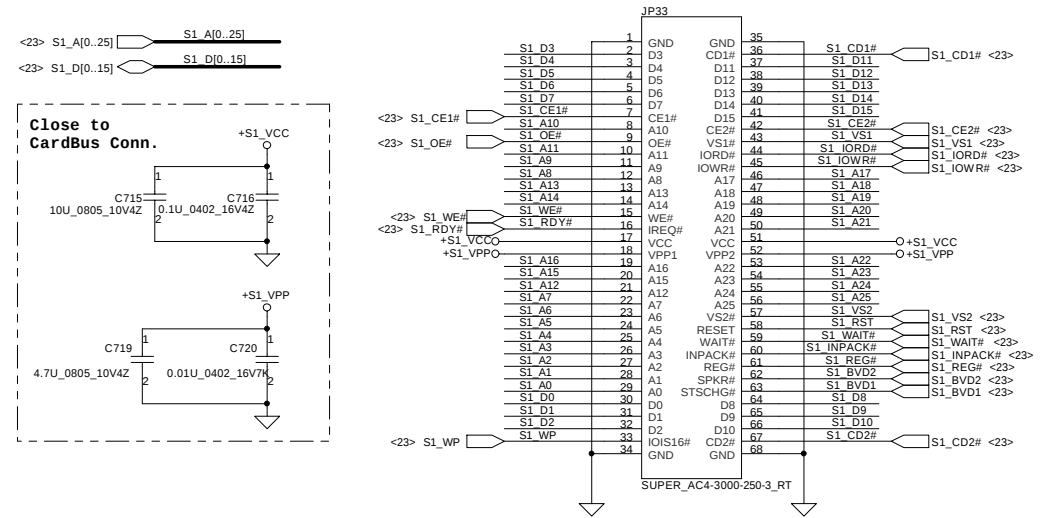
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SCHEMATIC, M/B LA-2601			
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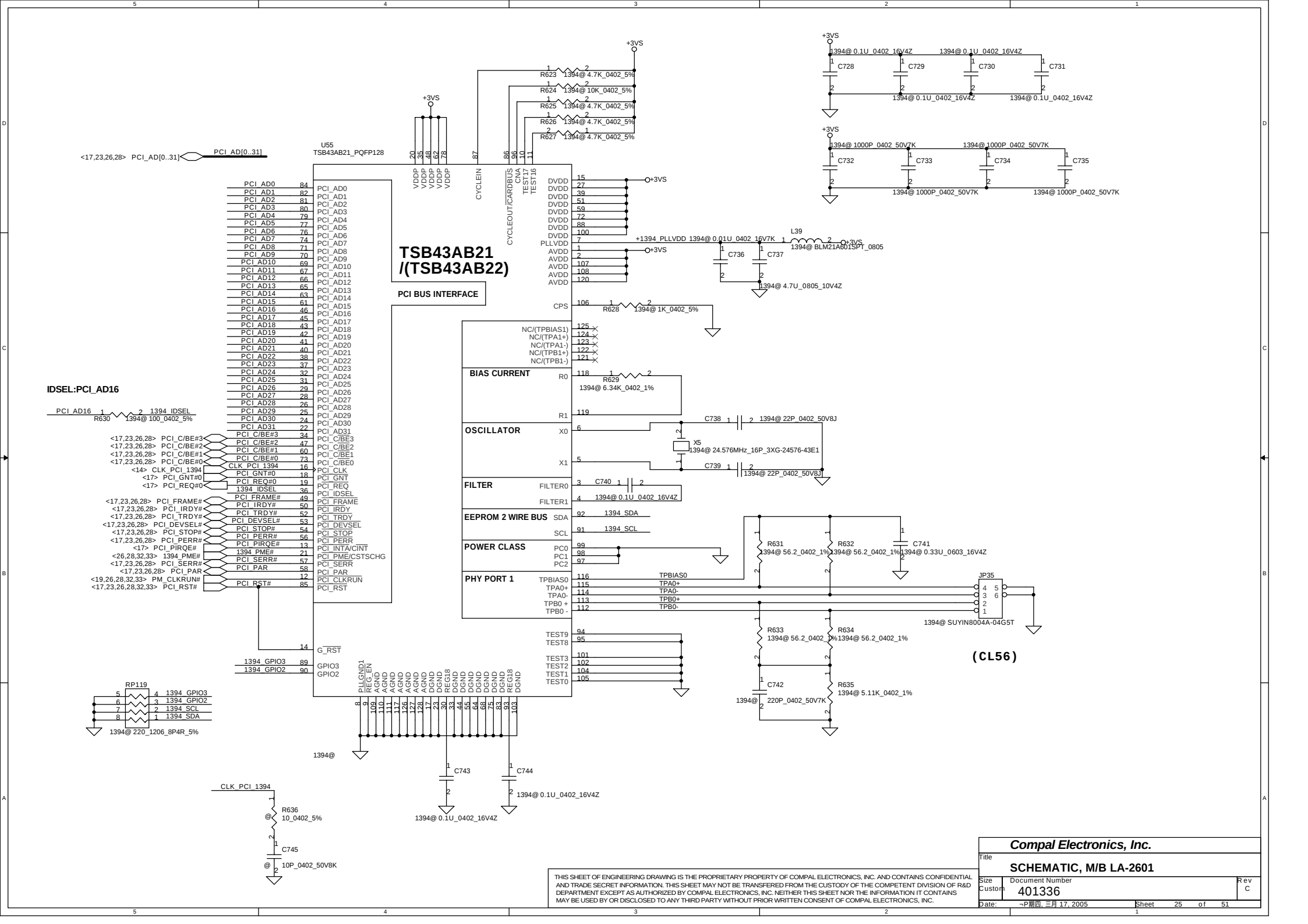


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PCMCIA Power Controller

**CardBus Socket**



TSB43AB21
(TSB43AB22)

PCI BUS INTERFACE

NC(TPBIAS1)
NC(TPA1+)
NC(TPA1-)
NC(TPB1+)
NC(TPB1-)

BIAS CURRENT

R0

1394@ 6.34K_0402_1%

R1

1394@ 22P_0402_50V8J

OSCILLATOR

X0

X1

3 C740 1 2

4 1394@ 0.1U_0402_16V4Z

FILTER

FILTER0

FILTER1

EEPROM 2 WIRE BUS

SDA

SCL

POWER CLASS

PC0

PC1

PC2

PHY PORT 1

TPBIAS0

TPA0+

TPA0-

TPB0+

TPB0-

TEST9

TEST8

TEST3

TEST2

TEST1

TEST0

PLUGND1

REG_EN

AGND

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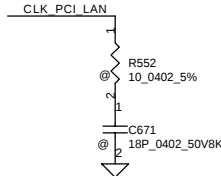
EN_WOL# = Low,
System can wake on LAN
(keep Low when Power On)

VGS(th) = -0.45V
IDmax = 2.3A

+3VS for BCM5788
+3V_LAN for BCM4401

<17,23,25,28> PCI_AD[0..31]

U49A
PCI_AD31 B8 AD31
PCI_AD30 A8 AD30
PCI_AD29 C7 AD29
PCI_AD28 C6 AD28
PCI_AD27 B6 AD27
PCI_AD26 B5 AD26
PCI_AD25 A5 AD25
PCI_AD24 B4 AD24
PCI_AD23 B2 AD23
PCI_AD22 B1 AD22
PCI_AD21 C1 AD21
PCI_AD20 D3 AD20
PCI_AD19 D2 AD19
PCI_AD18 D1 AD18
PCI_AD17 E3 AD17
PCI_AD16 E2 AD16
PCI_AD15 L2 AD15
PCI_AD14 L1 AD14
PCI_AD13 M3 AD13
PCI_AD12 M2 AD12
PCI_AD11 M1 AD11
PCI_AD10 N2 AD10
PCI_AD9 N3 AD9
PCI_AD8 P3 AD8
PCI_AD7 N4 AD7
PCI_AD6 P4 AD6
PCI_AD5 M5 AD5
PCI_AD4 P5 AD4
PCI_AD3 P6 AD3
PCI_AD2 P7 AD2
PCI_AD1 M7 AD1
PCI_AD0 N7 AD0

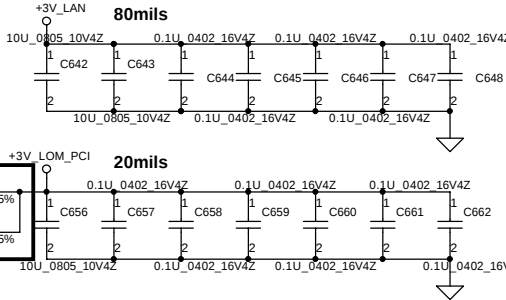
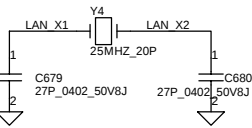


<17,23,25,28> PCI_C/BE#3 C4 CBE3
<17,23,25,28> PCI_C/BE#2 F3 CBE2
<17,23,25,28> PCI_C/BE#1 I3 CBE1
<17,23,25,28> PCI_C/BE#0 M4 CBE0

100_0402_5%
PCI_AD17 R555 1 2 LAN_IDSEL A4 IDSEL
<17,23,25,28> PCI_FRAME# F2 FRAME
<17,23,25,28> PCI_IRDY# F1 IRDY
<17,23,25,28> PCI_TRDY# G3 TRDY
<17,23,25,28> PCI_DEVSEL# H3 DEVSEL
<17,23,25,28> PCI_STOP# J2 STOP
<17,23,25,28> PCI_PERR# A2 PERR
<17,23,25,28> PCI_SERR# J1 SERR
<17,23,25,28> PCI_PAR PAR
<14> CLK_PCI_LAN A3 CLK_CLK

<17> PCI_PIRQ# H2 INTA
<17,23,25,28,32,33> PCI_RST# C2 PCI_RST
<17> PCI_GNT# J3 GNT
<17> PCI_REQ# C3 REQ

+3V_LAN 1 R559 1K_0402_5% LAN_AUXPWR J12 VAUXPRSNT
<25,28,32,33> ONBD_LAN_PME# A4 M66EN(NC_F4) PME



TRD3+/(NC_E13)
TRD3-/(NC_E14)
TRD2+/(NC_D13)
TRD2-/(NC_D14)
TRD1+/(RD0P)
TRD1-/(RD0N)
TRD0+/(TD0P)
TRD0-/(TD0N)

REGSUP12/(NC_B9)
REGCTL12/(NC_B10)
REGSEN12/(REG18OUT)
REGSUP25/(REGSUP18)
REGCTL25/(NC_C11)
REGSEN25/(REGSUP18)

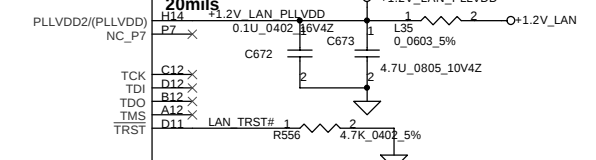
B11
C11 LAN_CTRL_2.5V
C10
VESD1
VESD2
VESD3

EEDATA/(SPROM_CS)
EECLK/(SPROM_CLK)

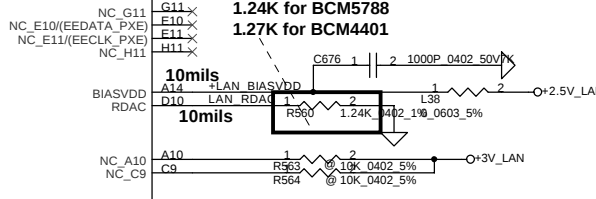
GPIO0/(NC_H12)
GPIO1/(NC_K13)
GPIO2/(NC_J13)

BCM5788M
/(BCM4401)

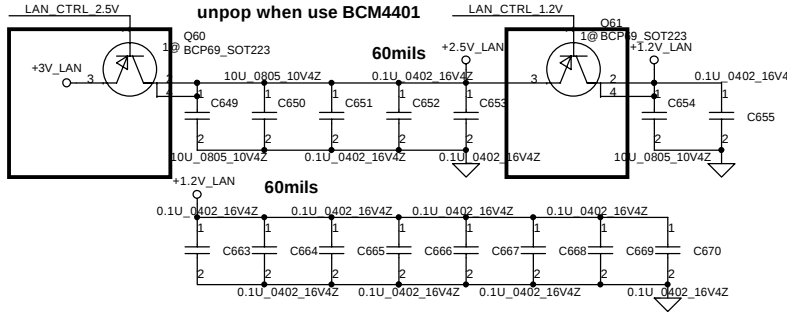
LINKLED/(LINKLED10)
SPD100LED/(LINKLED100)
SPD100LED/(COL_LED)
TRAFFICLED/(ACT_LED)



XTALVDD
XTALO
XTALI
NC_G11
NC_E10/(EEDATA_PXE)
NC_E11/(EECLK_PXE)
NC_H11



NC_A10
NC_C9

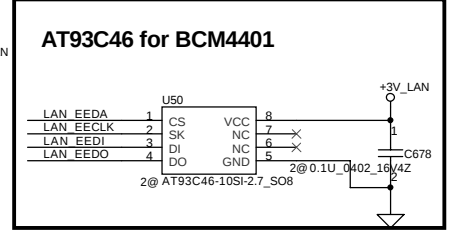


U49B
VDDC_E12
VDDC_H5
VDDC_H6
VDDC_H7
VDDC_H8
VDDC_J5
VDDC_J6
VDDC_J7
VDDC_J8
VDDC_J9
VDDC_J10
VDDC_K5
VDDC_K6
VDDC_K7
VDDC_K8
VDDC_K9
VDDC_K10
VDDC_L5
VDDC_L10
VDDC_M14
VDDC_N14
VDDC_P8
VDDC_P13
VDDC_P14
VSS_B7
VSS_D4
VSS_D5
VSS_D6
VSS_D7
VSS_D8
VSS_D9
VSS_E2
VSS_E5
VSS_E6
VSS_E7
VSS_E8
VSS_E9
VSS_F5
VSS_F6
VSS_F7
VSS_F8
VSS_F9
VSS_F10
VSS_G4
VSS_G5
VSS_G6
VSS_G7
VSS_G8
VSS_G9
VSS_G10
VSS_H9
VSS_K2
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VSS_M12
VSS_M13
VSS_N1
VSS_N12
VSS_N13

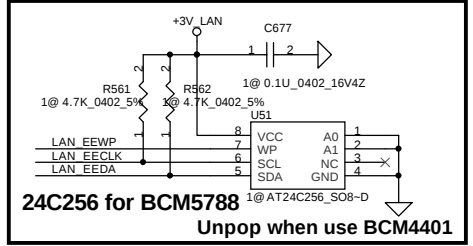
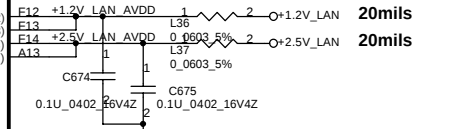
BCM5788M
/(BCM4401)

VDDIO-PCI_A7
VDDIO-PCI_B3
VDDIO-PCI_C5
VDDIO-PCI_E1
VDDIO-PCI_E4
VDDIO-PCI_G1
VDDIO-PCI_K3
VDDIO-PCI_L4
VDDIO-PCI_N6
VDDIO-PCI_P2
VDDP_K14/(NC_K14)VDDL_F12/(AVDD_F12)
VDDP_L13/(NC_L13)VDDL_F13/(AVDD_F13)
VDDP_P11/(NC_P11) AVDD_F14/(NC_F14)
AVDD_A13/(NC_A13)
VDDIO_A11
VDDIO_F11
VDDIO_K12
VDDIO_L12
NC_C8
CLKRUN
NC_H10
NC_J4
NC_K4
NC_J11/(GPIO_11)LOW_POWER/(TESTMODE)
NC_K11/(GPIO_0)
NC_L8
NC_L9
NC_L11/(VSS_L11)
NC_L14/(VSS_L14)
NC_M8
NC_M9/(VREF)
NC_N8/(EXT_POR)
NC_N9/(DOUT)
NC_P9/(DIN)

NC_C8
CLKRUN
NC_H10
NC_J4
NC_K4
NC_J11/(GPIO_11)LOW_POWER/(TESTMODE)
NC_K11/(GPIO_0)
NC_L8
NC_L9
NC_L11/(VSS_L11)
NC_L14/(VSS_L14)
NC_M8
NC_M9/(VREF)
NC_N8/(EXT_POR)
NC_N9/(DOUT)
NC_P9/(DIN)

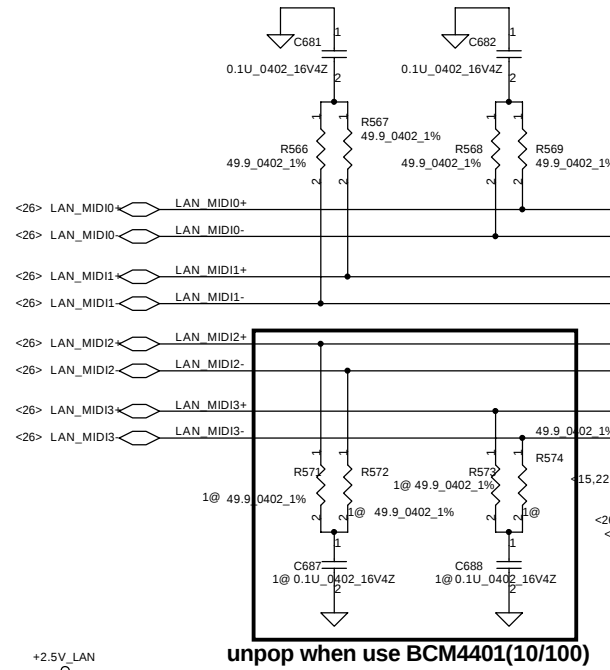
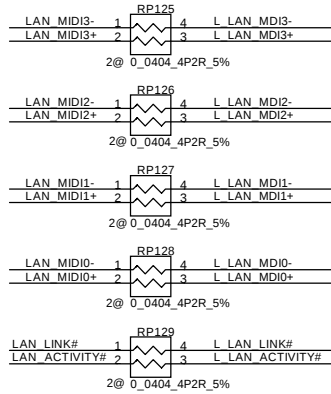


	5788	4401
L33		Pop
L34	Pop	
Q61	Pop	
Q60	Pop	
R560	1.24K	1.27K
U50		Pop
C678		Pop
U51	Pop	
R561	Pop	
C677	Pop	
R562	Pop	

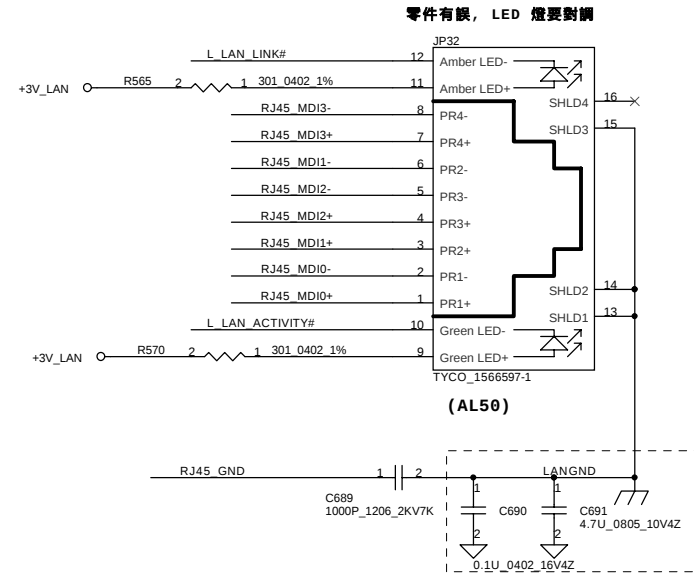
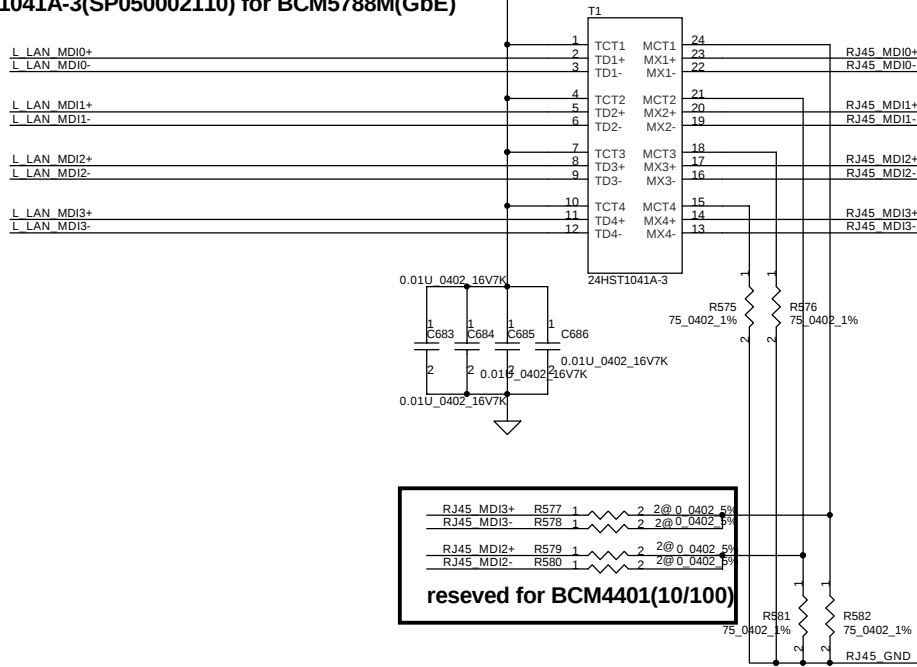


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24ST0023-3(SP050004200) for BCM4401(10/100)
24HST1041A-3(SP050002110) for BCM5788M(GbE)

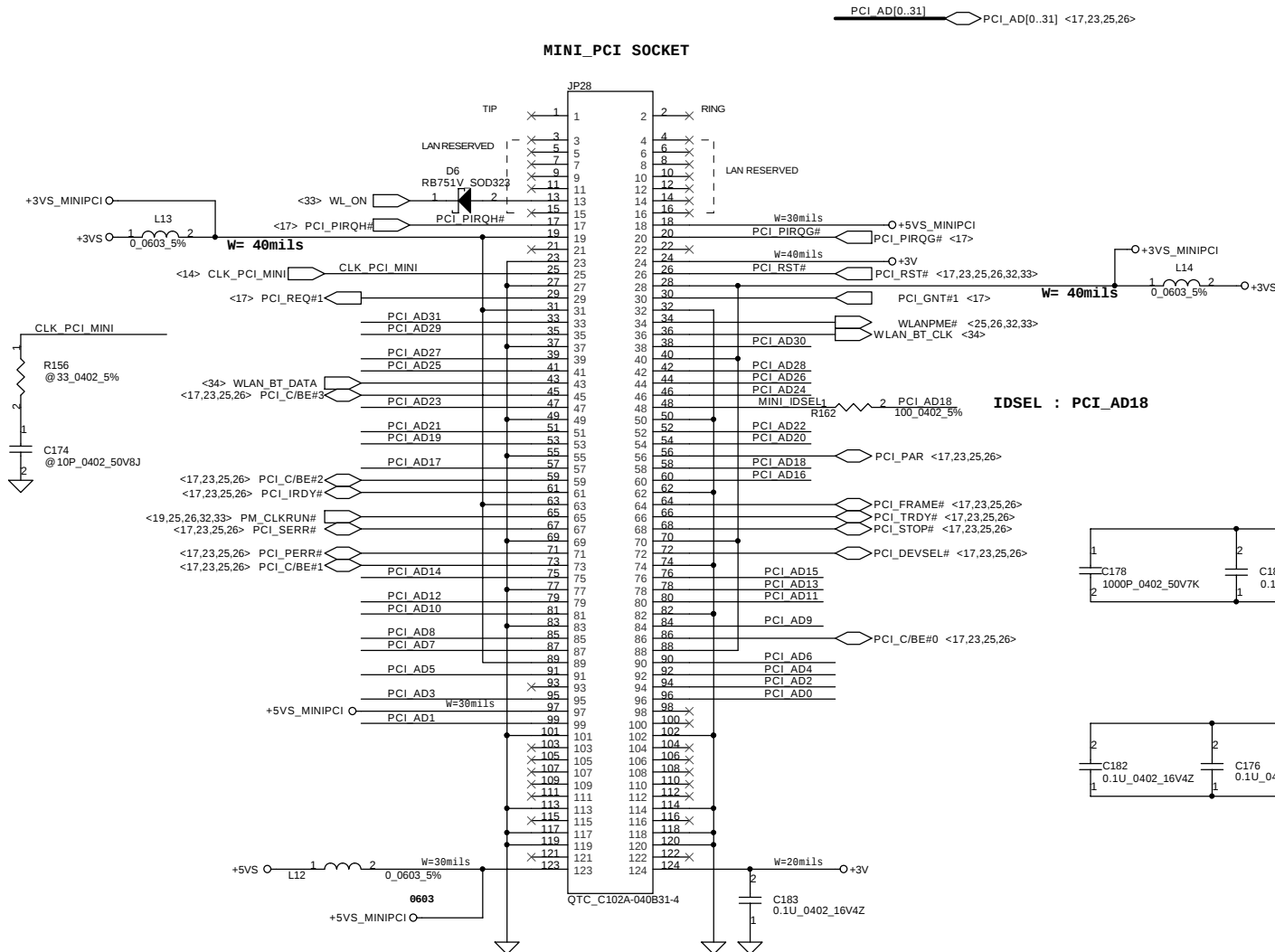


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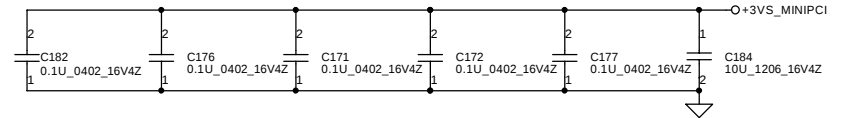
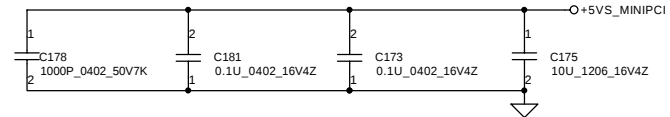
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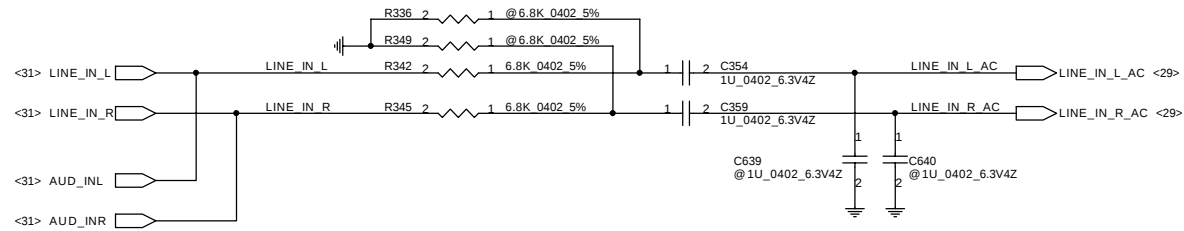
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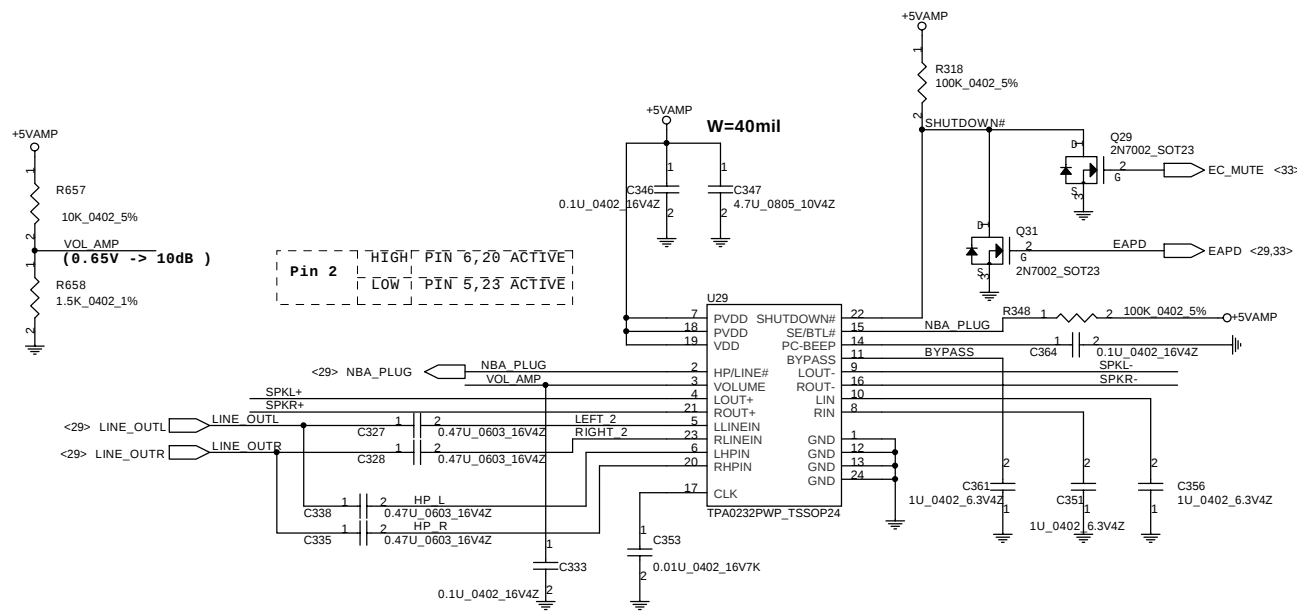
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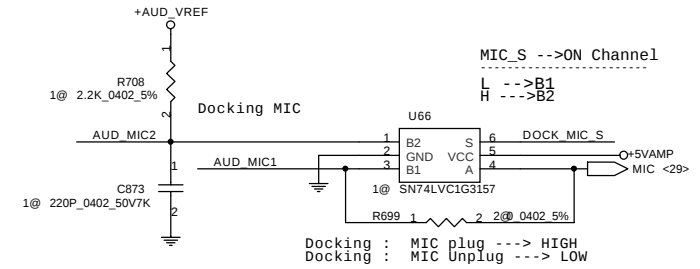
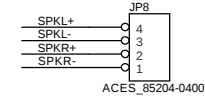


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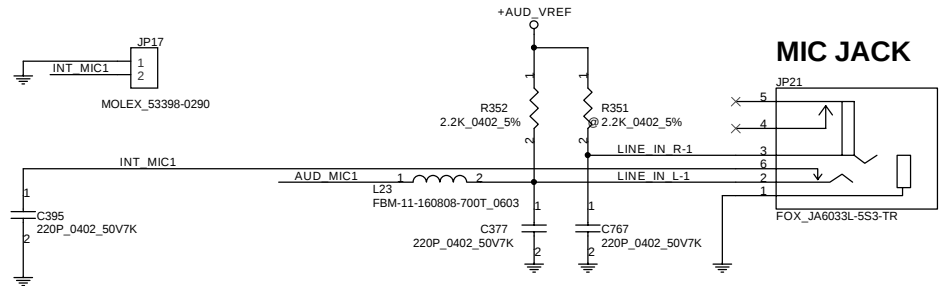
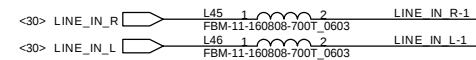
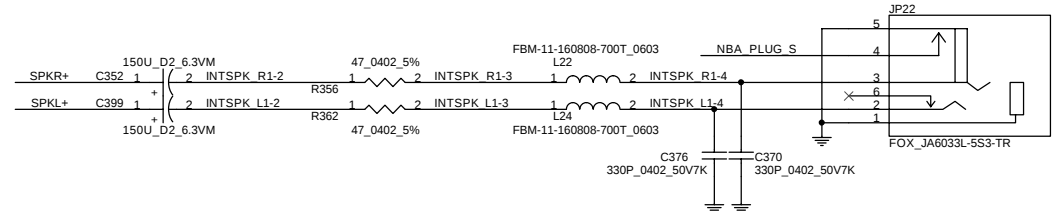
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Speaker Conn.



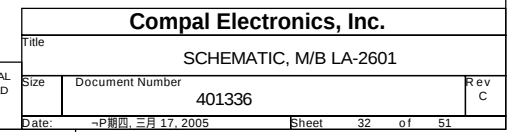
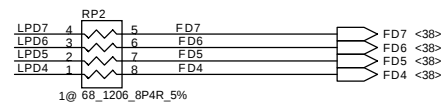
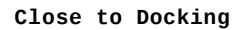
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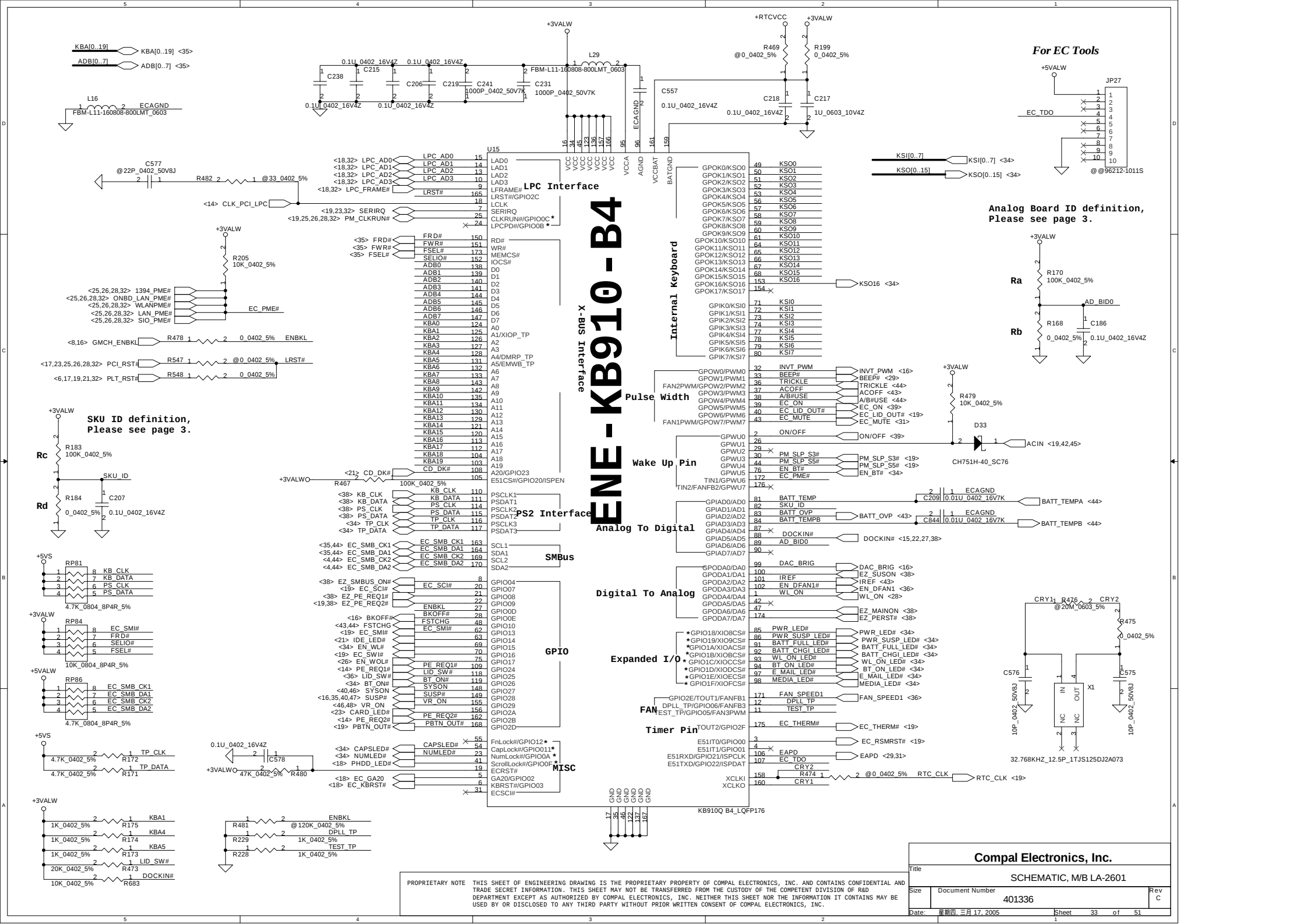
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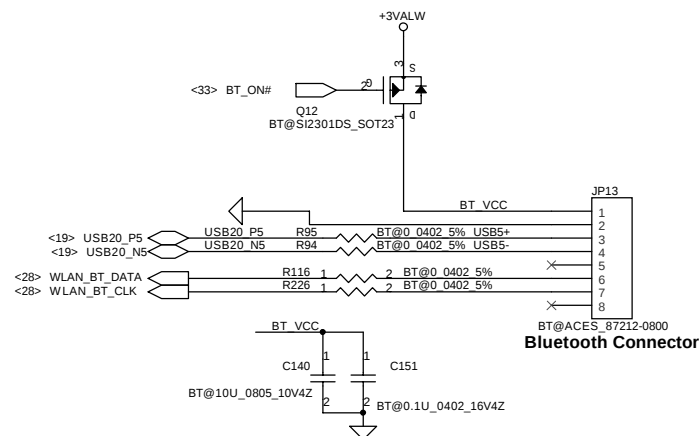
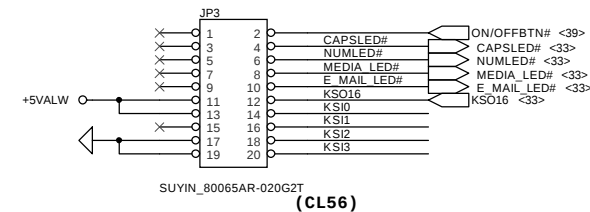
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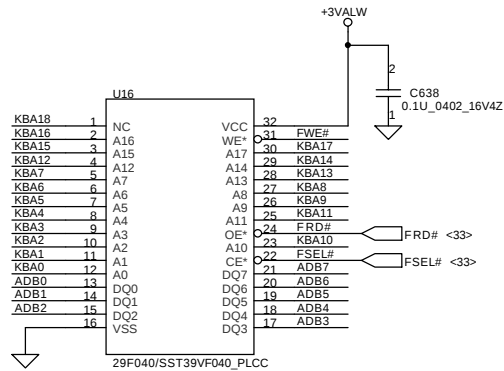




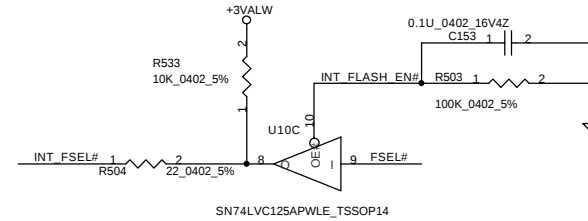
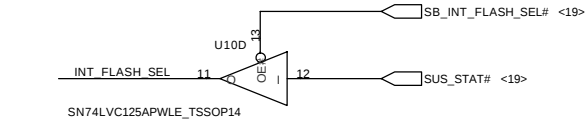
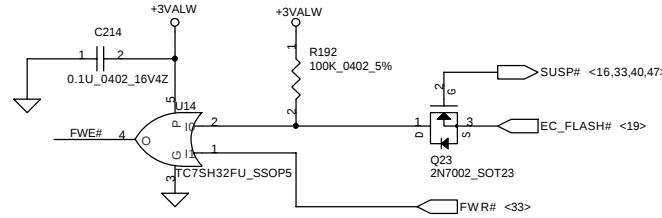
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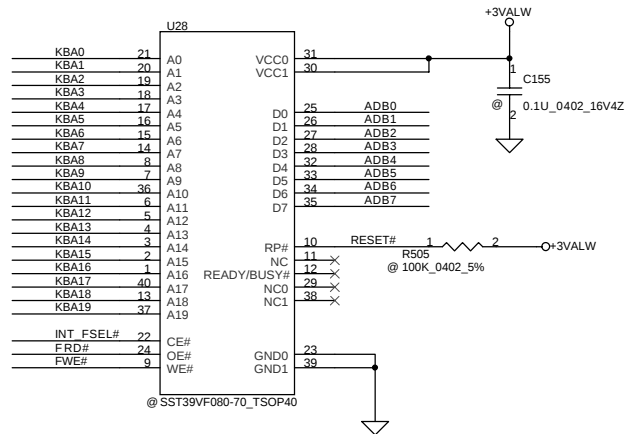
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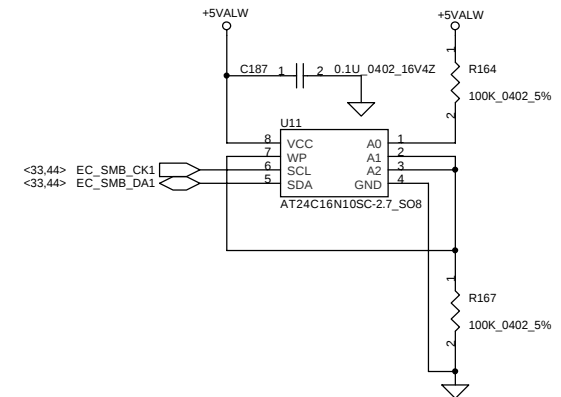
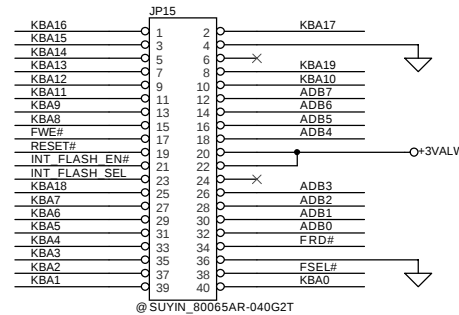
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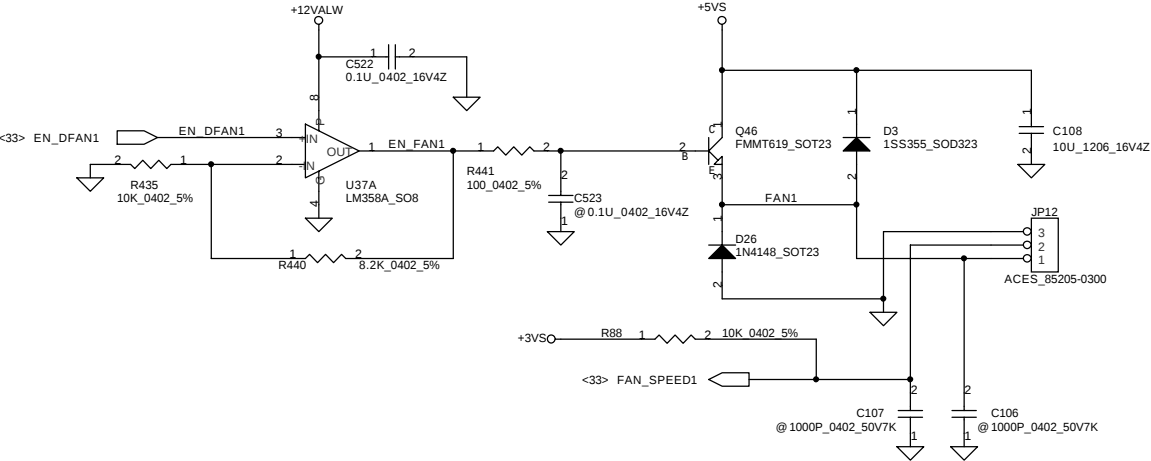
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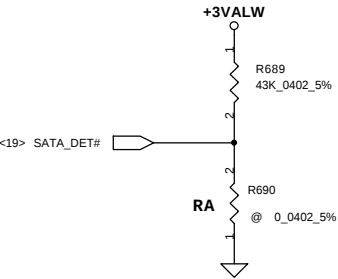
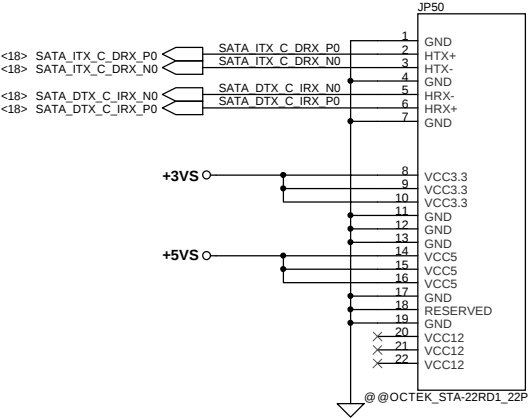
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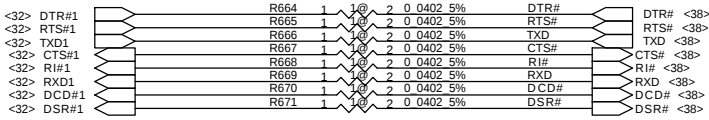


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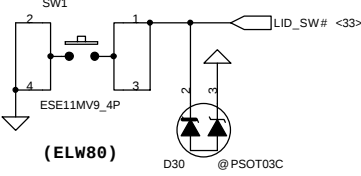
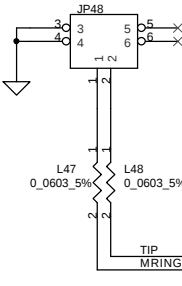


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Presence	POP
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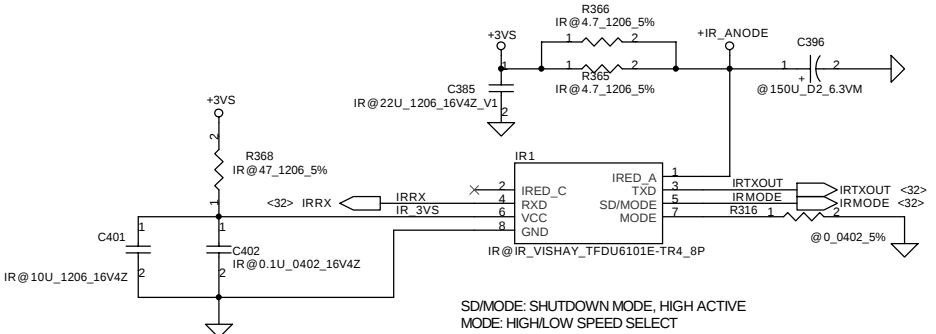
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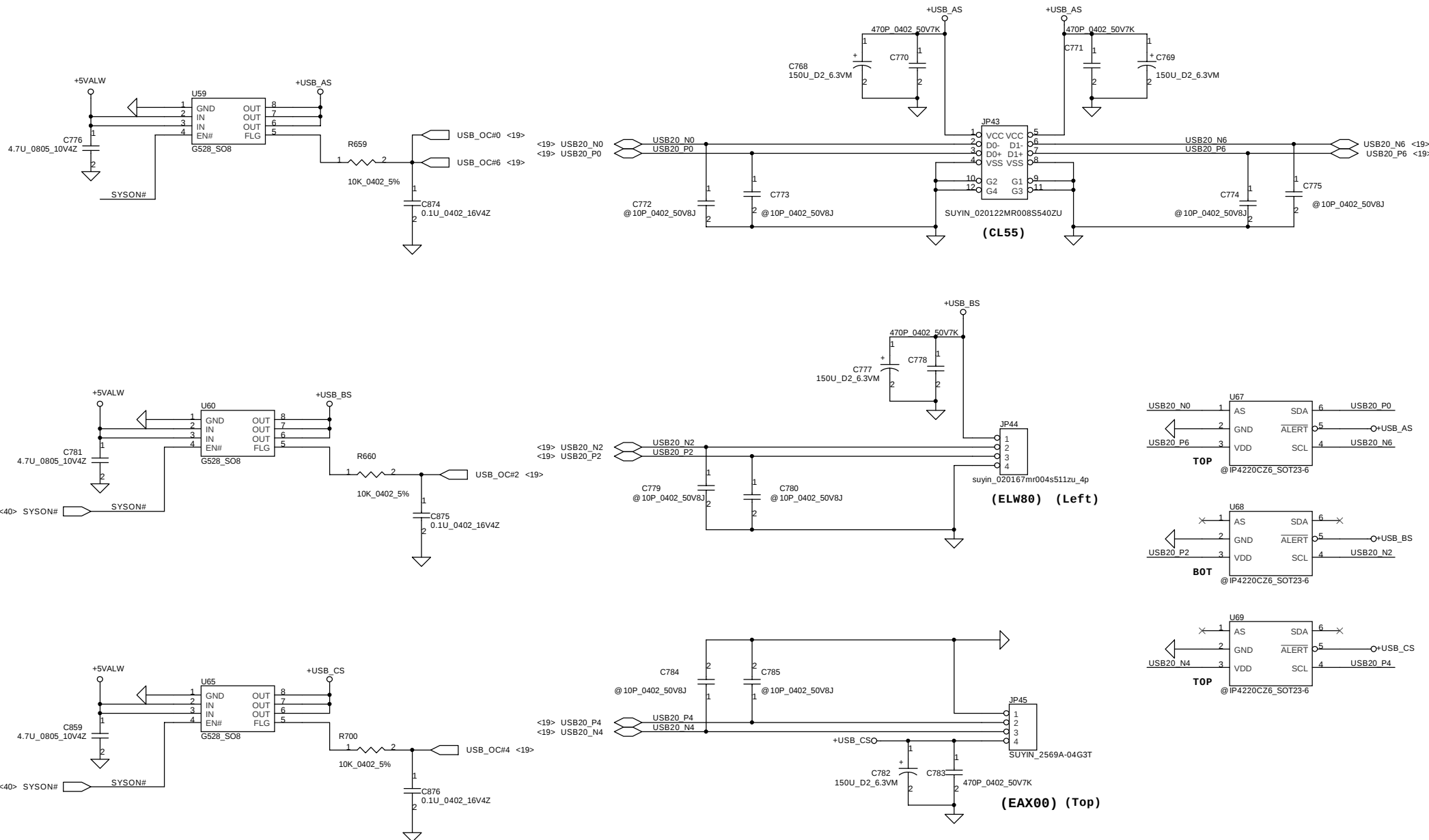


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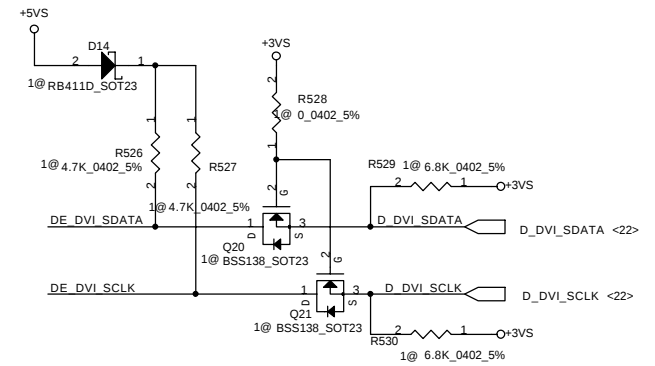
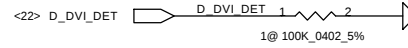
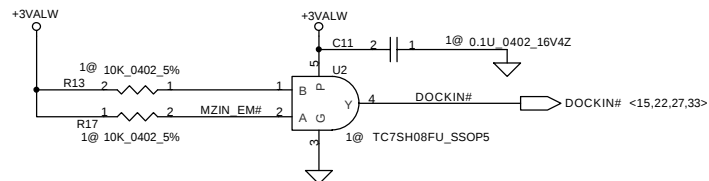
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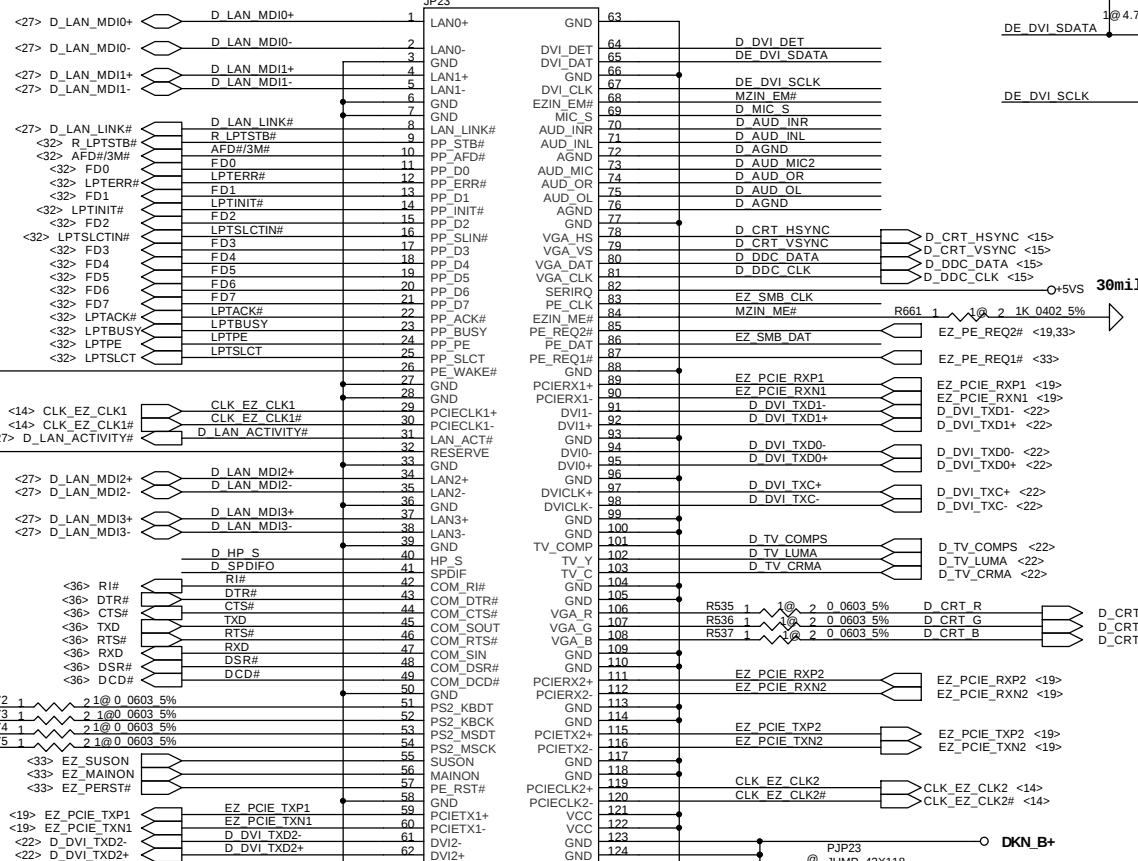


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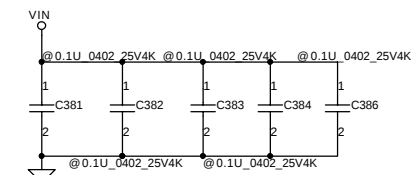
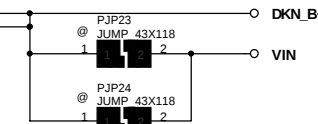
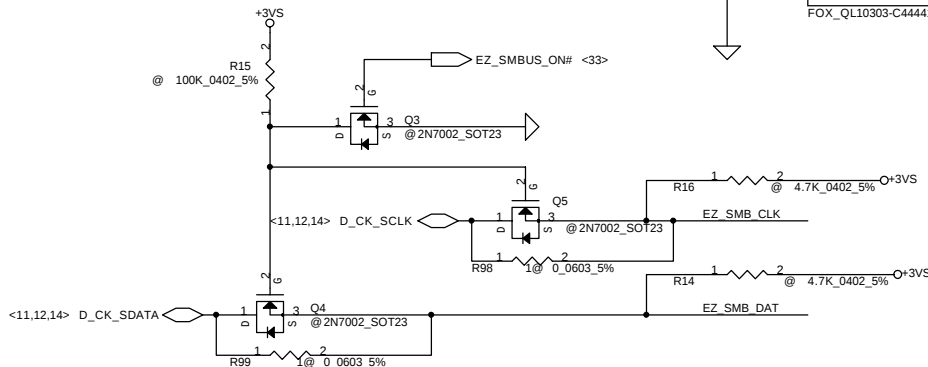


Docking Conn.



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D_AUD_INL	2
D_AGNL	3
D_AUD_MIC2	4
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D_MIC_S	9
D_SPDIFO	10

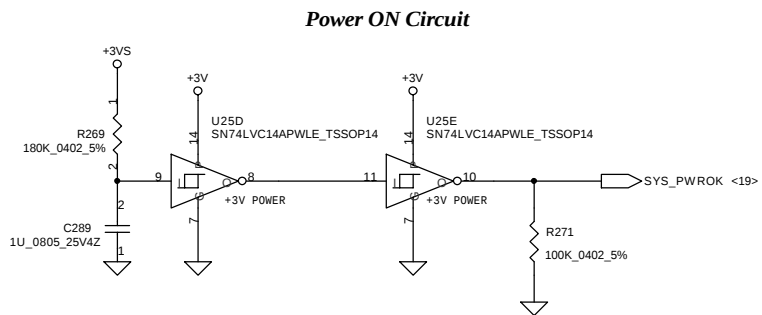
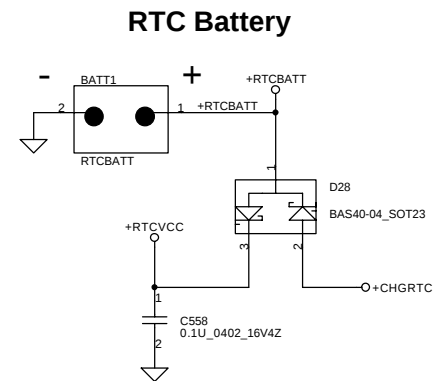
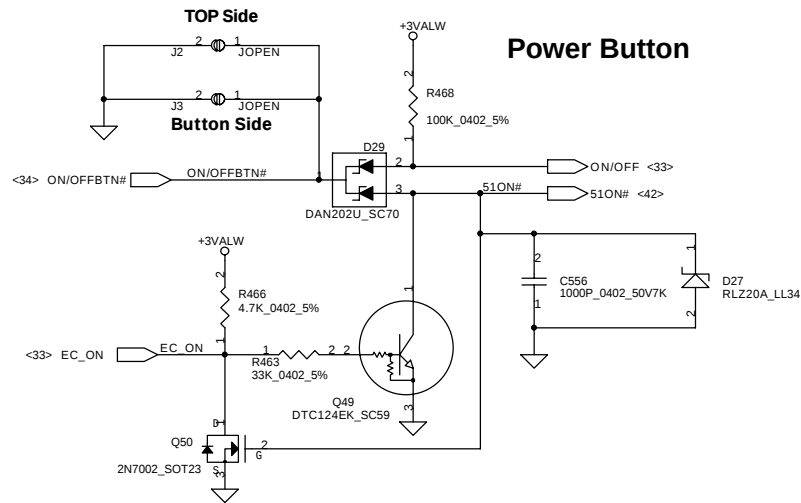
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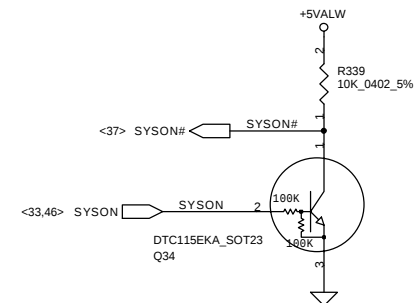
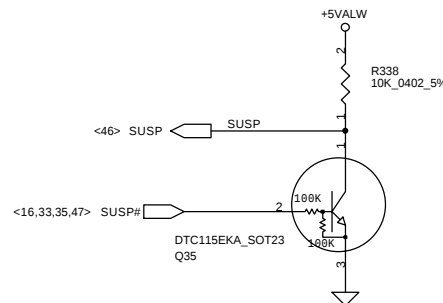
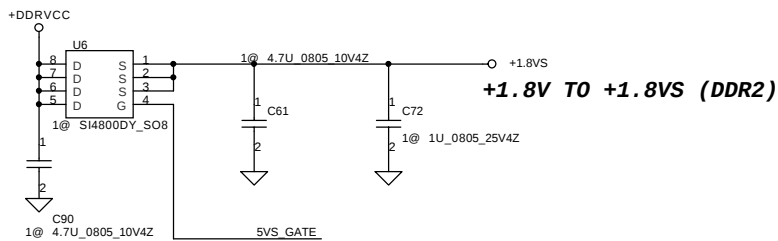
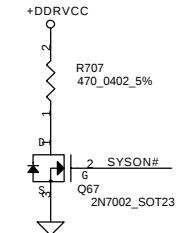
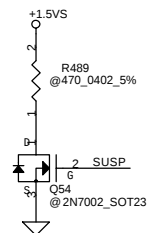
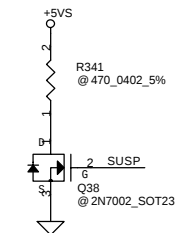
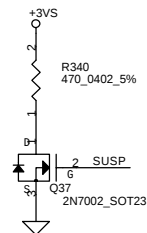
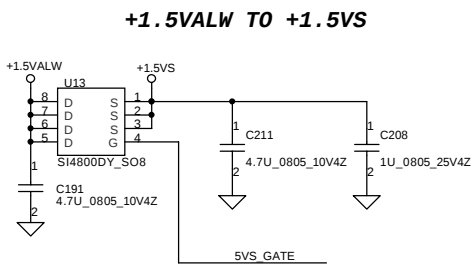
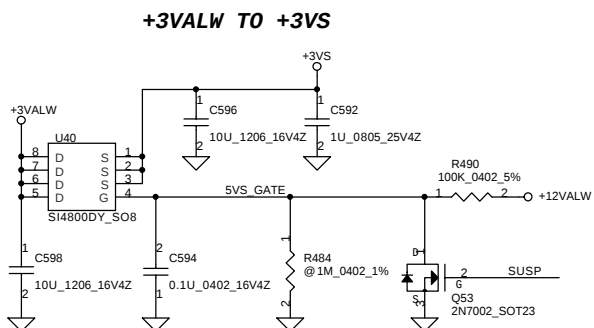
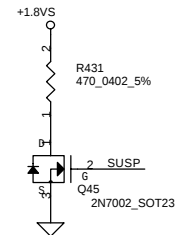
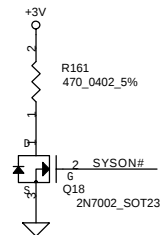
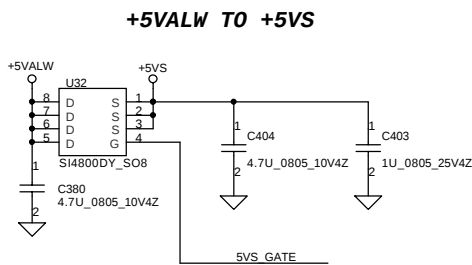
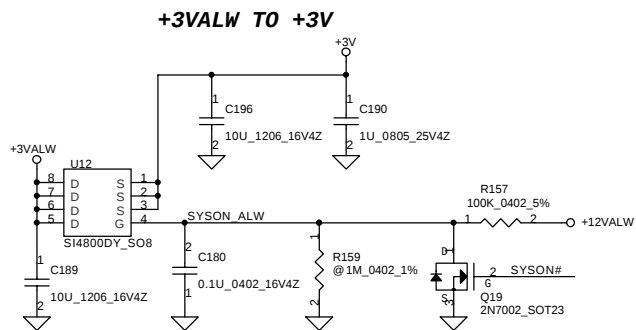
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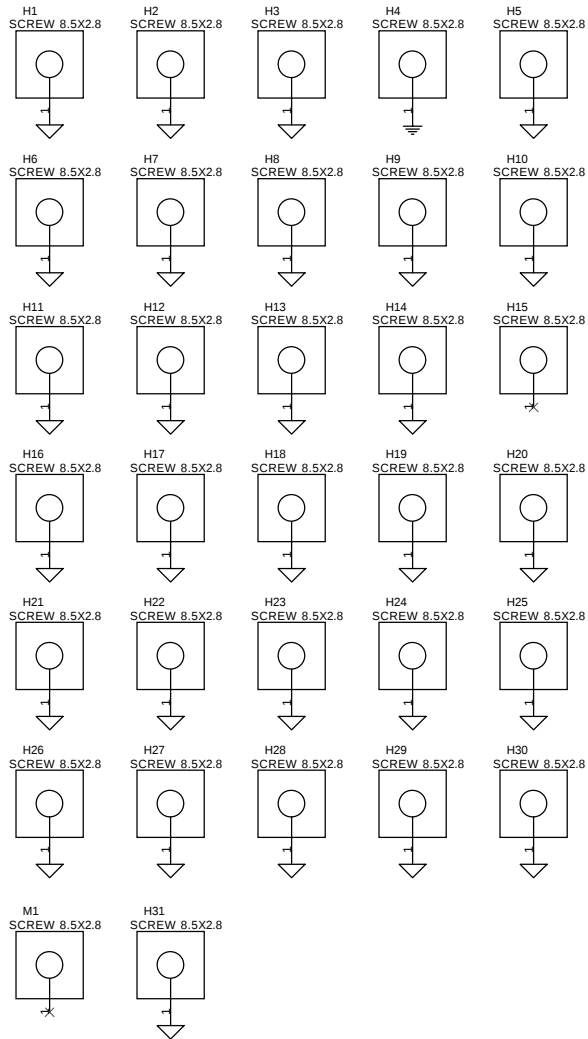
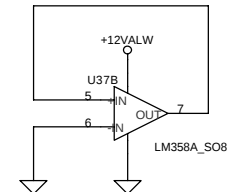
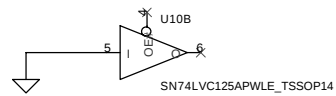
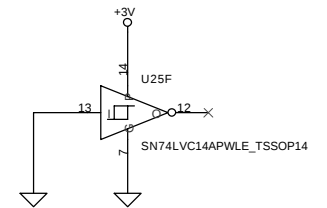
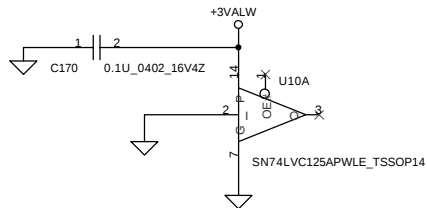
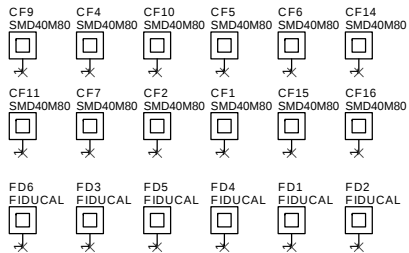
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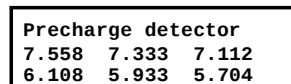
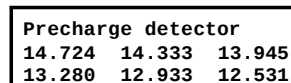
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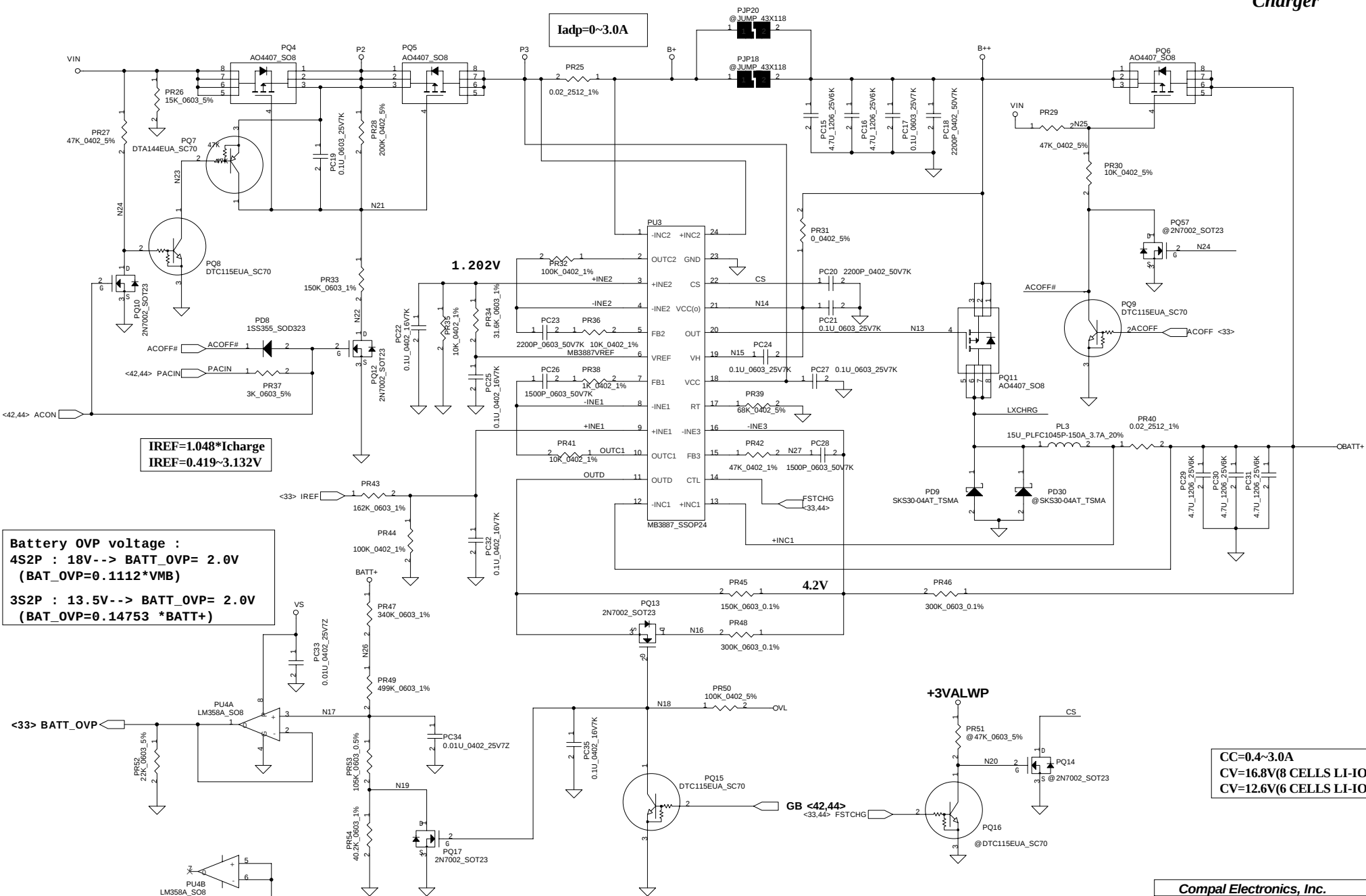
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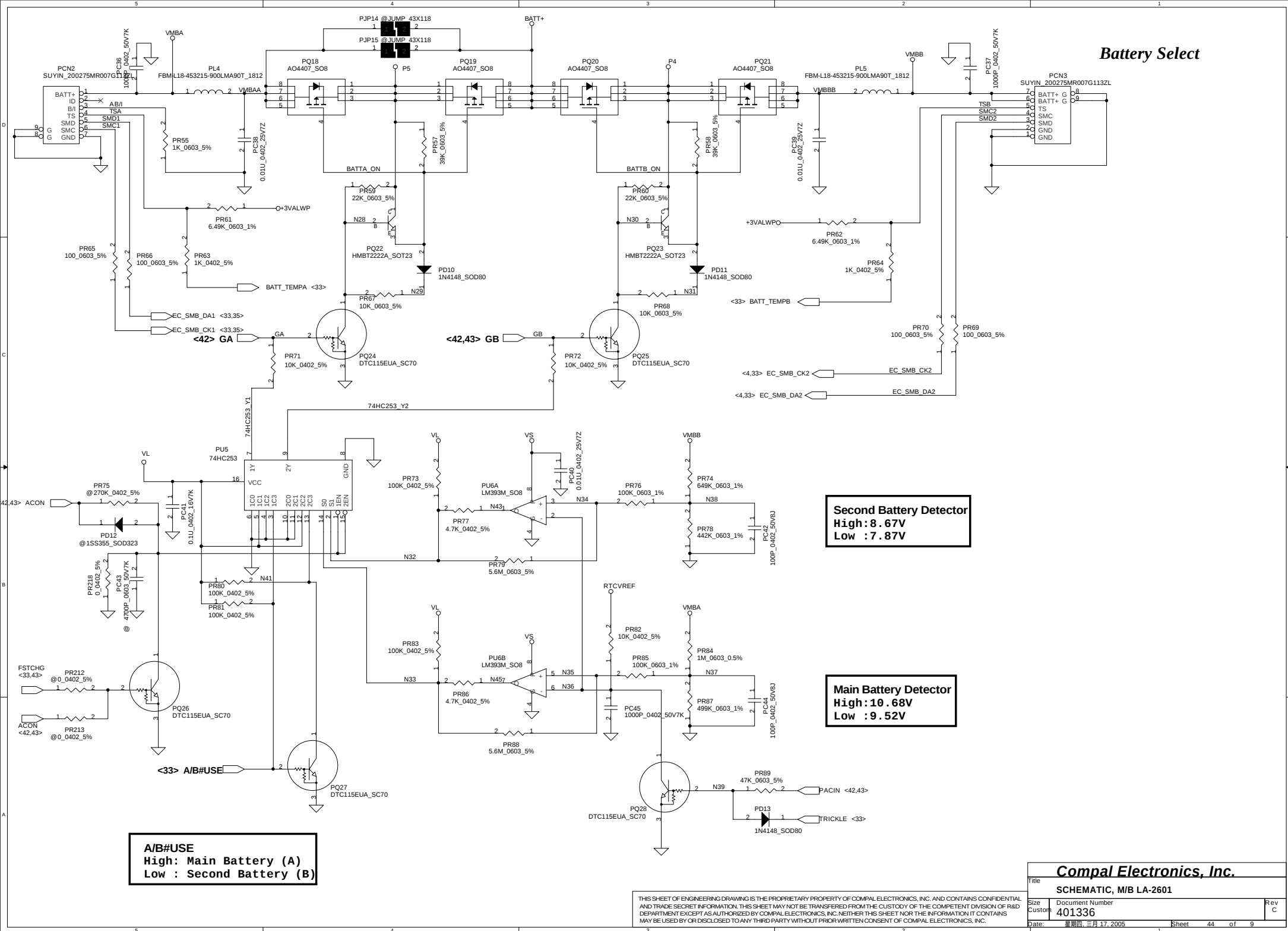
Charger



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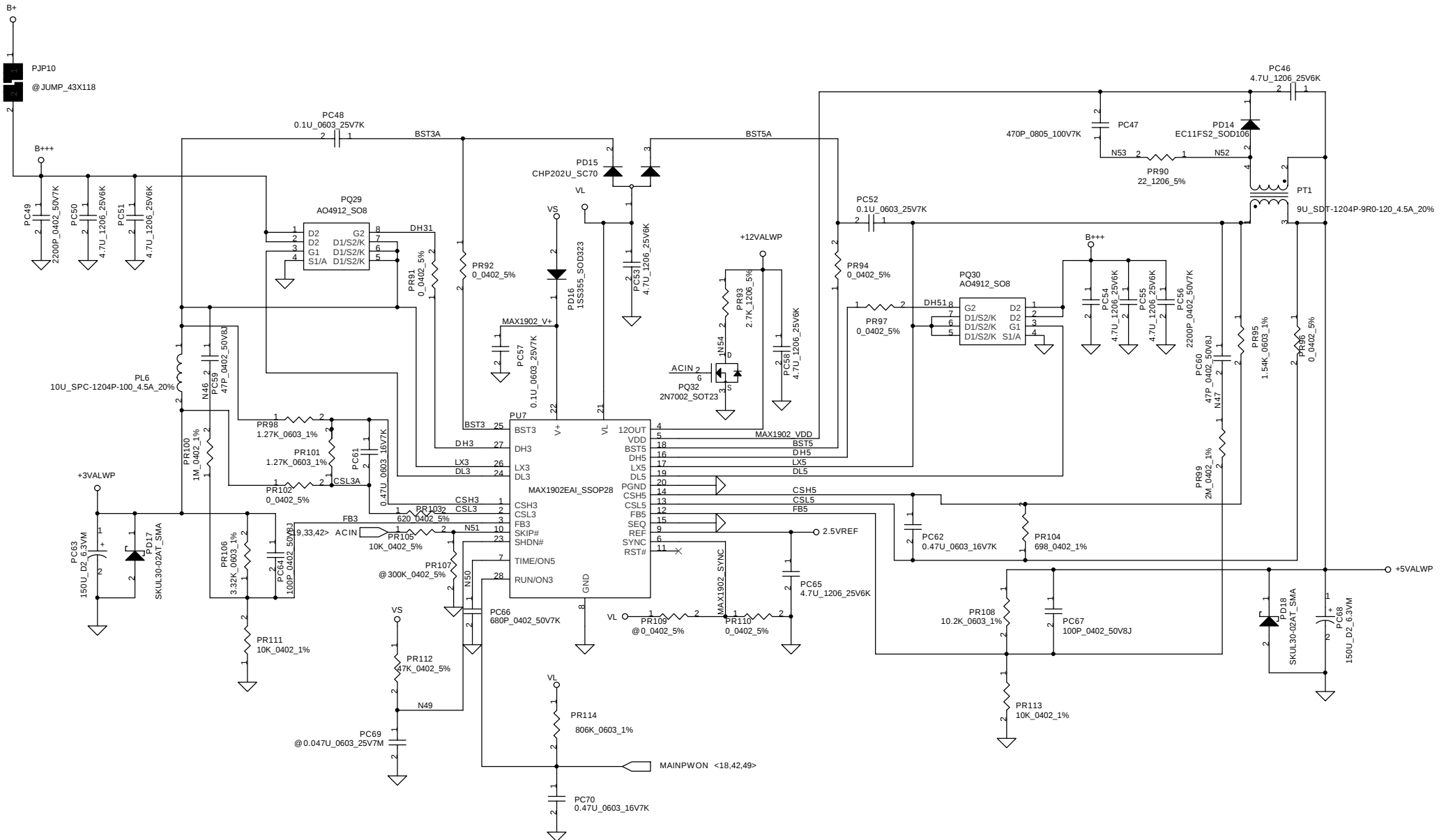
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Battery Select



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+3VALWP/+5VALWP/+12VALWP



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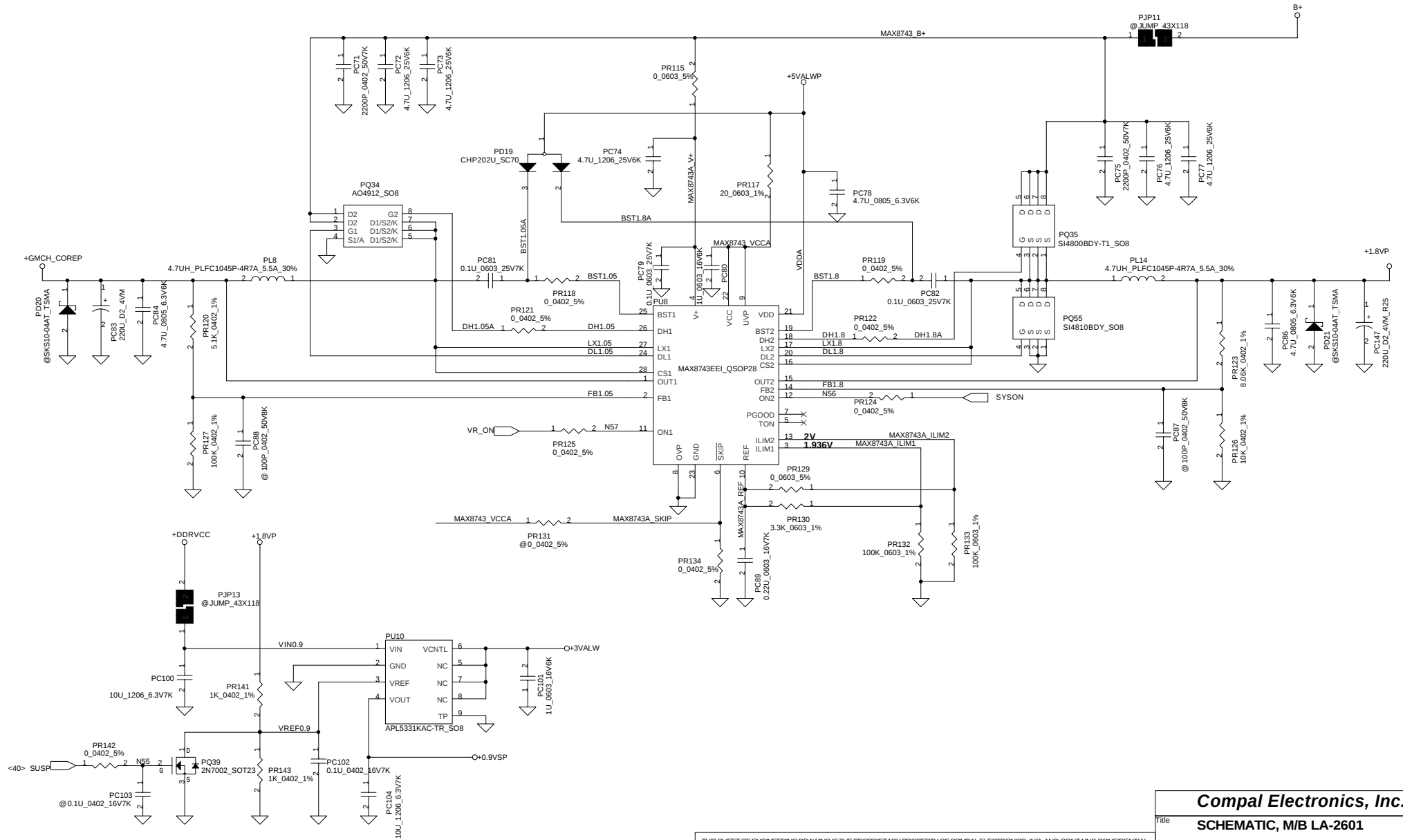
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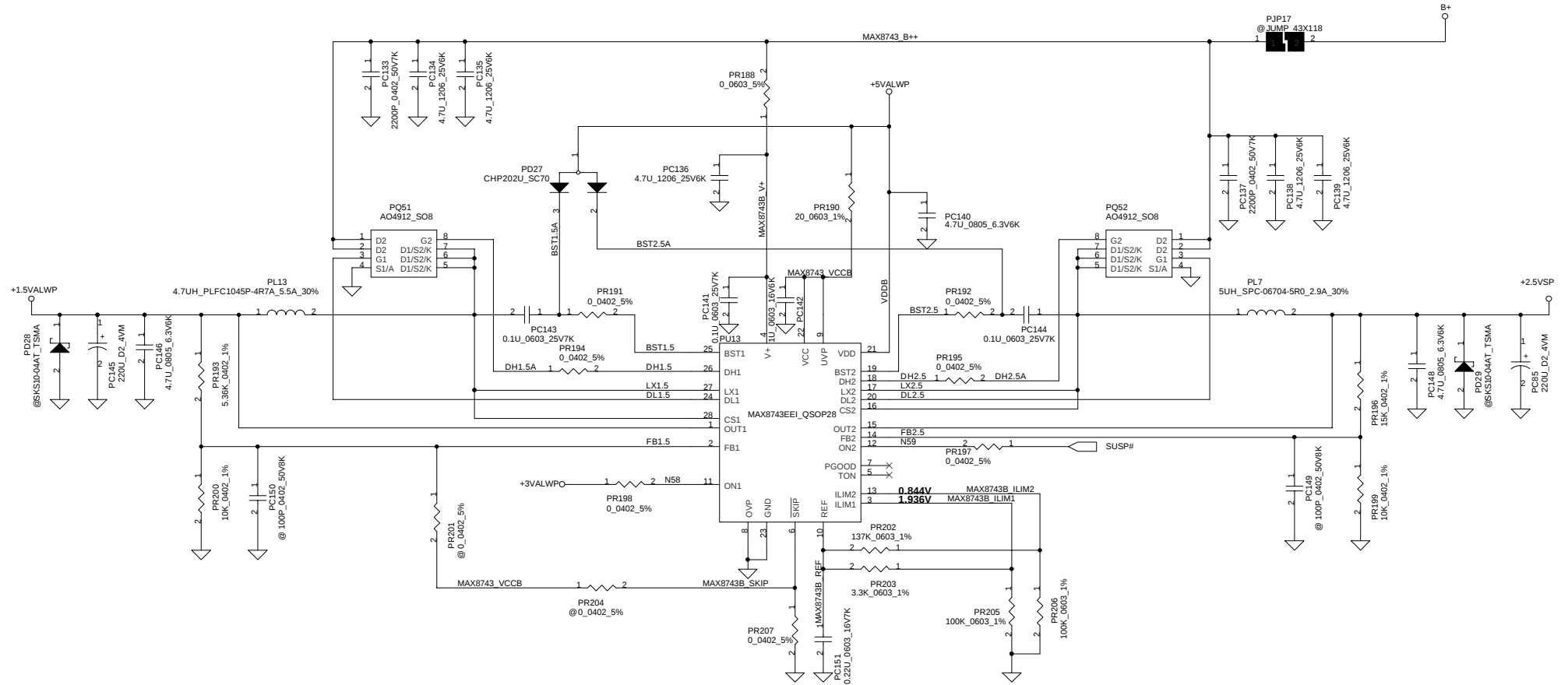
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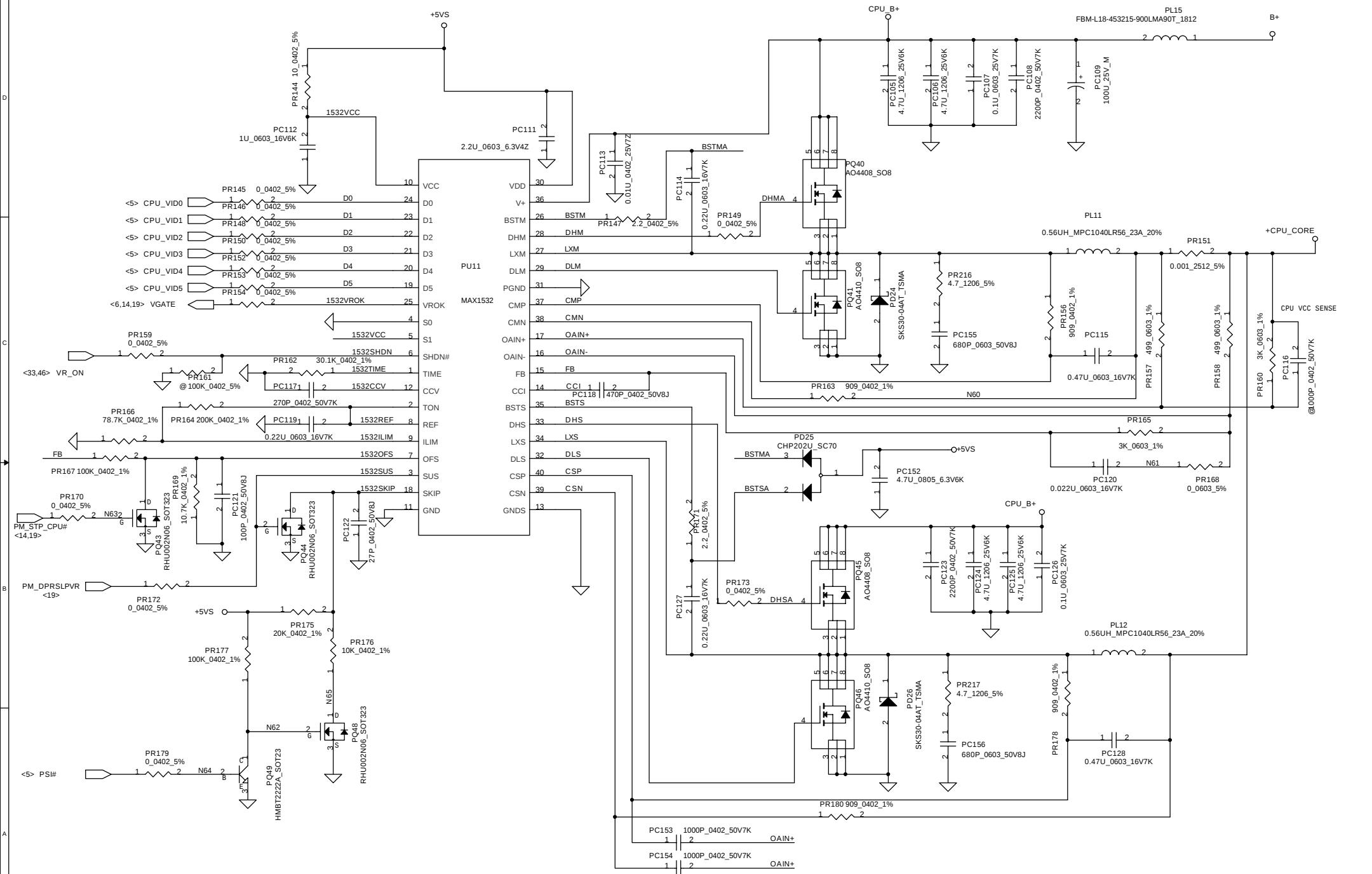
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+2.5VSP/+1.5VALWP



CPU CORE



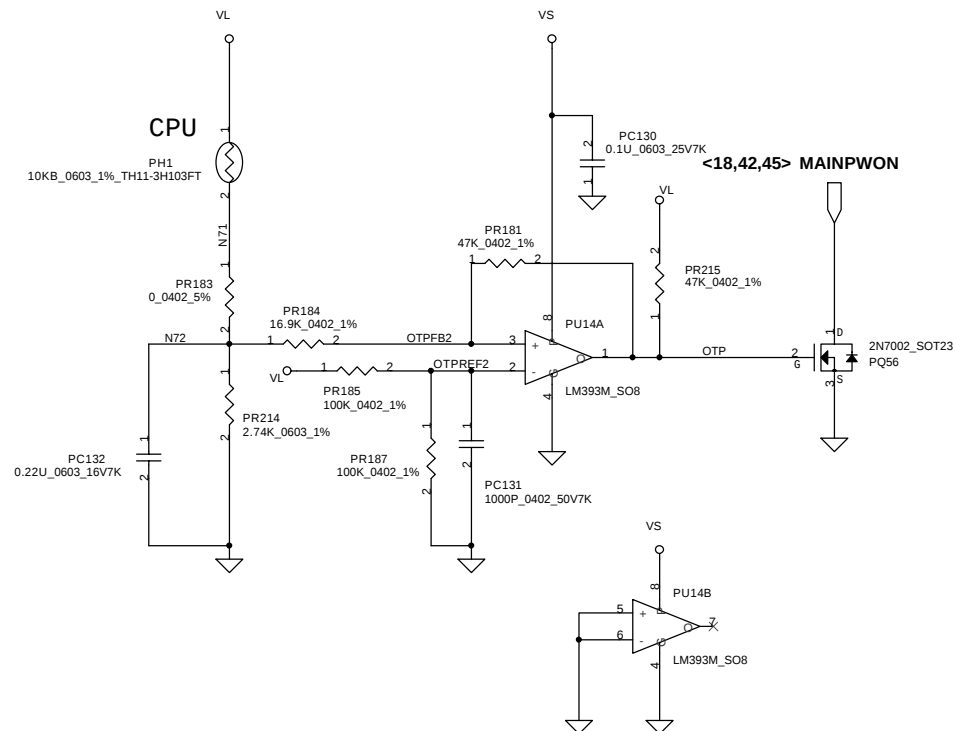
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PH1 under CPU botten side :
 CPU thermal protection at 90 $\pm$ 3 degree C
 Recovery at 50 $\pm$ 3 degree C



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Item	Reason for change	PG#	Modify List	Date	B.Ver#
1	Change DC JAcK and second battery connect.	42,44	Change DC JAcK from DC231105200 to DC231000500. Change second battery connect from	2004.10.28	
2	Modify OTP schematic.	45,49	Change PR114 from 47K_0402_5% to 806K_0603_1%; Change PR183 from 0_0402_5% to 1.37K_0402_1%; Change PR184 from 16.9K_0402_1% to 10K_0402_1%; Change PC132 from 0.22u_0603_16V to 1U_0603_16V; Change PC70 from 0.047uF to 0.47uF; Delete PR186, PR182, PU12 and PQ50.	2004.11.30	
3	To increase the pre-charge current.	42	Change PR21 from 169K_0603_1% to 200K_0603_1%; Change PR10 from 47_1206_5% to 33_1206_5%; Change PR9,PR11 and PR13 from 1.5K_1206_5% to 1K_1206_5%.	2004.12.28	
4	Change OTP schematic to EVT design.	49	Change PR183 from 1.37K_0402_1% to 0_0402_5%; Change PR184 from 10K_0402_1% to 16.9K_0402_1%; Change PC132 from 1U_0603_16V to 0.22u_0603_16V; Add PR215 47K_0402_1%; Add PR214 2.74K_0603_1%; Add PU14 LM393 and PQ56 2N7002.	2005.01.05	
5	For EMI request.	48	Add PR216 and PR217, 4.7_1206_5%; Add P155 and PC156, 680P_0603_50V; Add PL15, FBM-L18-453215-900LMA90T.	2005.01.10	
6	To increase 1.5V power plan to 1.57V by H/W request.	47	Delete PR201; Add PR200 10K_0402_1%; Add PR193 5.36K_0402_1%.	2005.01.10	

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EAT10 PIR List

***** Rev:0.2 PIR List 2004/08/10 Writer by Jason *****

P05 : Add @ in R63
Del @ in R56

P06 : Del @ in R54
Del JP9

P18: New add R512

P21 : New add R513
New add X3
New add C607

P21 : New add R534, R535

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