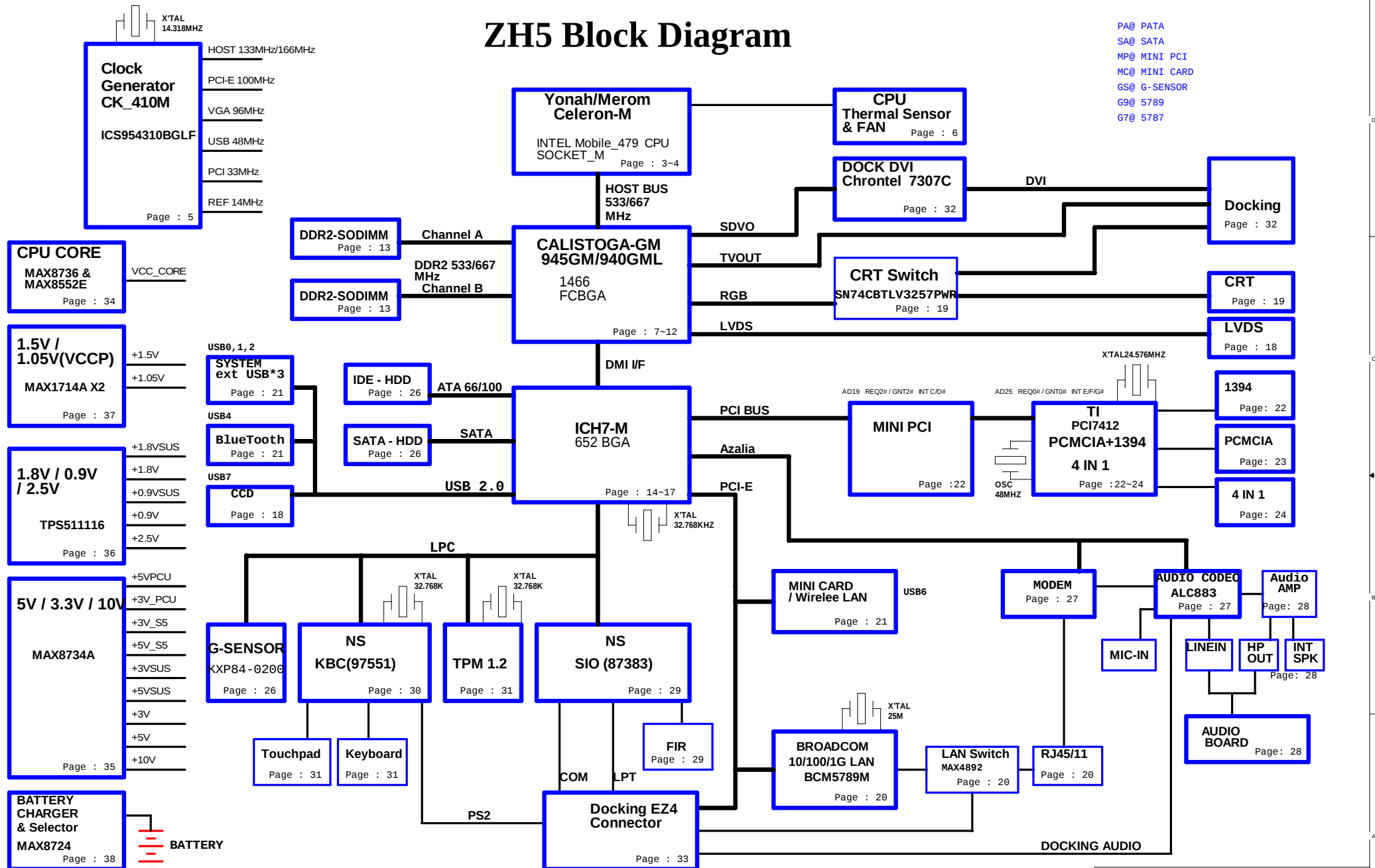


ZH5 Block Diagram

PA@ PATA
SA@ SATA
MP@ MINI PCI
MC@ MINI CARD
GS@ G-SENSOR
G9@ 5789
G7@ 5787



PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts
PCI7412	AD25	REQ0# / GNT0#	INT E/F/G#
MINI PCI	AD19	REQ2# / GNT2#	INT C/D#

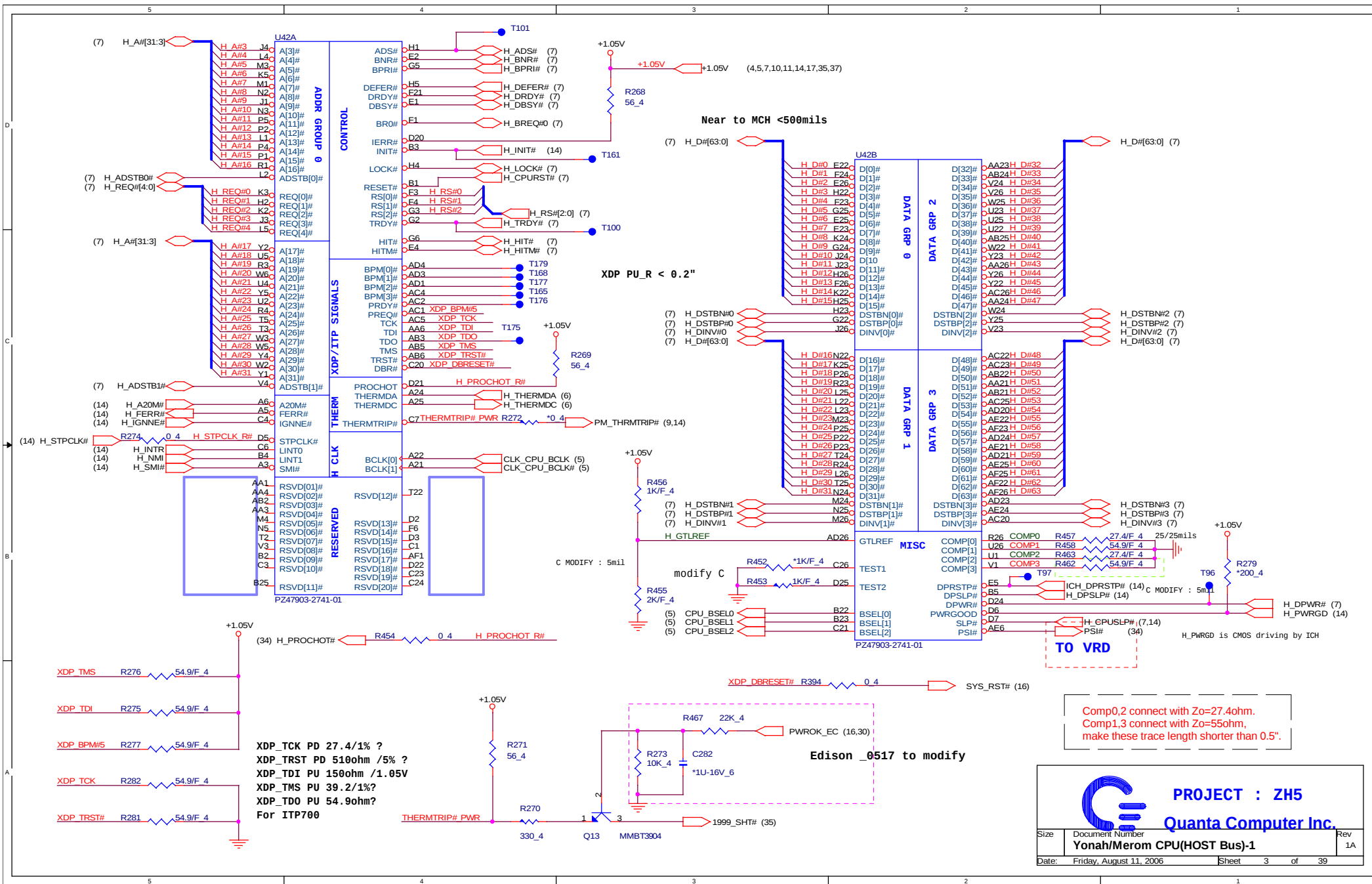
TABLE OF CONTENTS

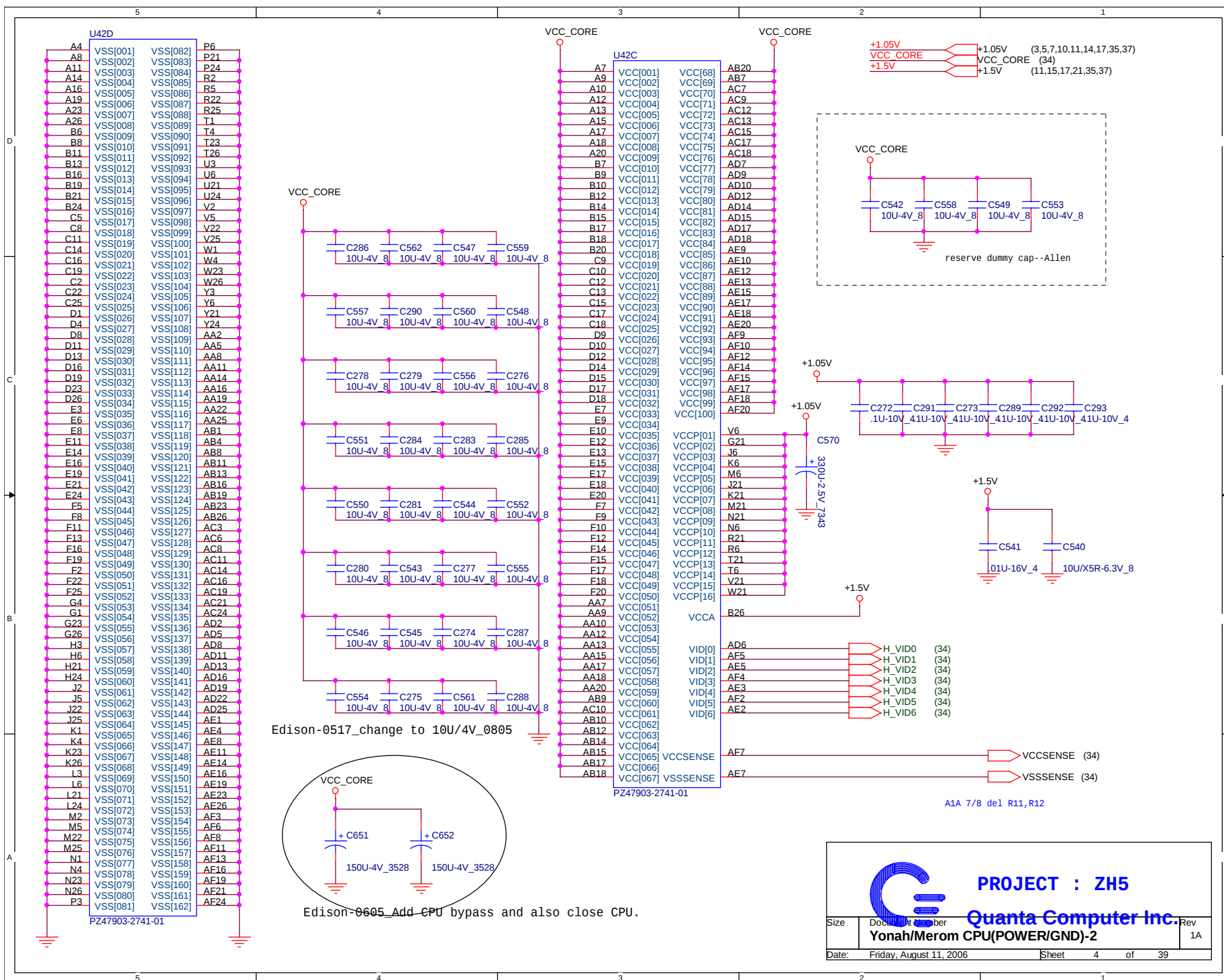
Page 01 :	BLOCK DIAGRAM
Page 02 :	TABLE OF CONTENTS
Page 03 :	Yonah/Merom CPU(HOST Bus)-1
Page 04 :	Yonah/Merom CPU(POWER/NC)-2
Page 05 :	CLOCK GENERATOR
Page 06 :	THERMAL SENSOR & FAN
Page 07 :	GMCH HOST Bus(1 of 6)
Page 08 :	GMCH DDRII MEMORY (2 of 6)
Page 09 :	GMCH DMI & VEDIO (3 of 6)
Page 10 :	GMCH POWER & STRAP(4 of 6)
Page 11 :	GMCH LVDS/DAC-PWR2 (5 of 6)
Page 12 :	GMCH GND (6 of 6)
Page 13 :	DDRII SO-DIMM & TERMINATOR
Page 14 :	ICH7-M RTC/SATA/IDE/AZALIA/CPU (1 OF 4)
Page 15 :	ICH7-M MDI/PCI E/PCI/USB (2 OF 4)
Page 16 :	ICH7-M GPIO/SMBus (3 of 4)
Page 17 :	ICH7-M POWER & GND (4 of 4)
Page 18 :	LVDS CONNECTOR & CCD
Page 19 :	CRT PORT
Page 20 :	GigaLAN BCM5787MKMLG & RJ45/RJ11
Page 21 :	PCMCIA /Mini Card /USB / Blue Tooth
Page 22 :	PCI7412-IEEE1394
Page 23 :	PCI7412-PCMCIA
Page 24 :	PCI7412-CARD READER
Page 25 :	HOLES
Page 26 :	SATA/PATA HDD /G SENSOR
Page 27 :	CODEC ALC883 & MDC
Page 28 :	AUDIO AMP & SPEAK/LIN OUT
Page 29 :	SIO (87383)
Page 30 :	EC Controller PC97551 & FLASH
Page 31 :	SWITCH,LED,KB,TP,TPM1.2,EMI PAD
Page 32 :	DOCK DVI CH7307C
Page 33 :	DOCK EZ4 CONNECTOR
Page 34 :	CPU CORE (MAX8736ETL+)
Page 35 :	SYSTEM +5V/+3V(MAX8734A)
Page 36 :	DDRII POWER 1.8VSUS / 0.9VTERM
Page 37 :	CHIPSET POWER +1.5V / VCCP(+1.05V)
Page 38 :	BATTERY CHARGER & SELECTOR



PROJECT : ZH5
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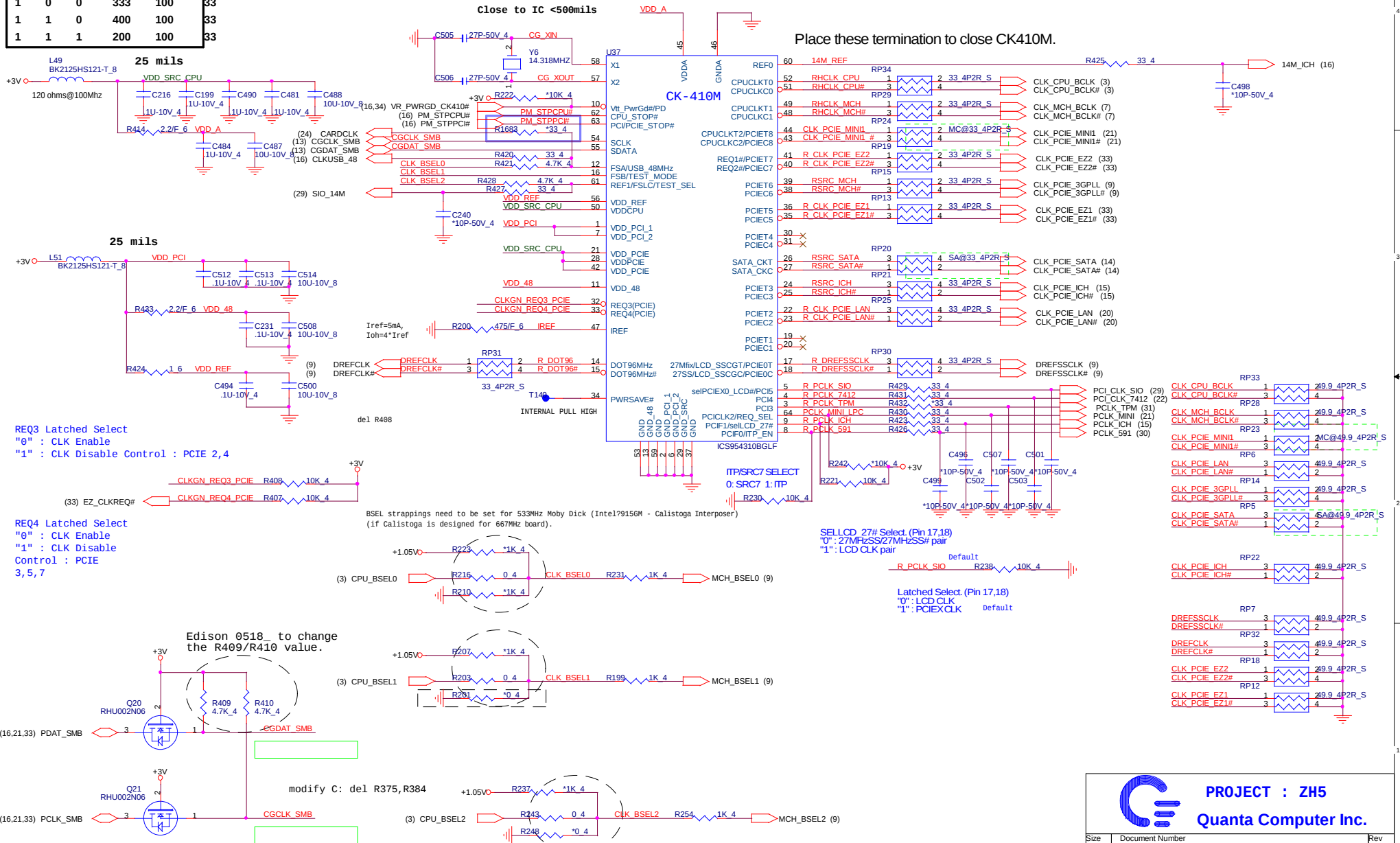
Size	Document Number	Rev
	TABLE OF CONTENTS	1A
Date:	Friday, August 11, 2006	Sheet 2 of 39

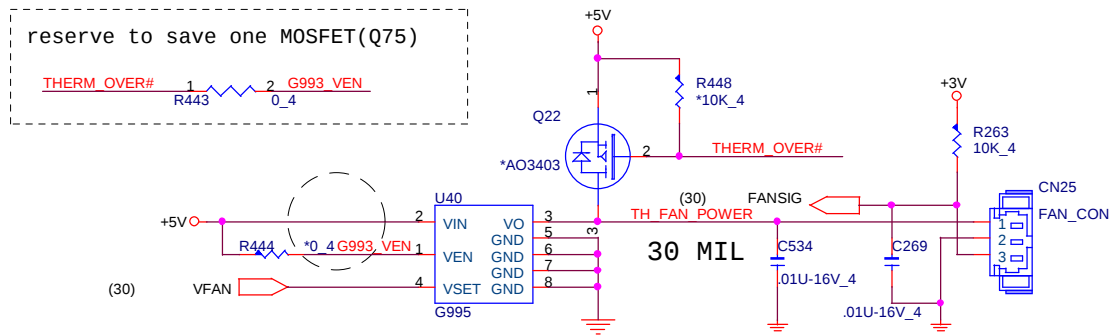




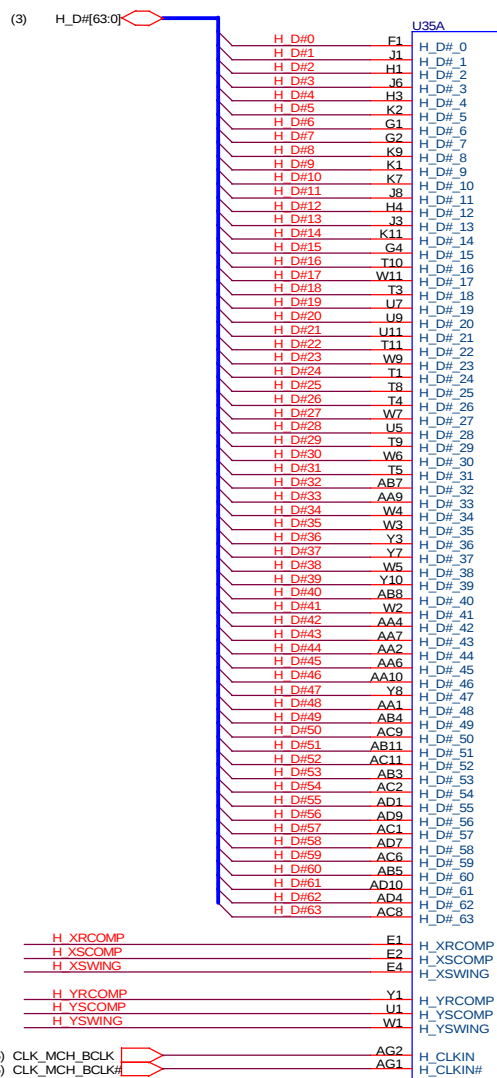
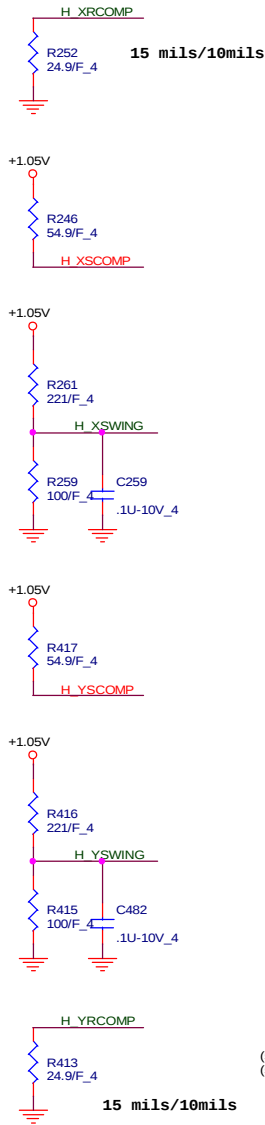
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	200	100	33

Default

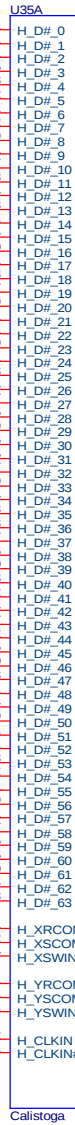




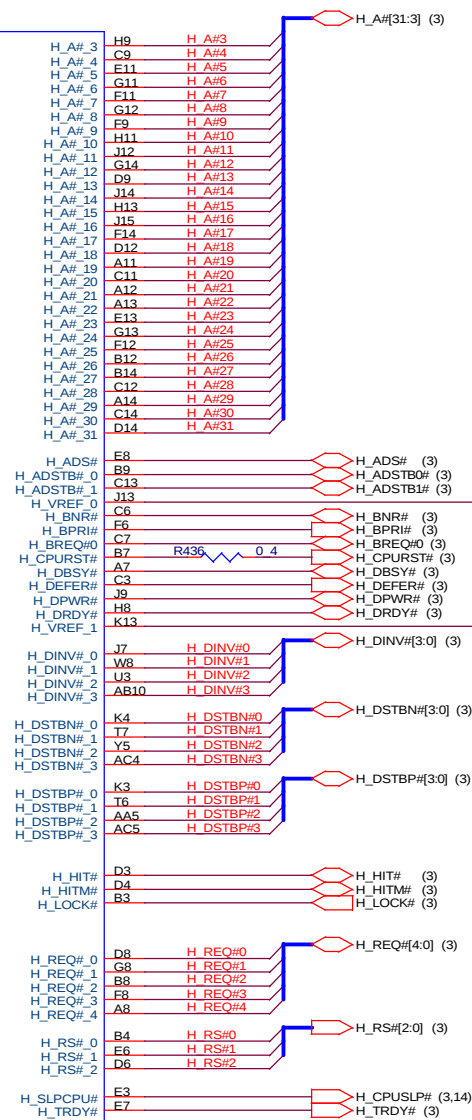
Size	Document Number CPU THERMAL SENSOR & FAN	Rev 1A
Date:	Friday, August 11, 2006	Sheet 6 of 39



Short Stub < 100mils
extract from same point



HOST



+1.05V +1.05V (3,4,5,10,11,14,17,35,37)

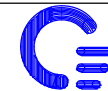
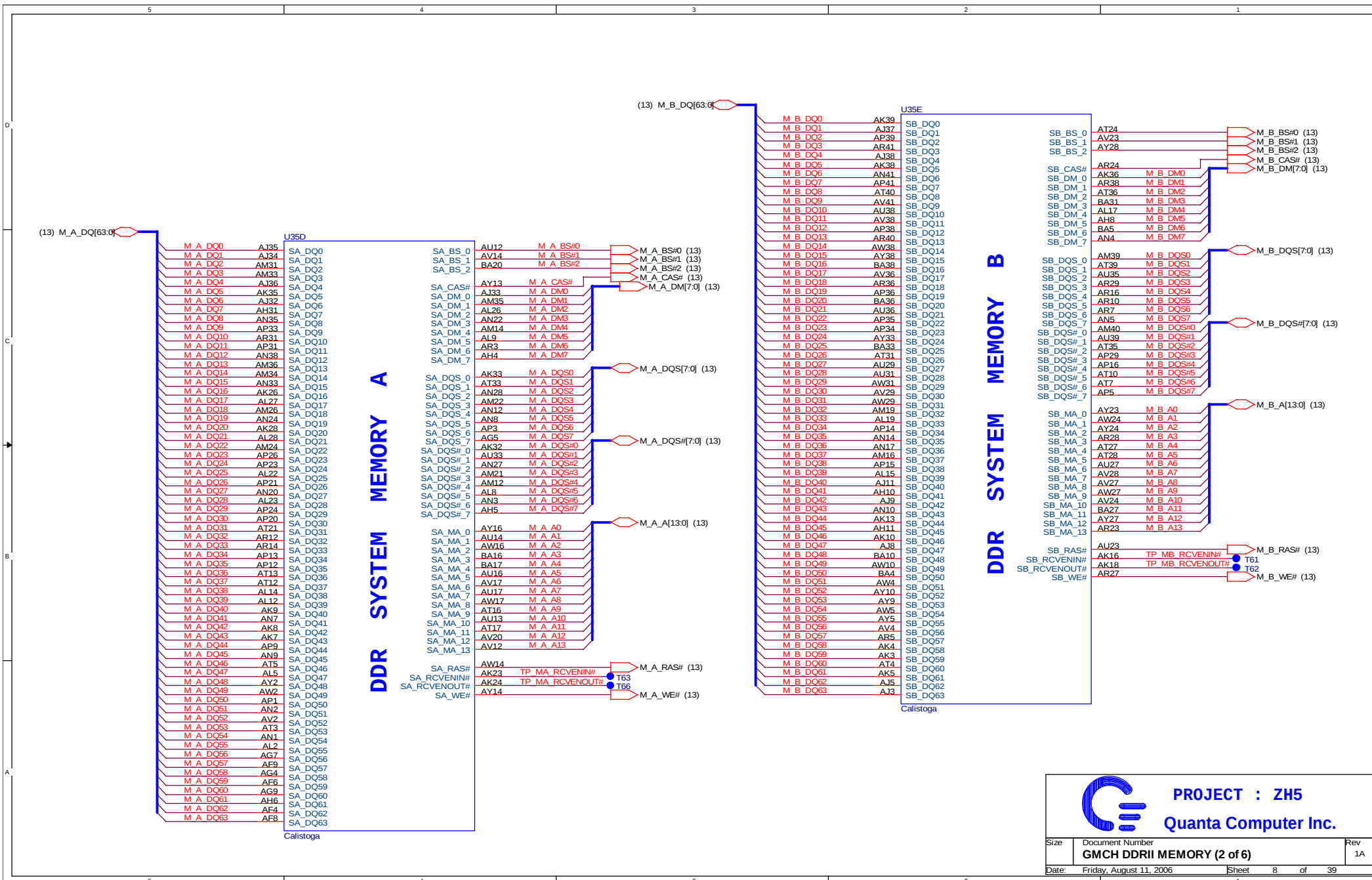
H_VREF :10 mils/20 mils space

COMPONENTS	P/N
945GM	AJ009450T04

PROJECT : ZH5
Quanta Computer Inc.

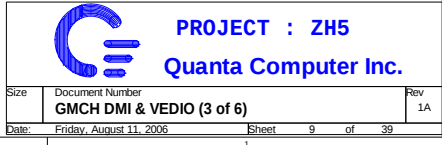
Size	Document Number	Rev
	GMCH HOST Bus(1 OF 6)	1A

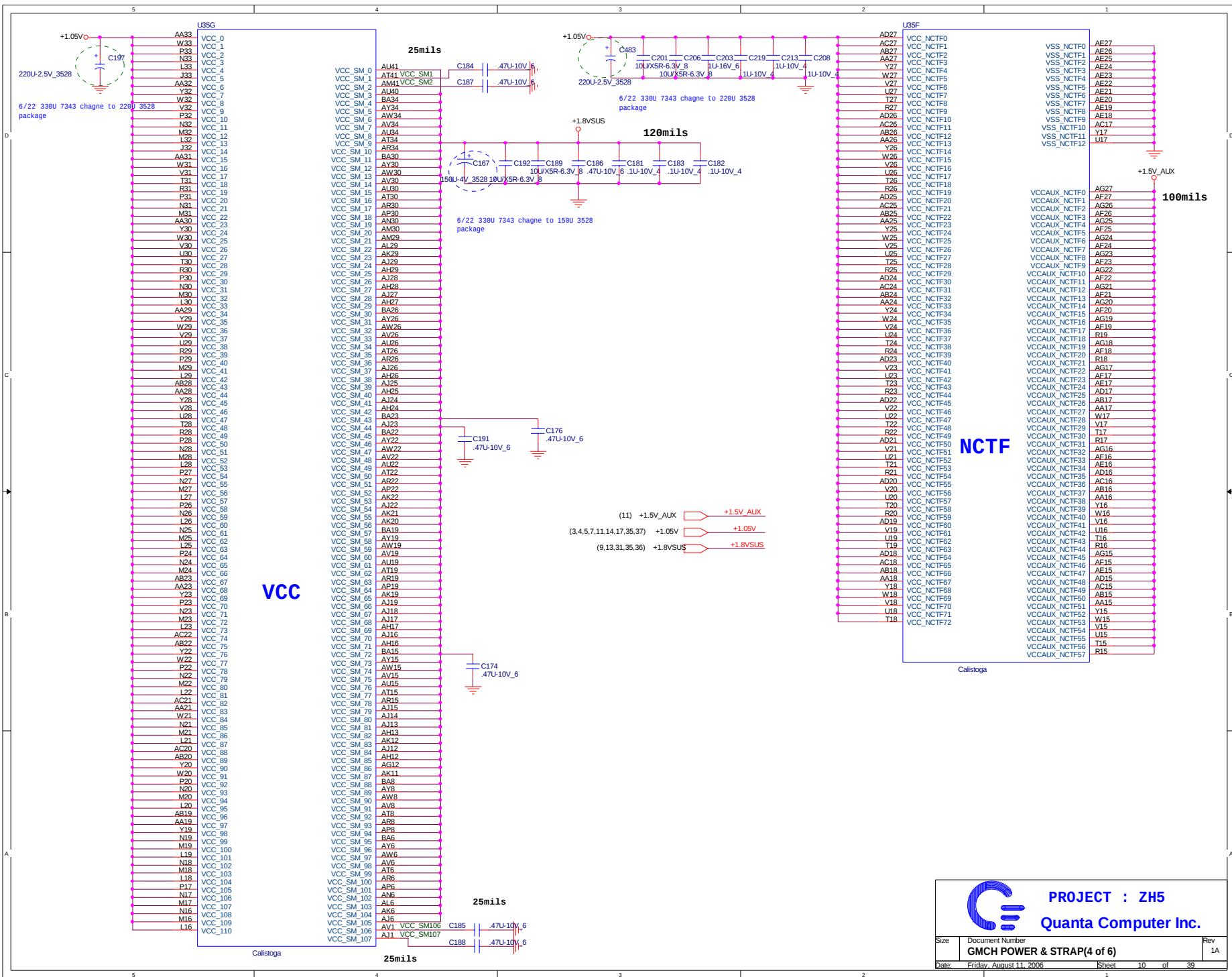
Date: Friday, August 11, 2006 Sheet 7 of 39

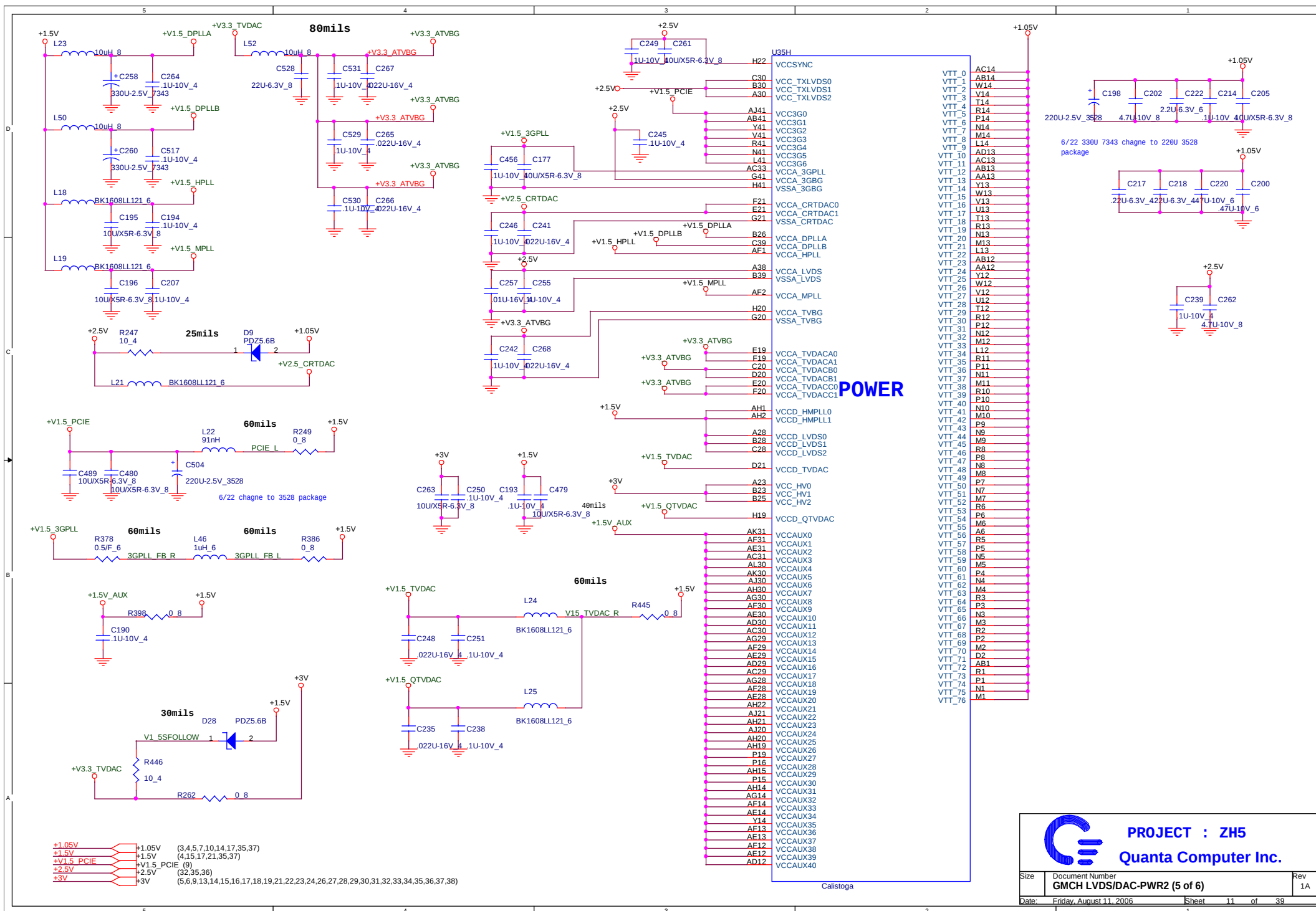


PROJECT : ZH5
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Size	Document Number GMCH DDRII MEMORY (2 of 6)	Rev 1
Date:	Friday, August 11, 2006	Sheet 8 of 39



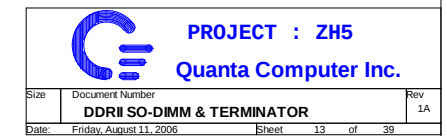
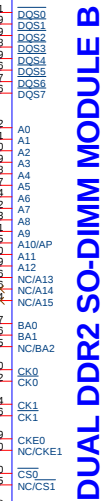
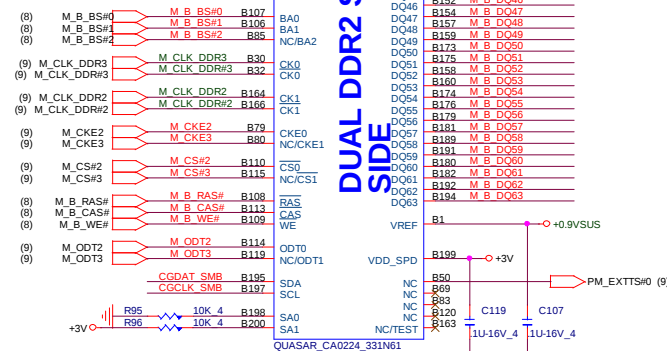
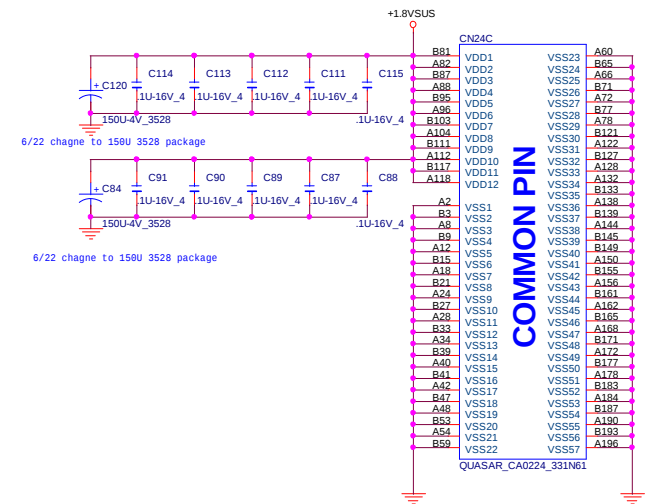
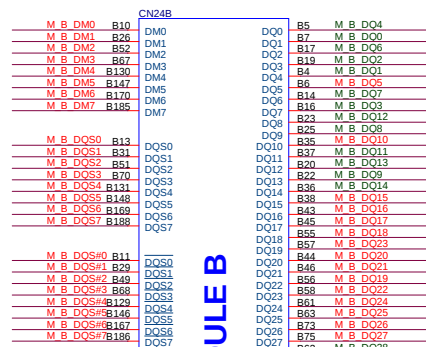
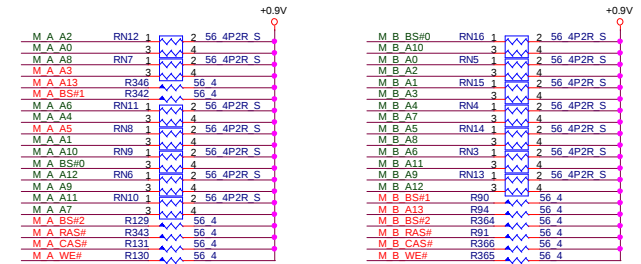
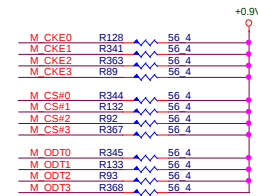




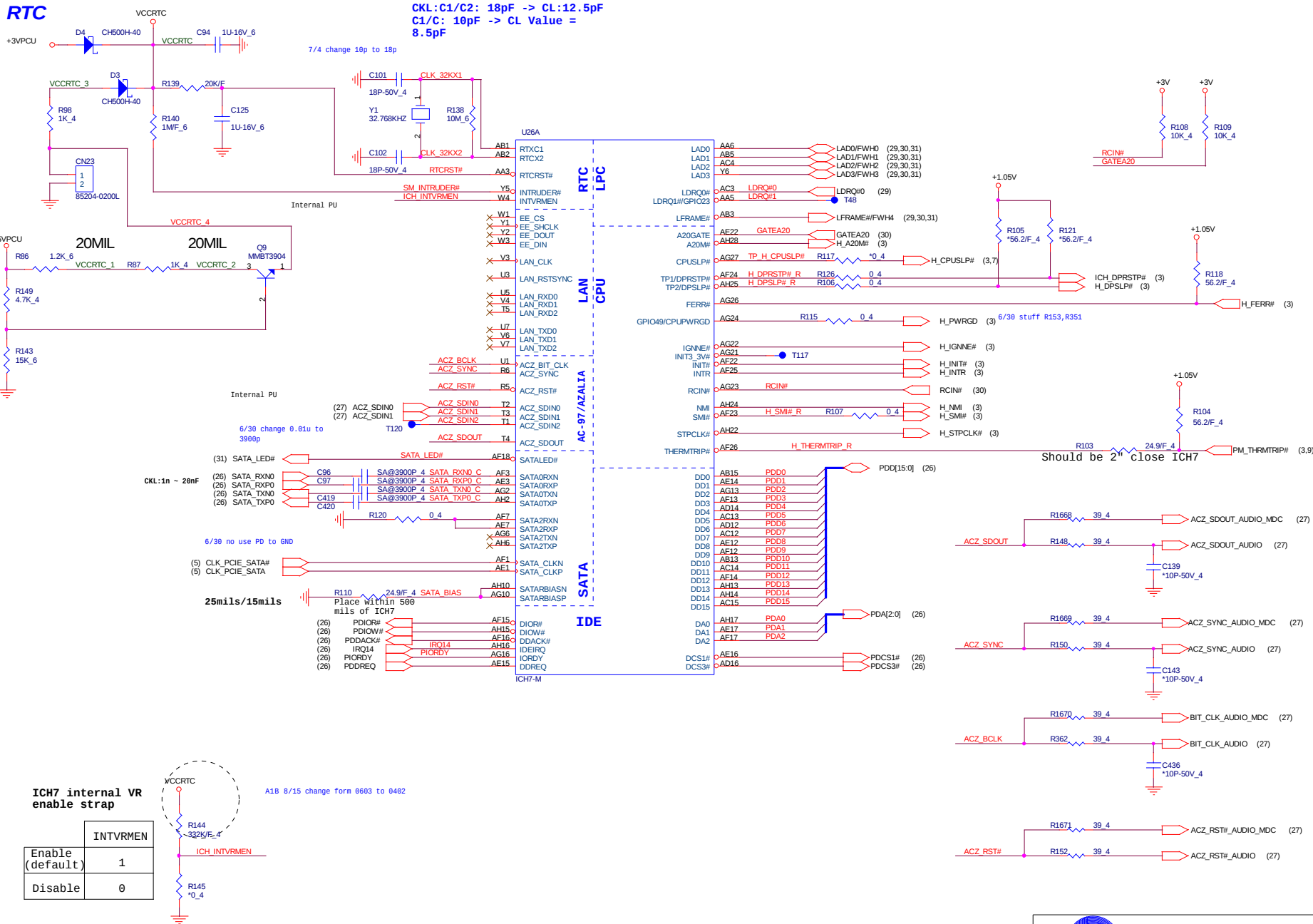


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DDR2 TERMINATOR



RTC



modify C

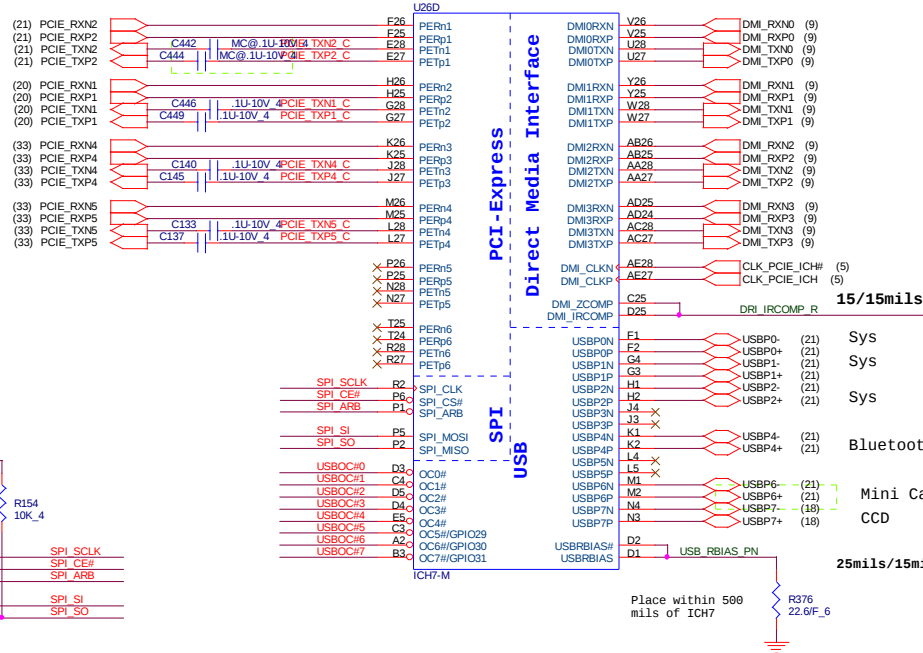
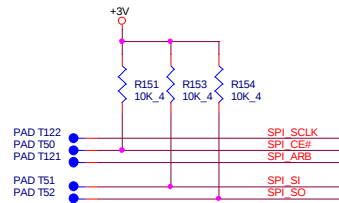
MINI CARD PCI-E

5789M

EZ4

EZ4

A1A 8/1 chagne PCI-E signal for different SB use



15/15mils
Place within 500
mils of ICH7

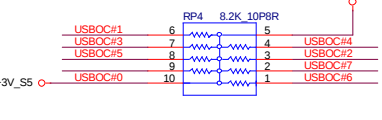
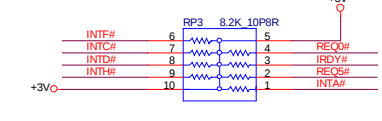
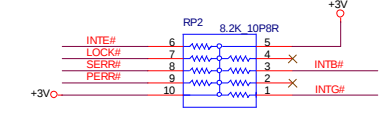
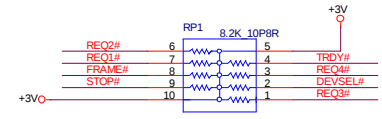
Sys
Sys
Sys

Bluetooth Module

Mini Card
CCD

25mils/15mils

Place within 500
mils of ICH7

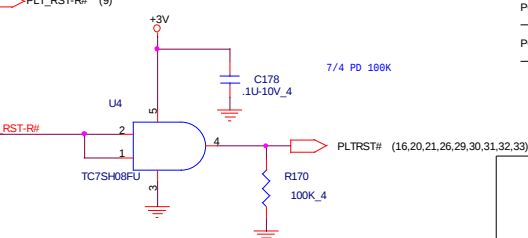


CKL use 10Kohm

ICH7 Boot BIOS select

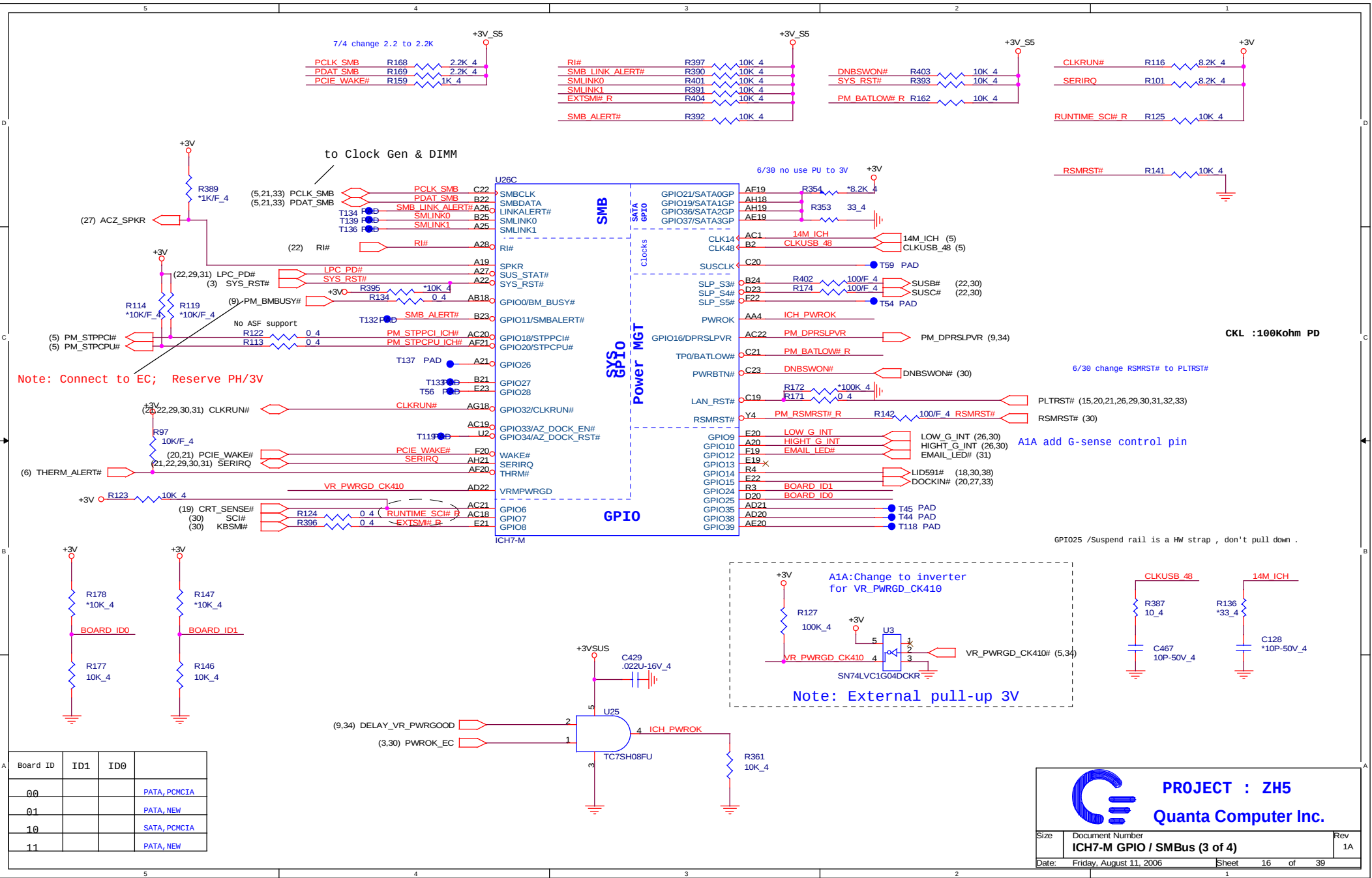
	STRAP	GNT5# R1	GNT4# R2
LPC (default)	11	UNSTUFF	UNSTUFF
PCI	10	UNSTUFF	STUFF
SPI	01	STUFF	UNSTUFF

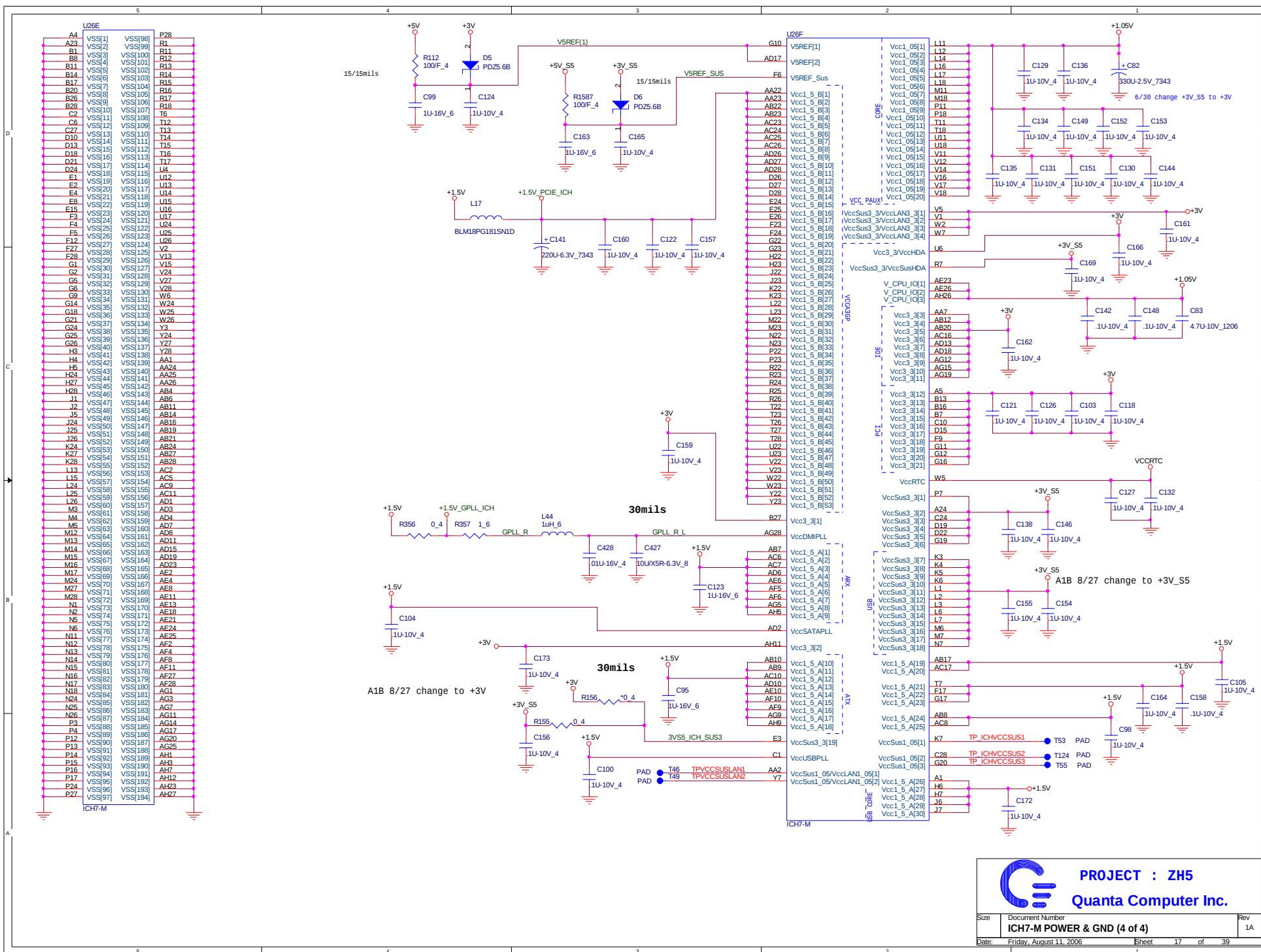
PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts
PCI7411	AD25	REQ0# / GNT0#	INT E/F/G#

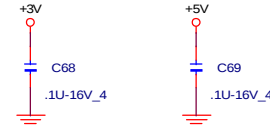
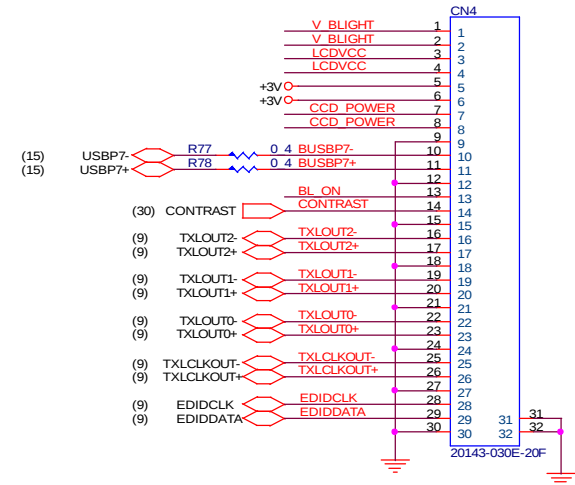
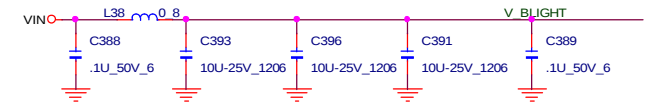
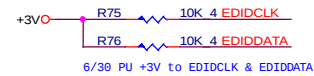
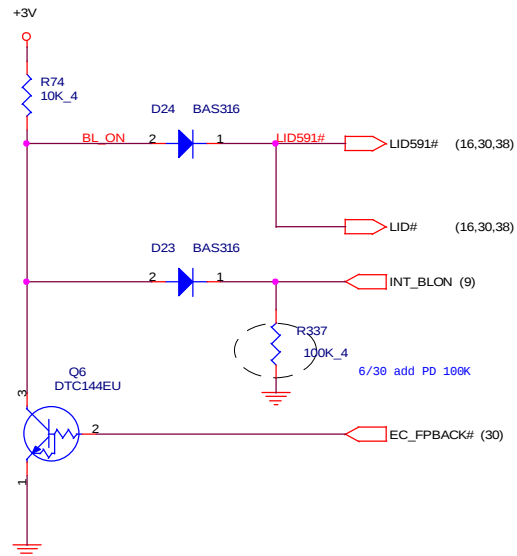


Don't connect to PCI device / Express card

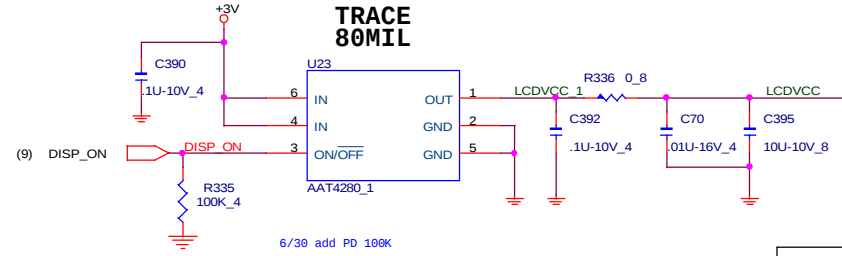
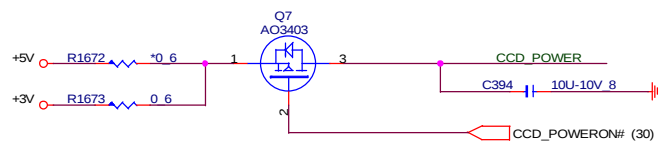
PROJECT : ZH5
Quanta Computer Inc.







CAMERA MODULE CONNECTOR





PROJECT : ZH5
Quanta Computer Inc.

Size	Document Number	Rev
	LVDS CONNECTOR & CCD	1A
Date:	Friday, August 11, 2006	Sheet 18 of 39

(9) VGA_RED
(9) VGA_GRN
(9) VGA_BLU

(9) VSYNC
(9) HSYNC

(9) CRTDCLK
(9) CRTDDAT

(33) PR_INSERT_5V

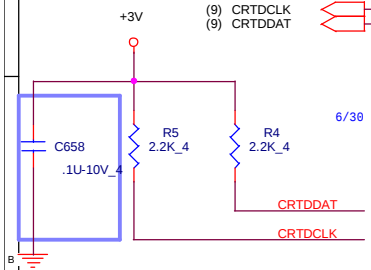
VGA RED
VGA GRN
VGA BLU

U6
VCC
A0
A1
B0
B1
C0
C1
C2
D0
D1
SE
EN#
SN74CBTLV3257PWR

+5V
VGA_RED_SYS
VGA_RED_PR (33)
VGA_GRN_SYS
VGA_GRN_PR (33)
VGA_BLU_SYS
VGA_BLU_PR (33)

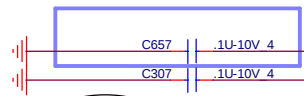
+5V
C311
.1U-10V_4
+5V
C1
.1U-10V_4
+5V
C327
.1U-10V_4

SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

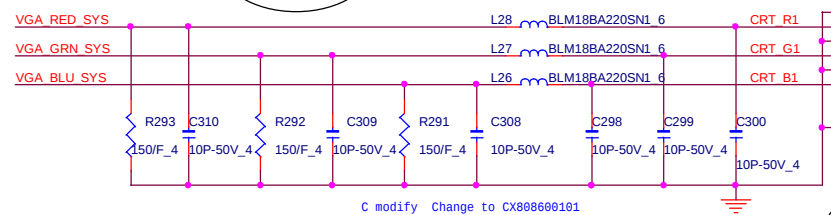


6/38 PU +3V to CRTDCLK & CRTDDATA

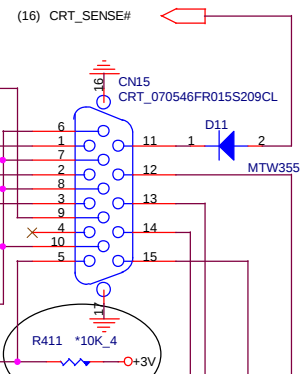
Edison 0523 Delete F2.



25 MIL

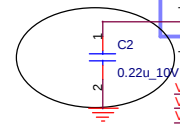


C modify Change to CX808600101



(30) DETECT_CRT

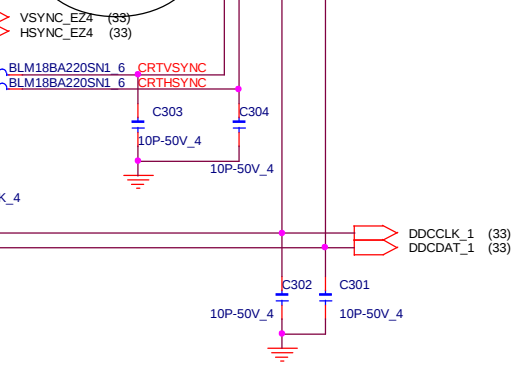
Edison 0530 Add 24 ohm resistor.



Edison 0530 Add 0.22uF capacitor, increase ESD to +-8KV.

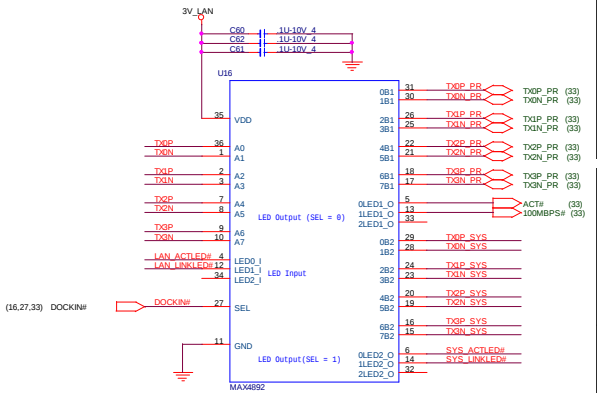
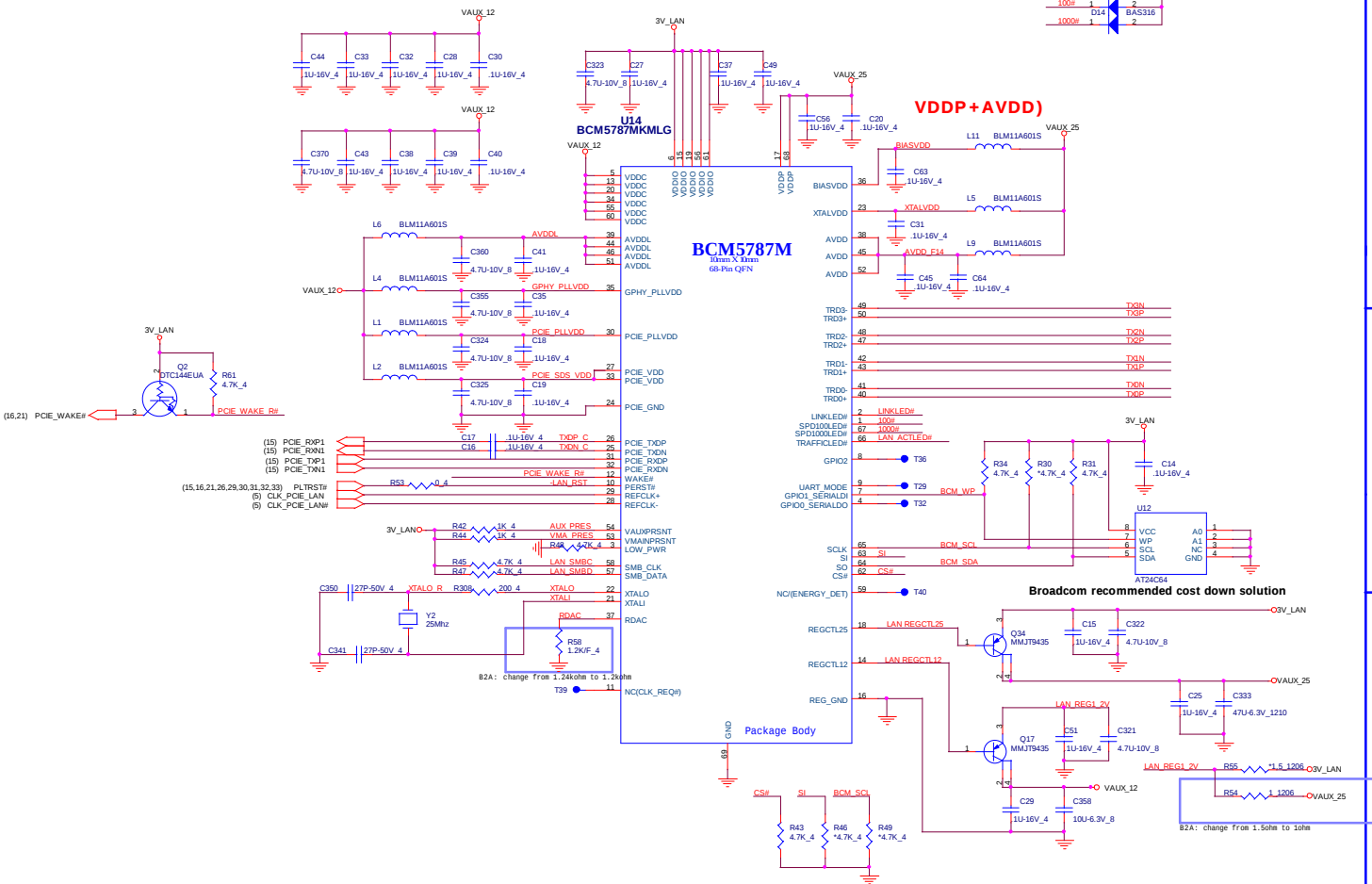
U10
VCC_SYNC
VCC_DDC
BYP
VCC_VIDEO
VIDEO_1
VIDEO_2
VIDEO_3
GND
CM2009

VSYNC1
HSYNC1
VSYNC
HSYNC
CRTVSYNC
CRTHSYNC
R59
R60
L29
L30
R10
R9
C303
C304
C302
C301

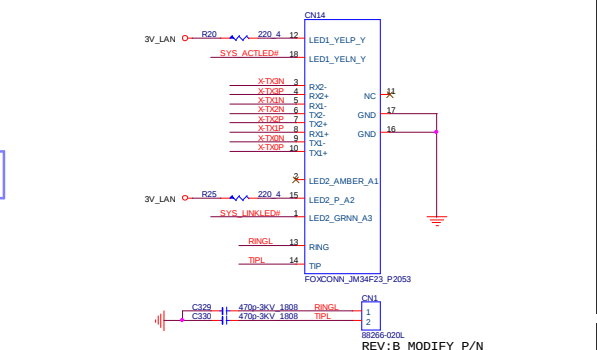
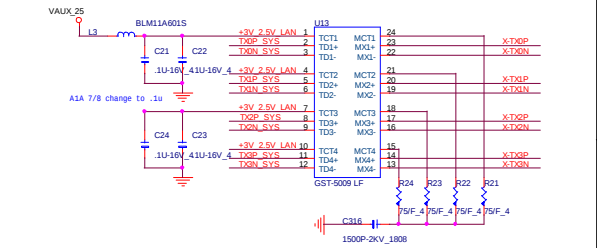


PROJECT : ZH5
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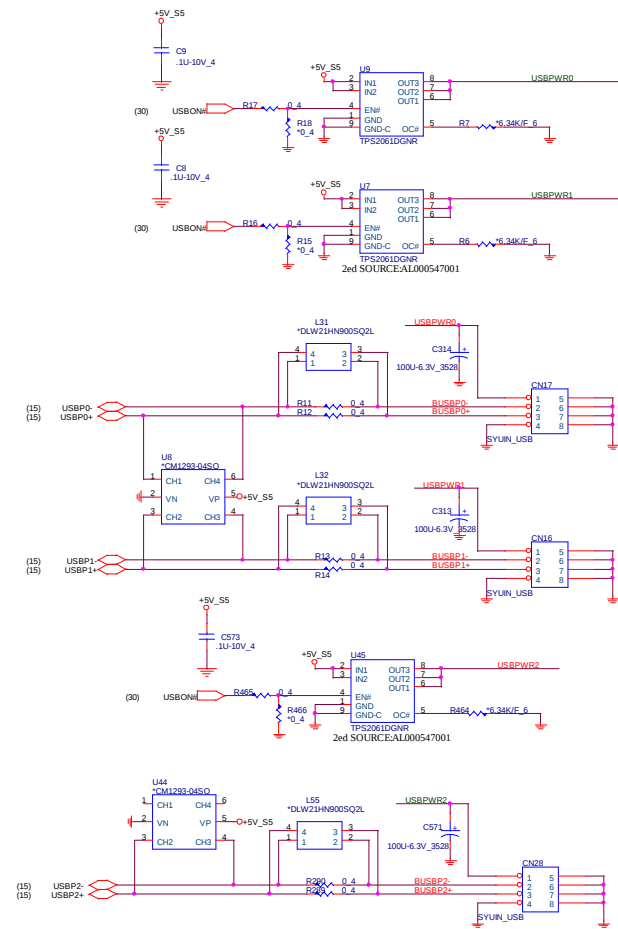
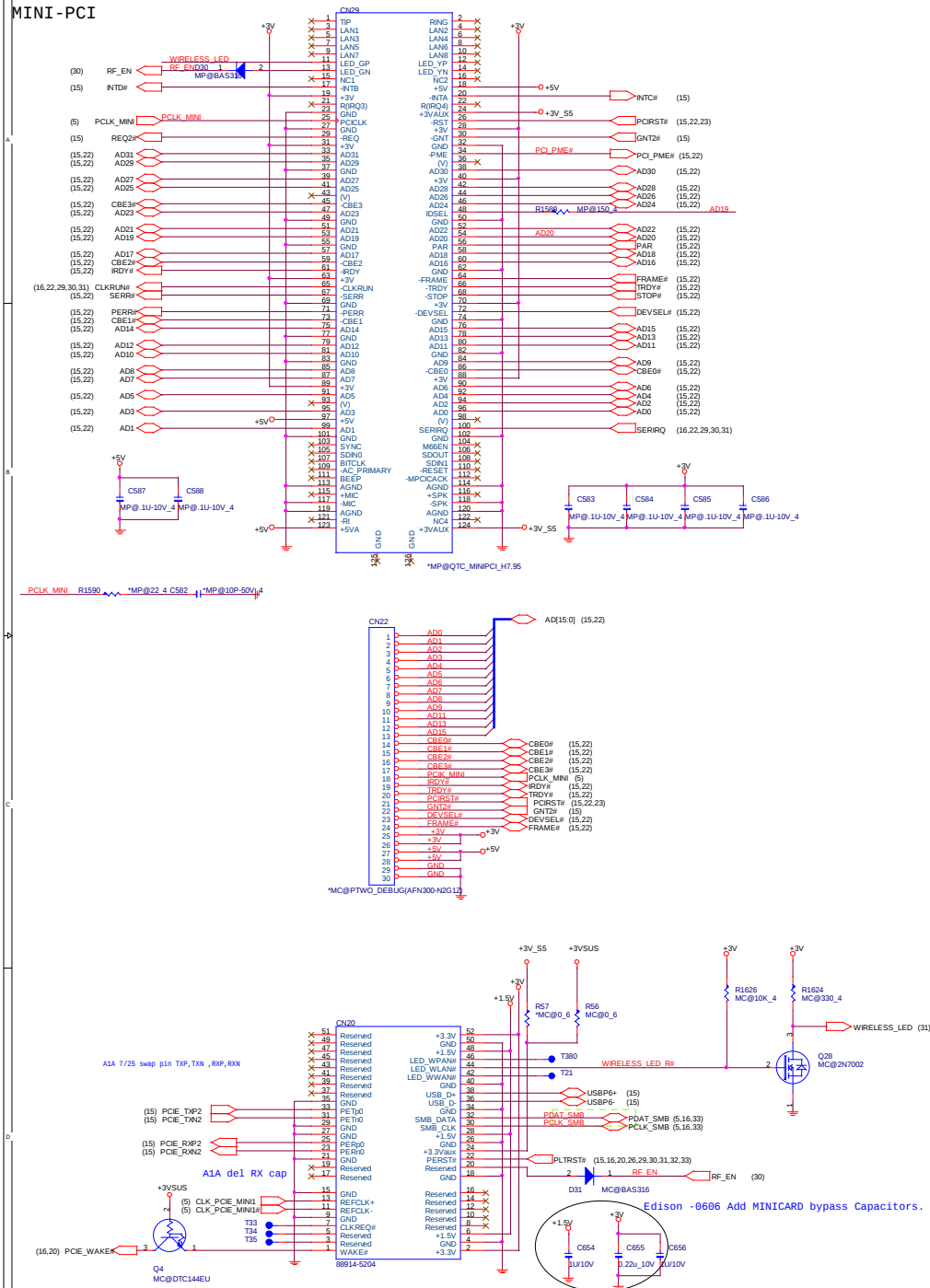
Size	Document Number	Rev
	CRT-PORT	1A
Date:	Friday, August 11, 2006	Sheet 19 of 39



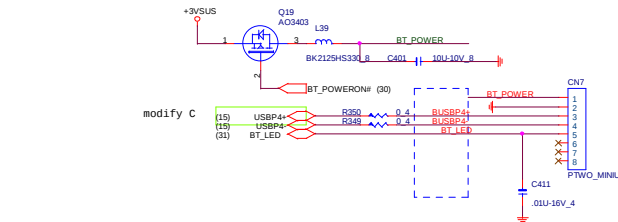
SEL	CONNECTION
0	Ax to xB1 : LEDx to xLED1
1	Ax to xB2 : LEDx to xLED2



MINI-PCI



BLUETOOTH MODULE CONNECTOR



Edison -0606 Add MINICARD bypass Capacitors.

PCI_CLK_7412 R1633 *22 4 C622 *10P-50V_4

U36A
(15) GNT0# REQ0#
(15,21) FRAME# IRDY#
(15,21) DEVSEL# TRDY#
(15,21) SERR# STOP#
(15,21) PERR# PAR#
(15,21) CBE3# CBE2#
(15,21) CBE1# CBE0#
(5) PCI_CLK_7412
(15,21,23) PCIRST#
(15,21) AD[0..31]

PCI Interface

1394 Interface

Miscellaneous

AD00 R11
AD01 P11
AD02 U11
AD03 V11
AD04 W11
AD05 R10
AD06 U10
AD07 V10
AD08 R09
AD09 U09
AD10 V09
AD11 W09
AD12 R12
AD13 V12
AD14 W12
AD15 R13
AD16 U13
AD17 V13
AD18 R14
AD19 U14
AD20 V14
AD21 W14
AD22 R15
AD23 V15
AD24 W15
AD25 R16
AD26 U16
AD27 V16
AD28 R17
AD29 U17
AD30 V17
AD31 W17

LATCH#VD3/VPPD0
CLOCK#VD1/VCCD0#
DATA#VD2/VPPD1
RSVD_03/VD0/VCCD1#/PS_MODE

A1A 7/11 Add avoid ekt

Q10
DTC144EU

Change connector(CN27) and Pin define--Edison 0628

B2A: Update Footprint

TPBIAS0
R217 56.2/F_4
R211 56.2/F_4
C232 1U-16V_6
R287 0_4
L54 *RFCMF1632140M2T
R288 0_4
R285 0_4
L53 *RFCMF1632140M2T
R286 0_4

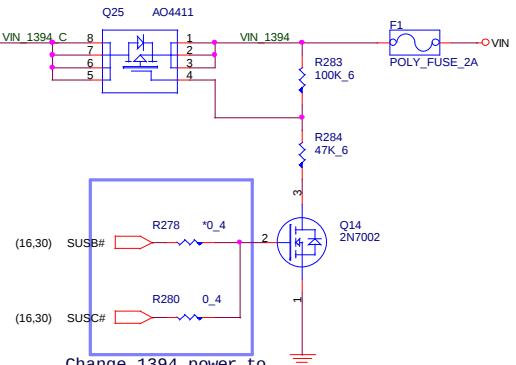
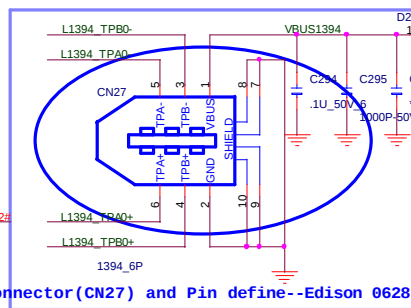
Swap L53 and L54--Edison 0628.

TPBOP
TPBON
R204 56.2/F_4
R208 56.2/F_4
C215 270P-25V_4
R206 5.1K/F_6

+3V
L20 BK1608HS800_6
1394_AVDD
C233 1U-10V_4
C227 1U-16V_6

PS_MODE
R180 *43K_6
R181 *43K_6

A1A 7/22 add PS_MODE to define switch power
Hi : serial
Lo: parallel

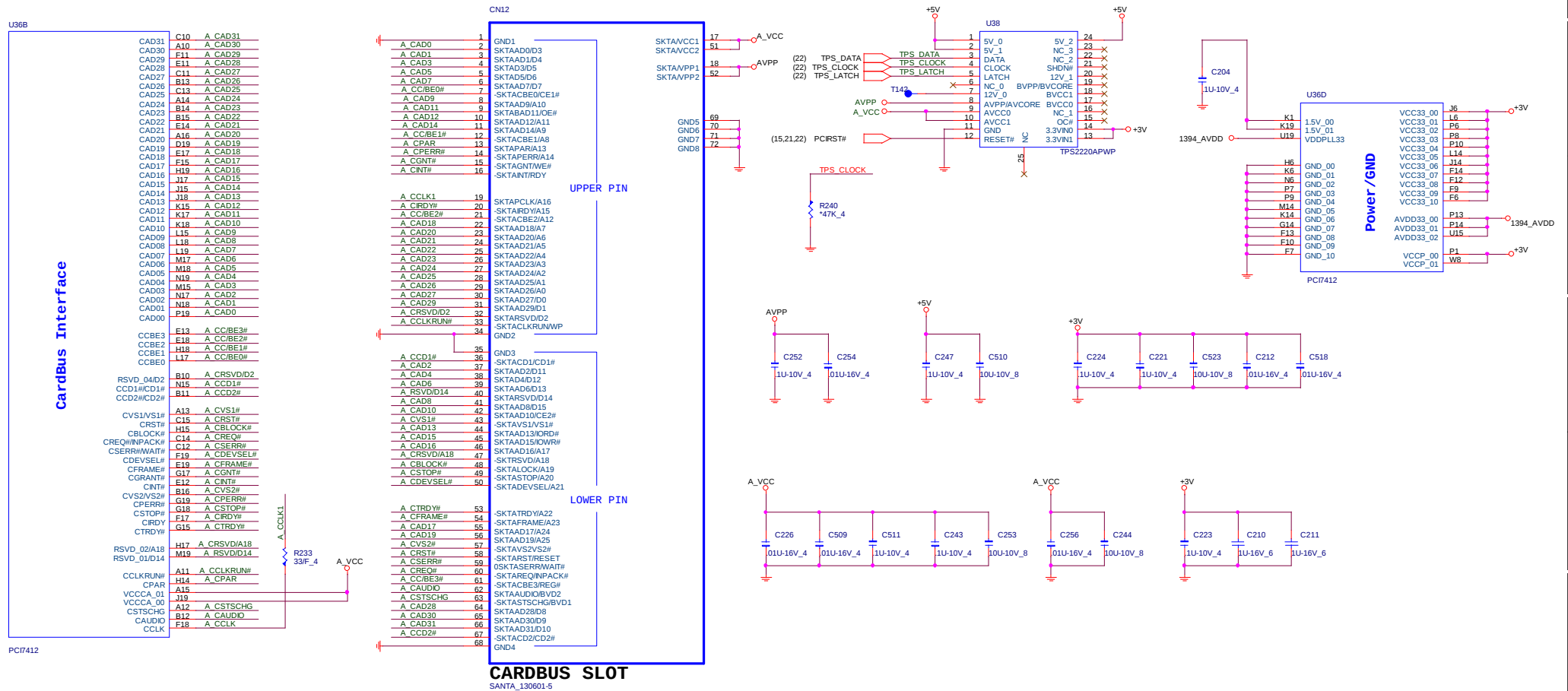


Change 1394 power to SUSC# unmount R278, Mount R280

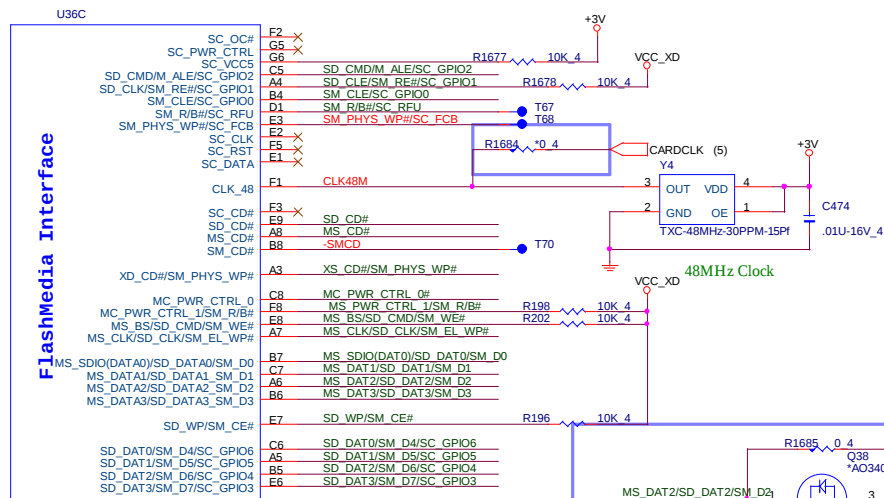


PROJECT : ZH5
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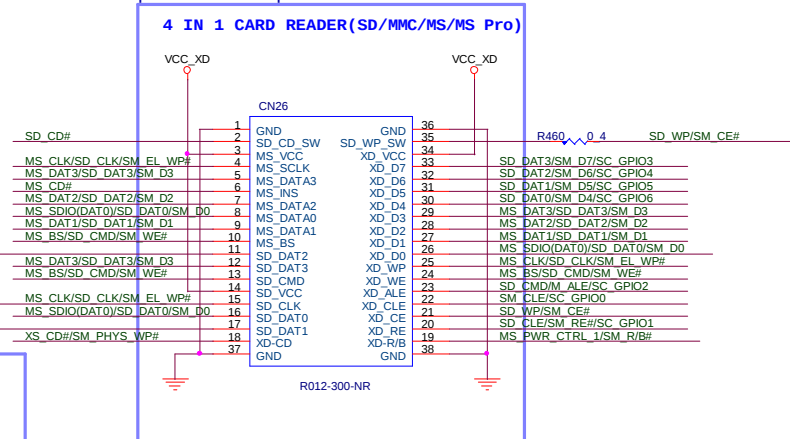
Size	Document Number	Rev
	PCI7412-IEEE1394	1A
Date:	Friday, August 11, 2006	Sheet 22 of 39



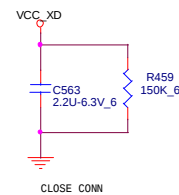
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B2A: Change CN26, modify pin define and update part for MS pro issue fixed



0605- EMI suggest

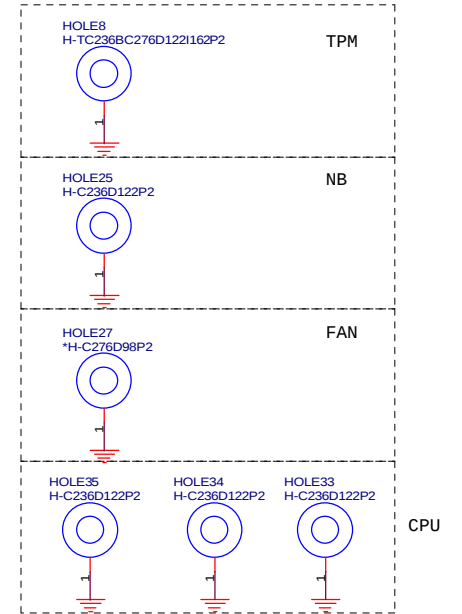
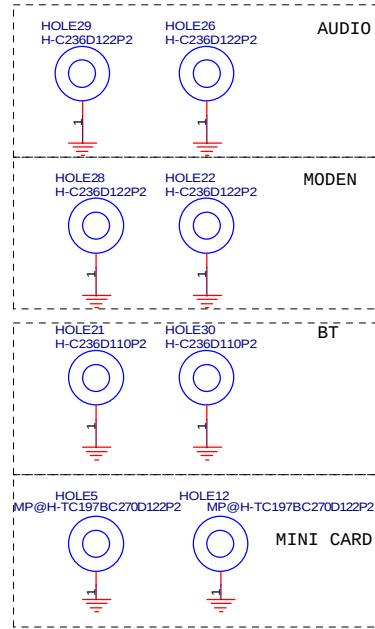
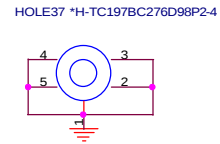
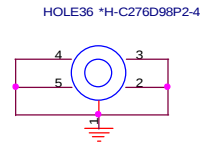
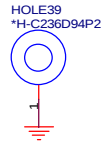
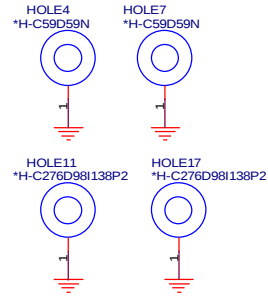
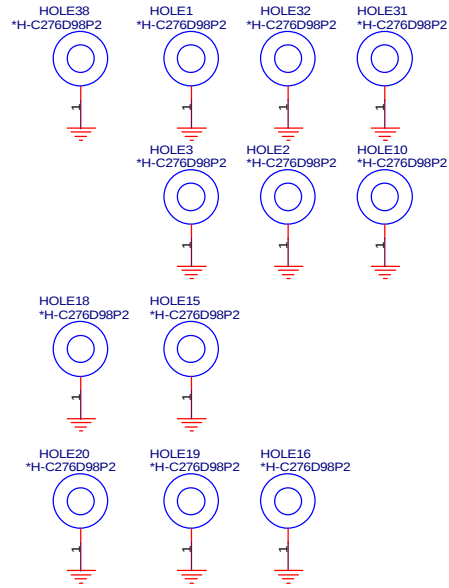


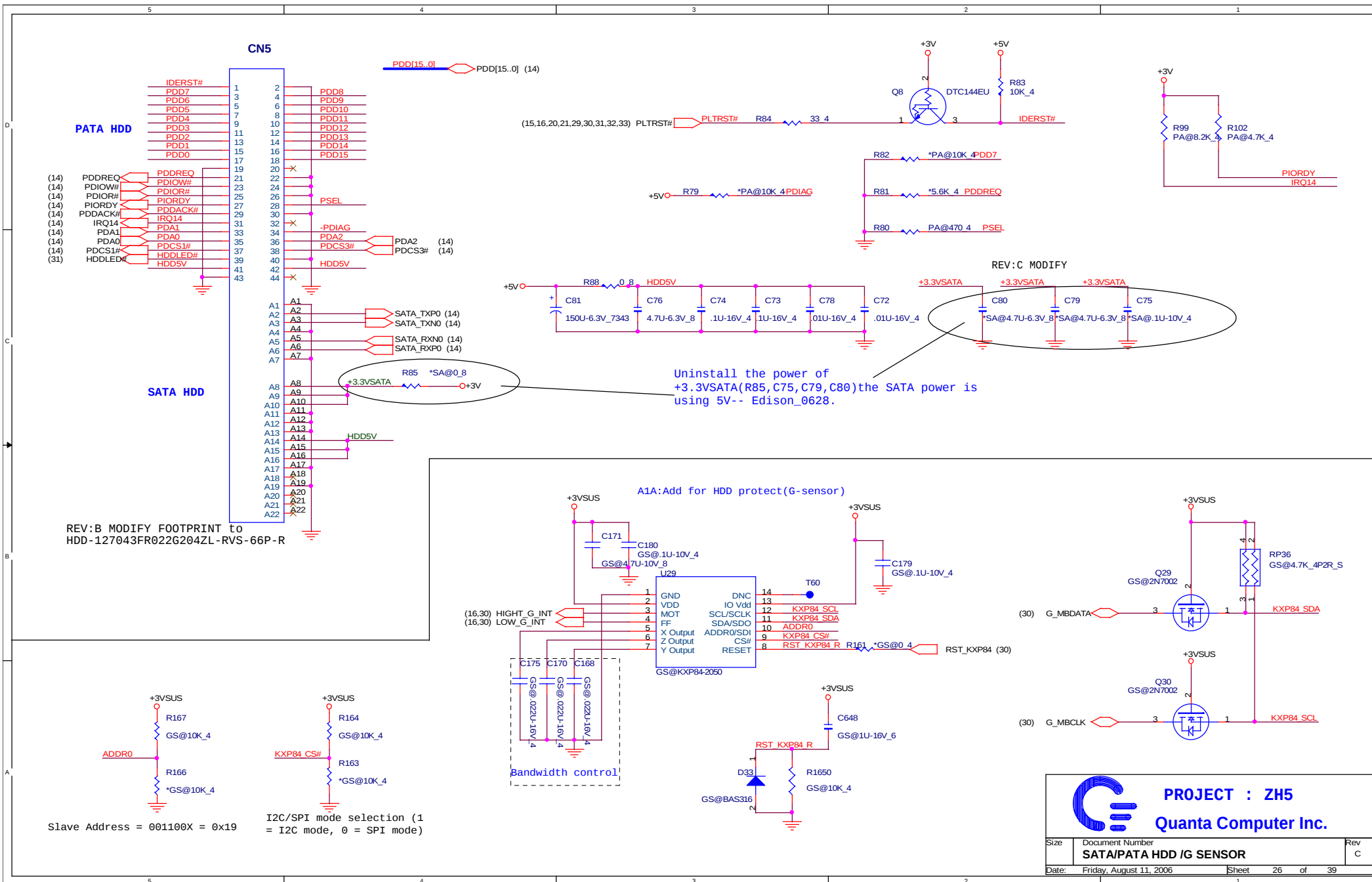
PROJECT : ZH5
Quanta Computer Inc.

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	PCI7412-CARD READER	1A
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TOP

BOT





LINE IN



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Package 0805 for THD+N performance for Vista Logo requirements.

Speaker & Hear-Phone Amplifier

U30 MAX9789A

MAX9789A
TQFN
32PIN

For Speaker

Layout Note:
The C521 close pin9,
The C451 close pin17.

Layout Note:
The C525 close pin18,
The C524 close pin8.

Add A-LDO
option
resistor
---Edison_0628

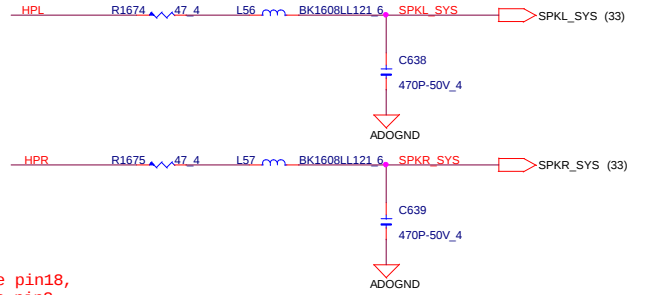
B2A: Unmount R563, Mount R565

GAIN1	GAIN2	GAIN
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

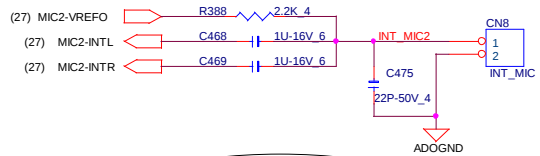
AUDIO BOARD

SPEAKER CONNECTOR

DOCK HEADPHONE



MIC



The Placement close to U27(ALC883/ALC268)

Docking Microphone Input for ALC883 ---Edison_0628



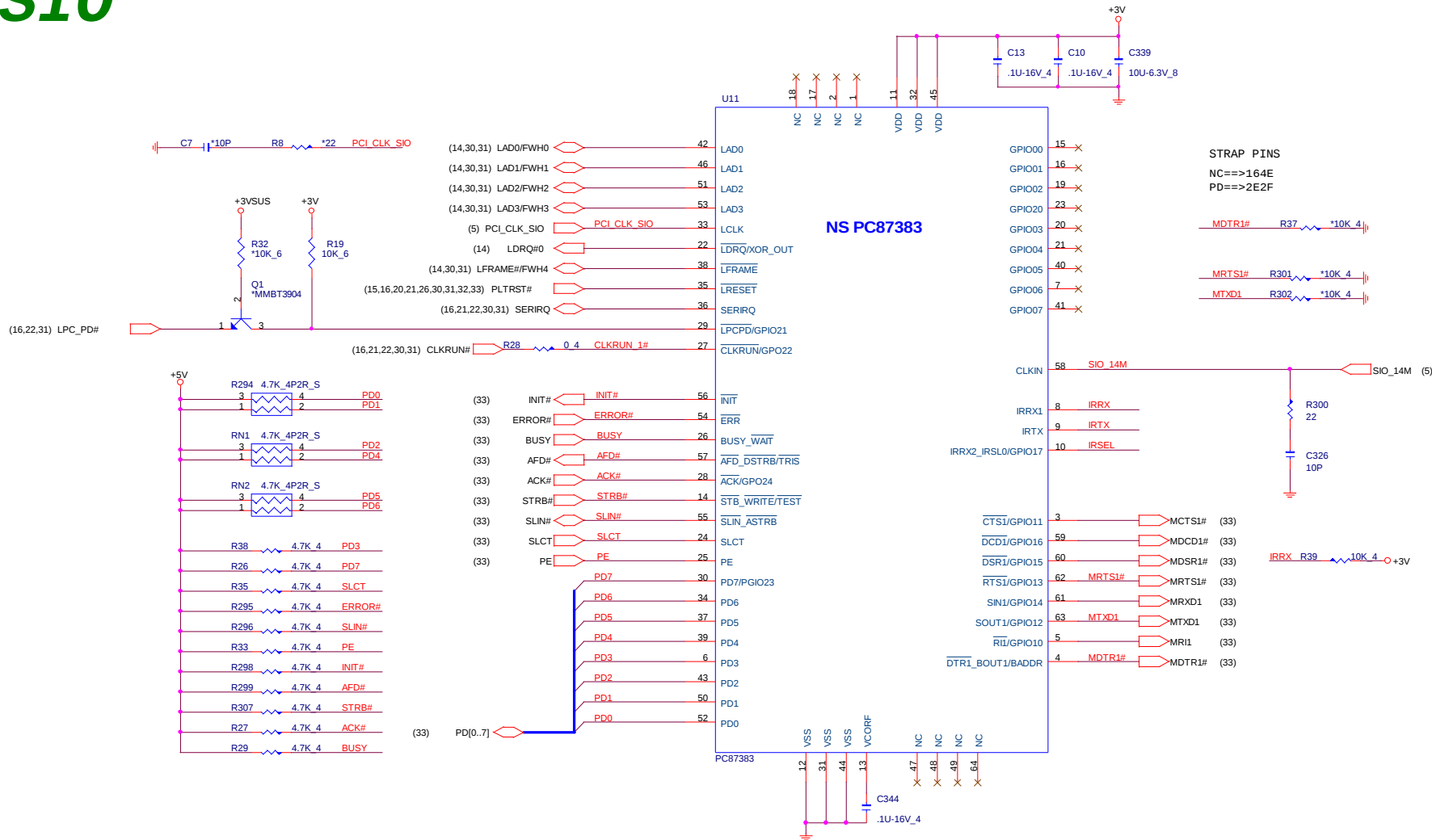
Docking Microphone Input for ALC268 ---Edison_0628



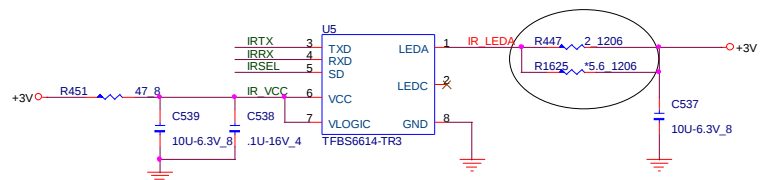
PROJECT : ZH5
Quanta Computer Inc.

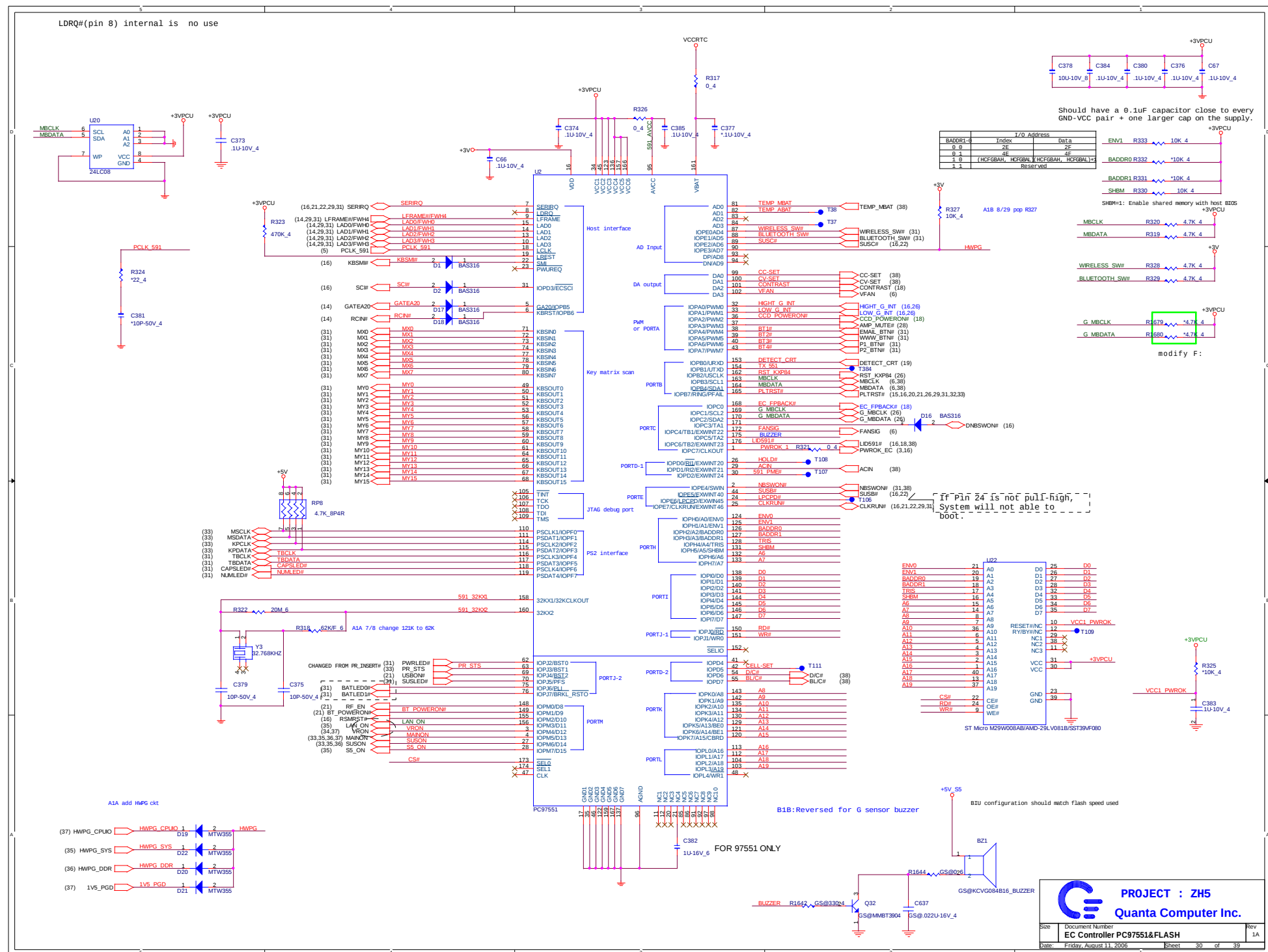
Size	Document Number	Rev
	AUDIO AMP & SPEAK/LIN OUT	1A
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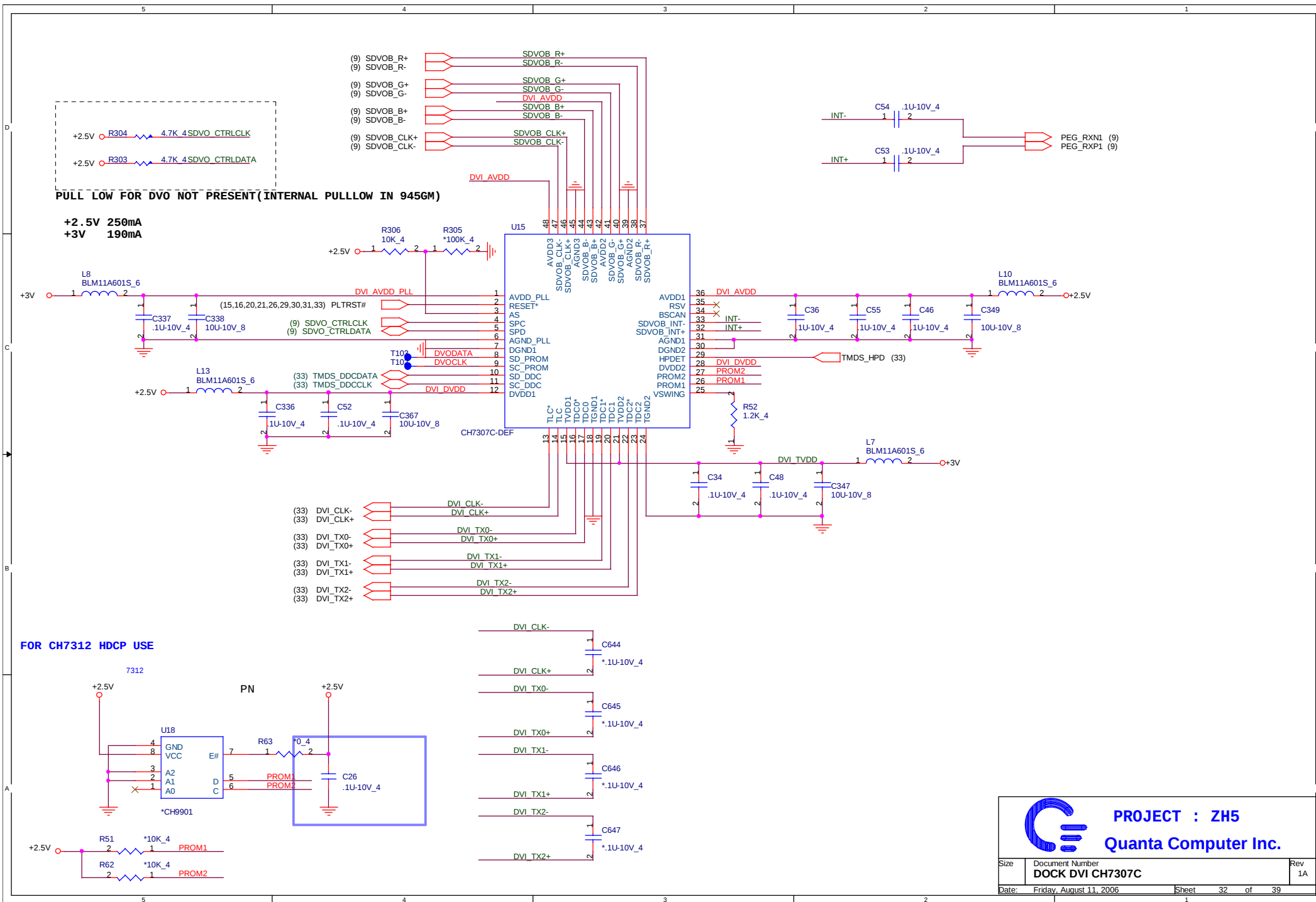
SIO

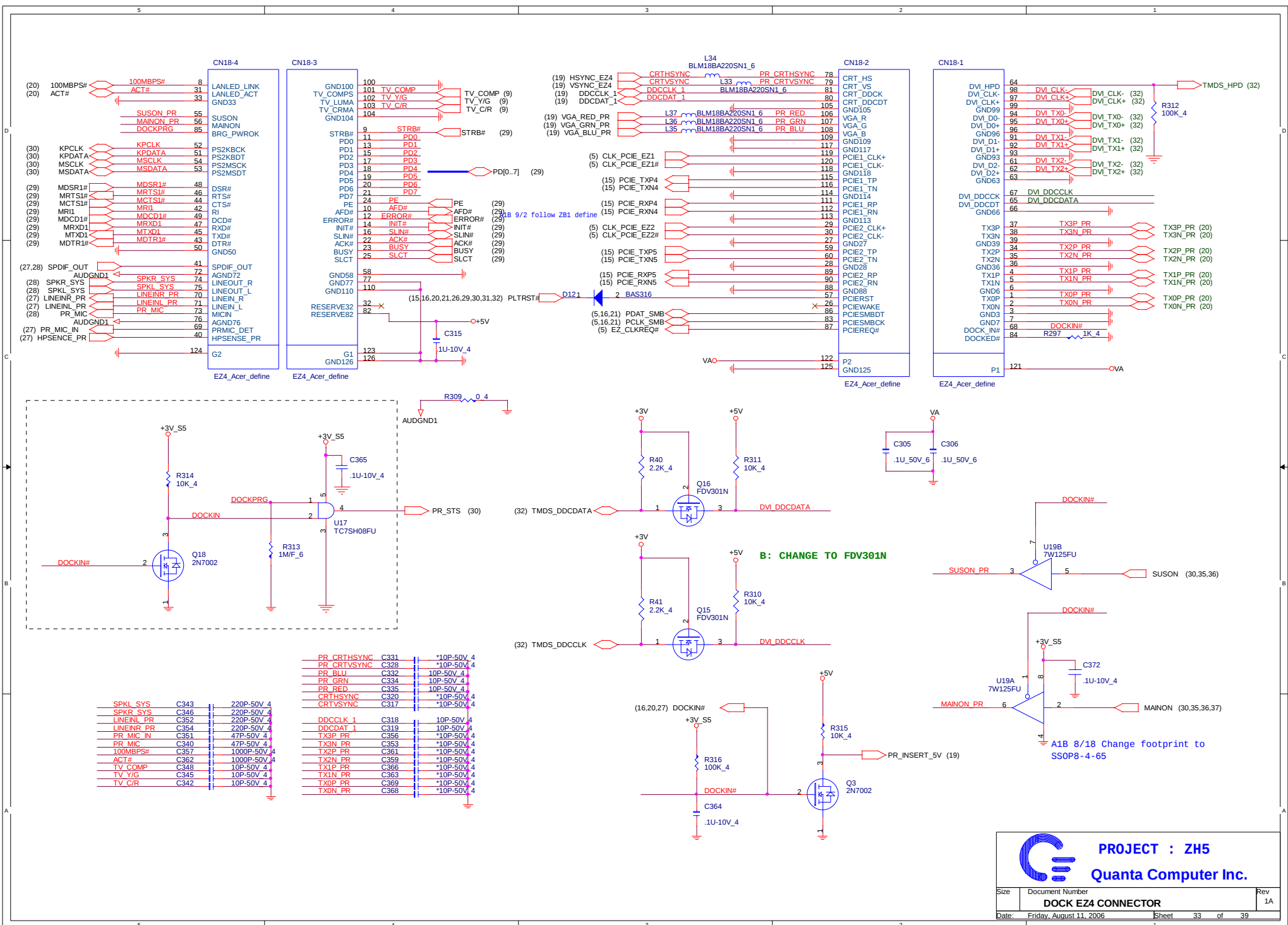


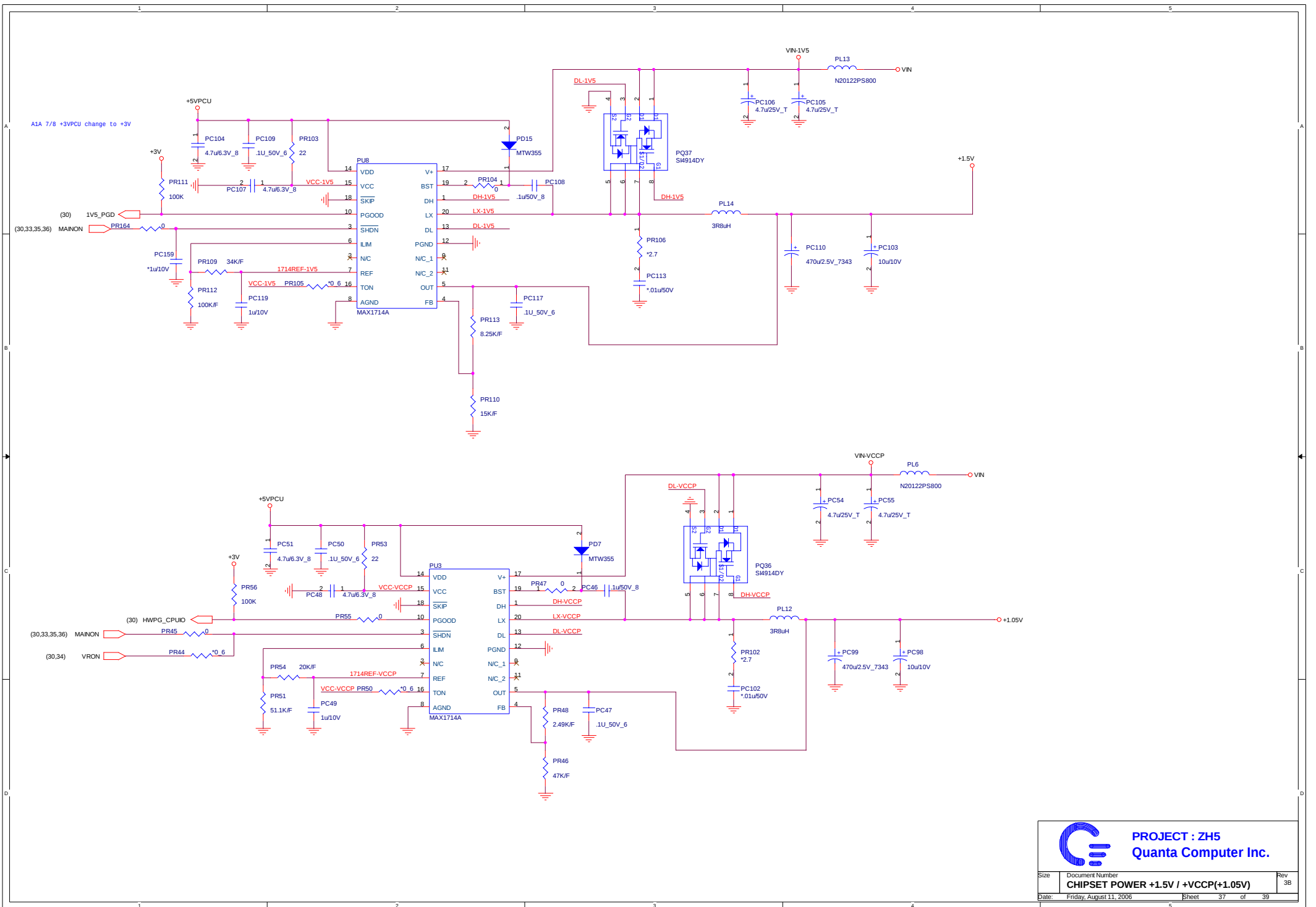
8/29 change trace to 30 mil











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Quanta Computer Inc.

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	CHIPSET POWER +1.5V / +VCCP(+1.05V)	38
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