

Compal Confidential

DCL53 Schematics Document

Banias uFCBGA/uFCPGA Package with Montara-GM Core Logic

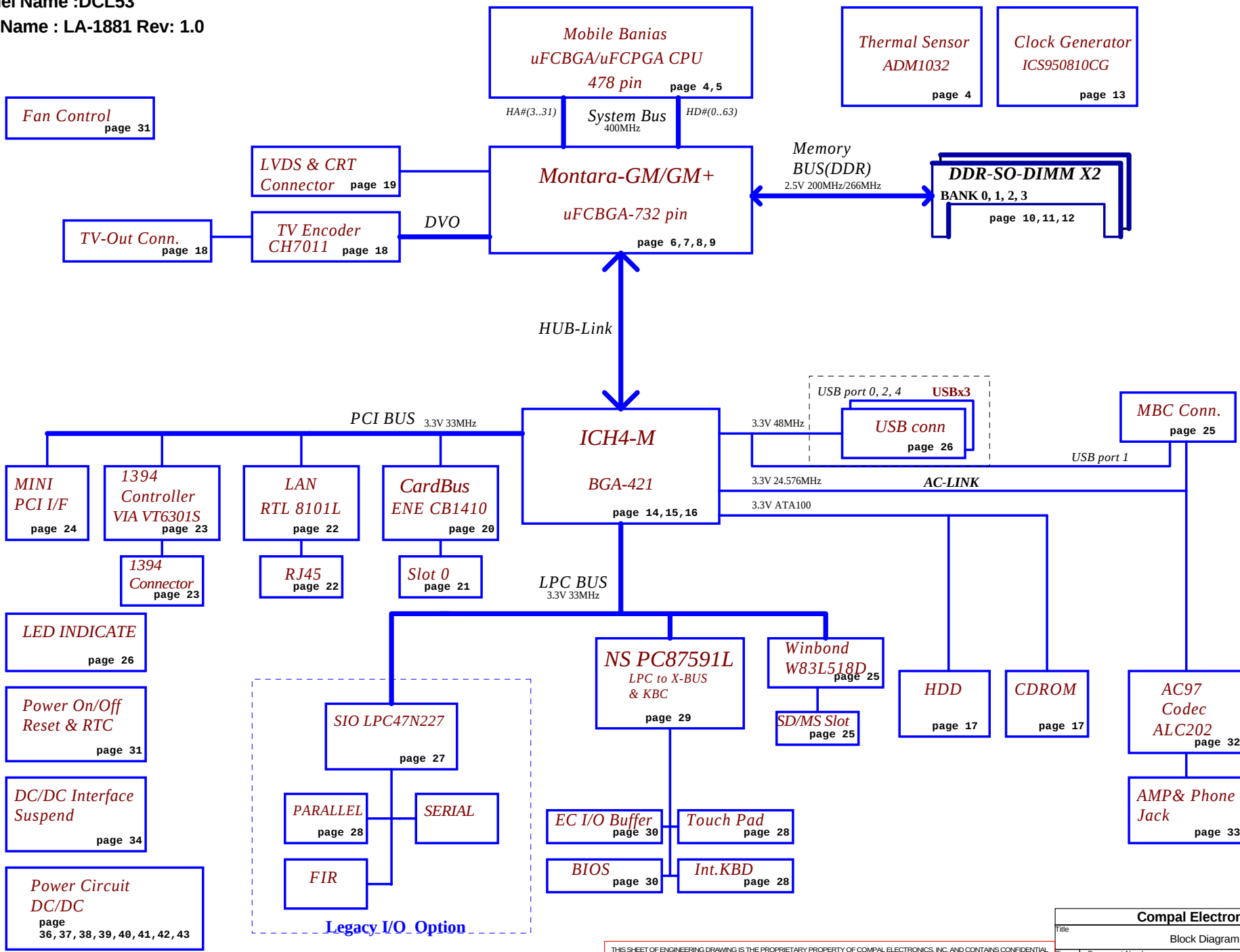
2003-06-12

REV: 1.0

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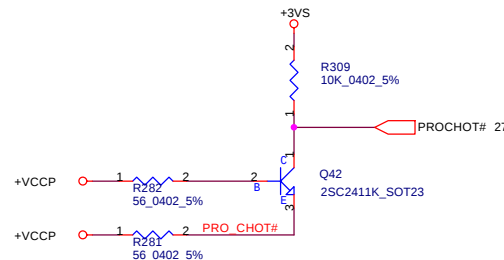
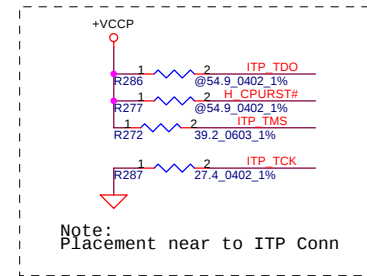
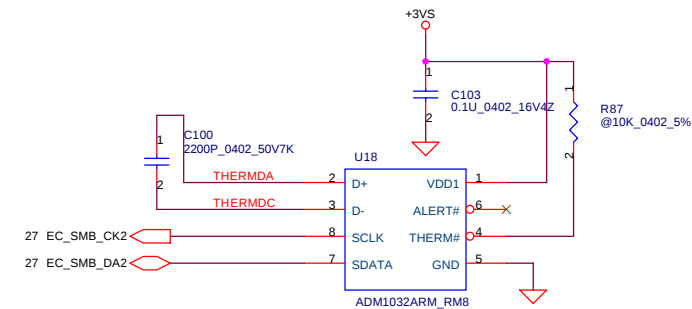
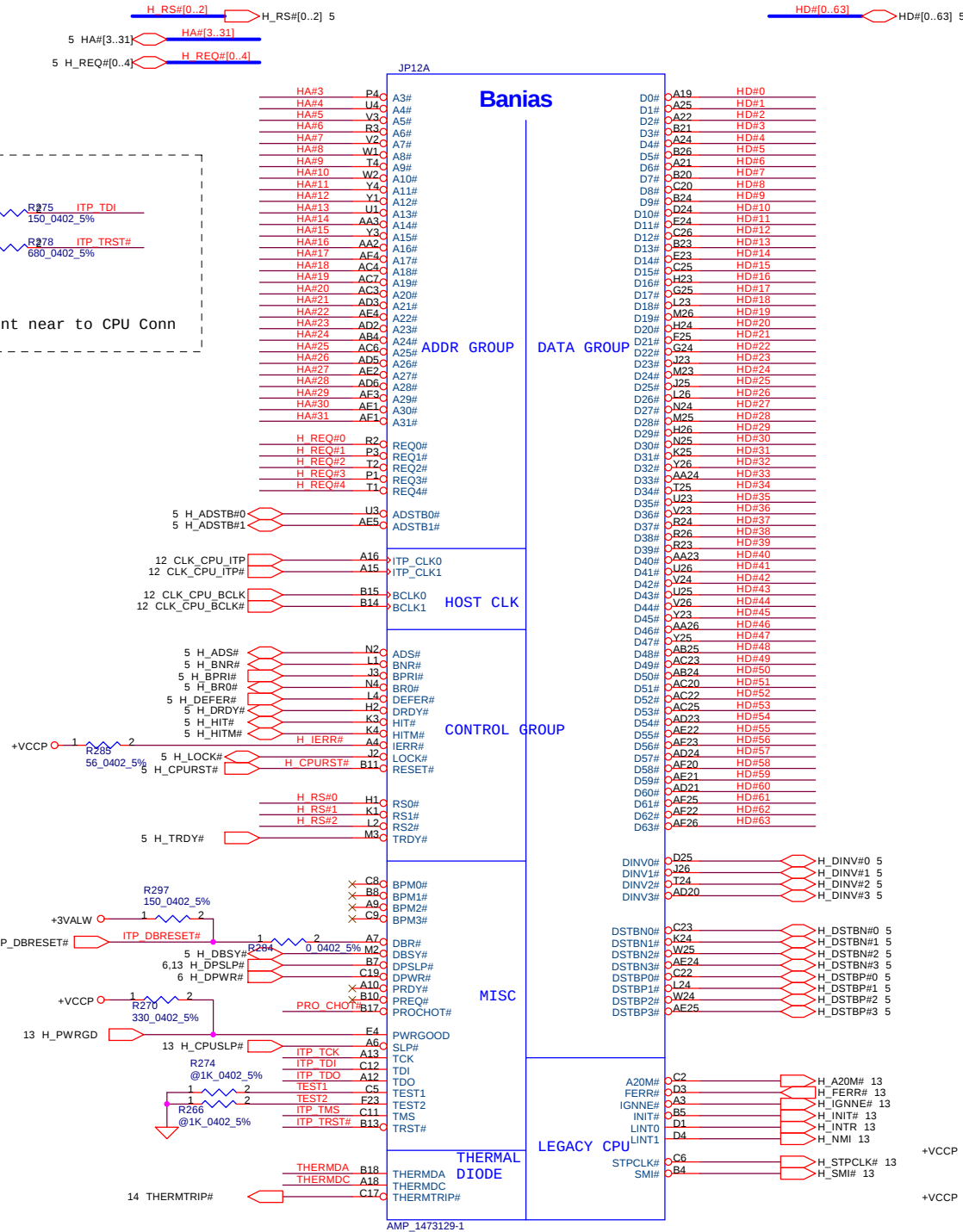
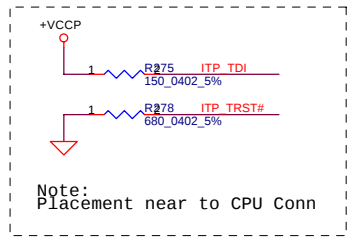
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Title		Cover Sheet	
Size	Document Number		Rev
	DCL53 LA-1881		1.0
Date:	Monday, August 11, 2003	Sheet	1 of 40

Model Name :DCL53
File Name : LA-1881 Rev: 1.0



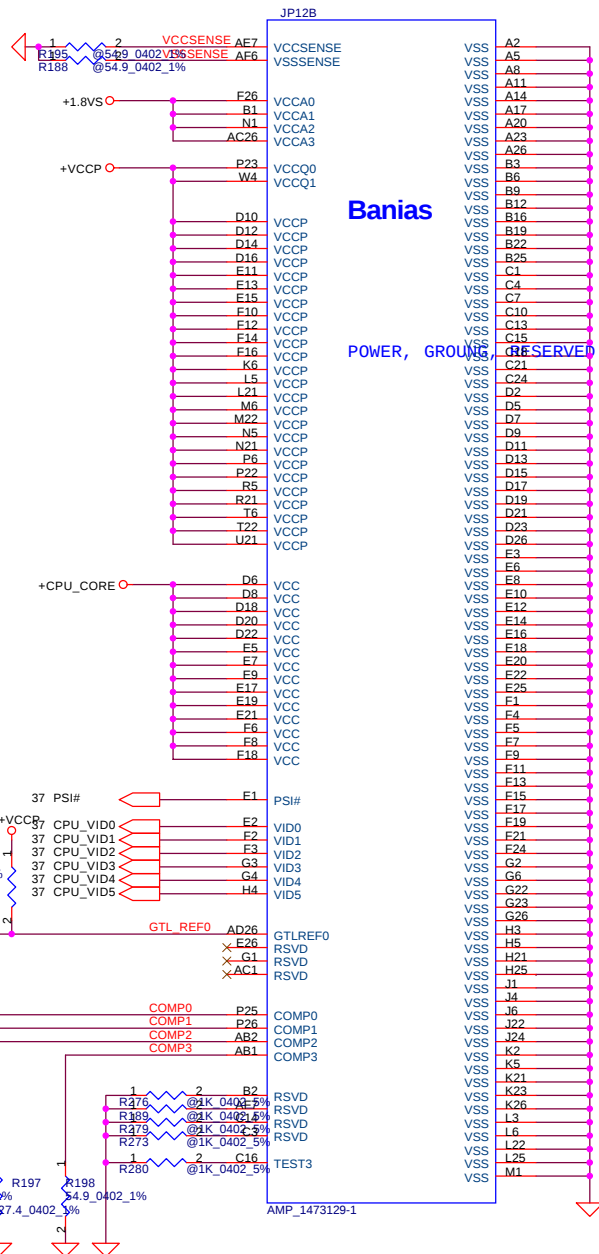
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Title			
Block Diagram			
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	DCL53 LA-1881		1.0
Date:	Monday, August 11, 2003		Sheet 2 of 40



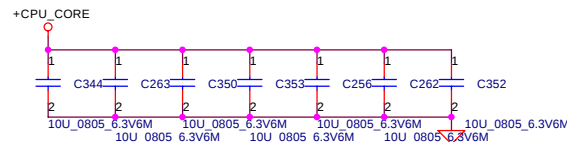
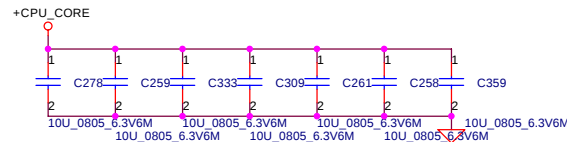
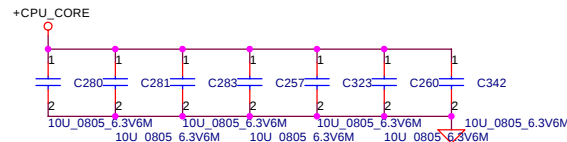
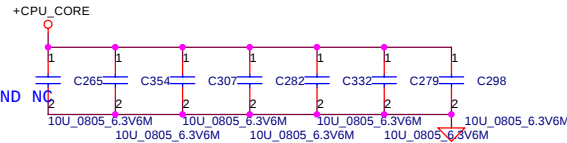
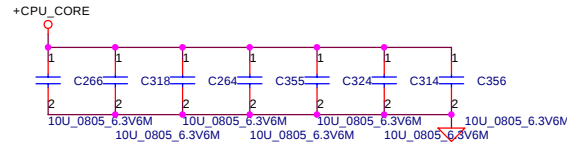
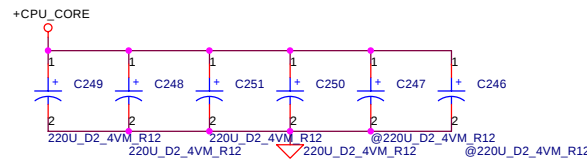
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Title Banias Processor in mFCPGA479 with ITP			
Size A3	Document Number DCL53 LA-1881		Rev 1.0
Date:	Monday, August 11, 2003	Sheet 3	of 40

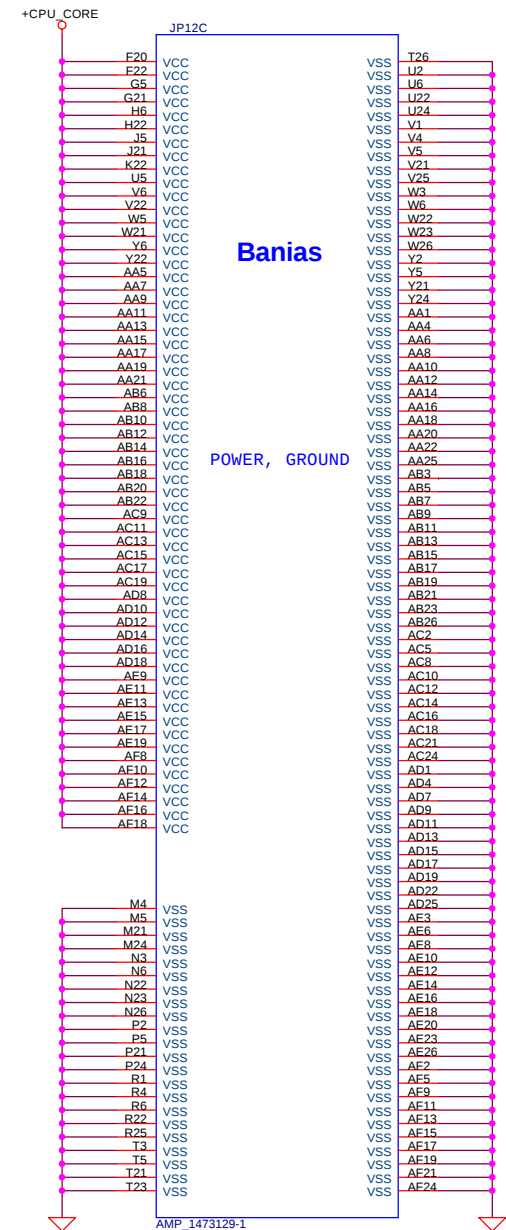
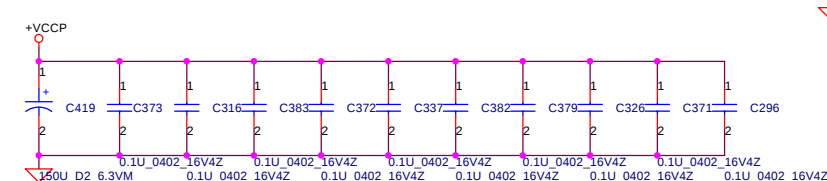
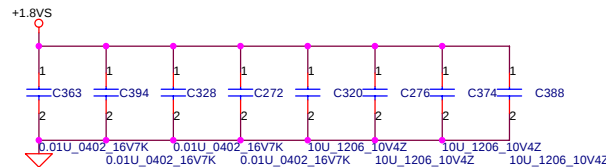


Banias

POWER, GROUND, RESERVED SIGNALS AND NC



Vcc-core Decoupling	C, uF	ESR, mohm	ESL, nH
SPCAP, Polymer	4X220uF	12m ohm/4	3.5nH/4
MLCC 0805 X5R	35X10uF	5m ohm/35	0.6nH/35



Banias

POWER, GROUND

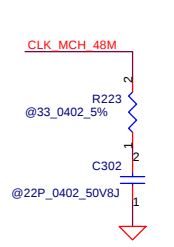
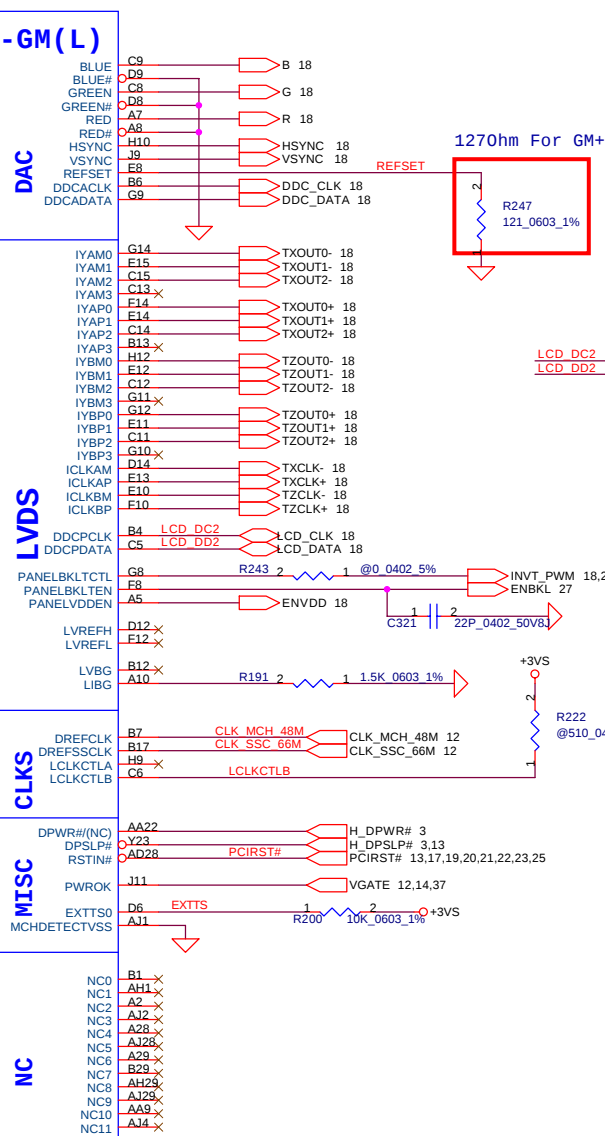
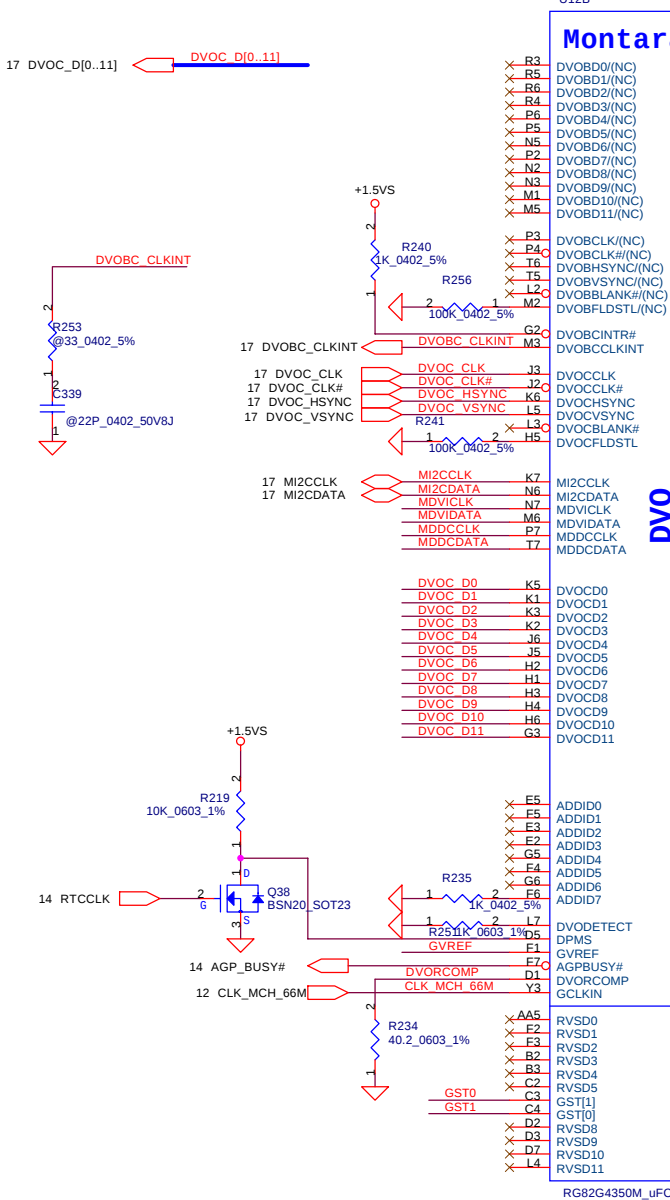
Compal Electronics, Inc.

Title: Banias Processor in mFCPGA479

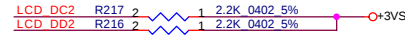
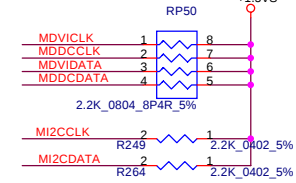
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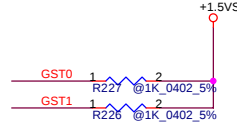
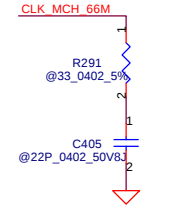
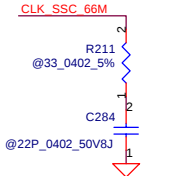
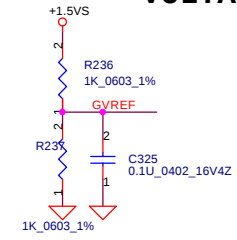
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I2C BUS PULL UP



DVO REF VOLTAGE



Starp pin list

LCLKCTLB: High for P4
N.C. for Banias(default low)
DVO DETECT: Low to enable DVO
High to disable DVO

GST0	GST1	PSB/Sys Mem/ GFX Core
0	0	400/266/200 (default)
0	1	400/200/200
1	0	400/200/133

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Title	MONTARA-GM-LVDS(2/4)		
Size	Document Number	DCL53 LA-1881	
Date	Monday, August 11, 2003	Sheet	6 of 40

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U12C

Montara-GM(L)

9 DDR_SMA0 DDR_SMA0 AC18 SMA0
9,10 DDR_SMA1 DDR_SMA1 AD14 SMA1
9,10 DDR_SMA2 DDR_SMA2 AD13 SMA2
9 DDR_SMA3 DDR_SMA3 AD10 SMA3
9,10 DDR_SMA4 DDR_SMA4 AD11 SMA4
9,10 DDR_SMA5 DDR_SMA5 AC13 SMA5
DDR_SMA6 AD8 SMA6
DDR_SMA7 AD7 SMA7
DDR_SMA8 AC6 SMA8
DDR_SMA9 AC9 SMA9
DDR_SMA10 AC19 SMA10
DDR_SMA11 AD5 SMA11
DDR_SMA12 AB5 SMA12

DDR_SQ09 AG2 SQ09
DDR_SQ01 AH5 SQ01
DDR_SQ02 AH8 SQ02
DDR_SQ03 AE12 SQ03
DDR_SQ04 AH17 SQ04
DDR_SQ05 AE21 SQ05
DDR_SQ06 AH24 SQ06
DDR_SQ07 AH27 SQ07
DDR_SQ08 AD15 SQ08

9 DDR_SWE# DDR_SWE# AD25 SWE#
9 DDR_SRAS# DDR_SRAS# AC21 SRAS#
9 DDR_SCAS# DDR_SCAS# AC24 SCAS#

9 DDR_CLK0 AB2 SCK0
9 DDR_CLK0# AA2 SCK0#
9 DDR_CLK1 AC26 SCK1
9 DDR_CLK1# AB25 SCK1#
9 DDR_CLK2 AC3 SCK2
9 DDR_CLK2# AD4 SCK2#
10 DDR_CLK3 AC2 SCK3
10 DDR_CLK3# AD2 SCK3#
10 DDR_CLK4 AB23 SCK4
10 DDR_CLK4# AB24 SCK4#
10 DDR_CLK5 AA3 SCK5
10 DDR_CLK5# AB4 SCK5#

9,10 DDR_CKE0 AC7 SCKE0
9,10 DDR_CKE1 AB7 SCKE1
10 DDR_CKE2 AC9 SCKE2
10 DDR_CKE3 AC10 SCKE3
9,10 DDR_SCS#0 AD23 SCS#0
9,10 DDR_SCS#1 AD26 SCS#1
10 DDR_SCS#2 AC22 SCS#2
10 DDR_SCS#3 AC25 SCS#3

9 DDR_SBS0 DDR_SBS0 AD22 SBA0
9 DDR_SBS1 DDR_SBS1 AD20 SBA1

DDR_SDM0 AE5 SDM0
DDR_SDM1 AE6 SDM1
DDR_SDM2 AE9 SDM2
DDR_SDM3 AH12 SDM3
DDR_SDM4 AD19 SDM4
DDR_SDM5 AD21 SDM5
DDR_SDM6 AD24 SDM6
DDR_SDM7 AH28 SDM7
DDR_SDM8 AH15 SDM8

10 DDR_SMA_B1 DDR_SMA_B1 AD16 SMA_B1
10 DDR_SMA_B2 DDR_SMA_B2 AC12 SMA_B2
10 DDR_SMA_B4 DDR_SMA_B4 AF11 SMA_B4
10 DDR_SMA_B5 DDR_SMA_B5 AD10 SMA_B5

AC15 RCVENOUT#
AC16 RCVENIN#
MRCOMP AB1 SMRCOMP
MVSWINGL AJ22 SMVSWINGL
MVSWINGH AJ19 SMVSWINGH

RG82G4350M_uFCBGA732

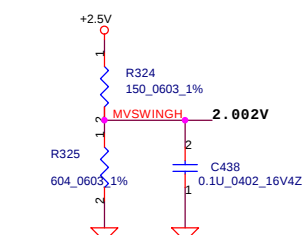
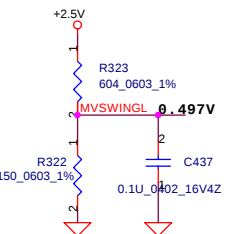
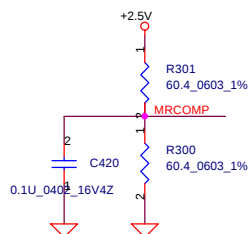
MEMORY

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SDQ1 AE3 DDR_SQ01
SDQ2 AF4 DDR_SQ02
SDQ3 AH2 DDR_SQ03
SDQ4 AD3 DDR_SQ04
SDQ5 AE2 DDR_SQ05
SDQ6 AG4 DDR_SQ06
SDQ7 AH3 DDR_SQ07
SDQ8 AD6 DDR_SQ08
SDQ9 AG5 DDR_SQ09
SDQ10 AE7 DDR_SQ10
SDQ11 AG7 DDR_SQ11
SDQ12 AF5 DDR_SQ12
SDQ13 AH4 DDR_SQ13
SDQ14 AE7 DDR_SQ14
SDQ15 AH6 DDR_SQ15
SDQ16 AG8 DDR_SQ16
SDQ17 AH9 DDR_SQ17
SDQ18 AG10 DDR_SQ18
SDQ19 AH7 DDR_SQ19
SDQ20 AD9 DDR_SQ20
SDQ21 AF10 DDR_SQ21
SDQ22 AE11 DDR_SQ22
SDQ23 AH10 DDR_SQ23
SDQ24 AG11 DDR_SQ24
SDQ25 AH11 DDR_SQ25
SDQ26 AG13 DDR_SQ26
SDQ27 AF14 DDR_SQ27
SDQ28 AG11 DDR_SQ28
SDQ29 AD12 DDR_SQ29
SDQ30 AE13 DDR_SQ30
SDQ31 AH13 DDR_SQ31
SDQ32 AH16 DDR_SQ32
SDQ33 AG17 DDR_SQ33
SDQ34 AE19 DDR_SQ34
SDQ35 AD18 DDR_SQ35
SDQ36 AE18 DDR_SQ36
SDQ37 AH18 DDR_SQ37
SDQ38 AG19 DDR_SQ38
SDQ39 AD20 DDR_SQ39
SDQ40 AG20 DDR_SQ40
SDQ41 AG20 DDR_SQ41
SDQ42 AE22 DDR_SQ42
SDQ43 AE22 DDR_SQ43
SDQ44 AH19 DDR_SQ44
SDQ45 AE22 DDR_SQ45
SDQ46 AH21 DDR_SQ46
SDQ47 AG22 DDR_SQ47
SDQ48 AE23 DDR_SQ48
SDQ49 AH23 DDR_SQ49
SDQ50 AE24 DDR_SQ50
SDQ51 AH25 DDR_SQ51
SDQ52 AG23 DDR_SQ52
SDQ53 AE23 DDR_SQ53
SDQ54 AE25 DDR_SQ54
SDQ55 AG25 DDR_SQ55
SDQ56 AE26 DDR_SQ56
SDQ57 AG28 DDR_SQ57
SDQ58 AE28 DDR_SQ58
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SDQ60 AG26 DDR_SQ60
SDQ61 AE26 DDR_SQ61
SDQ62 AE27 DDR_SQ62
SDQ63 AD27 DDR_SQ63

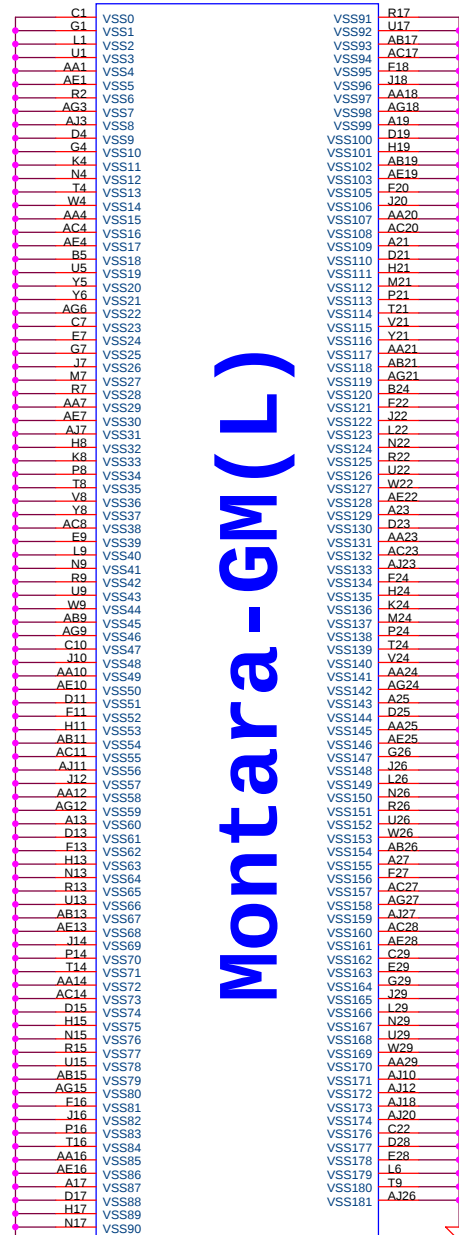
SDQ64 AG14 DDR_CB0
SDQ65 AE14 DDR_CB1
SDQ66 AG17 DDR_CB2
SDQ67 AG16 DDR_CB3
SDQ68 AH14 DDR_CB4
SDQ69 AE15 DDR_CB5
SDQ70 AE16 DDR_CB6
SDQ71 AF17 DDR_CB7

SMVREF0 AJ24 +SDREF
C436 0.1U_0402_16V4Z
C437 0.1U_0402_16V4Z

DDR REF & SWING VOLTAGE



U12D

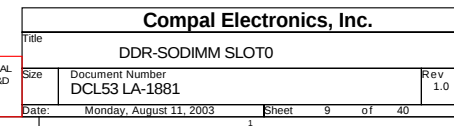


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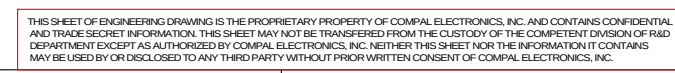
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			DCL53 LA-1881		
Date:			Monday, August 11, 2003	Sheet	7 of 40

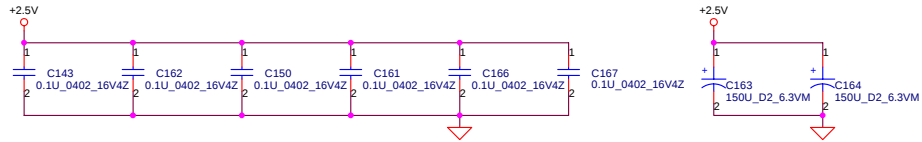
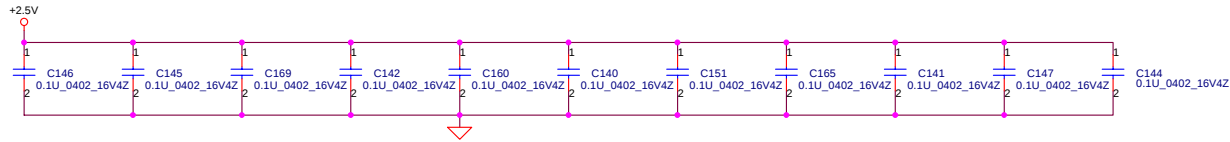
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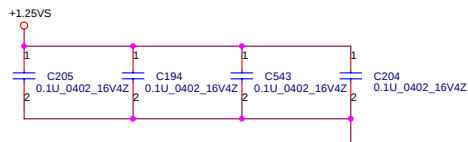
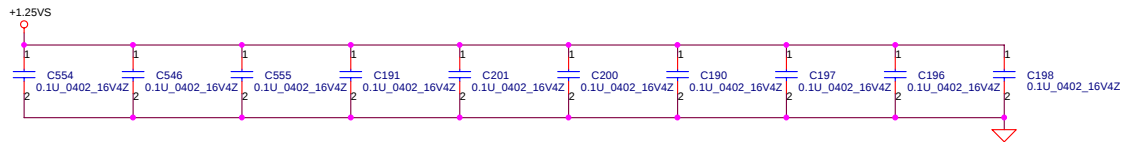
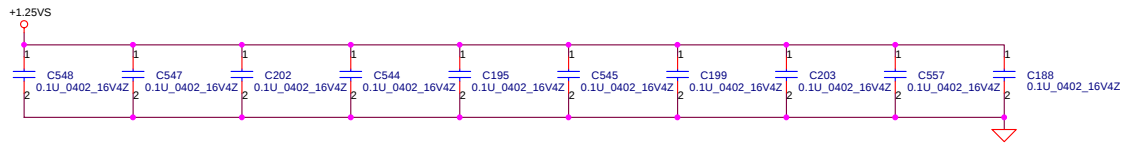
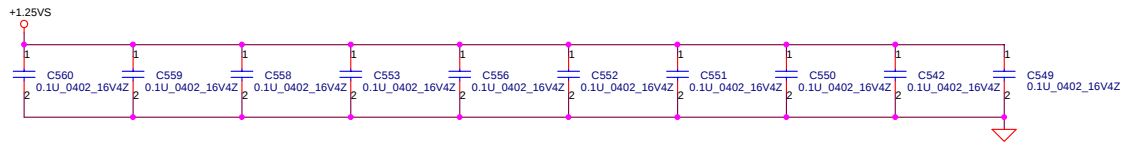
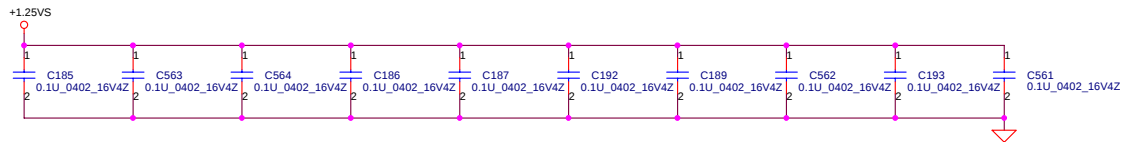
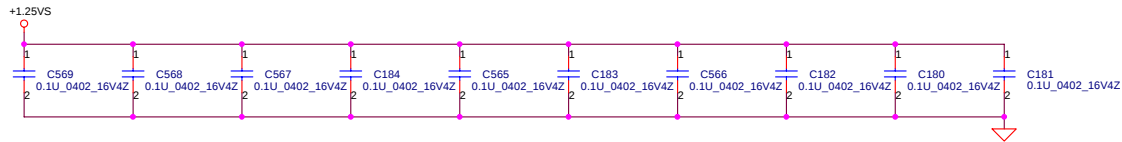
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Layout note :
Distribute as close as possible
to DDR-SODIMM.



Layout note :
Place one cap close to every 2 pull up resistors termination to
+1.25V



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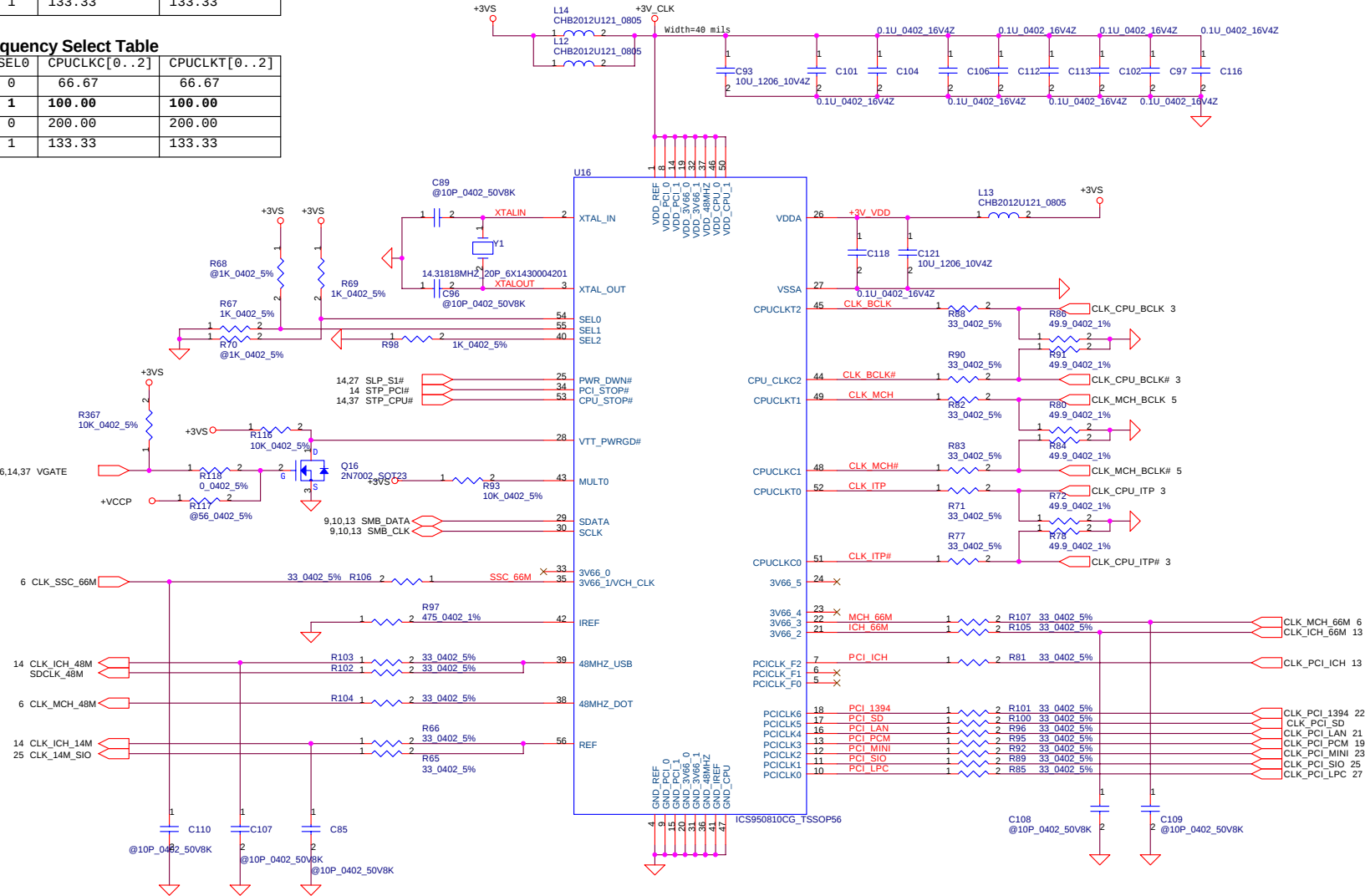
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DDR SODIMM Decoupling			
Size	Document Number		Rev
	DCL53 LA-1881		1.0
Date:	Monday, August 11, 2003	Sheet	11 of 40

ICS950810 Frequency Select Table

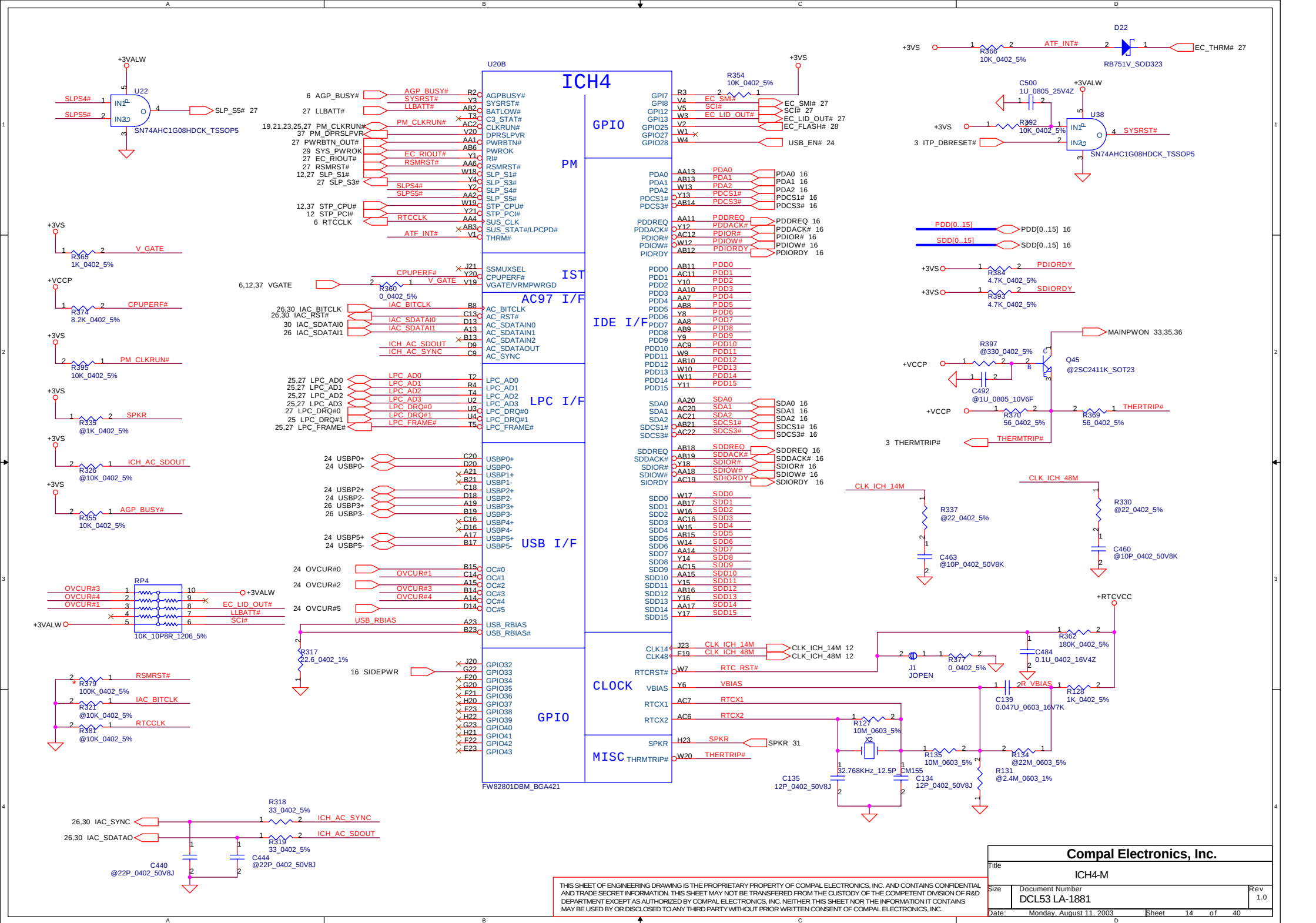
SEL2	SEL1	SEL0	CPUCLKC[0..2]	CPUCLKT[0..2]
0	0	0	166.67	166.67
0	0	1	100.00	100.00
0	1	0	200.00	200.00
0	1	1	133.33	133.33

CY28346-2 Frequency Select Table

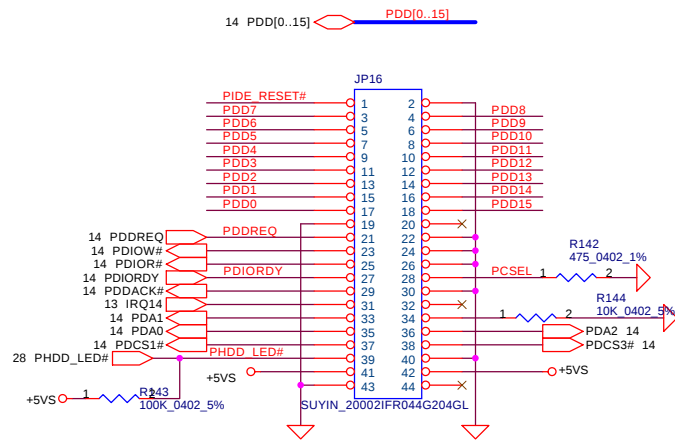
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0	0	0	66.67	66.67
0	0	1	100.00	100.00
0	1	0	200.00	200.00
0	1	1	133.33	133.33



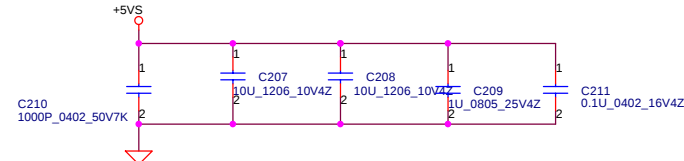
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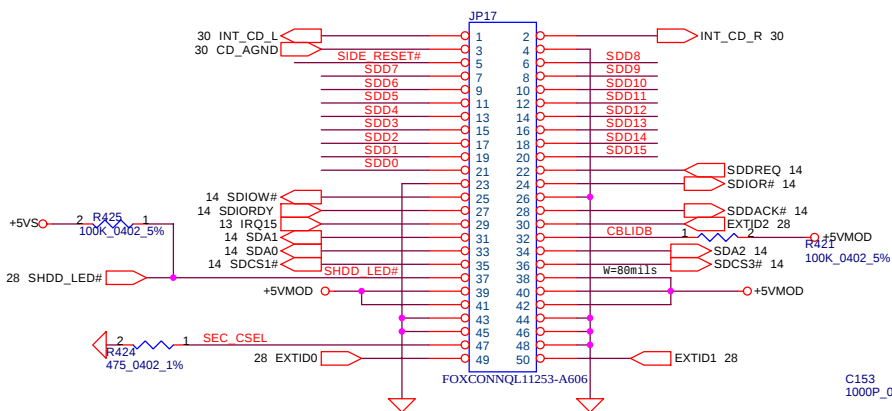
HDD Connector



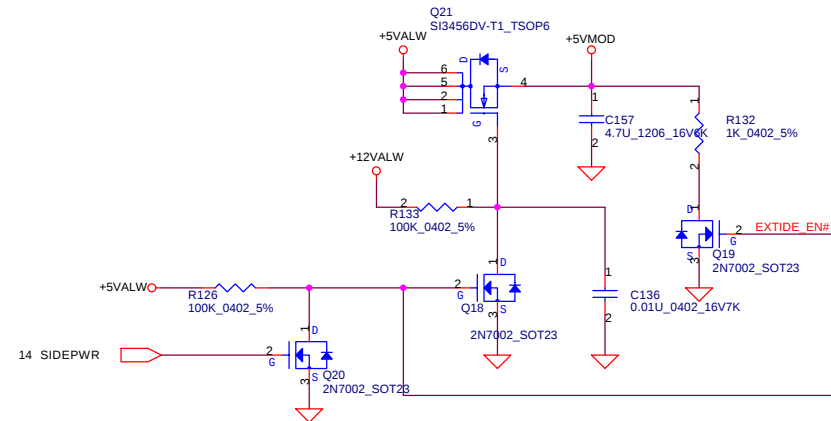
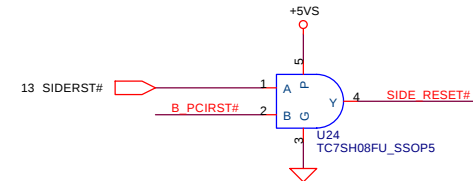
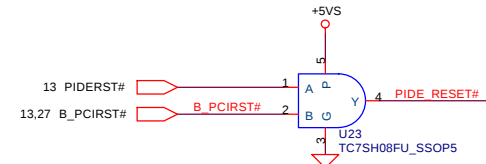
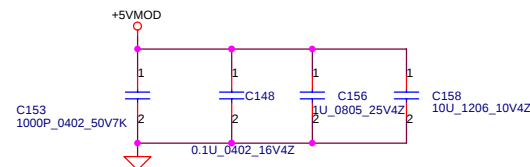
Placea caps. near HDD
CONN.



CD-ROM Connector

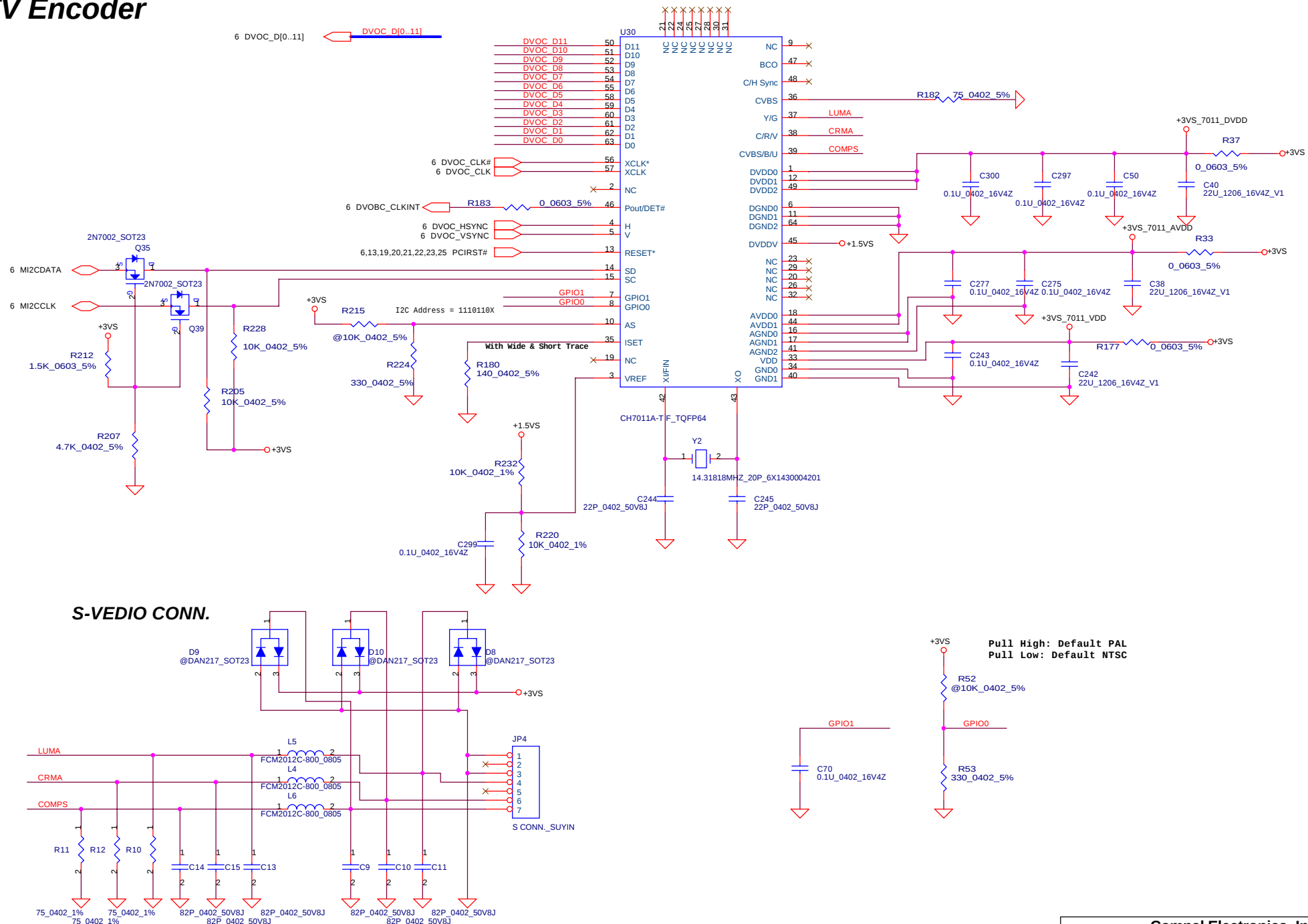


*Placea caps. near CDROM
CONN.*



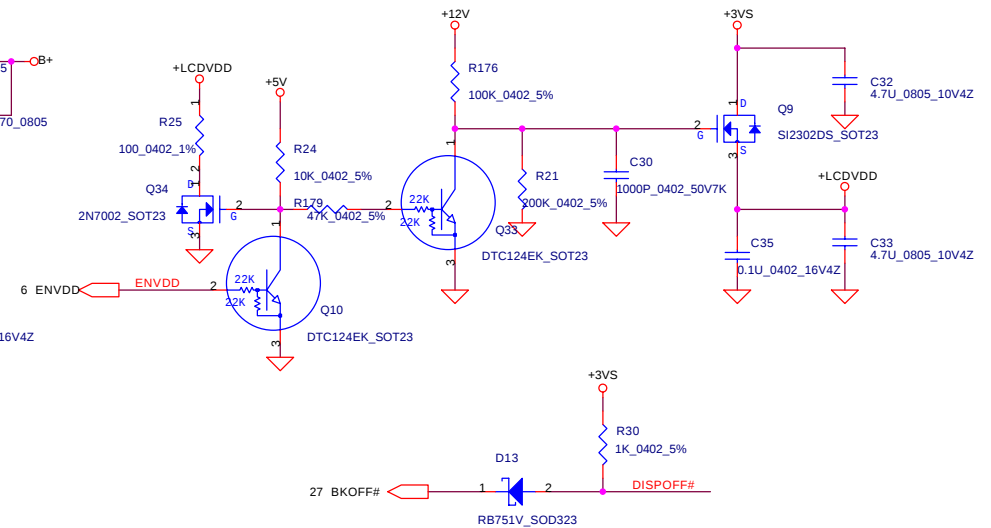
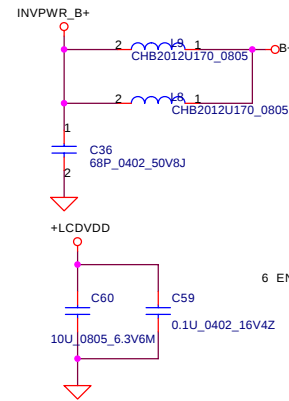
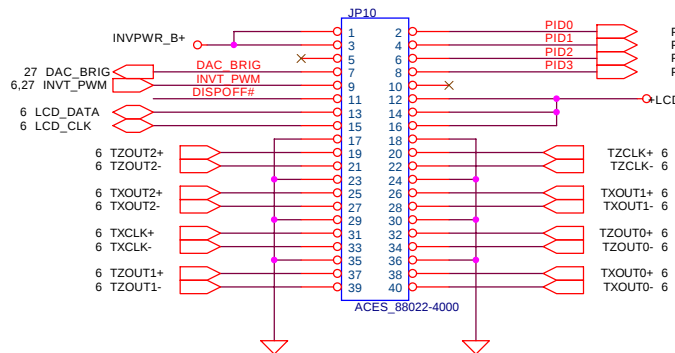
SI3456DV: N CHANNEL
VGS: 4.5V, RDS: 65 mOHM
Id(MAX): 5.1A
VGS, +20V

TV Encoder

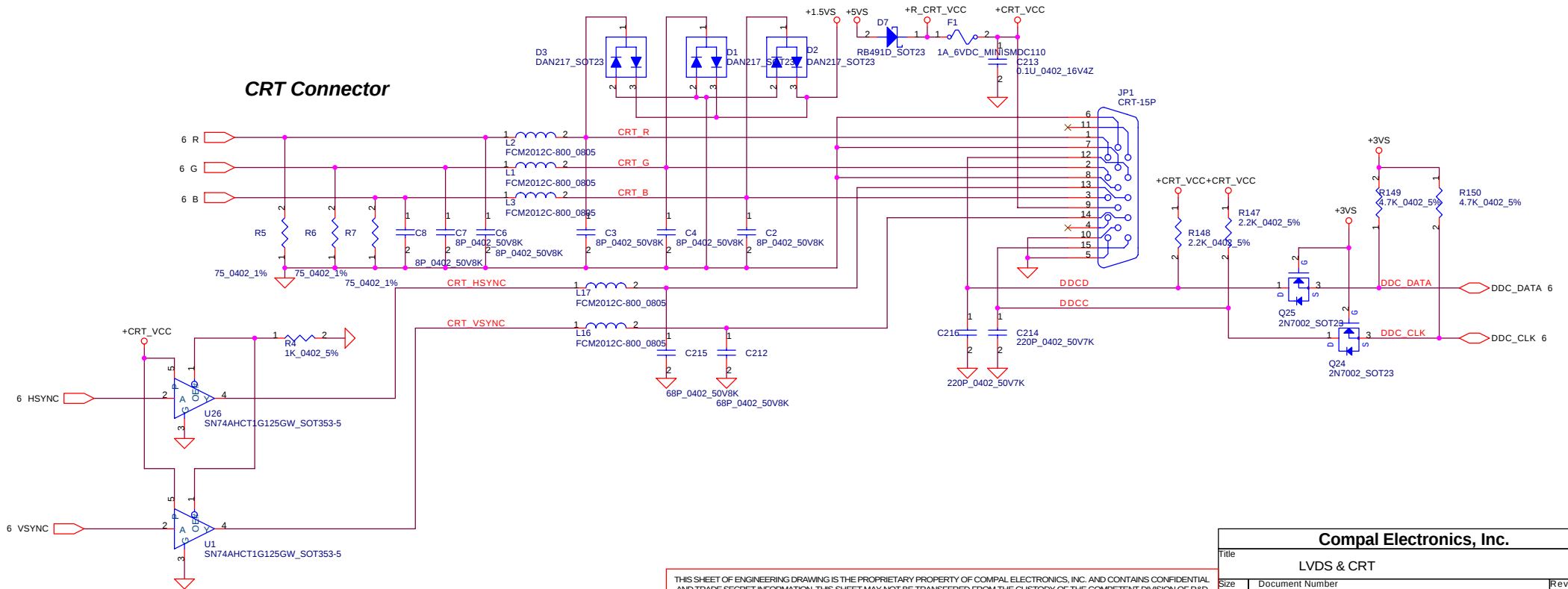


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LVDS Connector



CRT Connector



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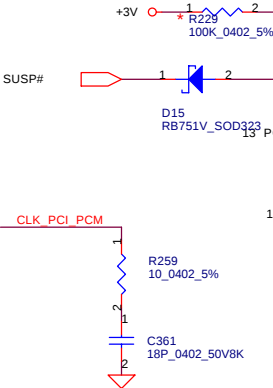
Compal Electronics, Inc.			
Title			
LVDS & CRT			
Size	Document Number		Rev
	DCL53 LA-1881		1.0
Date:	Monday, August 11, 2003	Sheet	18 of 40

13,21,22,23 PCI_AD[0..31]

PCI_AD31 3
PCI_AD30 4
PCI_AD29 5
PCI_AD28 7
PCI_AD27 8
PCI_AD26 9
PCI_AD25 10
PCI_AD24 11
PCI_AD23 15
PCI_AD22 16
PCI_AD21 17
PCI_AD20 19
PCI_AD19 23
PCI_AD18 24
PCI_AD17 25
PCI_AD16 26
PCI_AD15 38
PCI_AD14 39
PCI_AD13 40
PCI_AD12 41
PCI_AD11 43
PCI_AD10 45
PCI_AD9 46
PCI_AD8 47
PCI_AD7 49
PCI_AD6 51
PCI_AD5 52
PCI_AD4 53
PCI_AD3 54
PCI_AD2 55
PCI_AD1 56
PCI_AD0 57

PQFP 144
22.2 X 22.2 X 1.60

13,21,22,23 PCI_C/BE#3 12
13,21,22,23 PCI_C/BE#2 27
13,21,22,23 PCI_C/BE#1 37
13,21,22,23 PCI_C/BE#0 48
6,13,17,20,21,22,23,25 PCIRST# 20
13,21,22,23 PCI_FRAME# 28
13,21,22,23 PCI_IRDY# 29
13,21,22,23 PCI_TRDY# 31
13,21,22,23 PCI_DEVSEL# 32
13,21,22,23 PCI_STOP# 33
13,21,22,23 PCI_PERR# 34
13,21,23 PCI_SERR# 35
13,21,22,23 PCI_PAR# 36
13 PCI_REQ#2 1
13 PCI_GNT#2 2
12 CLK_PCI_PCM 21
21,22,23,27 PCM_PME# 58
27,28,32,36,39,40 SUSP# 70
13 PCI_PIRQA# 60
27 PCM_RI# 61
13,25,27 SIRO 65
14,21,23,25,27 PM_CLKRUN# 66
20 CBRST# 66



21,22,23,27 PCM_PME#

13 PCI_PIRQA#

27 PCM_RI#

13,25,27 SIRO

14,21,23,25,27 PM_CLKRUN#

20 CBRST#

13 PCI_PIRQA#

27 PCM_RI#

13,25,27 SIRO

14,21,23,25,27 PM_CLKRUN#

20 CBRST#

13 PCI_PIRQA#

27 PCM_RI#

13,25,27 SIRO

14,21,23,25,27 PM_CLKRUN#

20 CBRST#

13 PCI_PIRQA#

27 PCM_RI#

13,25,27 SIRO

14,21,23,25,27 PM_CLKRUN#

20 CBRST#

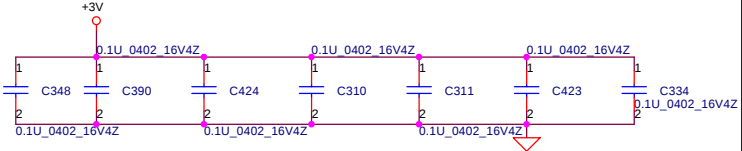
C/BE#3 12
C/BE#2 27
C/BE#1 37
C/BE#0 48
RST# 20
FRAME# 28
IRDY# 29
TRDY# 31
DEVSEL# 32
STOP# 33
PERR# 34
SERR# 35
PAR# 36
REQ# 1
GNT# 2
CLK 21
RI_OUT#/PME# 58
SUSPEND# 70
IDSEL 13
MFUNC0 60
MFUNC1 61
MFUNC2 65
MFUNC3 66
MFUNC4 67
MFUNC5 68
MFUNC6 69
VCC/GRST# 66

CAD31/D10 144
CAD30/D9 142
CAD29/D1 141
CAD28/D8 140
CAD27/D0 139
CAD26/A0 129
CAD25/A1 128
CAD24/A2 127
CAD23/A3 124
CAD22/A4 121
CAD21/A5 120
CAD20/A6 118
CAD19/A25 116
CAD18/A7 115
CAD17/A24 113
CAD16/A17 98
CAD15/IOWR# 96
CAD14/A9 97
CAD13/IORD# 93
CAD12/A11 95
CAD11/OE# 92
CAD10/CE2# 91
CAD9/A10 89
CAD8/D15 87
CAD7/D7 85
CAD6/D13 82
CAD5/D6 83
CAD4/D12 80
CAD3/D5 81
CAD2/D11 77
CAD1/D4 79
CAD0/D3 76

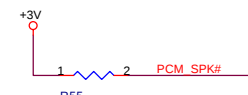
S1_REG# 125
S1_A12 112
S1_A8 99
S1_CE1# 88
S1_RST 119
S1_A23 111
S1_A15 110
S1_A22 109
S1_A21 107
S1_A20 105
S1_A14 104
S1_WAIT# 133
S1_A13 131
S1_INPACK# 101
S1_WE# 123
S1_A16 108
S1_BVD1 135
S1_WP 136
S1_A19 103
S1_RDY# 132
PCM_SPK# 62
S1_BVD2 134
S1_CD2# 137
S1_CD1# 125
S1_VS2 117
S1_VS1 131

S1_IOWR# 20
S1_IORD# 20
S1_OE# 20
S1_CE2# 20
S1_D15 20
S1_D7 20
S1_D13 20
S1_D6 20
S1_D12 20
S1_D5 20
S1_D11 20
S1_D4 20
S1_D3 20
S1_REG# 20
S1_A12 20
S1_A8 20
S1_CE1# 20
S1_RST 20
S1_A23 20
S1_A15 20
S1_A22 20
S1_A21 20
S1_A20 20
S1_A14 20
S1_WAIT# 20
S1_A13 20
S1_INPACK# 20
S1_WE# 20
S1_A16 20
S1_BVD1 20
S1_WP 20
S1_A19 20
S1_RDY# 20
PCM_SPK# 31
S1_BVD2 20
S1_CD2# 20
S1_CD1# 20
S1_VS2 20
S1_VS1 20

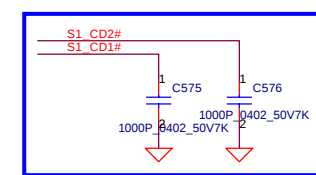
S1_D2
S1_A18
S1_D14



S1_A[0..25]
S1_D[0..15]



PCM_SPK#

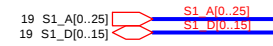
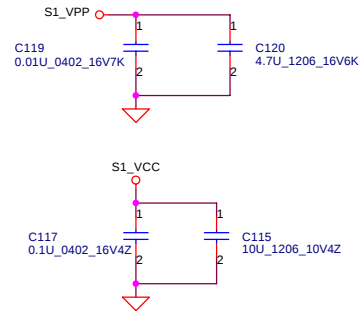
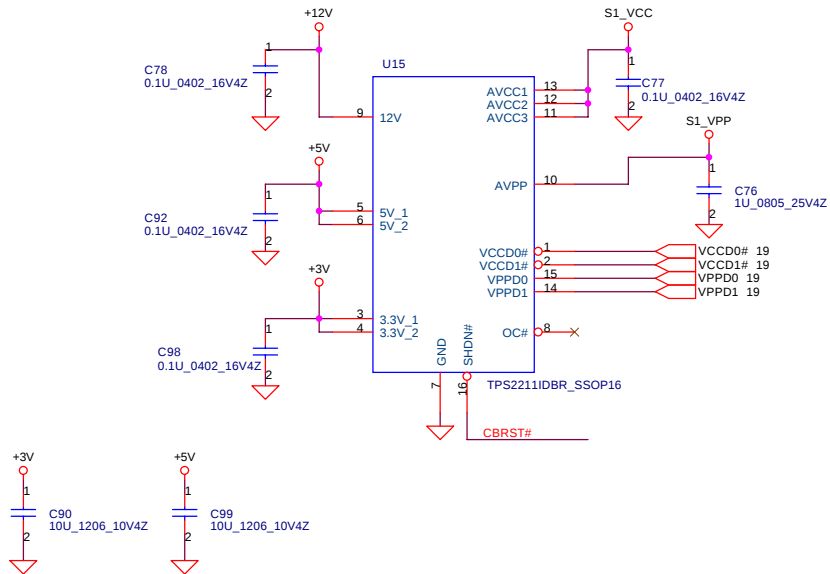


Reserve for CB1410 B0 Version

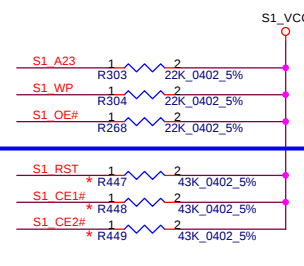
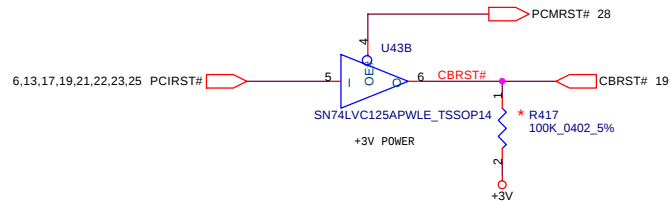
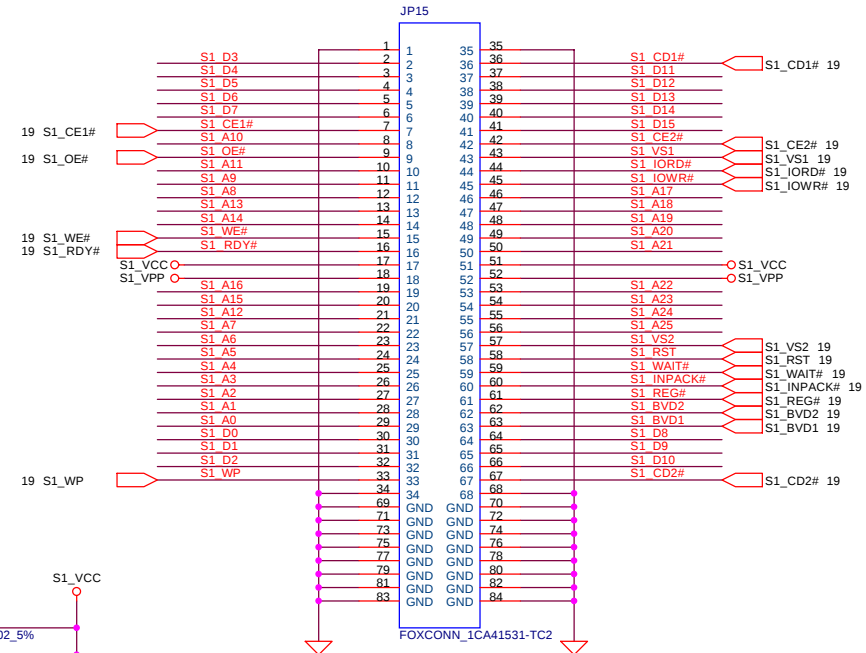
Compal Electronics, Inc.			
Title			
PCMCIA controller ENE CB1410			
Size	Document Number		Rev
	DCL53 LA-1881		1.0
Date:	Monday, August 11, 2003	Sheet	19 of 40

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PCMCIA Power Controller



CardBus Socket



Reserve for CB1410 B0 Version

13,19,22,23 PCI_AD[0..31]

U6
PCI AD0 47
PCI AD1 46
PCI AD2 45
PCI AD3 43
PCI AD4 42
PCI AD5 41
PCI AD6 40
PCI AD7 39
PCI AD8 36
PCI AD9 35
PCI AD10 34
PCI AD11 33
PCI AD12 32
PCI AD13 30
PCI AD14 29
PCI AD15 28
PCI AD16 15
PCI AD17 14
PCI AD18 13
PCI AD19 12
PCI AD20 11
PCI AD21 10
PCI AD22 9
PCI AD23 8
PCI AD24 96
PCI AD25 93
PCI AD26 92
PCI AD27 91
PCI AD28 89
PCI AD29 87
PCI AD30 86
PCI AD31 85

13,19,22,23 PCI_C/BE#0
13,19,22,23 PCI_C/BE#1
13,19,22,23 PCI_C/BE#2
13,19,22,23 PCI_C/BE#3

PCI AD17 1
LAN IDSEL 98

13,19,22,23 PCI_PAR 24
13,19,22,23 PCI_FRAME# 18
13,19,22,23 PCI_IRDY# 19
13,19,22,23 PCI_TRDY# 20
13,19,22,23 PCI_DEVSEL# 21
13,19,22,23 PCI_STOP# 23

13,19,22,23 PCI_PERR# 25
13,19,23 PCI_SERR# 26

13 PCI_REQ#3 83
13 PCI_GNT#3 82

13 PCI_PIRQF# 1
19,22,23,27 LAN_PME# 57

6,13,17,19,20,22,25 PCIRST# 97
12 CLK_PCI_LAN 97
14,19,23,25,27 PM_CLKRUN# 50

+3V 6
22 37
37 37
49 49
90 90
95 95

VDD 6
VDD 31
VDD 44
VDD 88
VDD 62
VDD 66
VDD 73

RTL8101L_LQFP100

LAN X1 1
LAN X2 1

C62 27P_0402_50V8J
C64 27P_0402_50V8J

LAN EECS 1
LAN EECCLK 2
LAN EEDI 3
LAN EEDO 4

U10
AT93C46-10SI-2.7_S08

VCC 8
SK 7
DI 6
DO 5
GND 5

+3V 1
C75 0.1U_0402_16V4Z

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

LAN RD+ 1
LAN RD- 2
LAN TD+ 3
LAN TD- 4

Power

LAN I/F

LAN I/F

AC-Link

AC-Link

Power

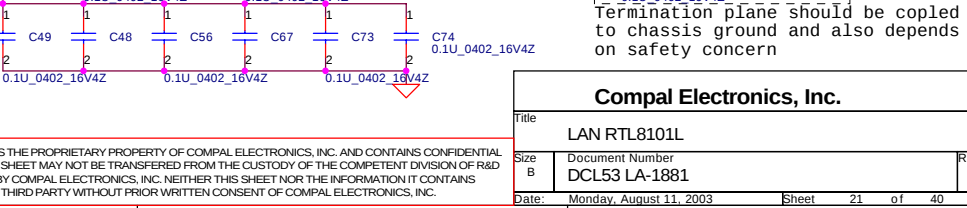
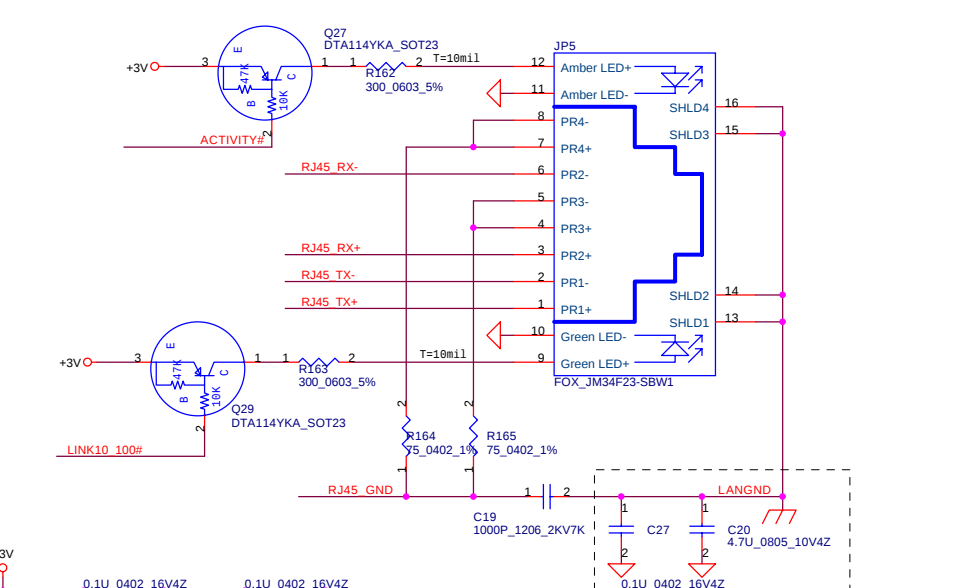
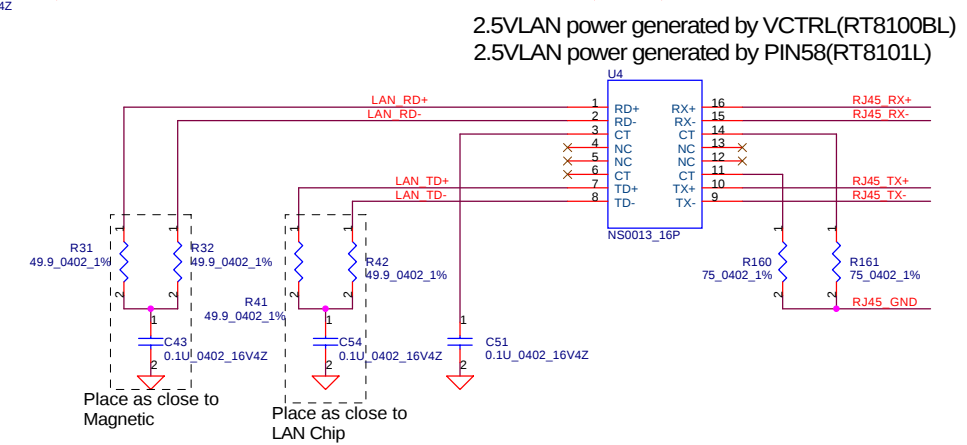
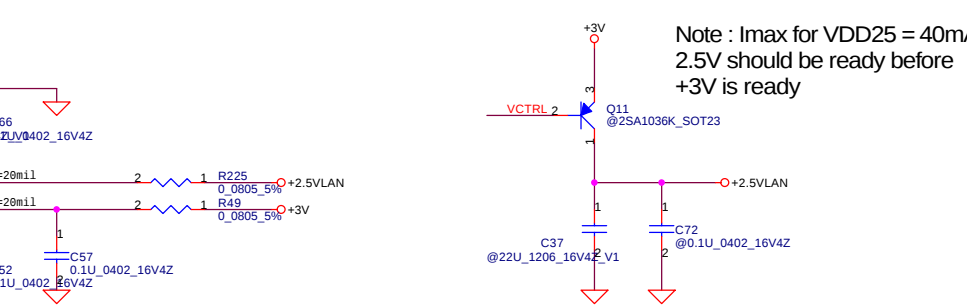
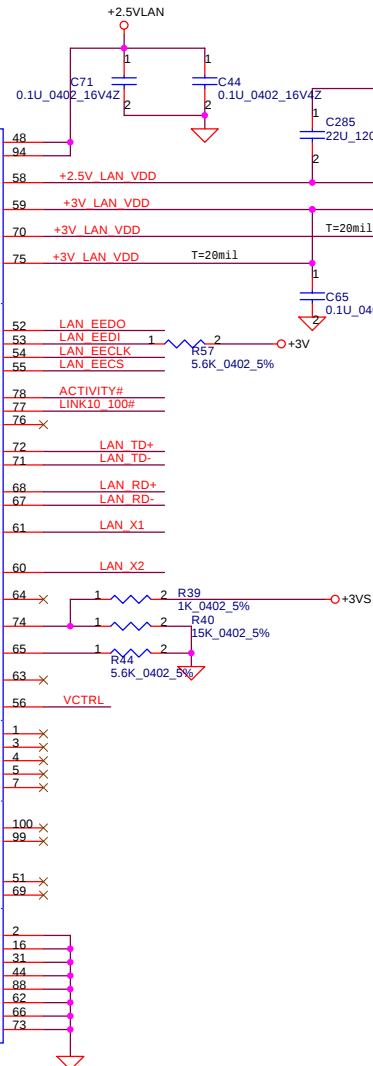
Power

Power

Power

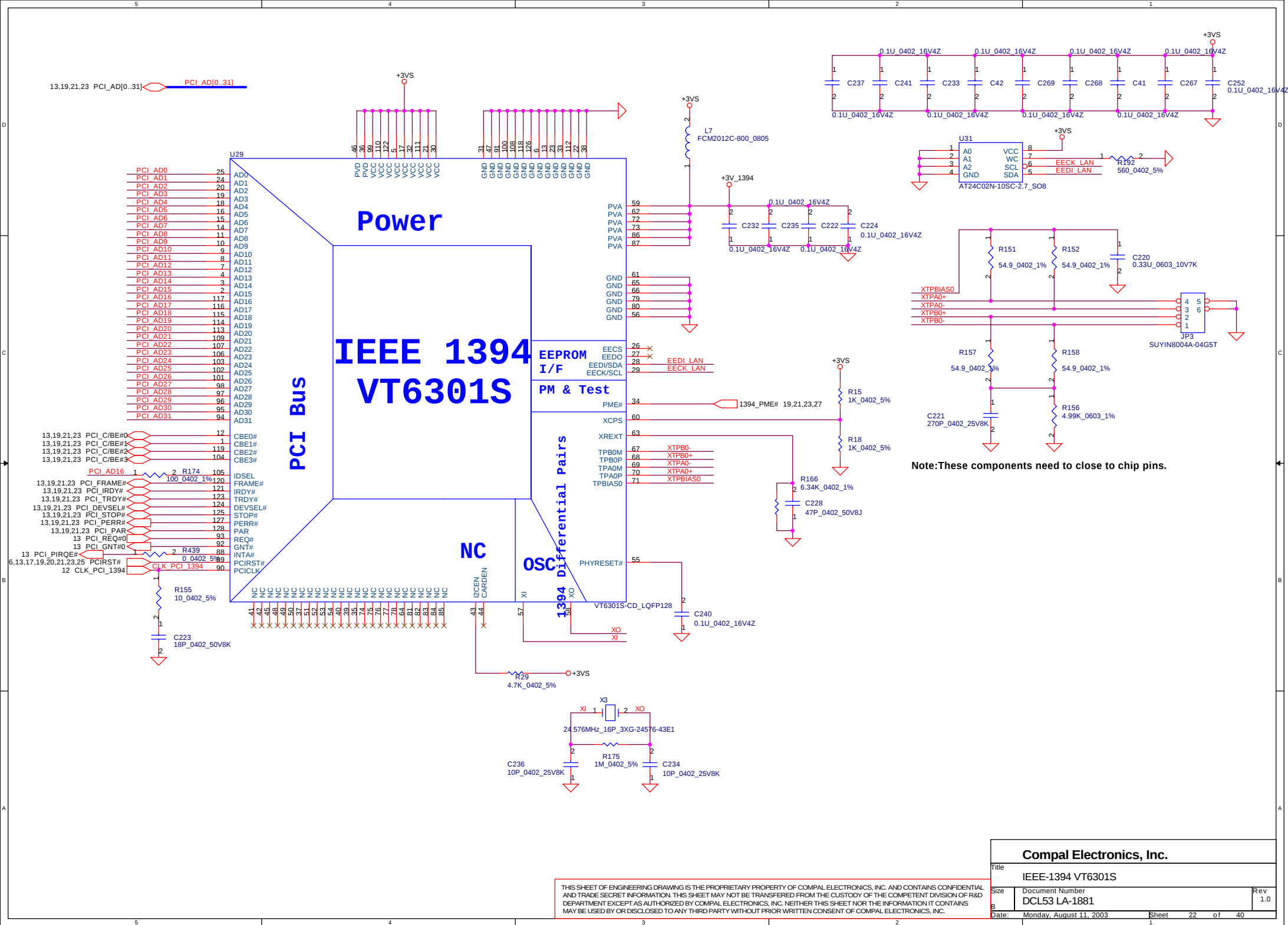
Power

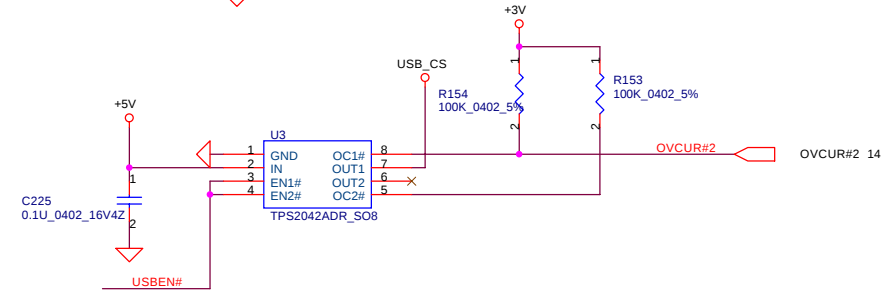
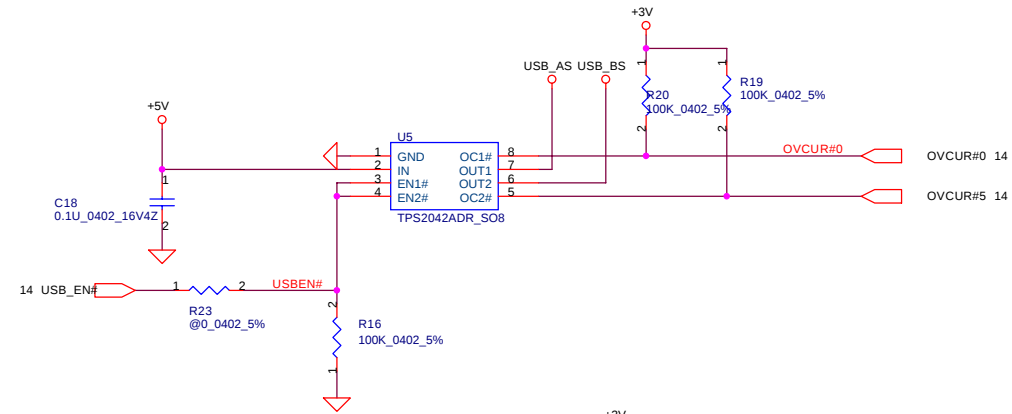
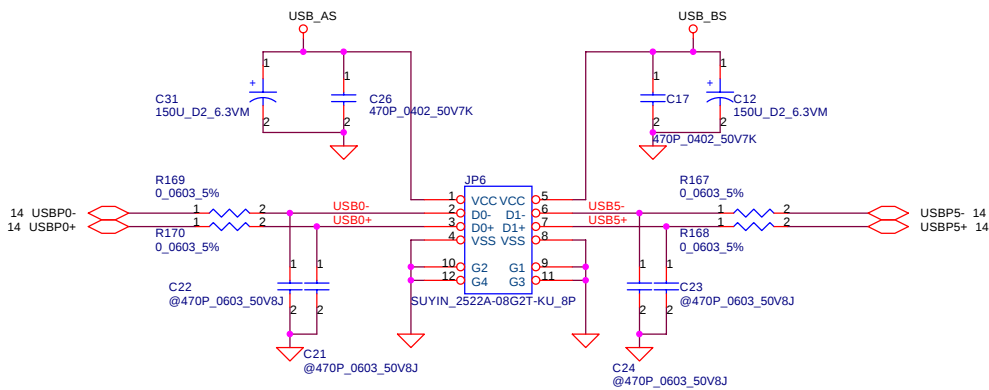
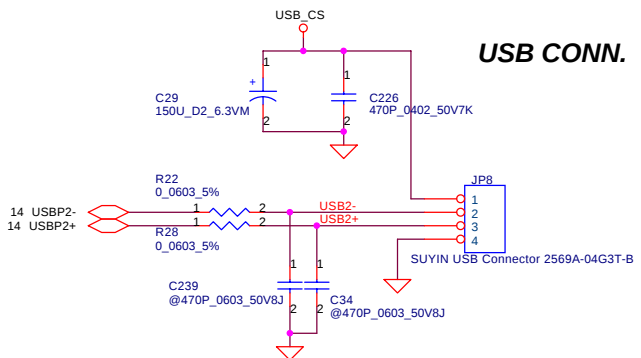
Power



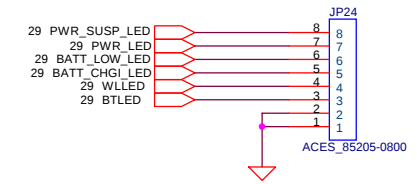
Compal Electronics, Inc.			
Title	LAN RTL8101L		
Size	Document Number	Rev	
B	DCL53 LA-1881	1.0	
Date:	Monday, August 11, 2003	Sheet	21 of 40

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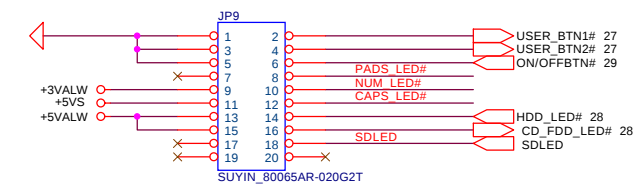




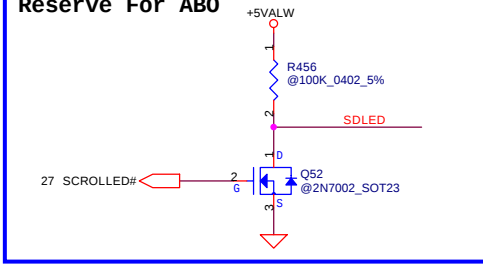
LED/B CONN.



POWER/B CONN.



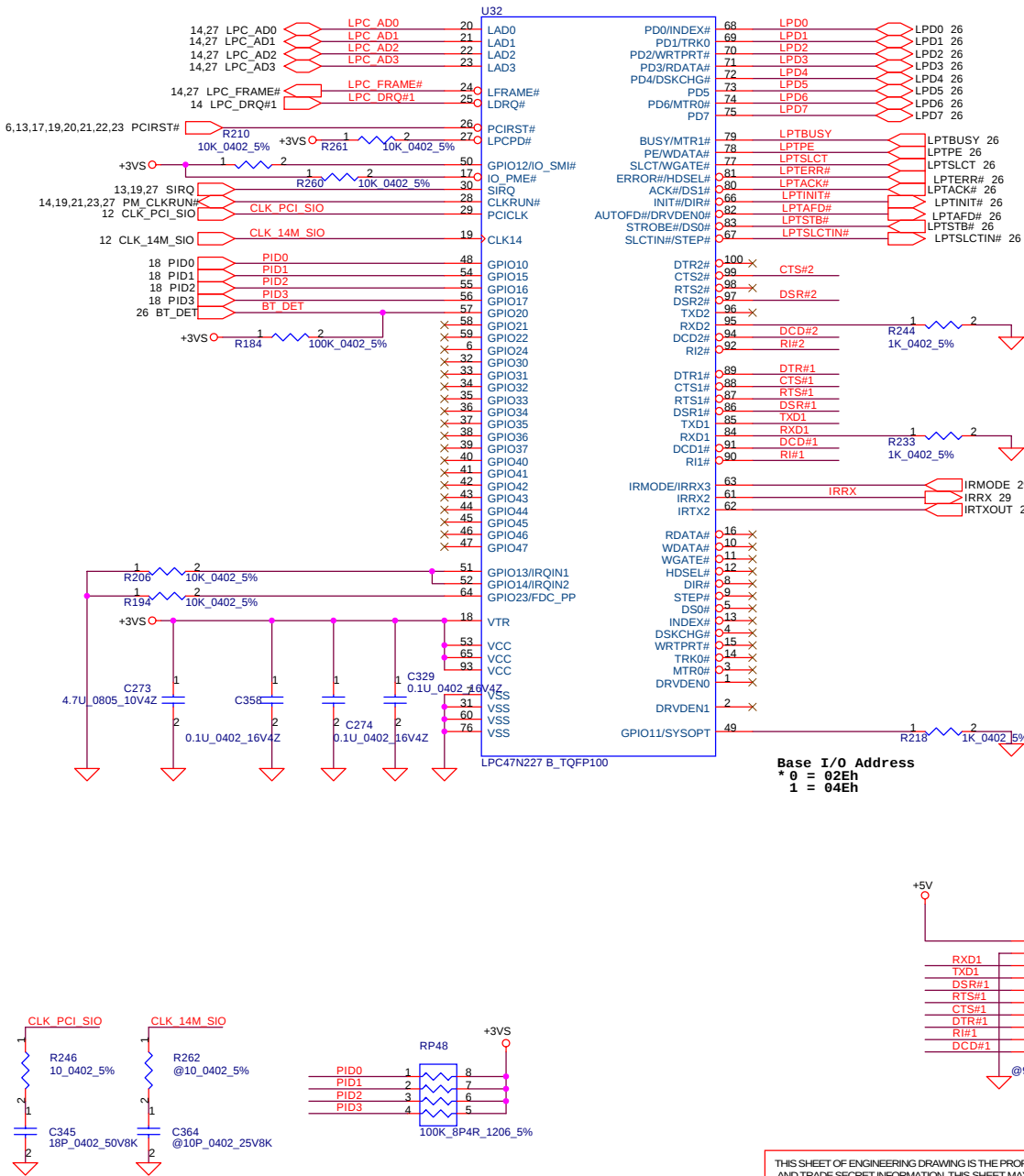
Reserve For ABO



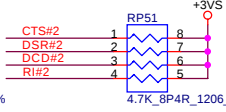
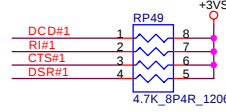
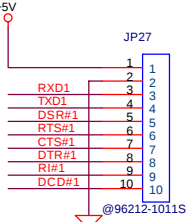
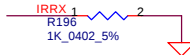
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Compal Electronics, Inc.		
Title		
USB & LED		
Size	Document Number	Rev
B	DCL53 LA-1881	1.0
Date:	Monday, August 11, 2003	Sheet 24 of 40

SUPER I/O SMsC FDC47N227



Base I/O Address
* 0 = 02Eh
1 = 04Eh

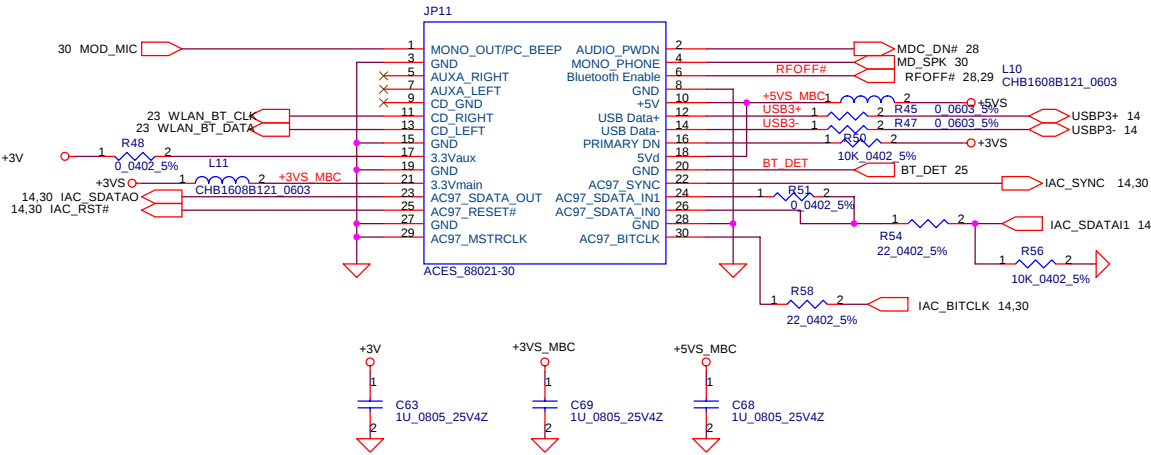


Compal Electronics, Inc.

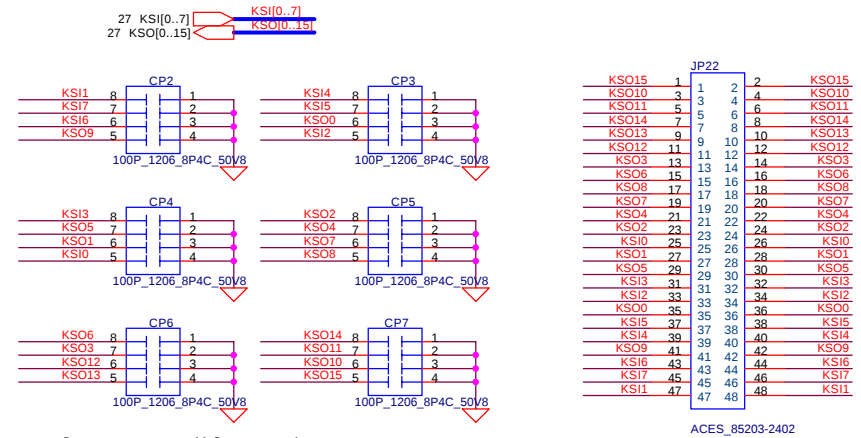
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Size		Document Number				Rev	
B		DCL53 LA-1881				1.0	
Date:		Monday, August 11, 2003		Sheet		25 of 40	

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MBC CONN.

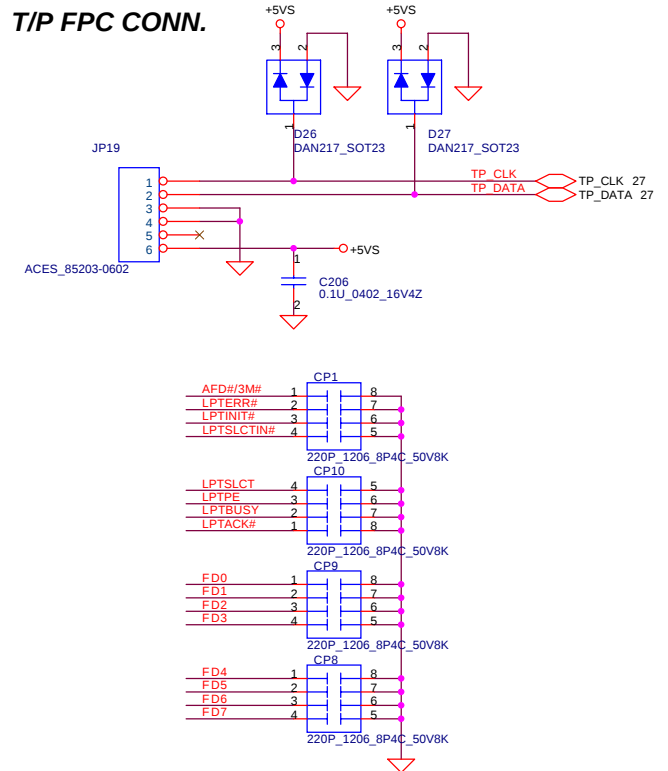


INT_KBD CONN.

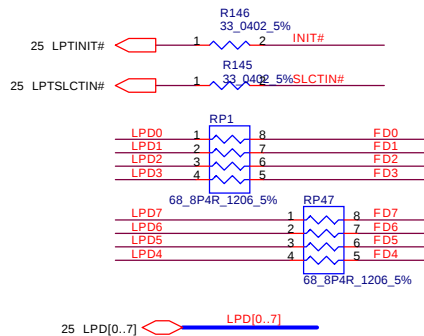
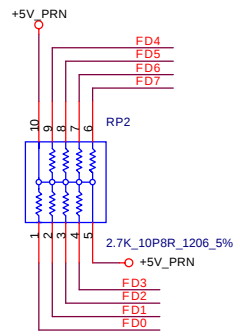
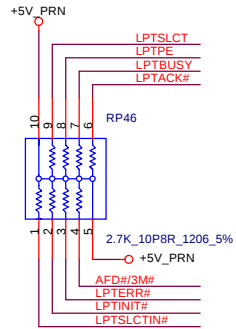
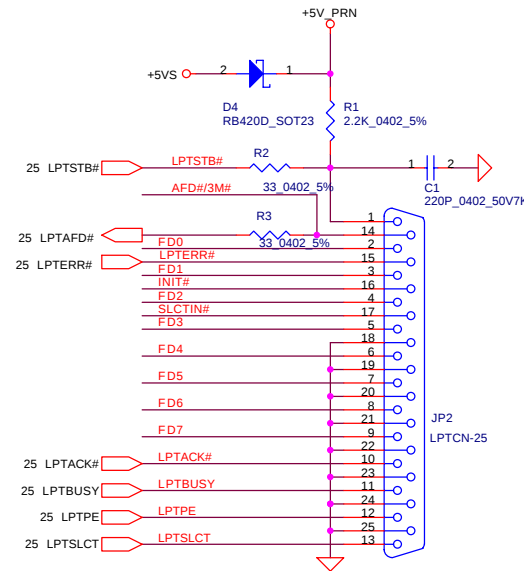


As close as possible to K/B connector

T/P FPC CONN.



PARALLEL PORT

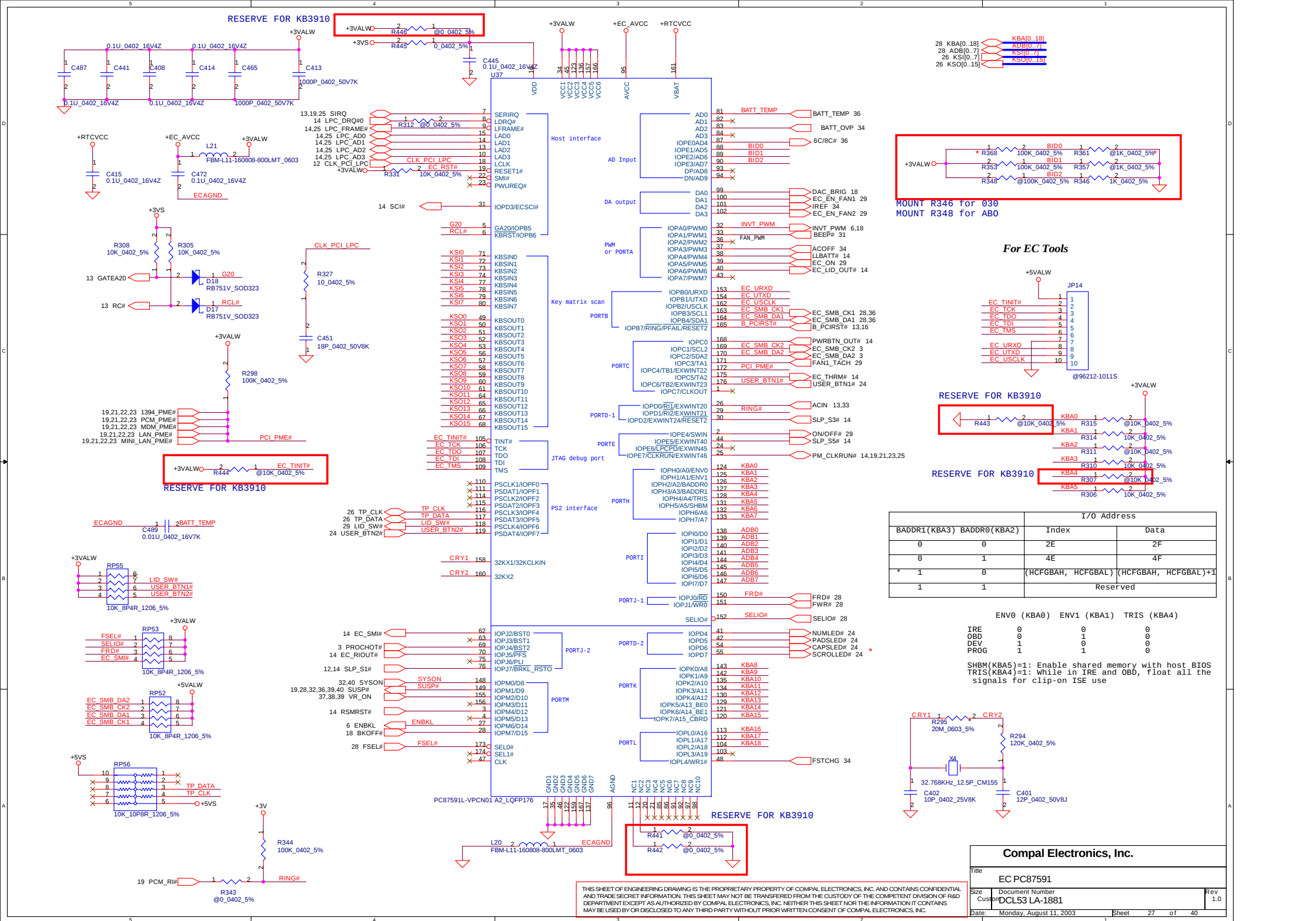


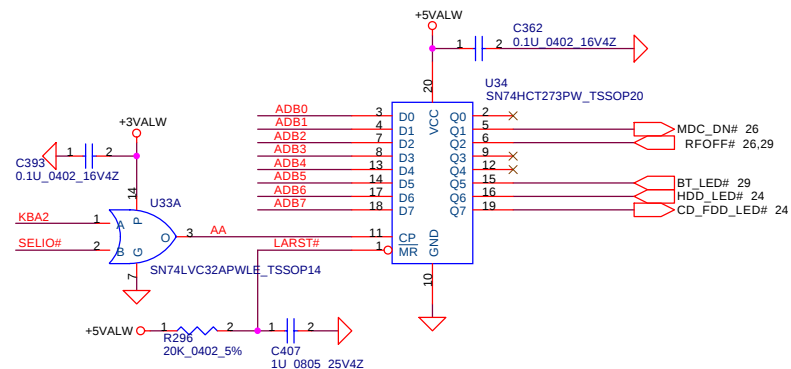
25 LPD[0..7] LPD[0..7]

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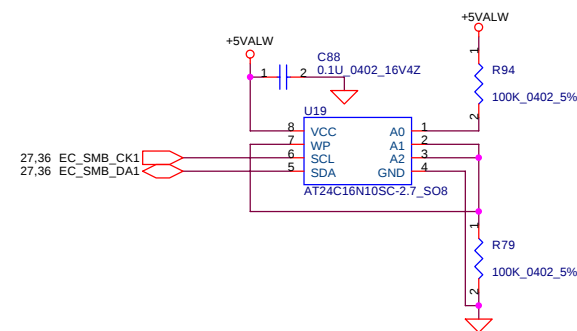
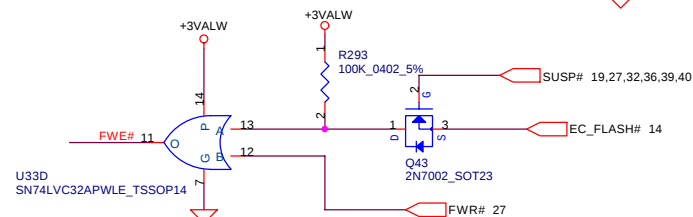
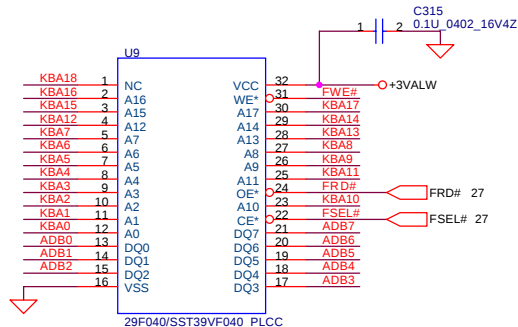
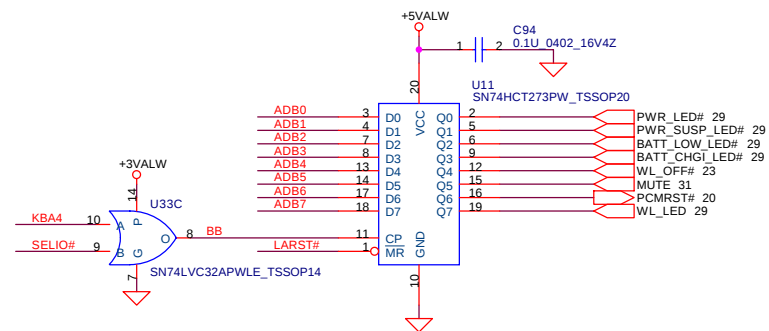
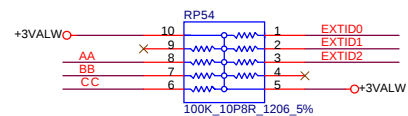
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PARALLEL/MDC PORT			
Size	Document Number		Rev
	DCL53 LA-1881		1.0
Date:	Monday, August 11, 2003	Sheet	26 of 40

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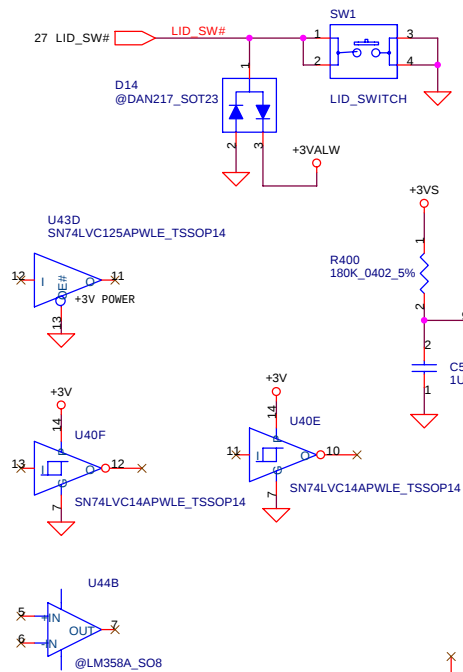




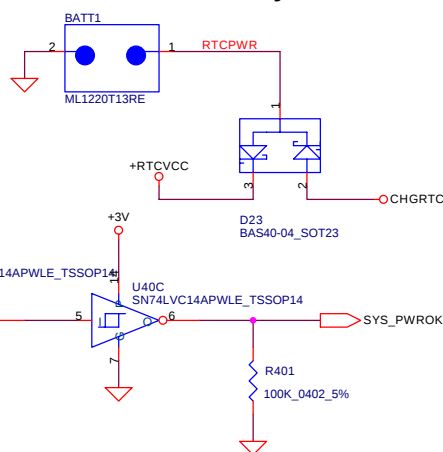
EXTID2	EXTID1	EXTID0	Module
0	0	0	FDD
0	0	1	CDROM
0	1	1	2ndHDD
1	1	1	NO Module



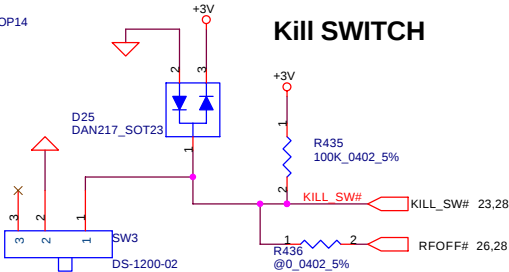
Power ON Circuit



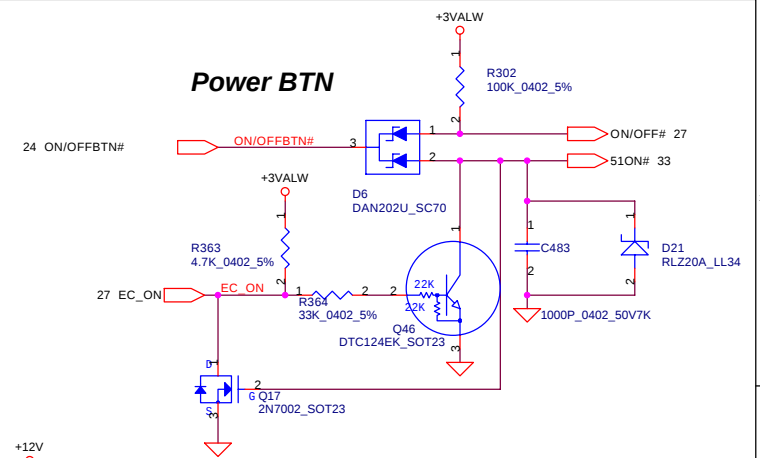
RTC Battery



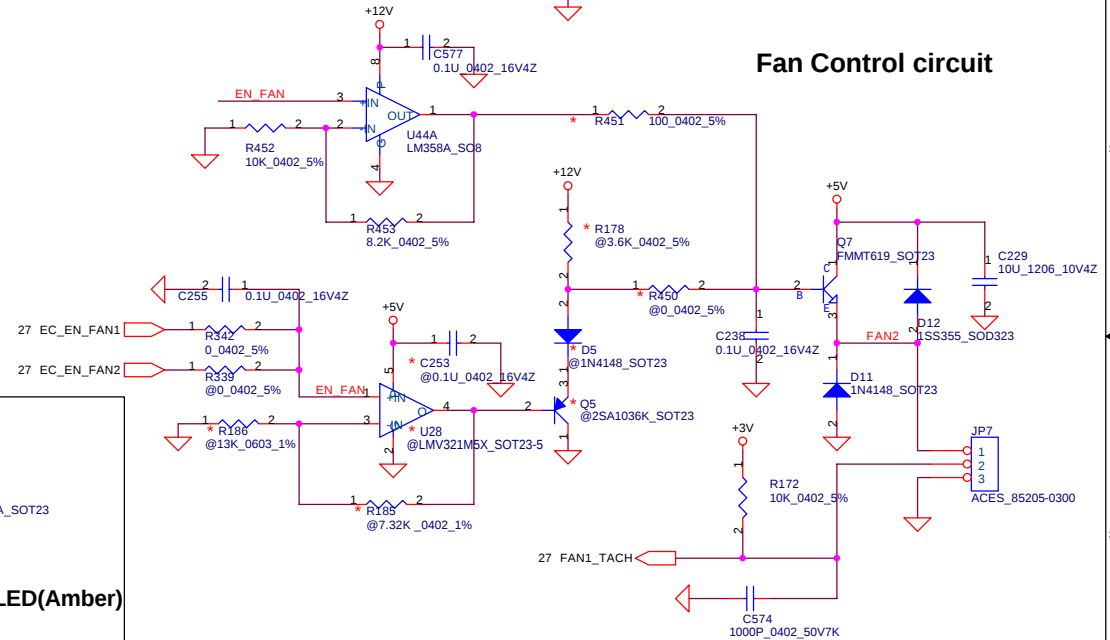
Kill SWITCH



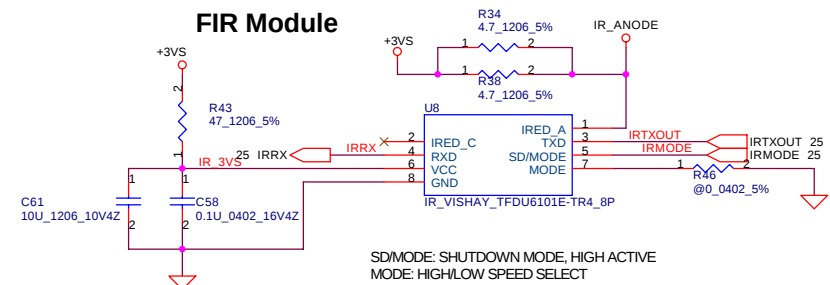
Power BTN



Fan Control circuit

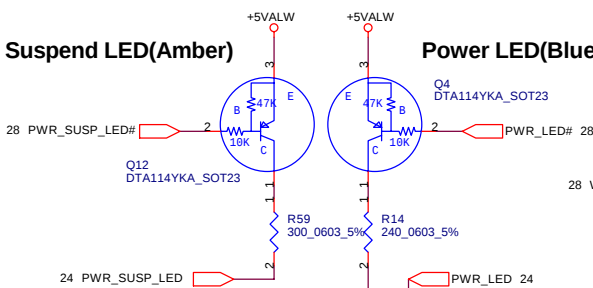


FIR Module

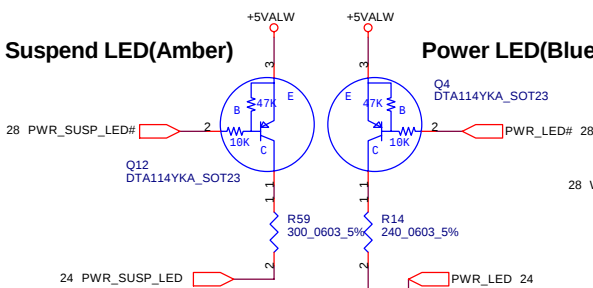


SD/MODE: SHUTDOWN MODE, HIGH ACTIVE
MODE: HIGH/LOW SPEED SELECT

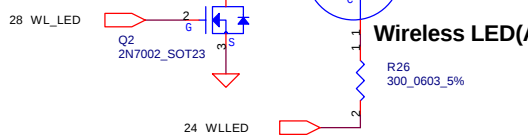
Suspend LED(Amber)



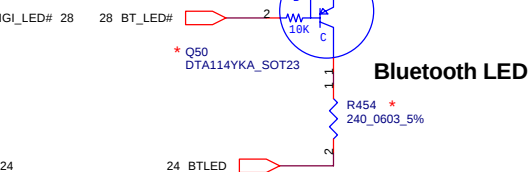
Power LED(Blue)



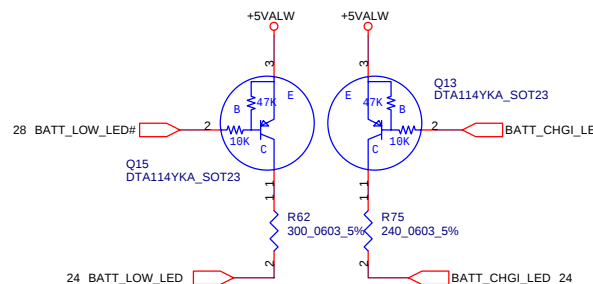
Wireless LED(Amber)



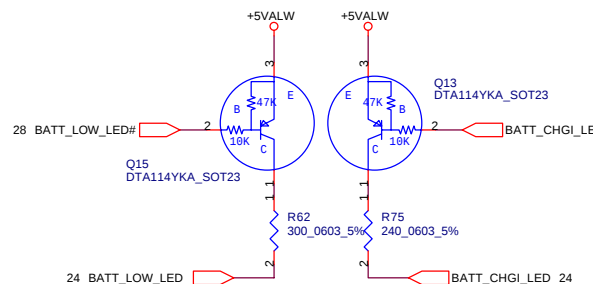
Bluetooth LED(Blue)



BATT Low LED(Amber)



BATT Charge LED(Blue)

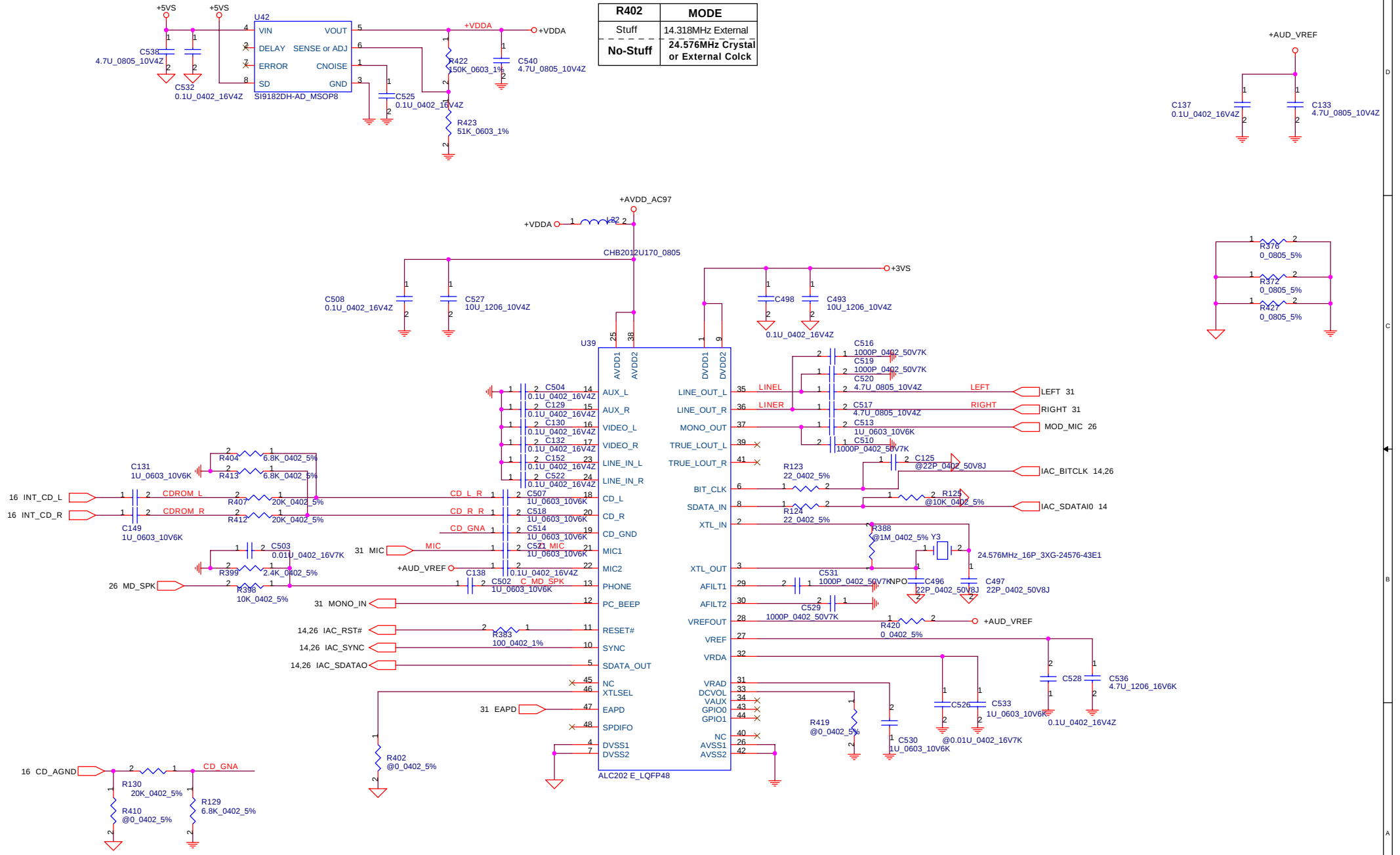


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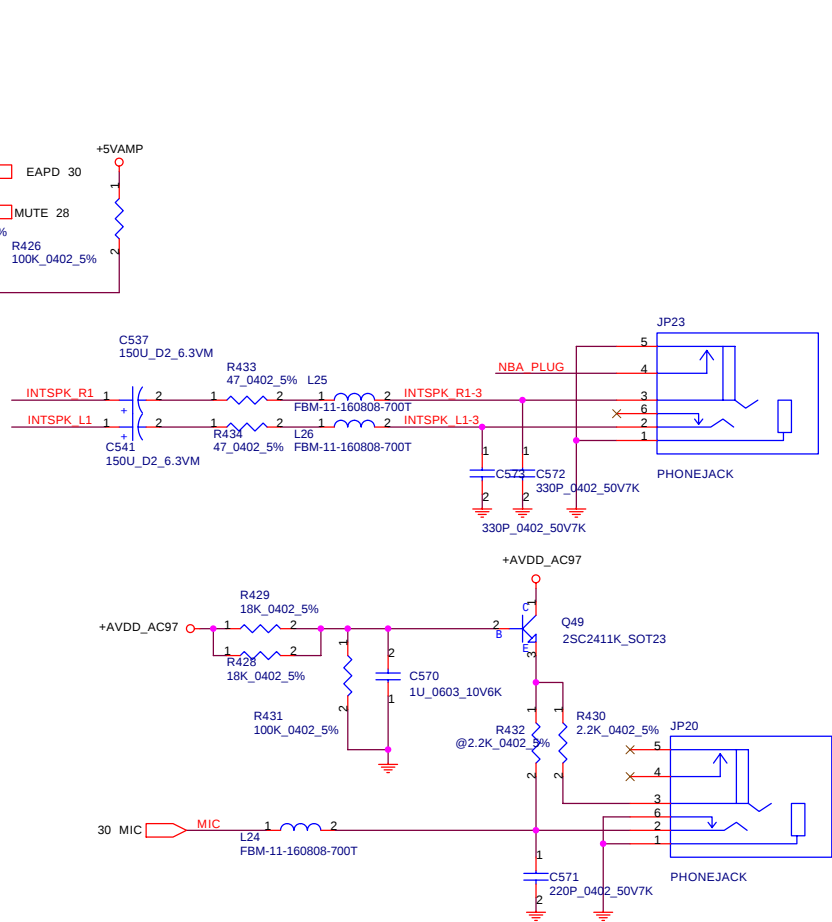
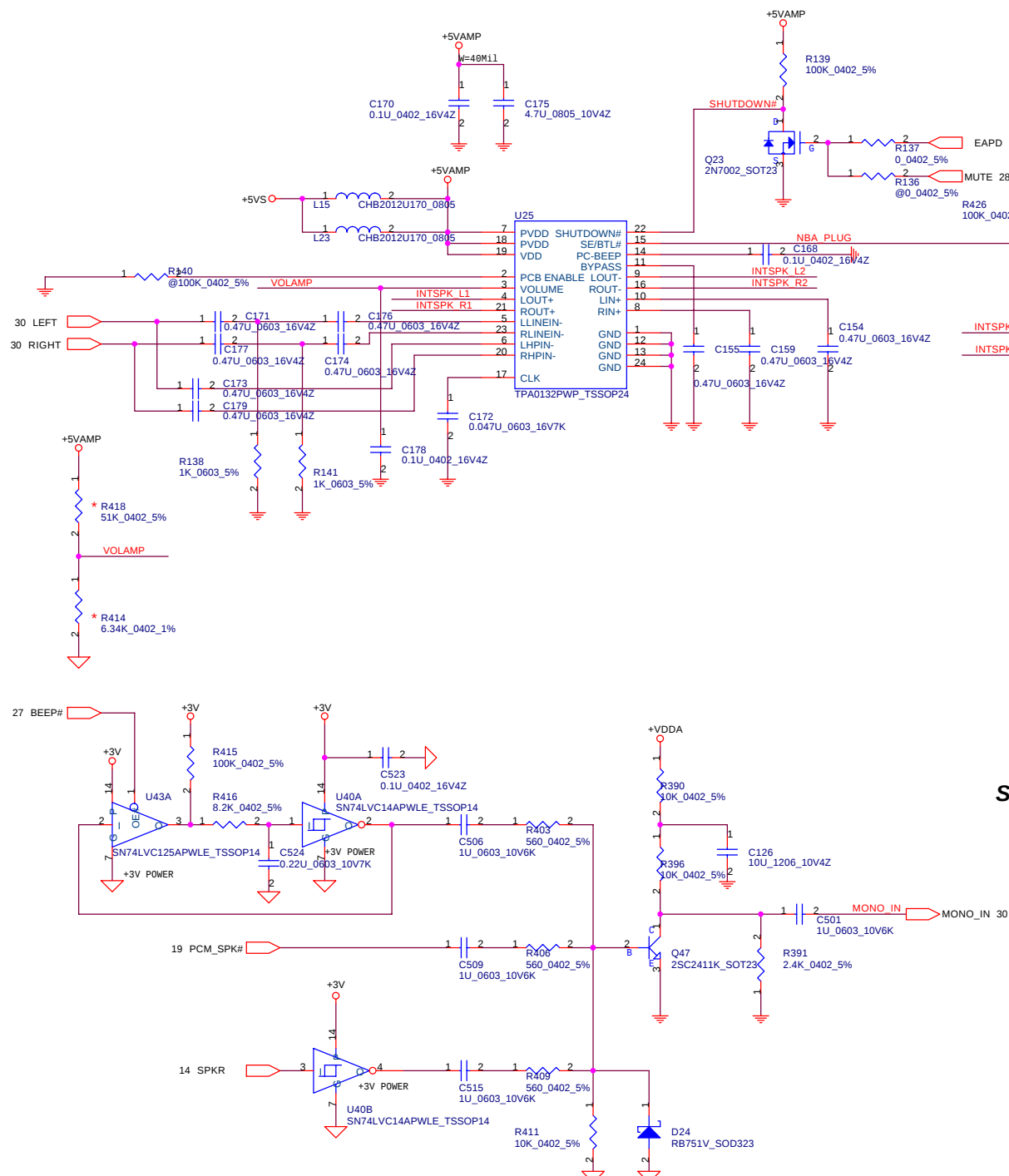
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Title	Power OK/Reset/RTC battery/Lid Switch/Int. KB		
Size	Document Number	Rev	
	DCL53 LA-1881	1.0	
Date:	Monday, August 11, 2003	Sheet	29 of 40

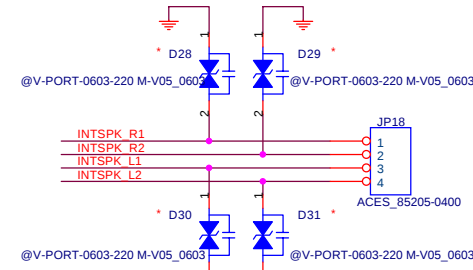
AC97 Codec



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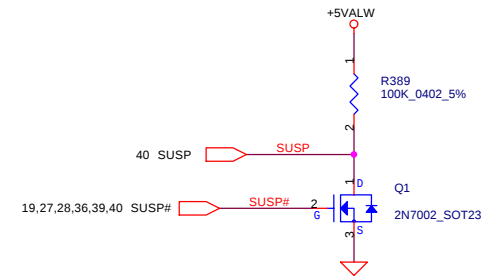
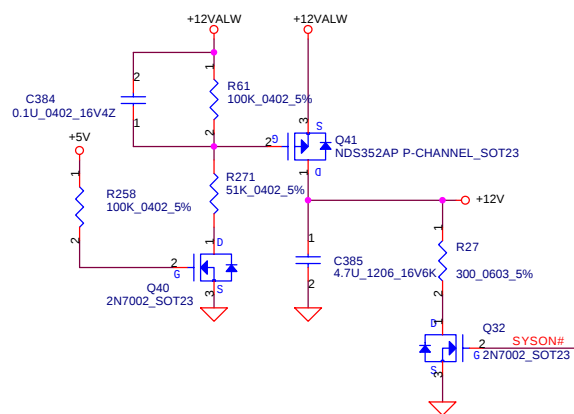
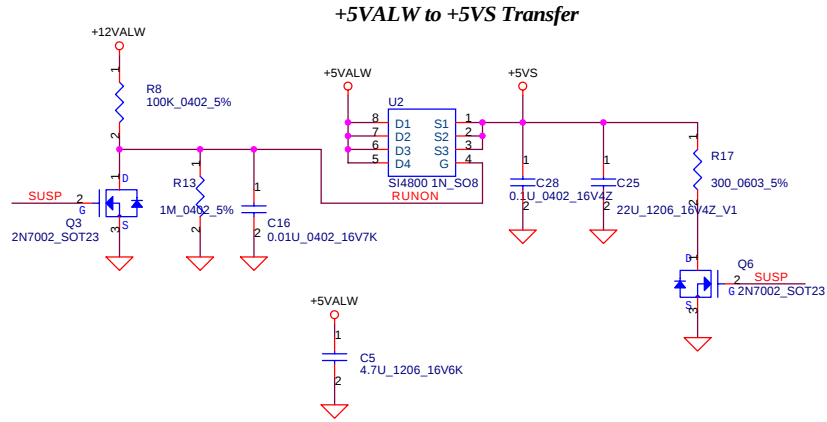
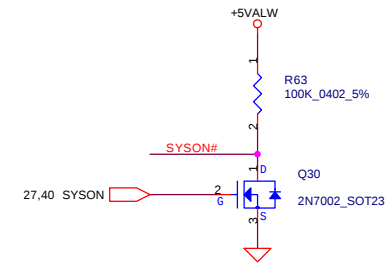
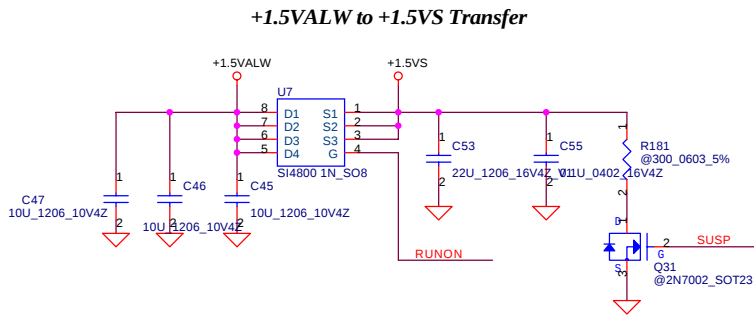
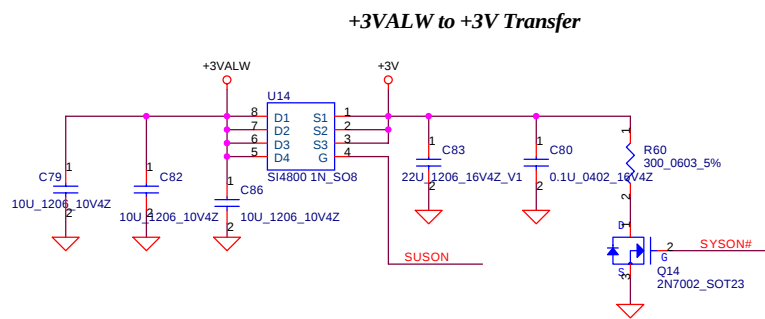
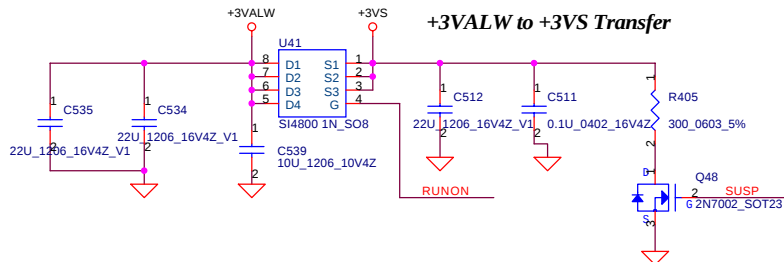
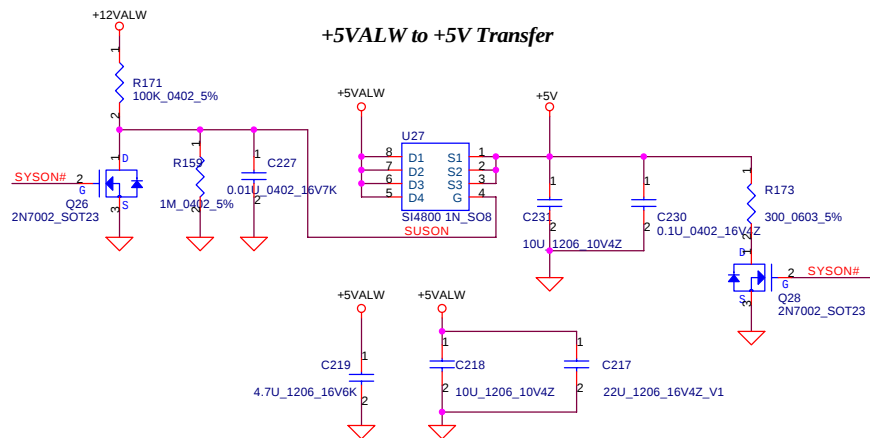


SPEAKER CONN.



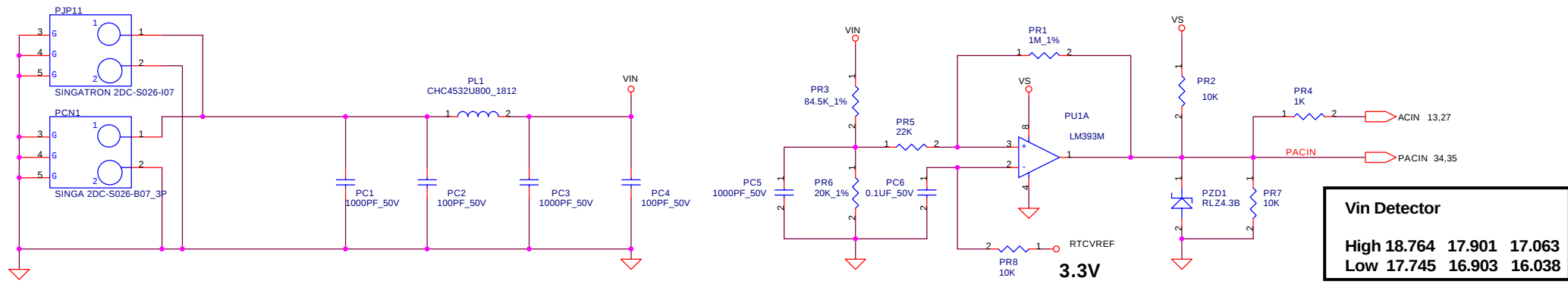
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Compal Electronics, Inc.			
Title			
AMP & Audio Jack			
Size	Document Number	Rev	
	DCL53 LA-1881	1.0	
Date:	Monday, August 11, 2003	Sheet	31 of 40

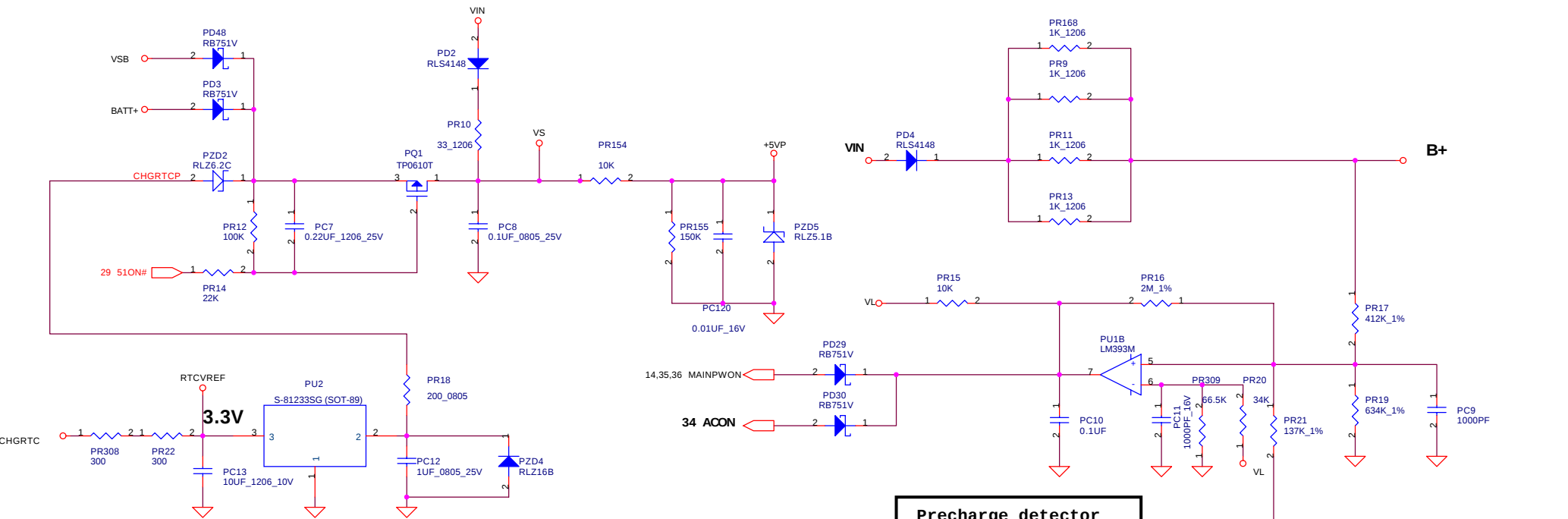


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Compal Electronics, Inc.			
Title DC/DC Circuit Interface			
Size	Document Number DCL53 LA-1881		Rev 1.0
Date: Monday, August 11, 2003	Sheet 32	of 40	

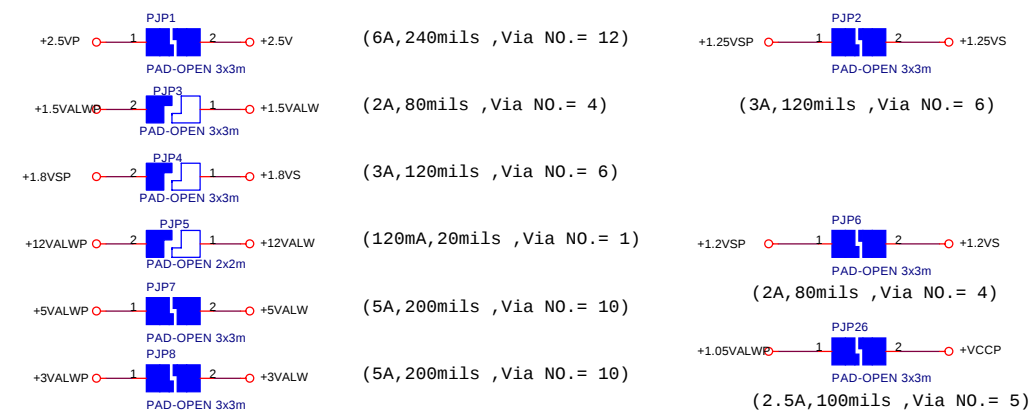


Vin Detector		
High	18.764	17.901 17.063
Low	17.745	16.903 16.038

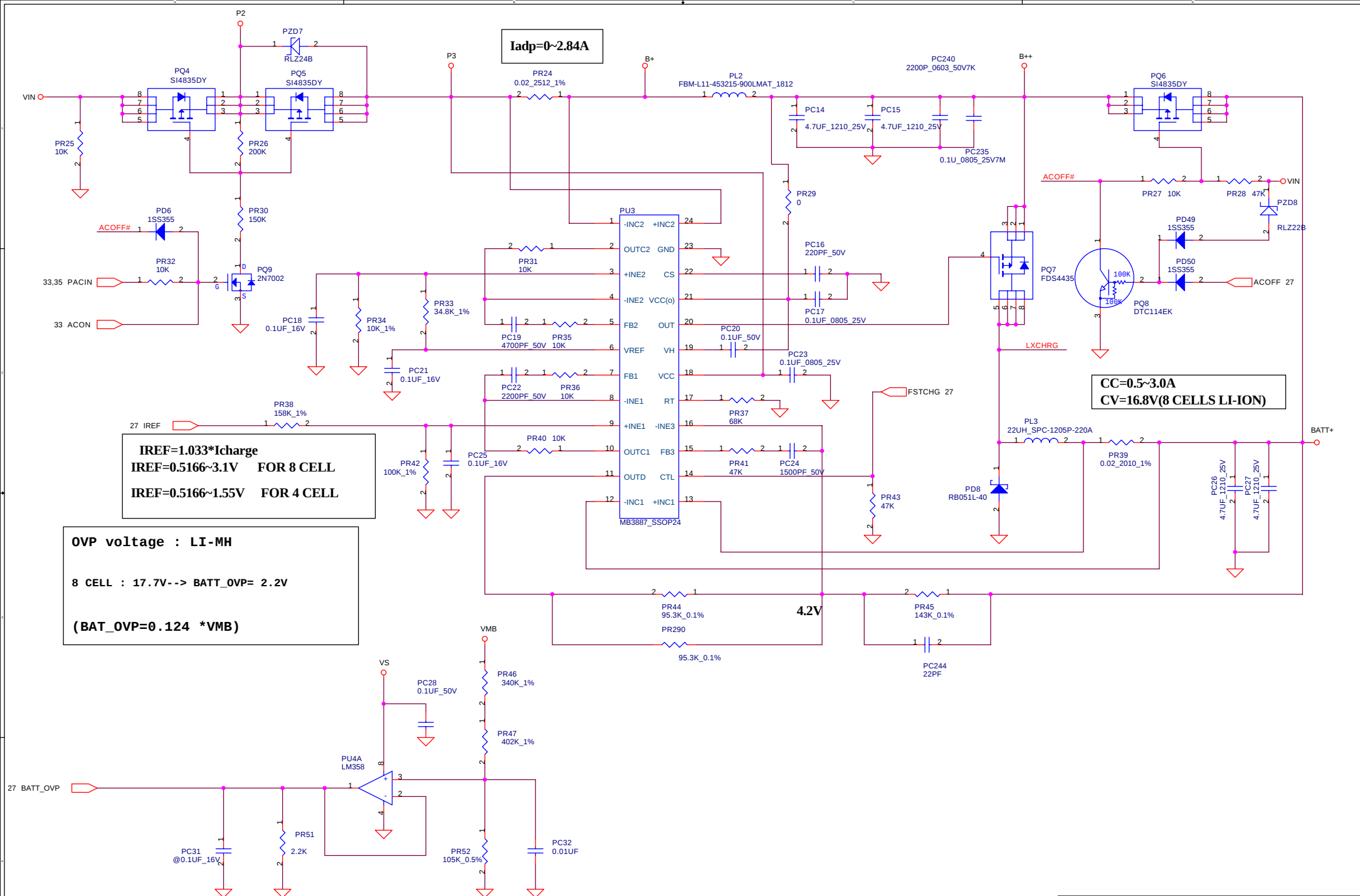


Precharge detector AC ADAPTOR		
16.69	16.05	15.42
15.6	15.02	14.35

Precharge detector AC ADAPTOR		
6.33	6.12	5.92
5.227	5.09	4.85



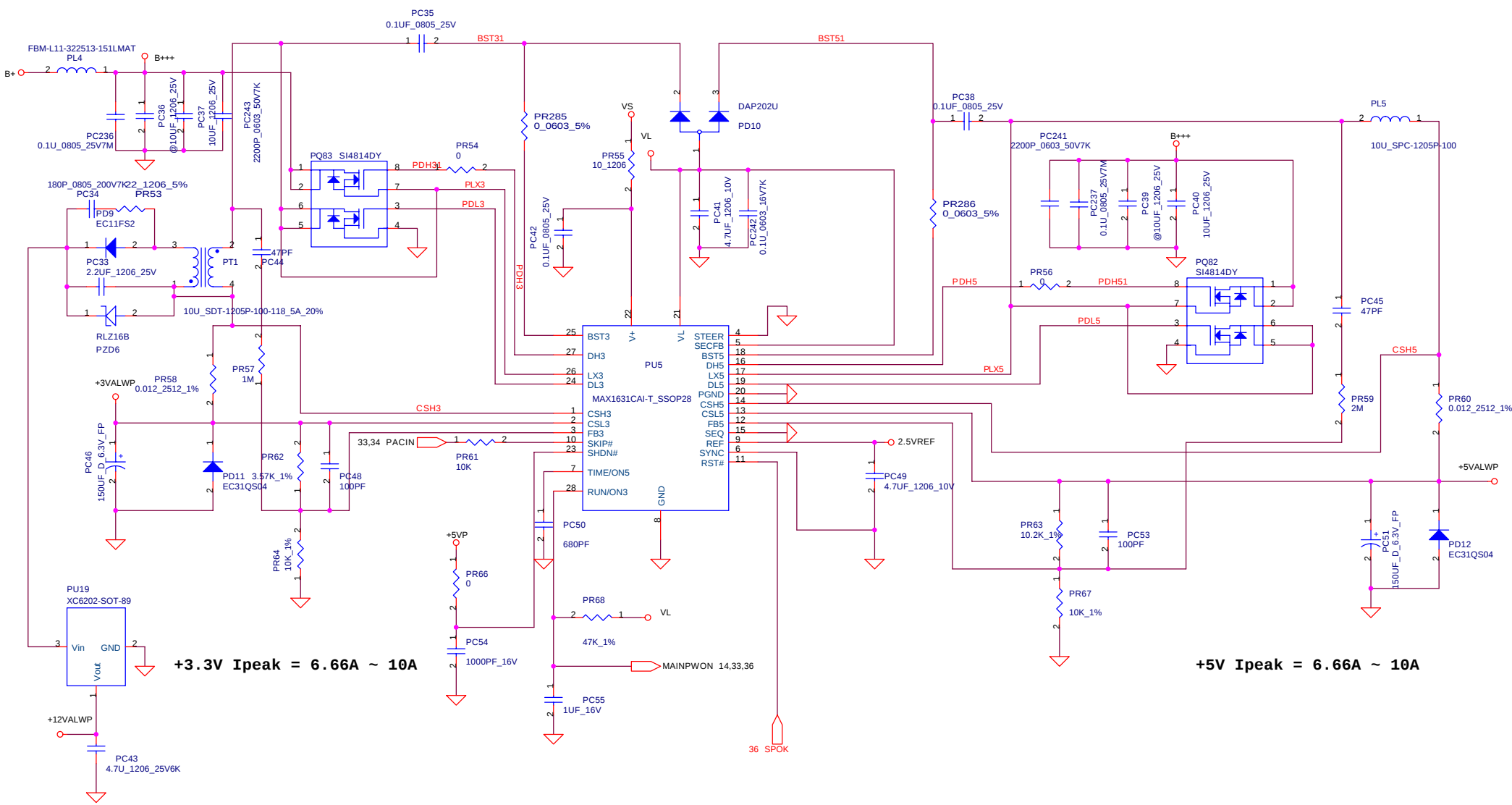
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Title		CHARGER	
Size	Document Number	DCL53 LA-1881	
B		1.0	
Date:	Monday, August 11, 2003	Sheet	34 of 40



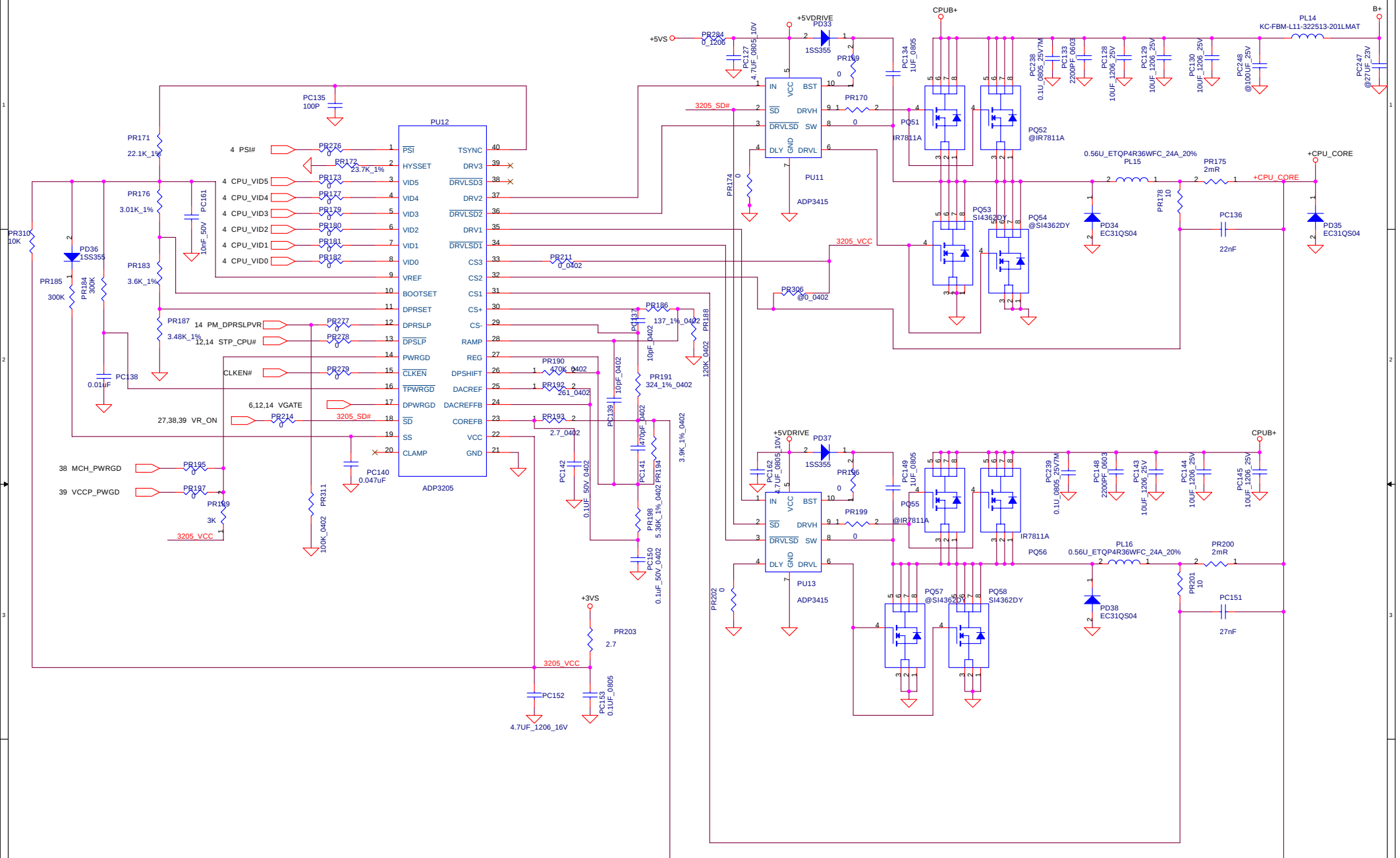
+3.3V Ipeak = 6.66A ~ 10A

+5V Ipeak = 6.66A ~ 10A

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Compal Electronics, Inc.			
Title	5V/3.3V/12V		
Size	B		
Document Number	DCL53 LA-1881		
Date:	Monday, August 11, 2003	Sheet	35 of 40
Rev	1.0		

CPU-CORE



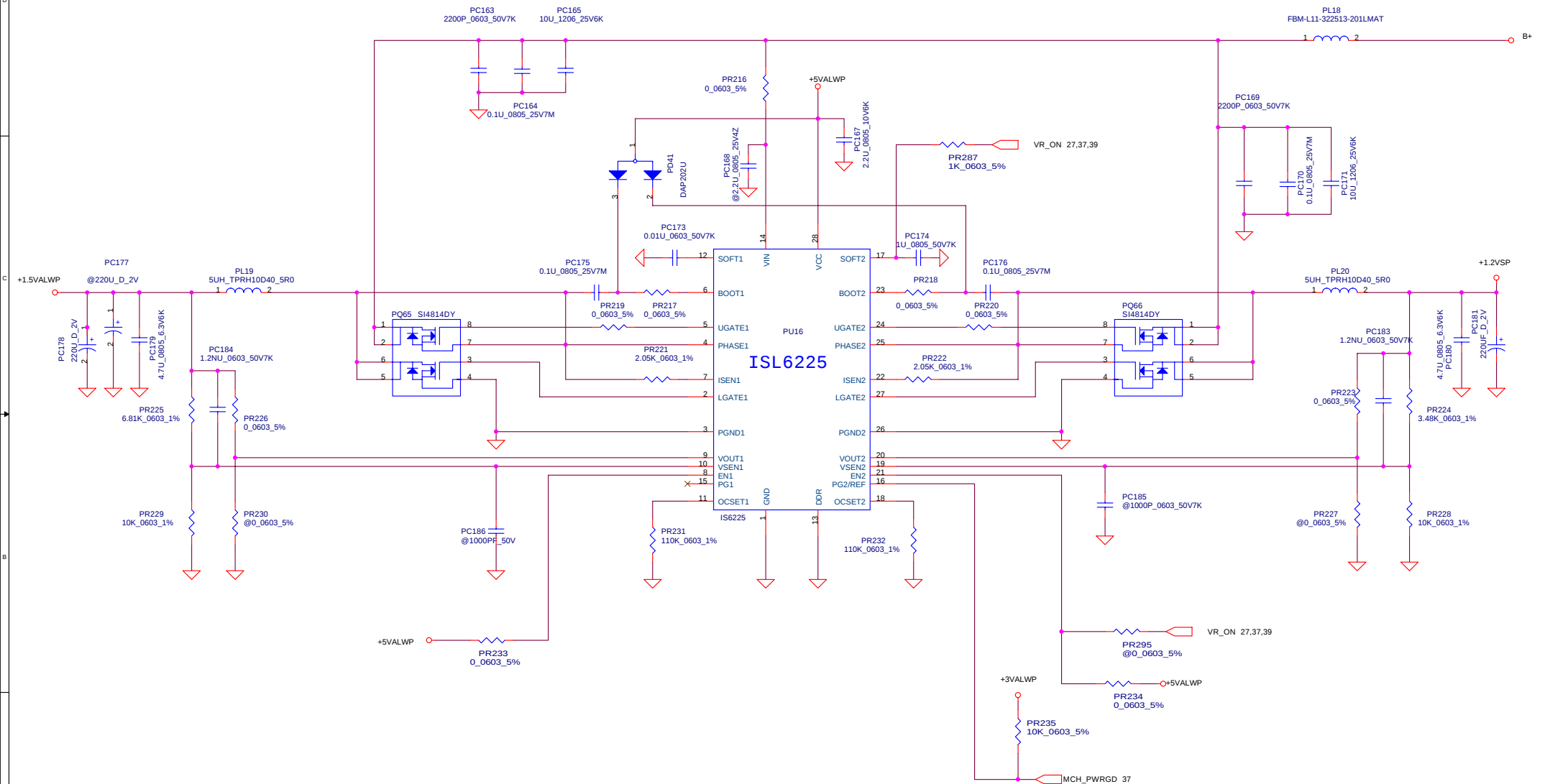
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+VCC_H_CORE

Size B	Document Number DCL53 LA-1881	Rev 1.0
Date: Monday, August 11, 2003		Sheet 37 of 40

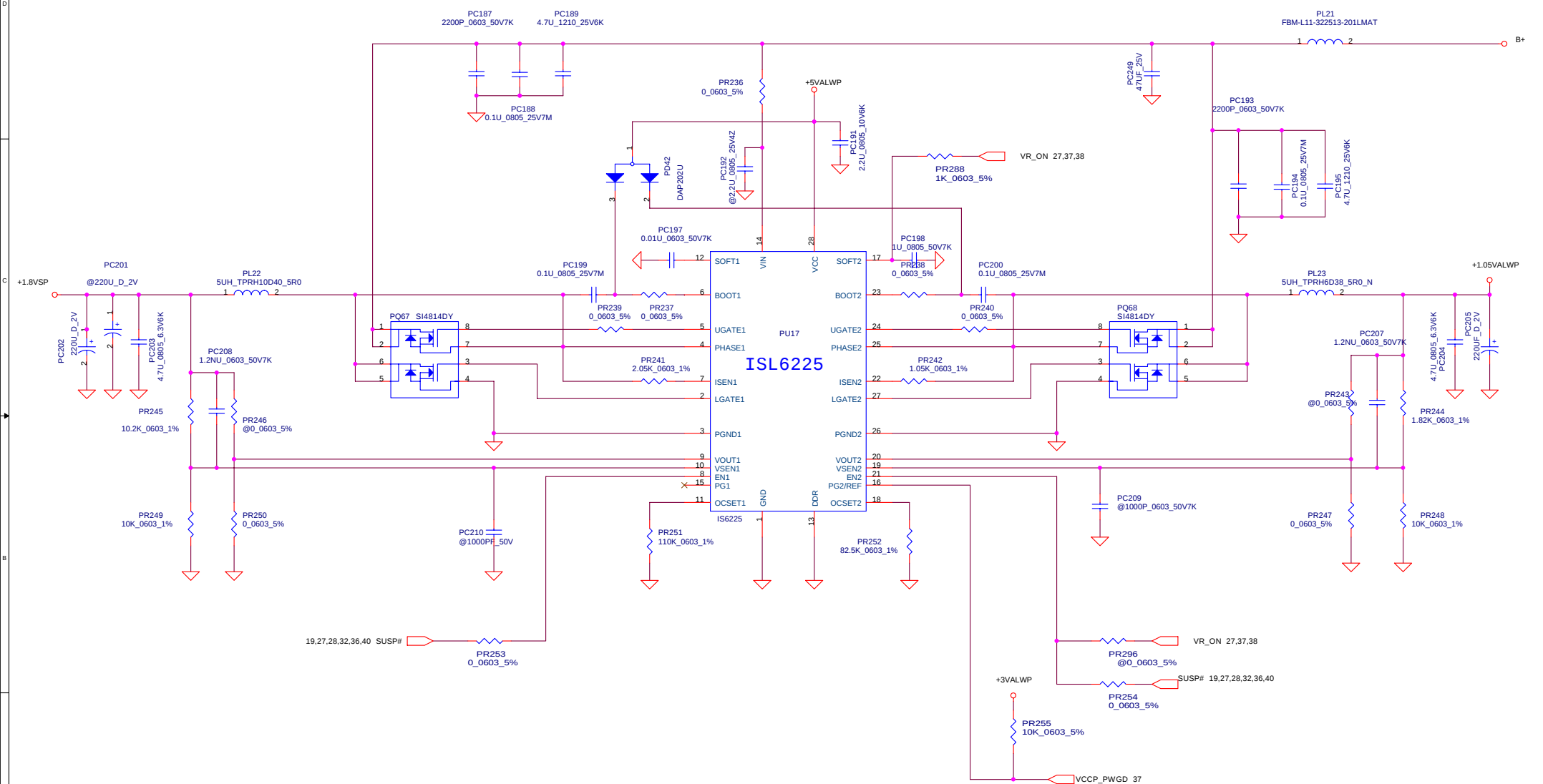
+1.5VALWPV/+1.2VSP



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Title			
1.5V & 1.2V			
Size	Document Number	Rev	
B	DCL53 LA-1881	1.0	
Date:	Monday, August 11, 2003	Sheet	38 of 40

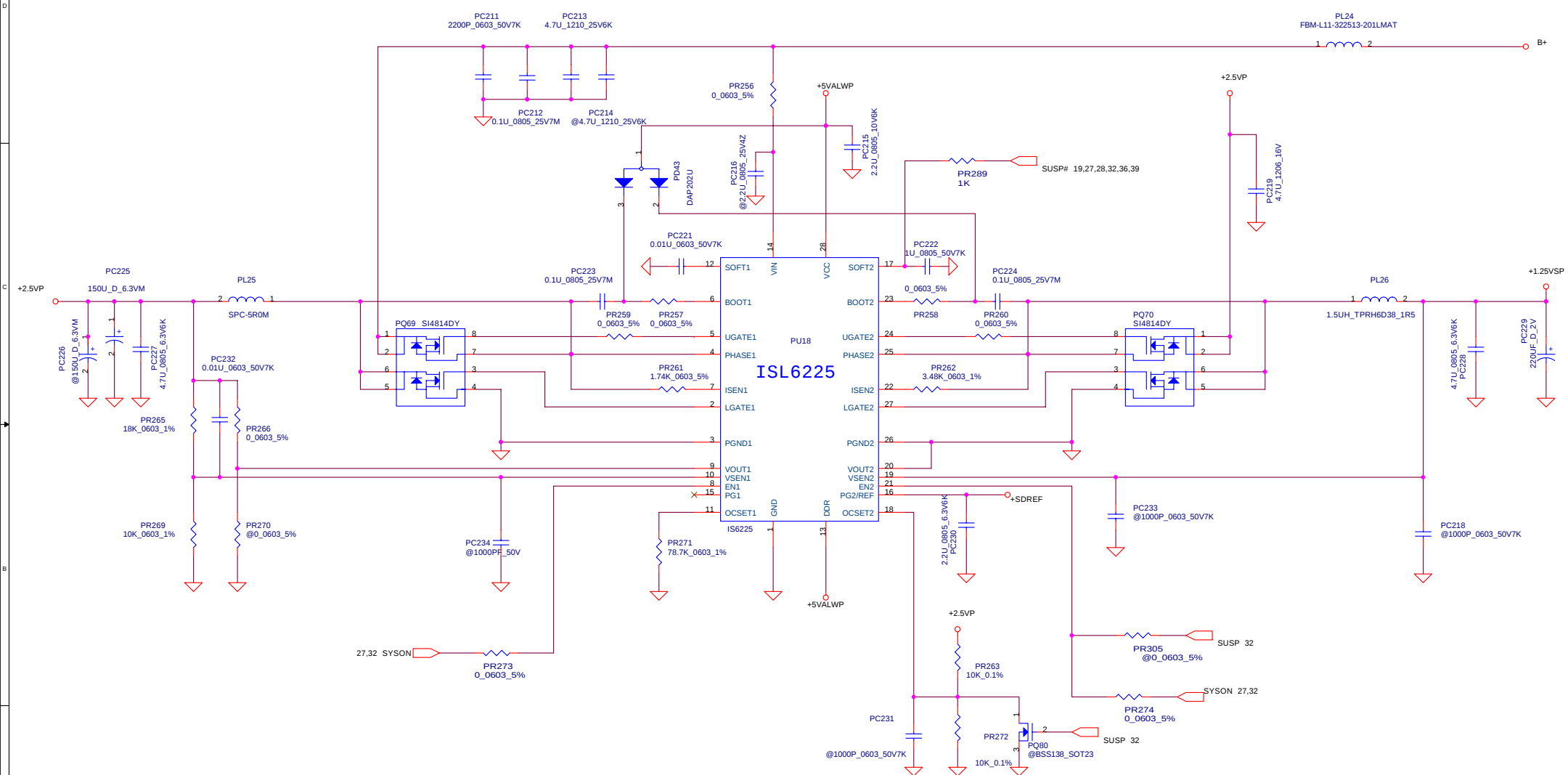
+1.8VALWP/+1.05VALWP



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Compal Electronics, Inc.			
Title			
1.8V & 1.05V			
Size	Document Number	Rev	
B	DCL53 LA-1881	1.0	
Date:	Monday, August 11, 2003	Sheet	39 of 40

+2.5VP/+1.25VSP



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Compal Electronics, Inc.			
Title			
2.5V & 1.25V			
Size	Document Number	Rev	
B	DCL53 LA-1881	1.0	
Date:	Monday, August 11, 2003	Sheet	40 of 40