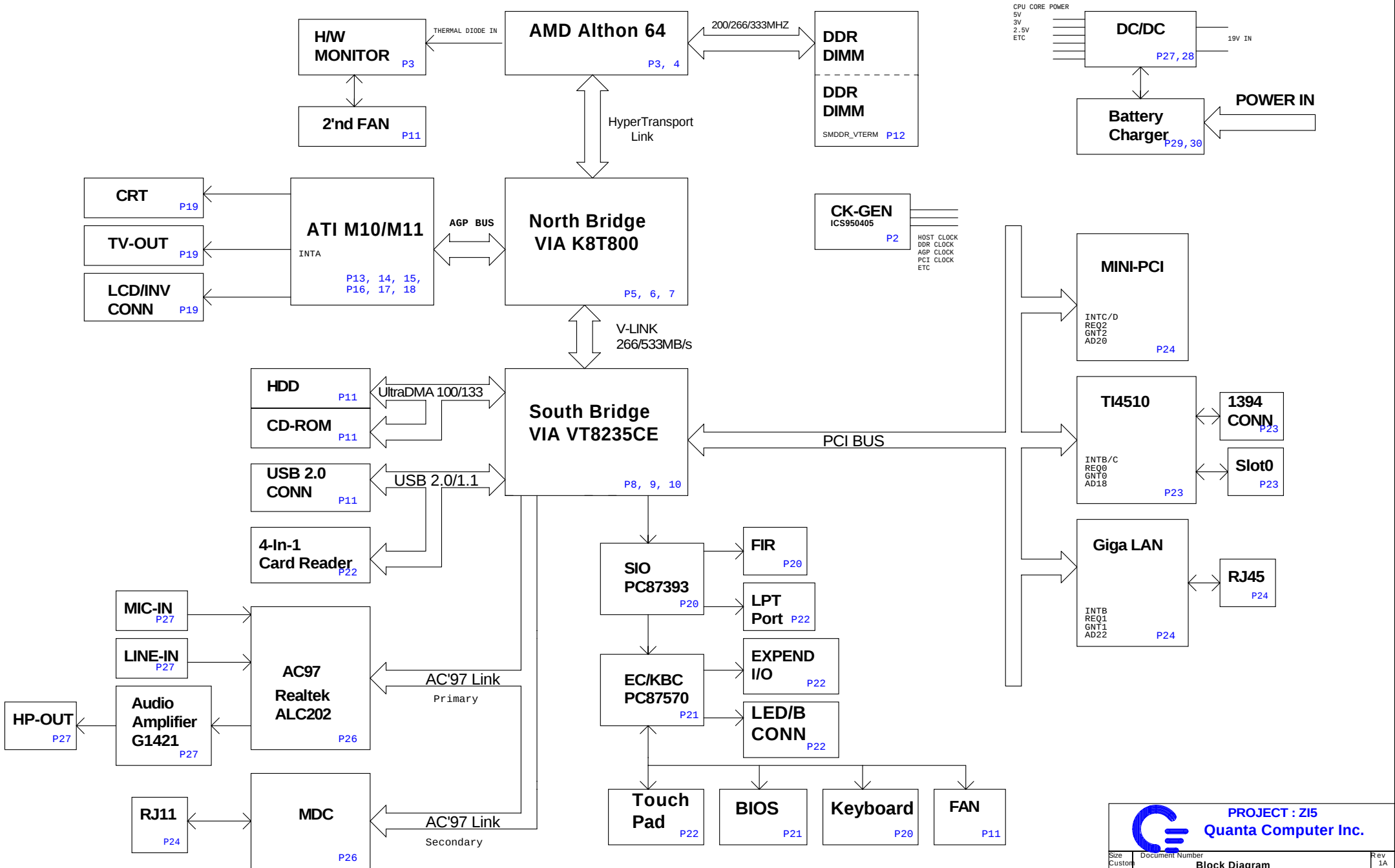
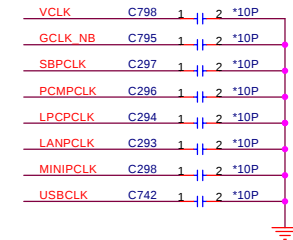
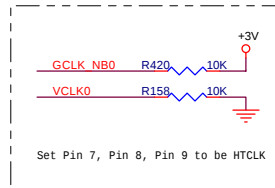
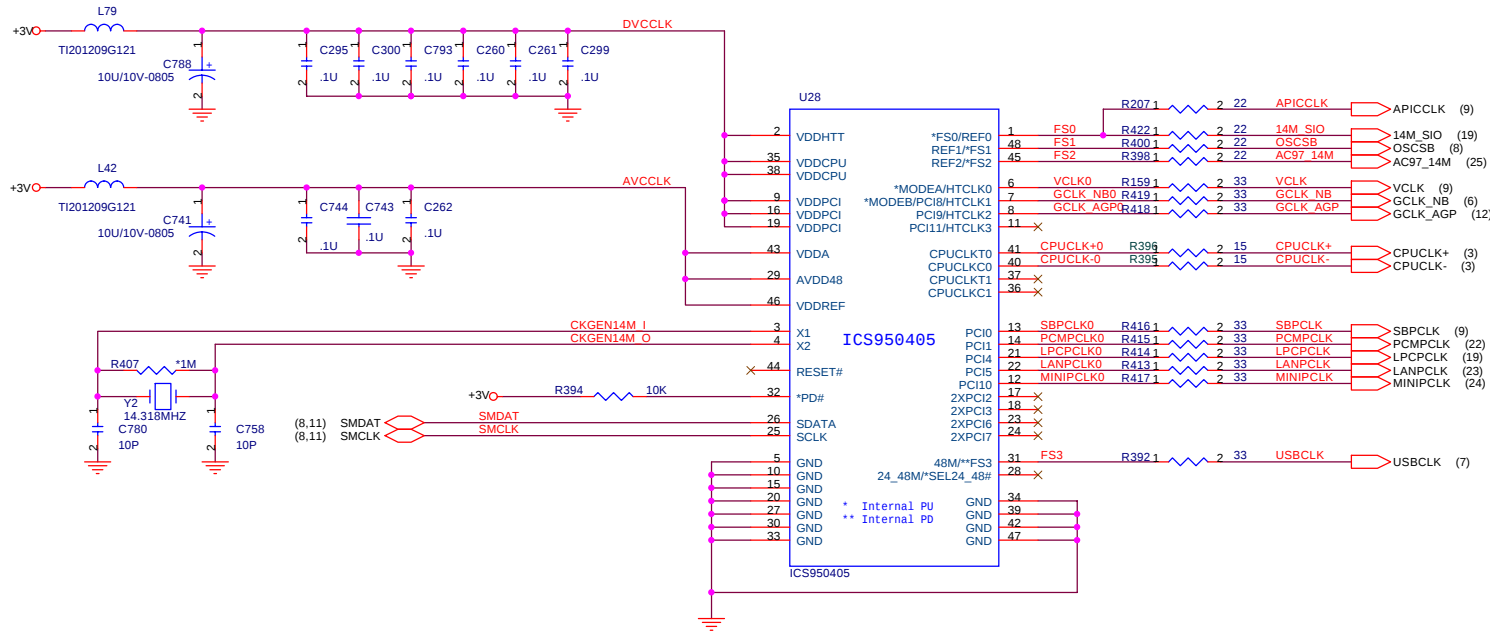
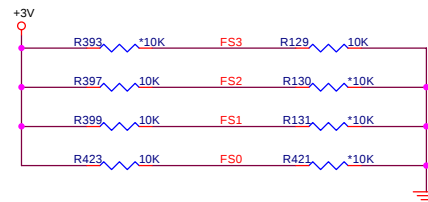


ZI5 SYSTEM BLOCK DIAGRAM

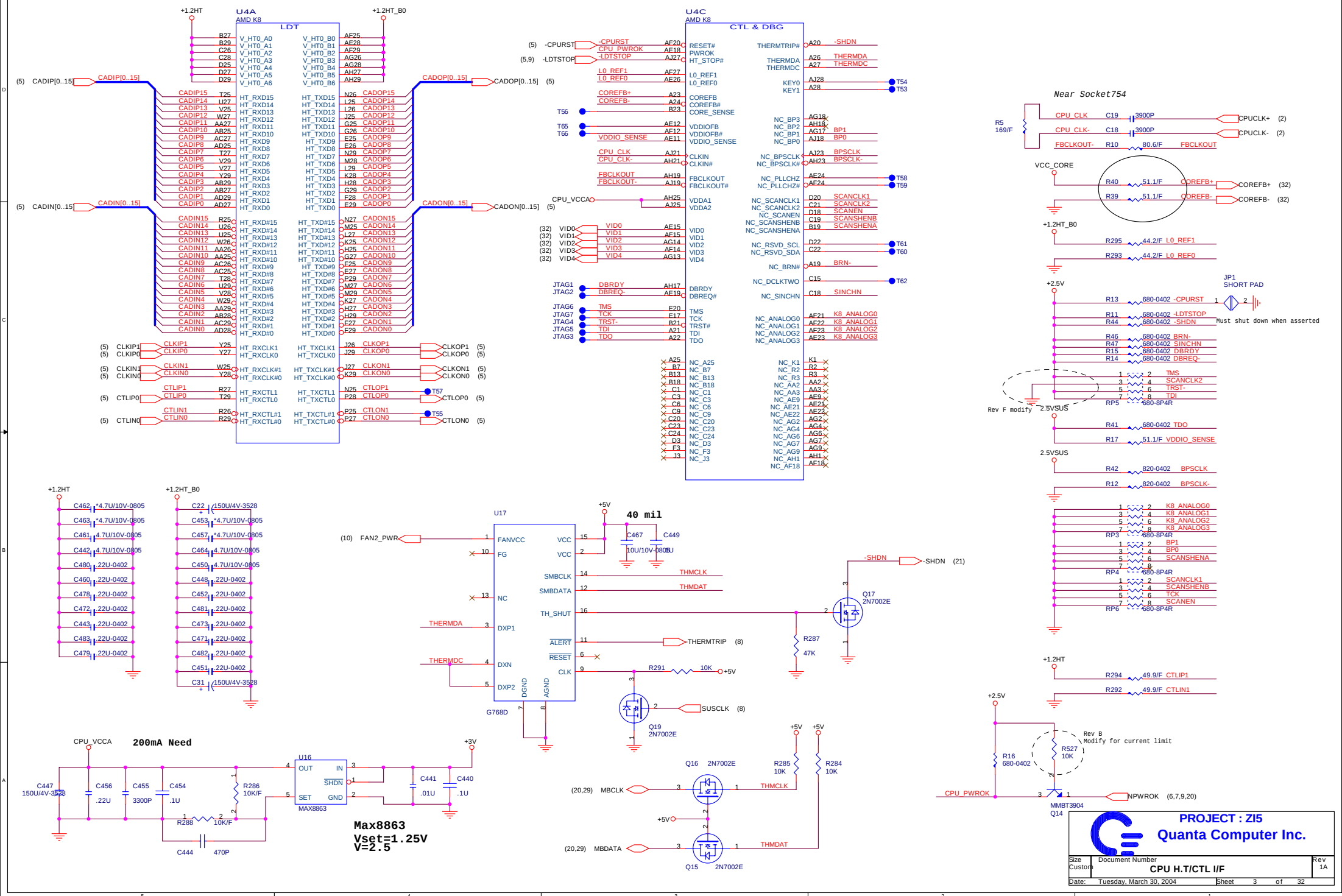




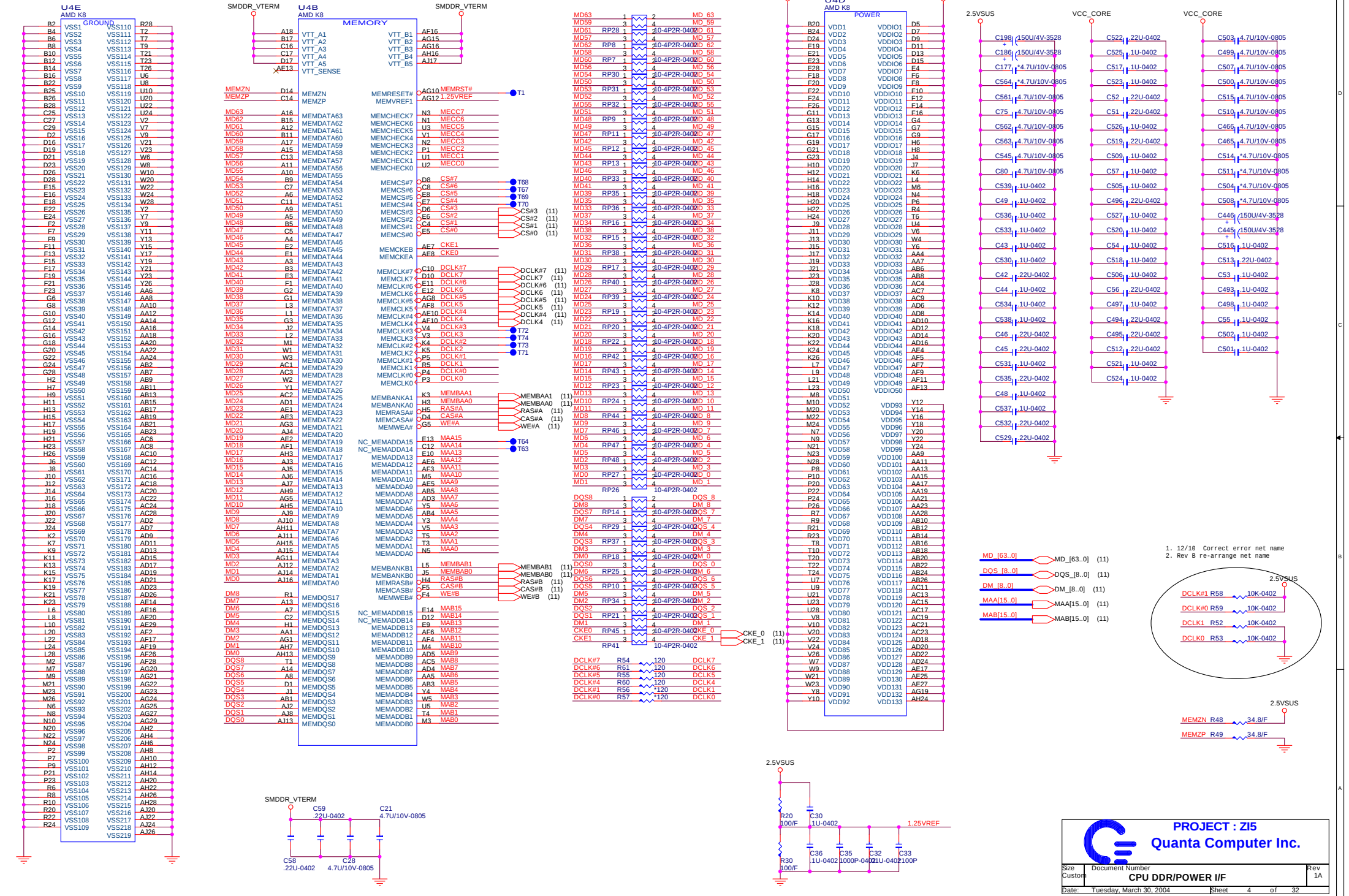
FS3	FS2	FS1	FS0	CPUCLK MHZ	HTT MHZ	PCCLK MHZ
0	0	0	0	100.00	67.27	33.63
0	0	0	1	133.00	66.95	33.48
0	0	1	0	166.00	67.30	33.60
0	0	1	1	202.00	67.30	33.67
0	1	0	0	100.00	66.70	33.38
0	1	0	1	133.00	66.70	33.34
0	1	1	0	166.00	66.70	33.40
0	1	1	1	200.00	66.70	33.40
1	0	0	0	150.00	66.00	33.00
1	0	0	1	180.00	66.00	33.00
1	0	1	0	210.00	66.00	33.00
1	0	1	1	240.00	66.00	33.00
1	1	0	0	270.00	66.70	33.00
1	1	0	1	300.00	66.70	33.00
1	1	1	0	330.00	66.70	33.00
1	1	1	1	360.00	66.70	33.00



CPU H.T/CTL I/F



CPU DDR/POWER I/F

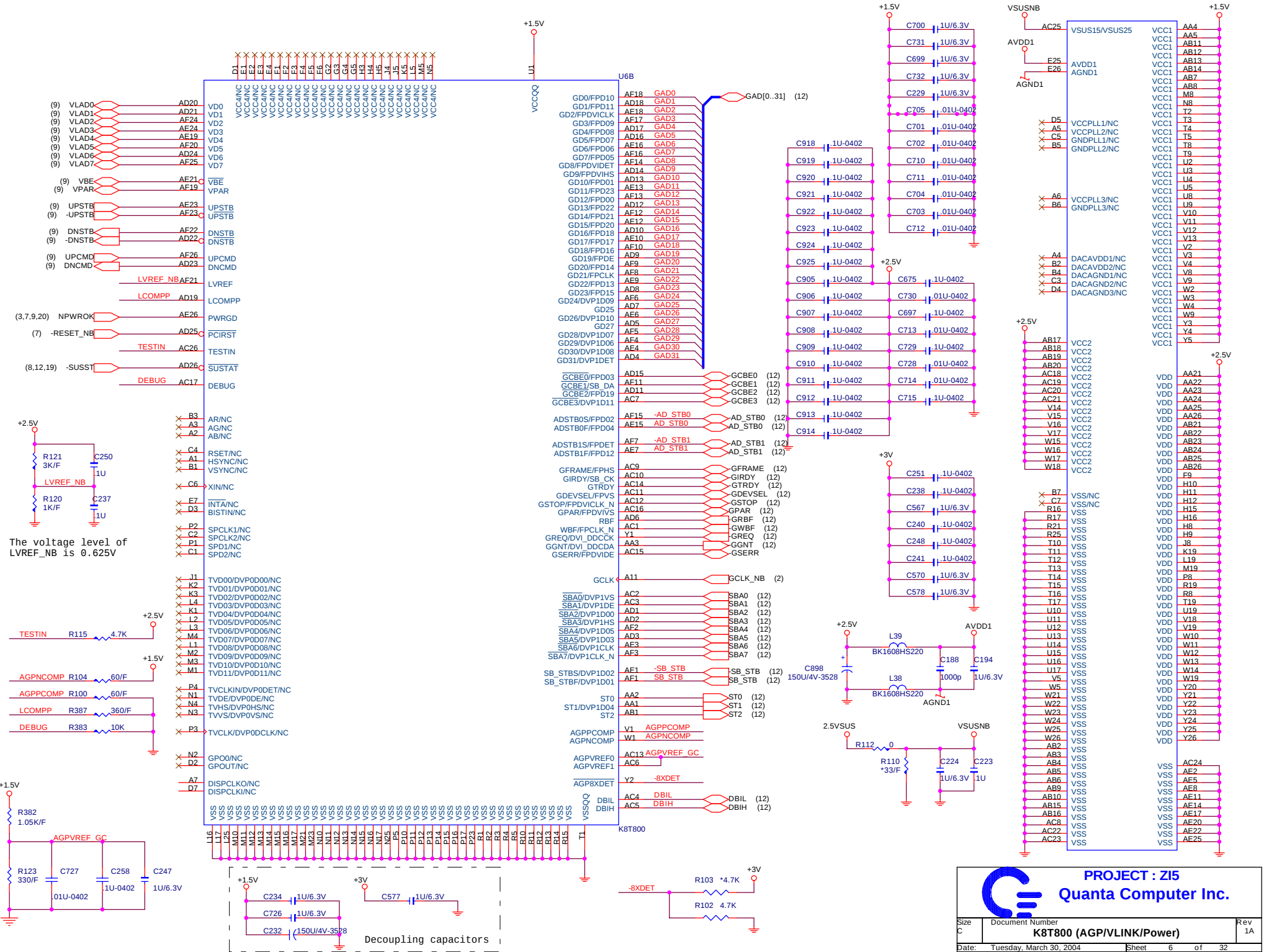




PROJECT : Z15

Quanta Computer Inc.

Size	Document Number	Rev
Custom	CPU DDR/POWER I/F	1A
Date	Tuesday, March 30, 2004	Sheet 4 of 32



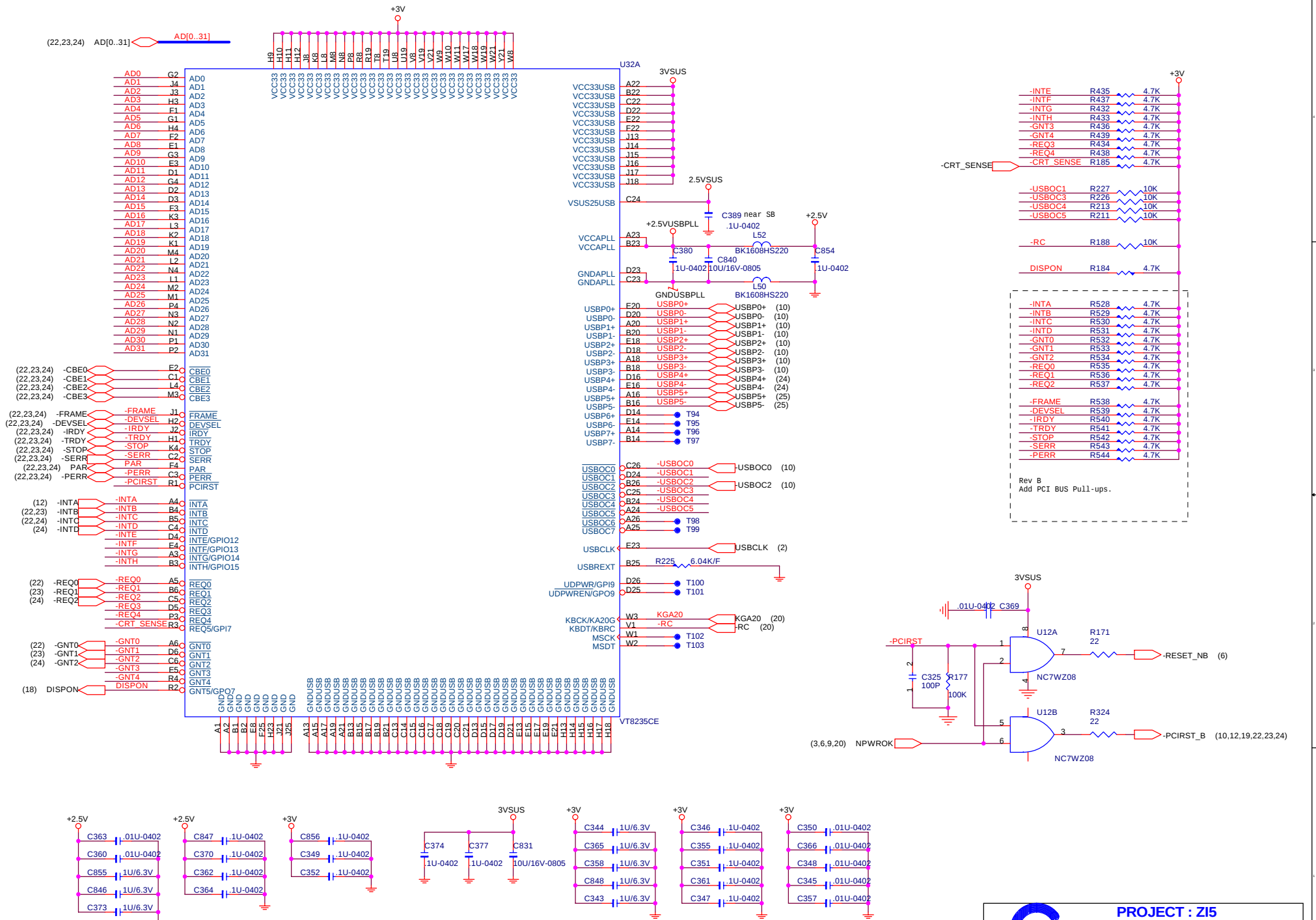
The voltage level of LVREF_NB is 0.625V

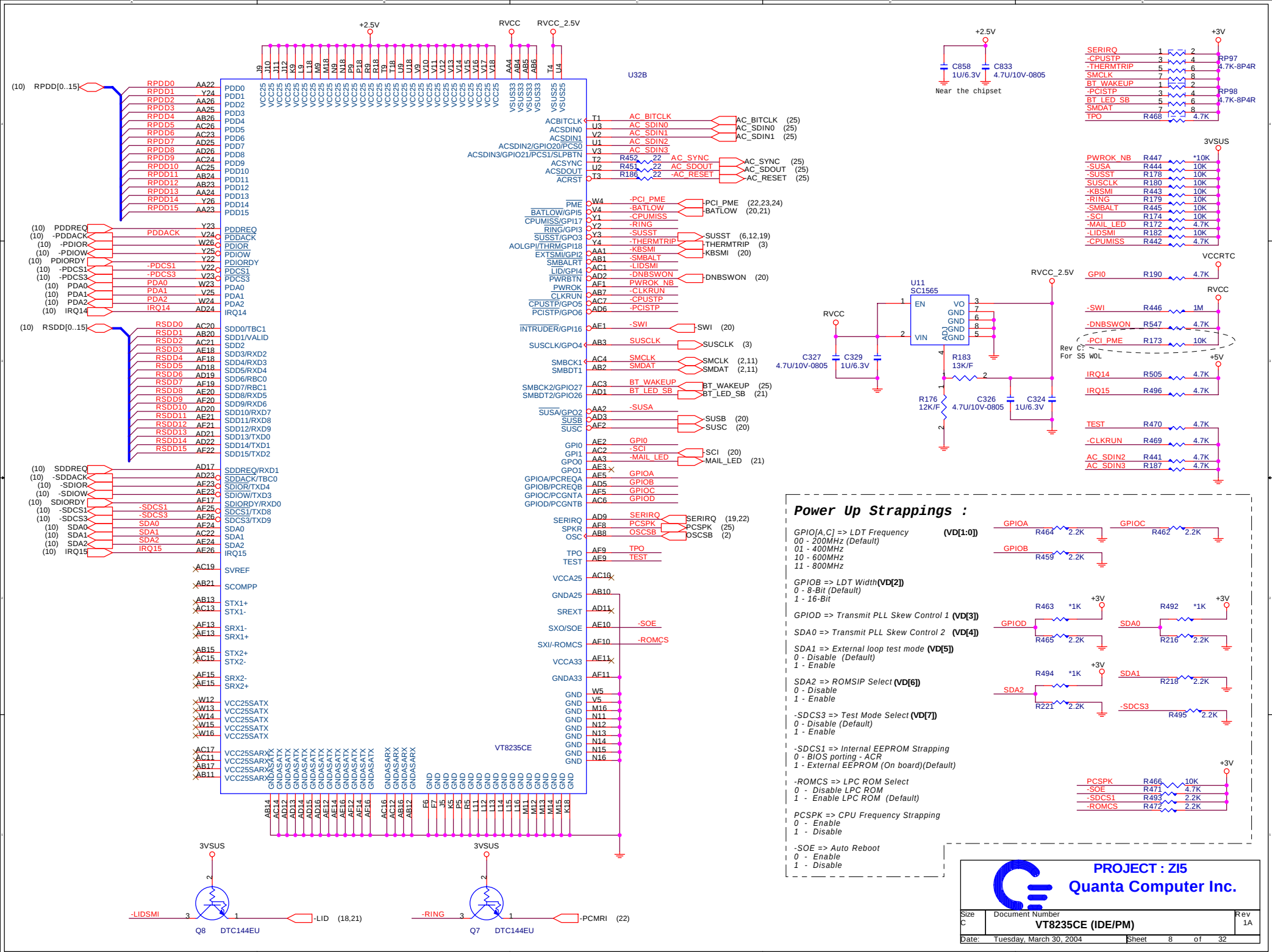


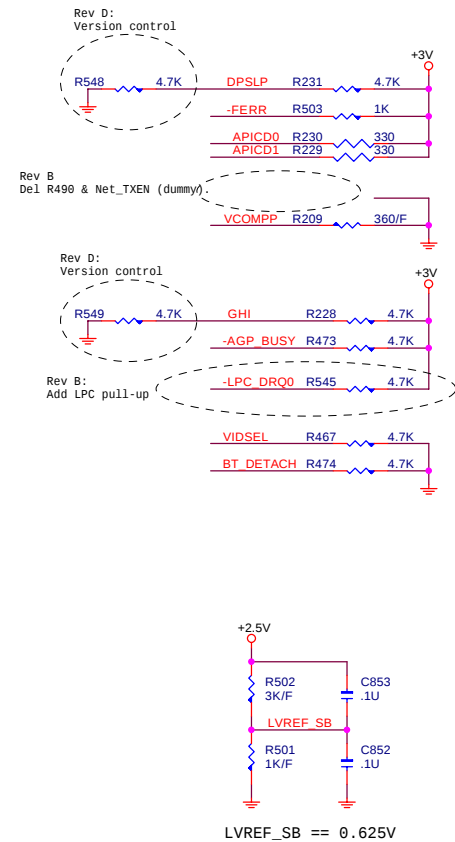
PROJECT : ZI5

Quanta Computer Inc.

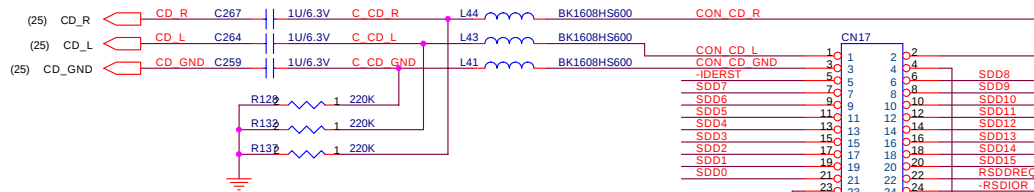
Size	Document Number	Rev
C	K8T800 (AGP/VLINK/Power)	1A
Date:	Tuesday, March 30, 2004	Sheet 6 of 32



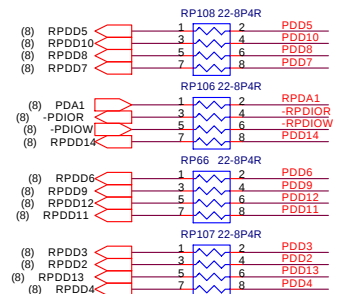
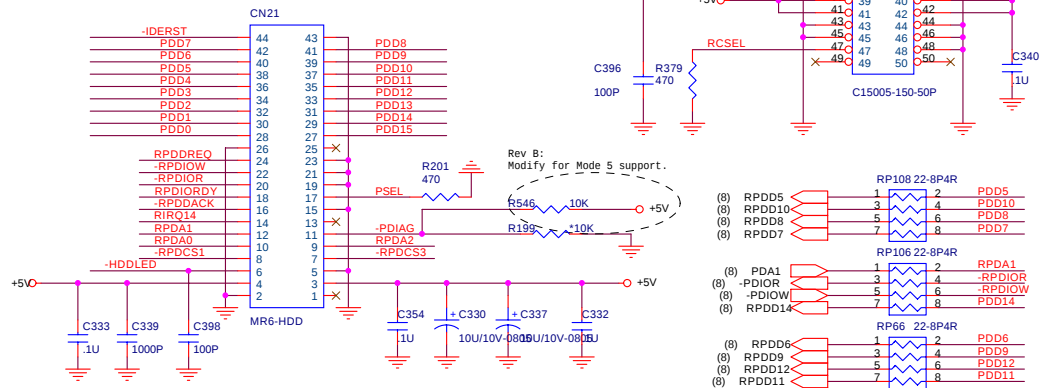




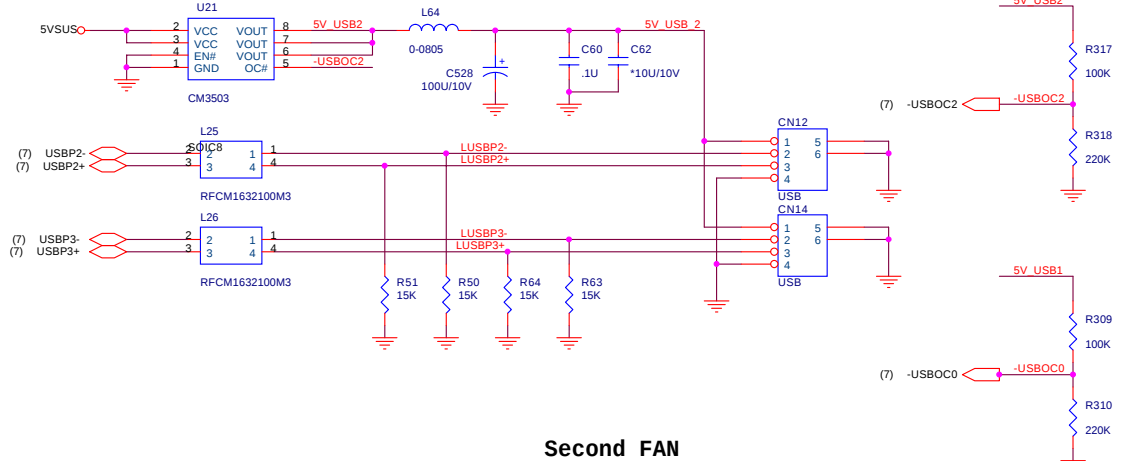
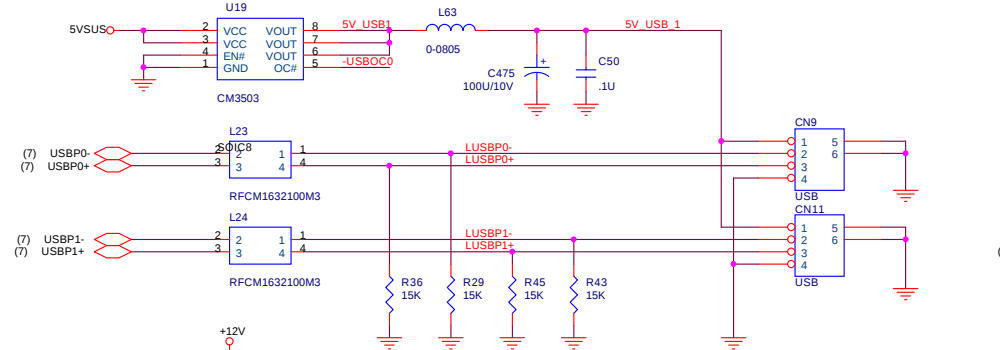
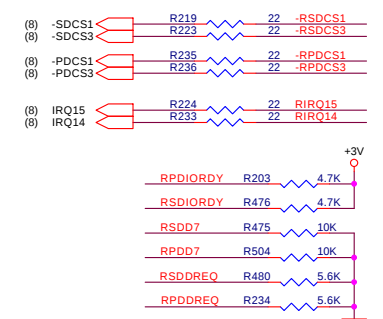
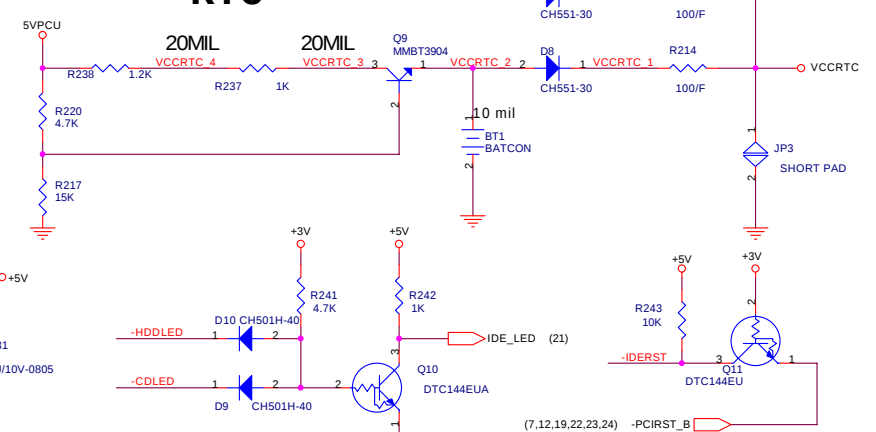
ODD CONNECTOR



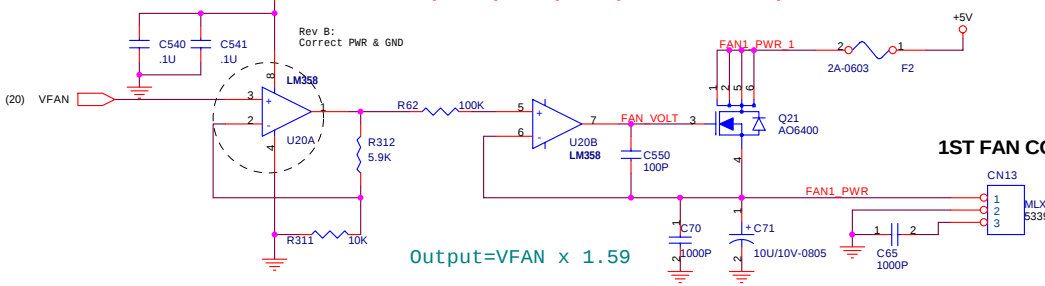
HDD connector



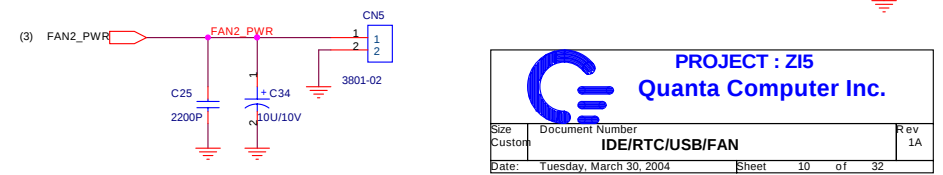
RTC

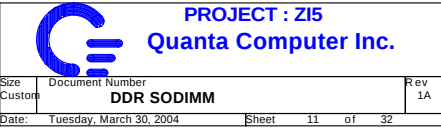


1ST FAN CONN

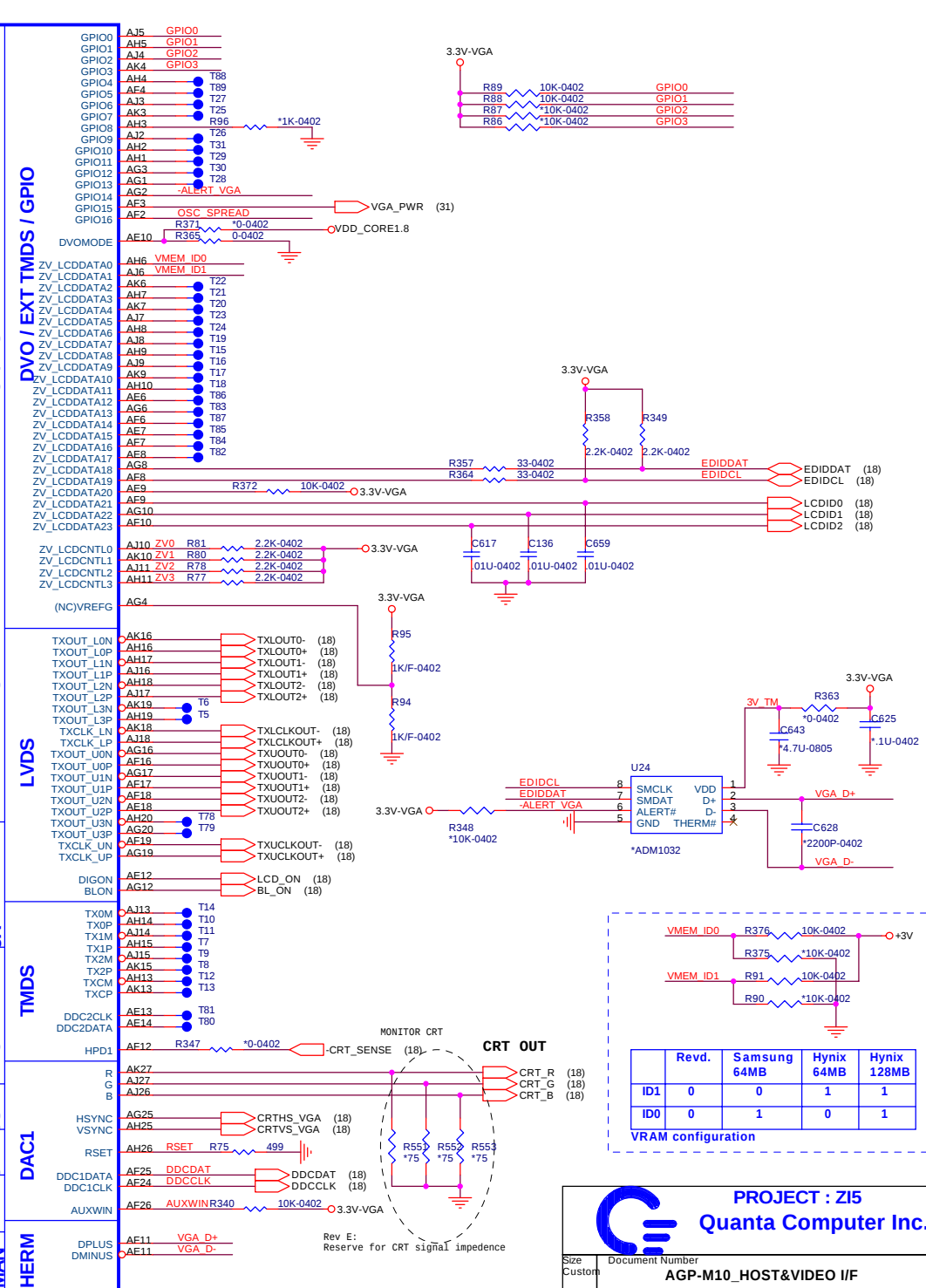
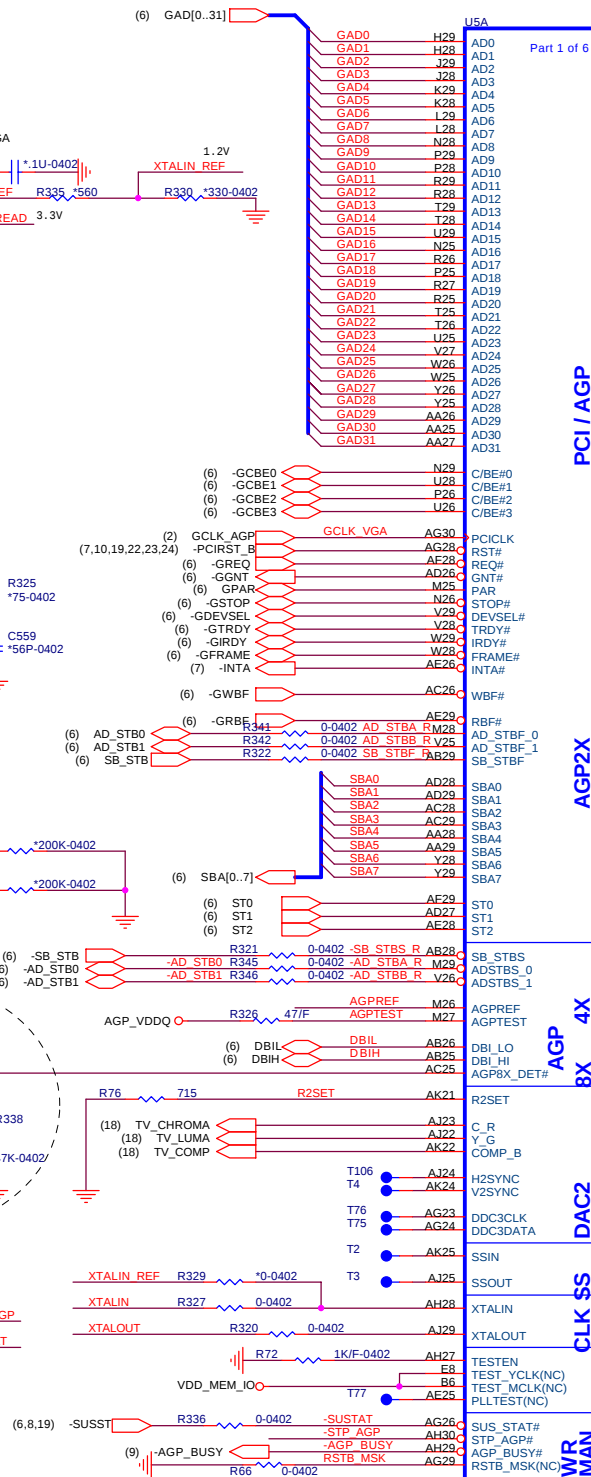
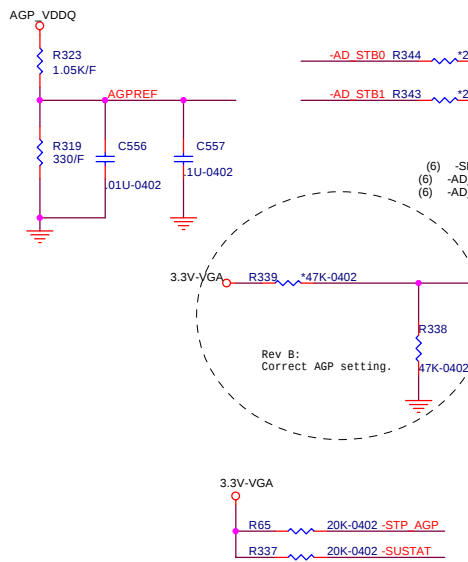


Second FAN



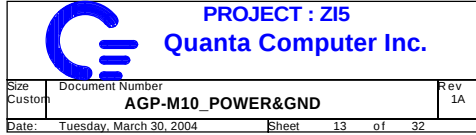


Place Y1 and caps close to GPU

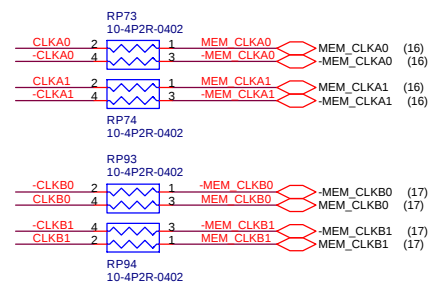
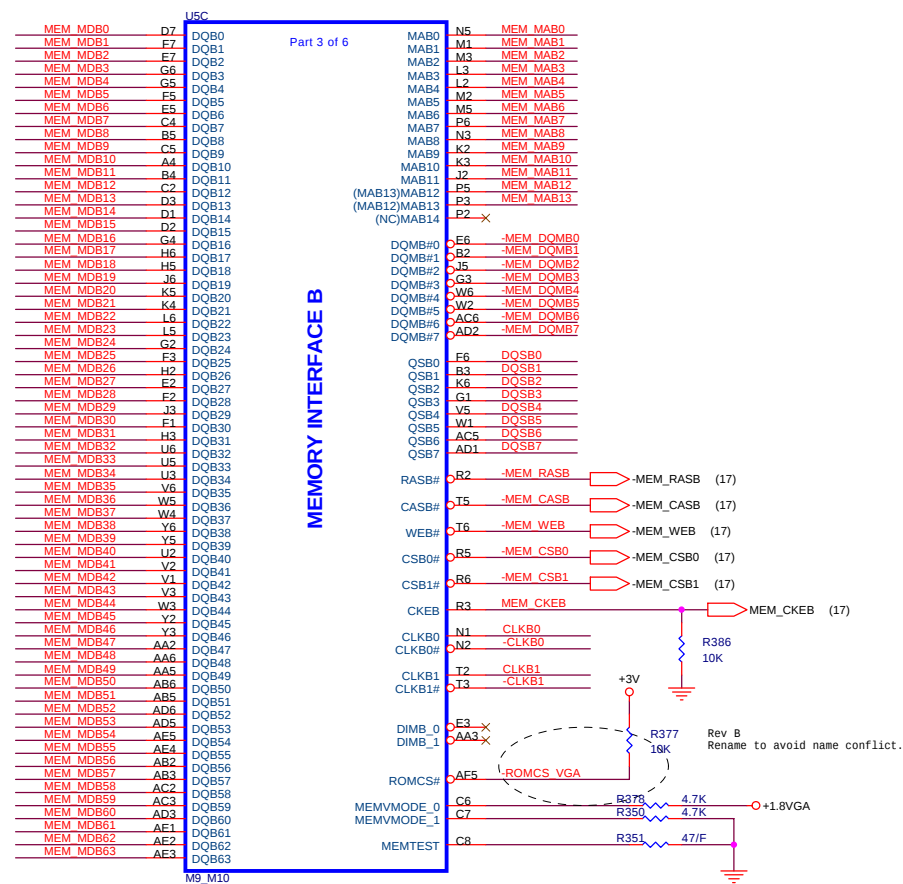


	Rev'd.	Samsung 64MB	Hynix 64MB	Hynix 128MB
ID1	0	0	1	1
ID0	0	1	0	1

 PROJECT : Z15
Quanta Computer Inc.



MEMORY CHANNEL B



MEMVMODE[1:0]	MEMORY IO VOLTAGE
0 1	2.5V (DDR)
1 0	1.8V (DDR)
1 1	3.3V (SDR)



PROJECT : ZI5
Quanta Computer Inc.

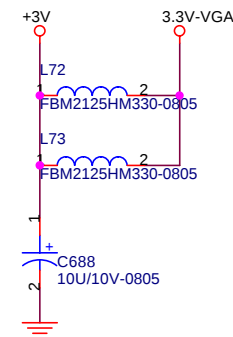
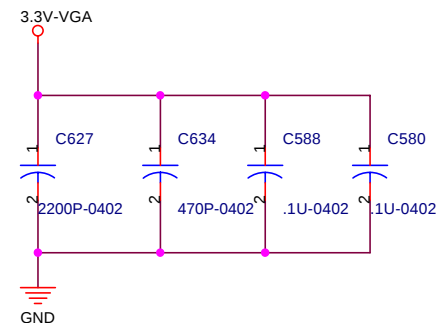
Rev D
Del L74 & L75 (Short)
Del L76 & L77 (Open)

+1.8VGA
3500mA

VDDRH, VDDQ AND VDDR1 VOLTAGE SELECTION

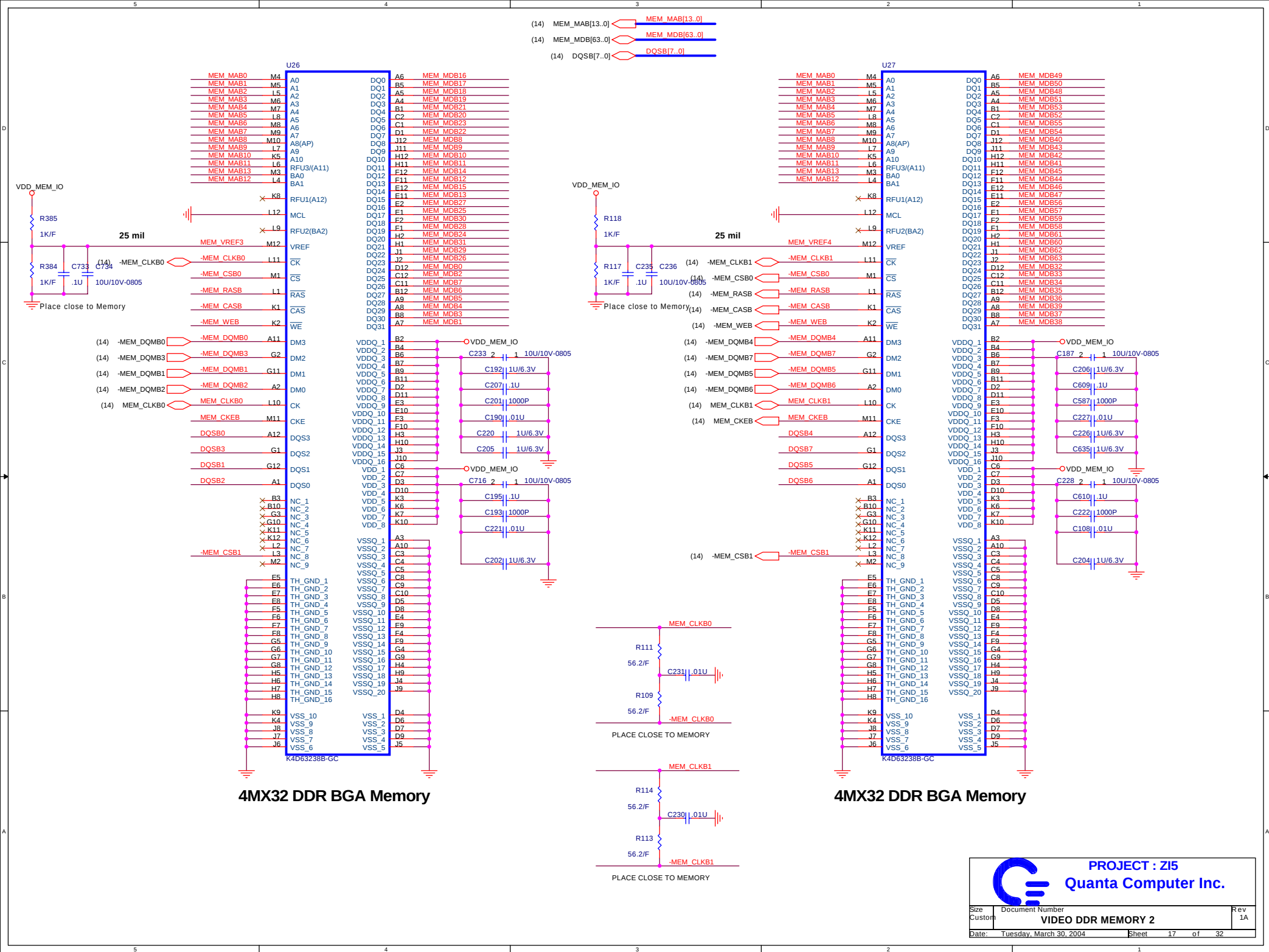
VDD_MEM_IO	FBa	FBb	FBc	FBd
1.8V (ELPIDA)	IN	OUT	OUT	IN
25V	OUT	IN	IN	OUT

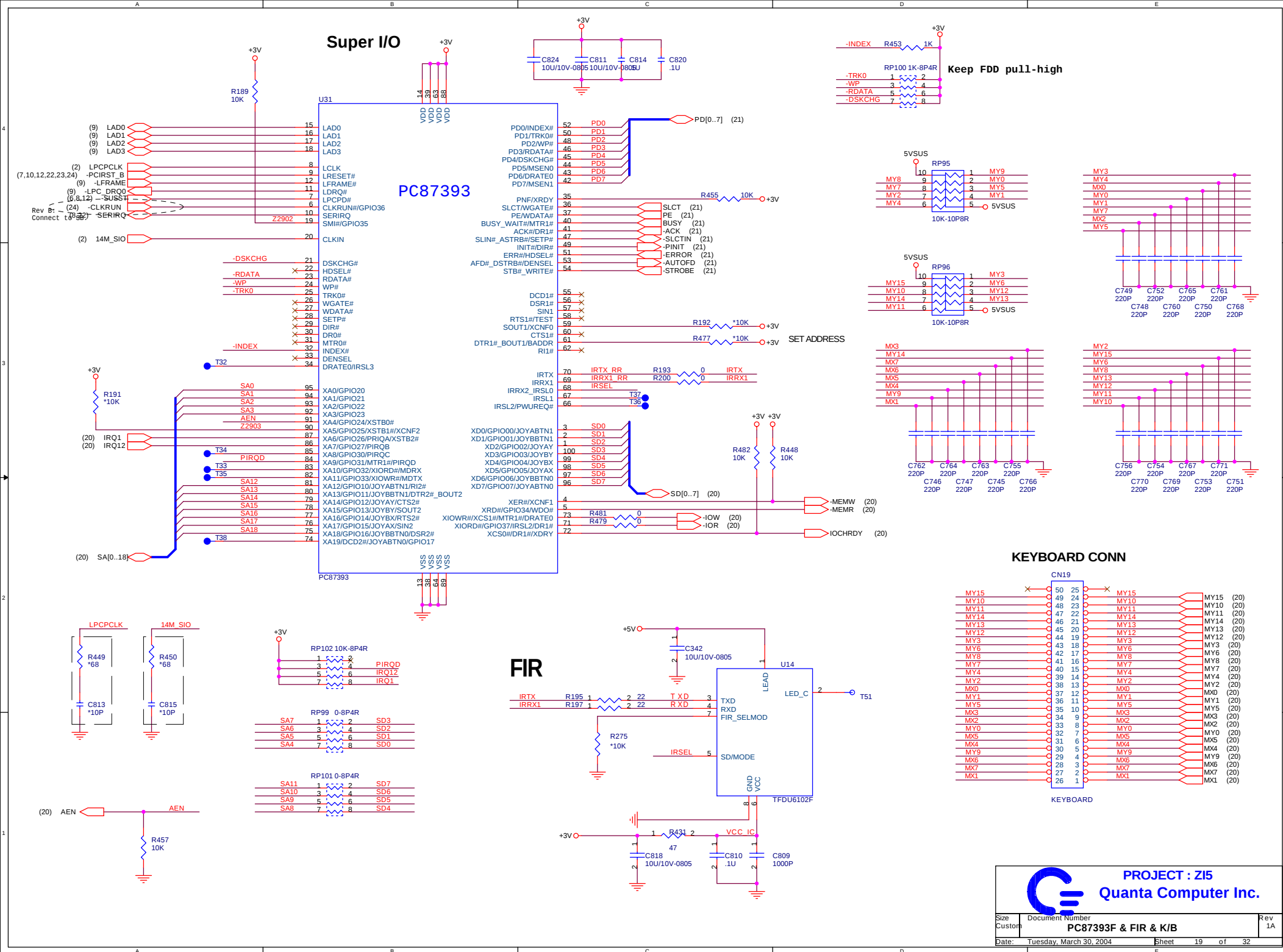
PLACE CAPS FOR THESE GROUNDS CLOSE TO ASIC AND RUN DEDICATED TRACES FROM PINS THAT JOIN TO GROUND PLANE WITH ONE VIA AT CAP

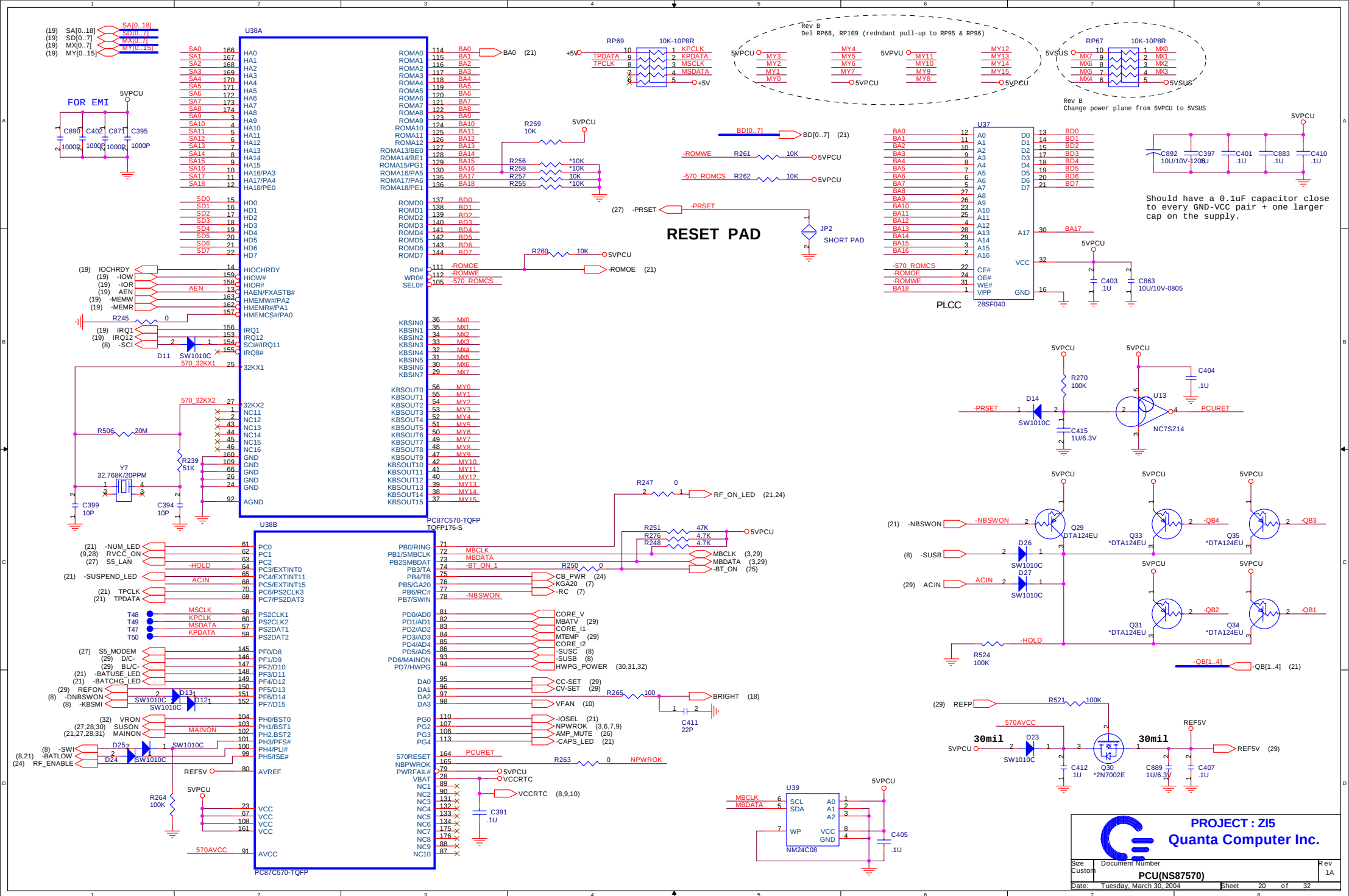


PROJECT : ZI5
Quanta Computer Inc.

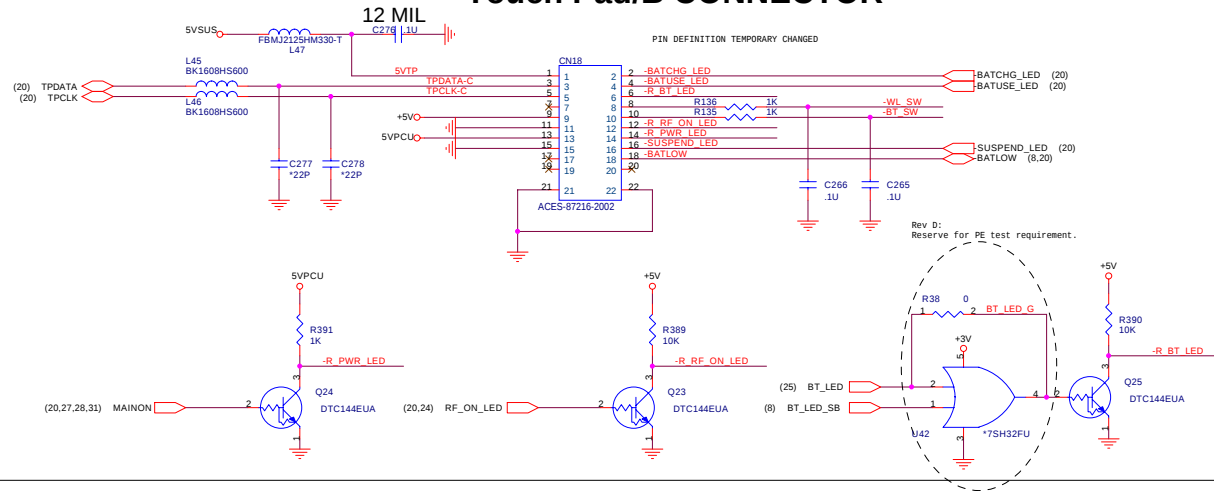
Size Custom	Document Number VGA POWER	Rev 1A
Date: Tuesday, March 30, 2004	Sheet 15 of 32	



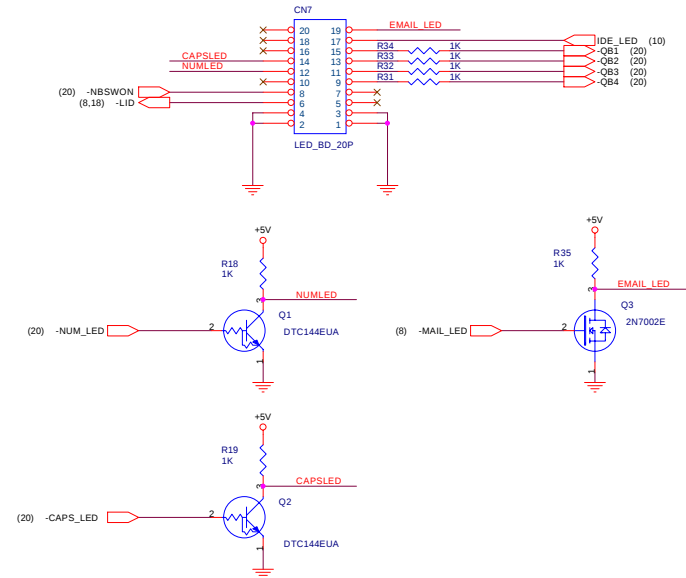




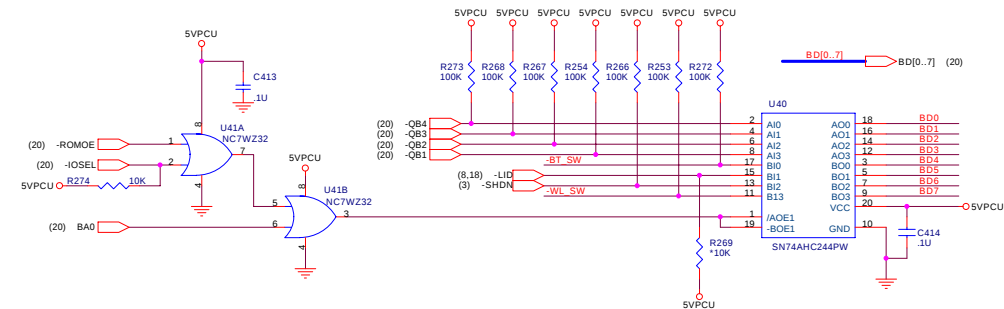
Touch Pad/B CONNECTOR



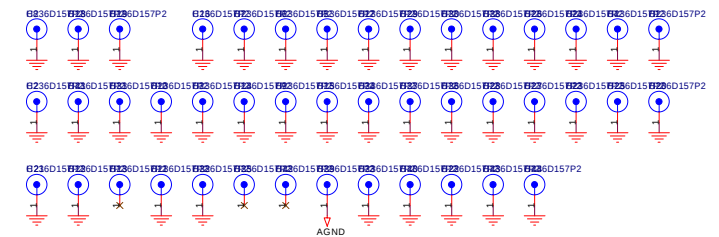
LED/B CONNECTOR



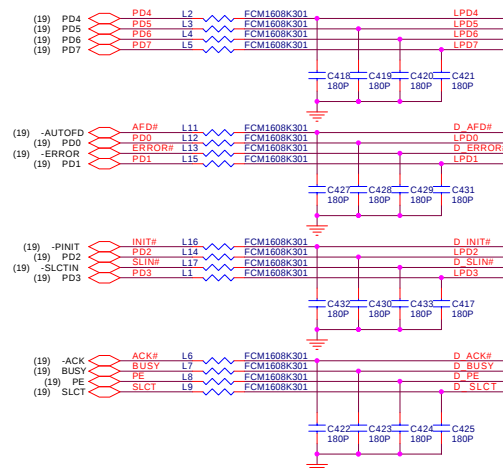
570 EXPEND I/O



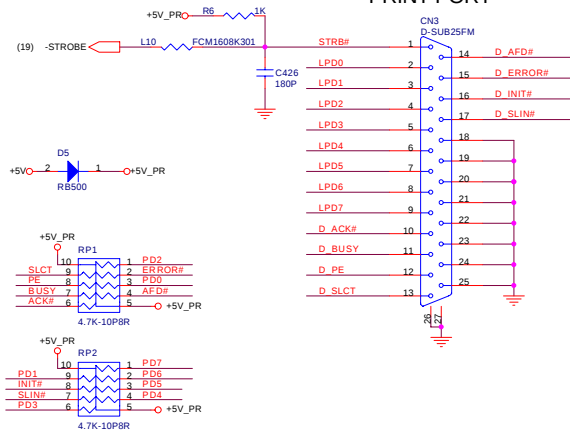
Screw Holes



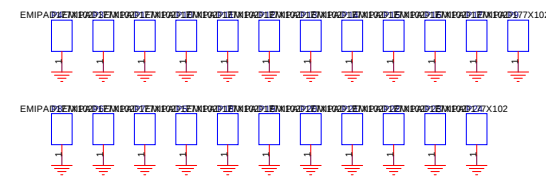
LPT

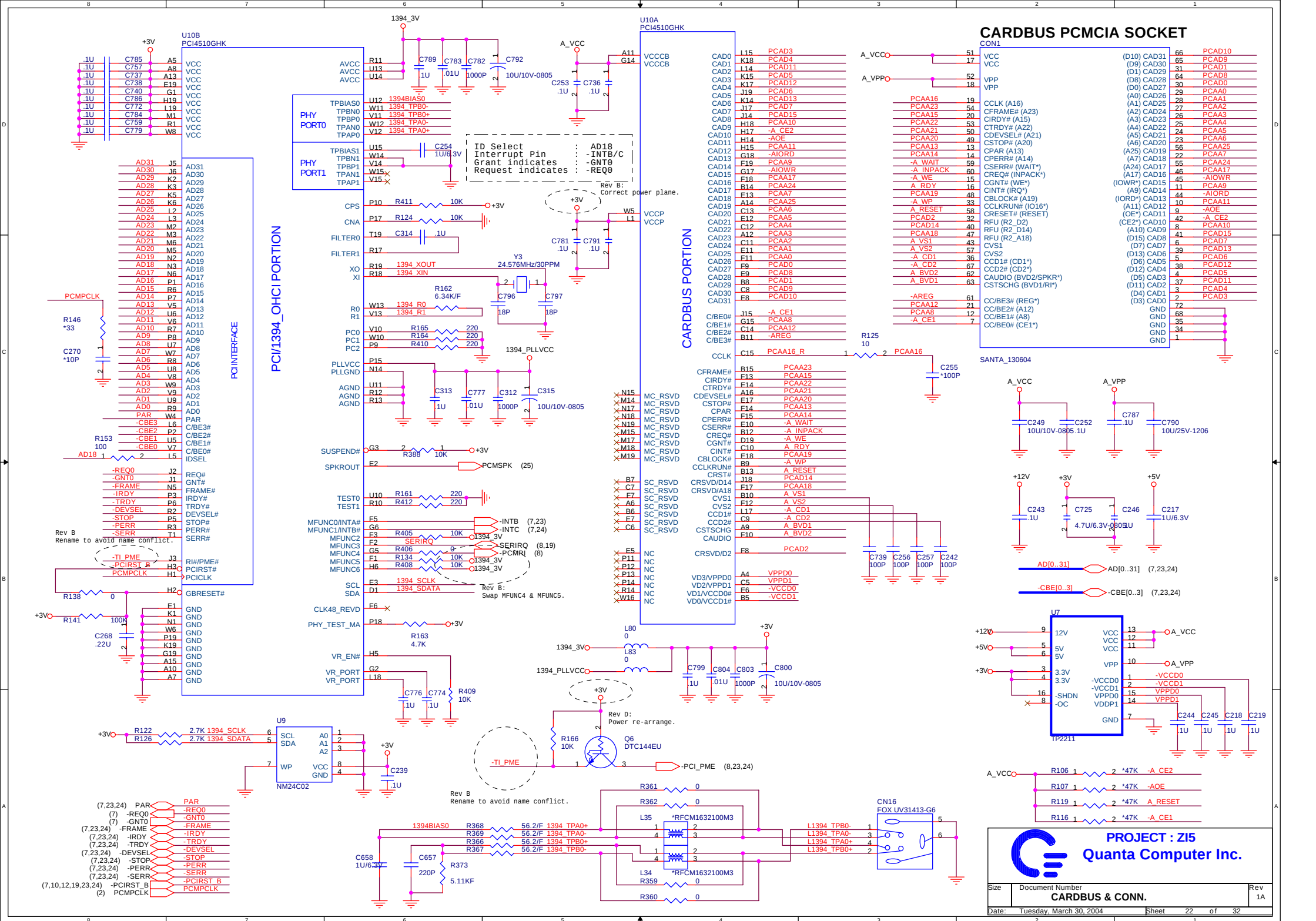


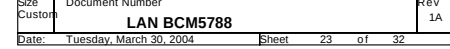
PRINT PORT



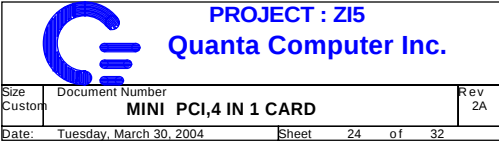
EMI Pads

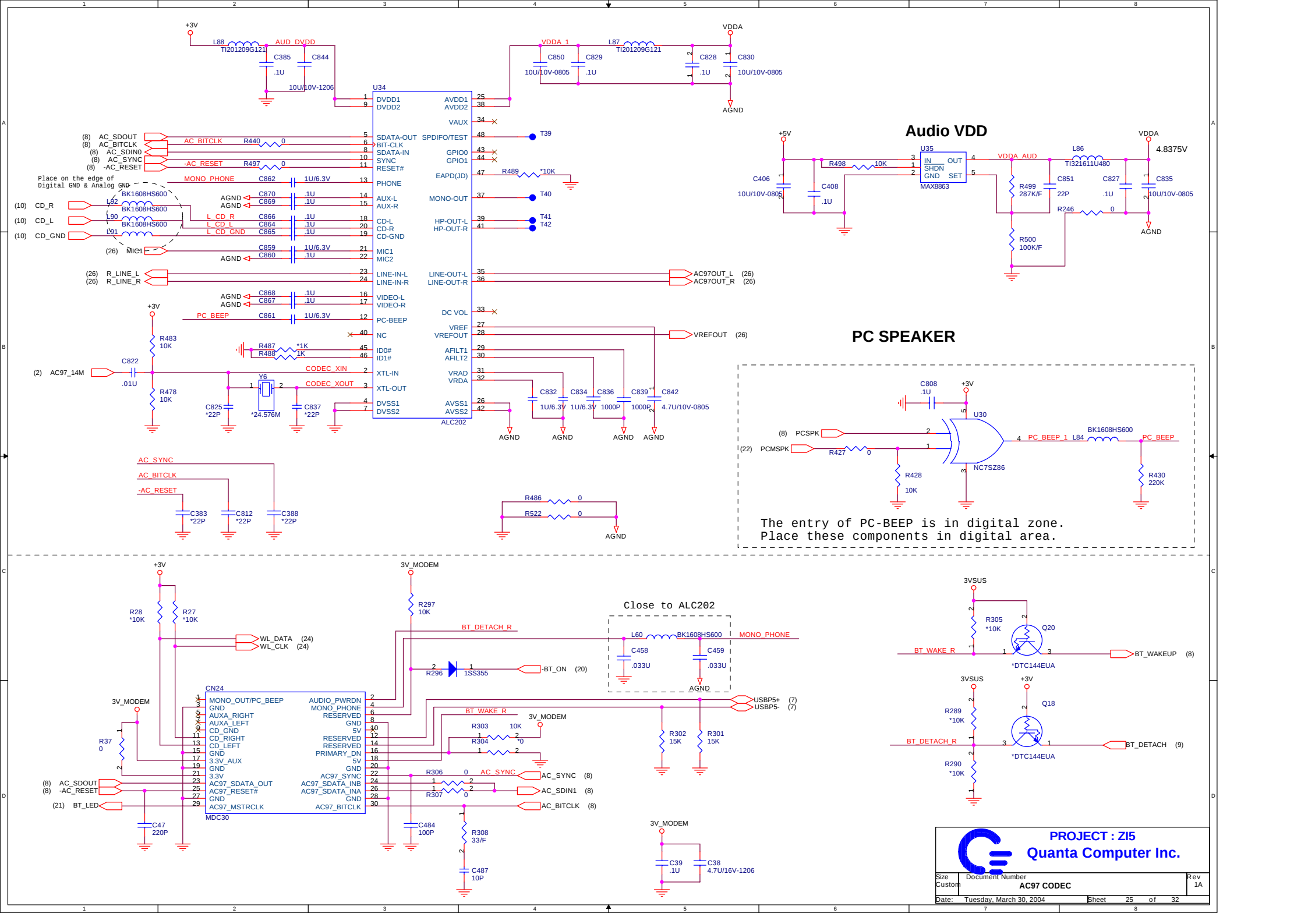




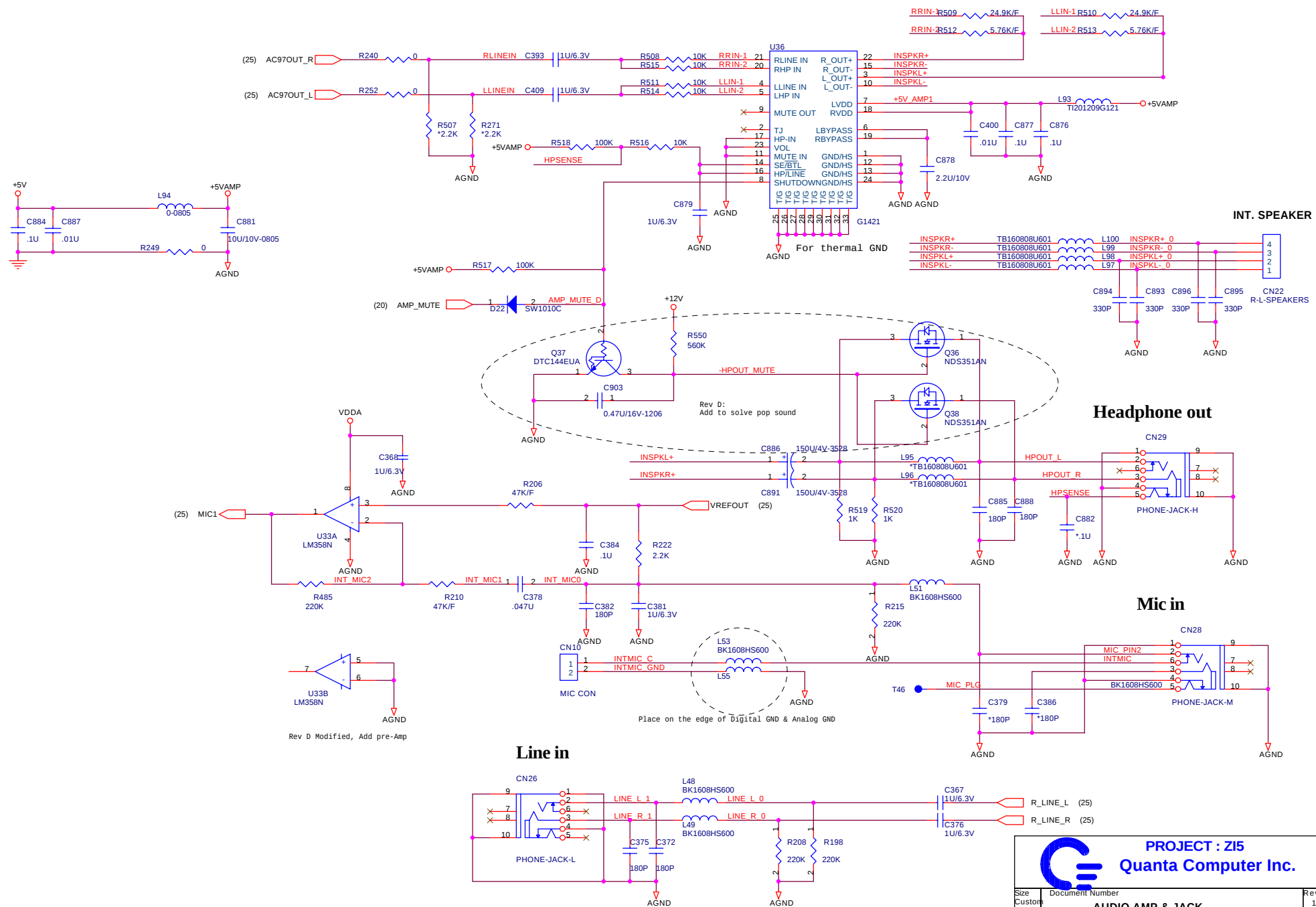


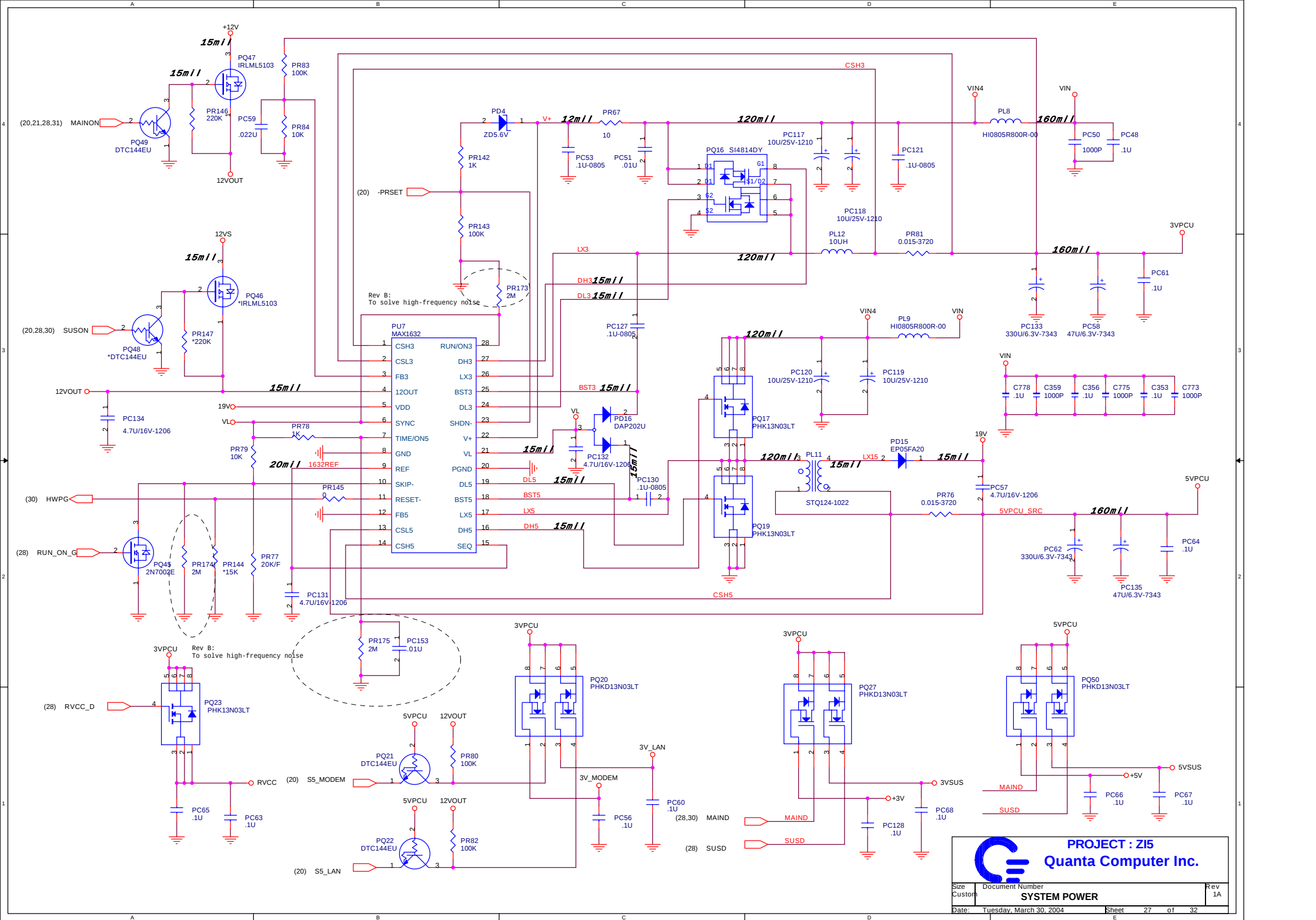
TYPE III MINI PCI SOCKET

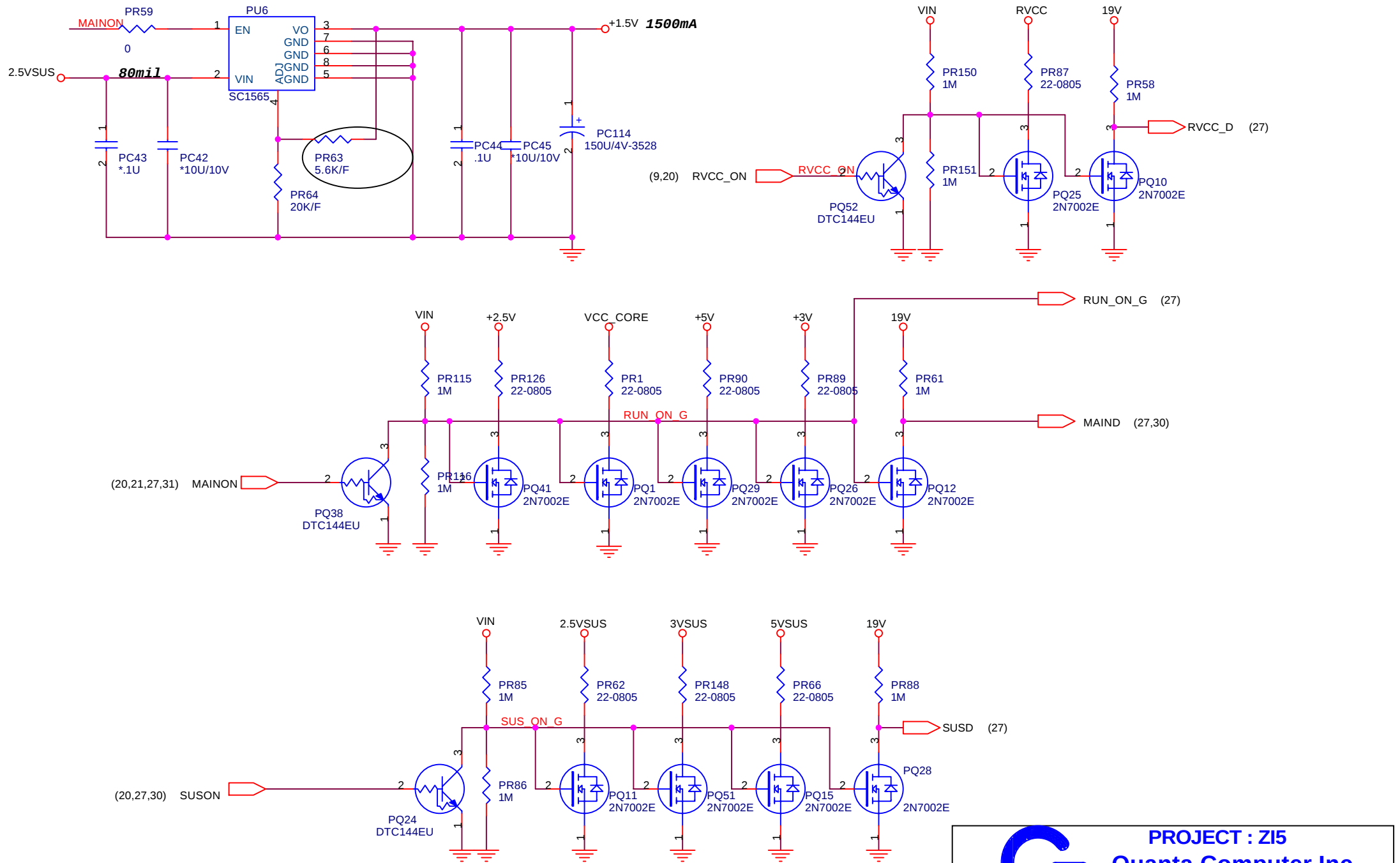


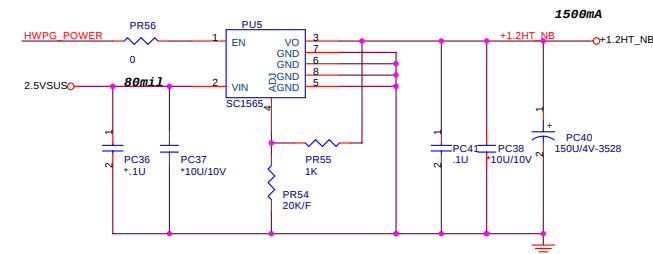
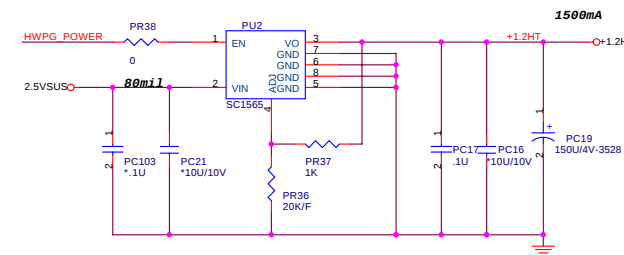
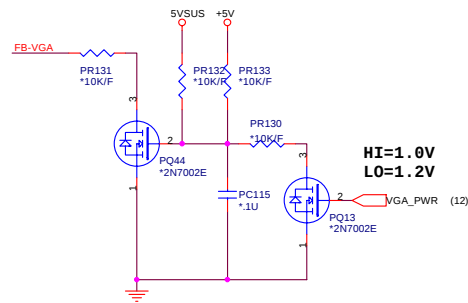
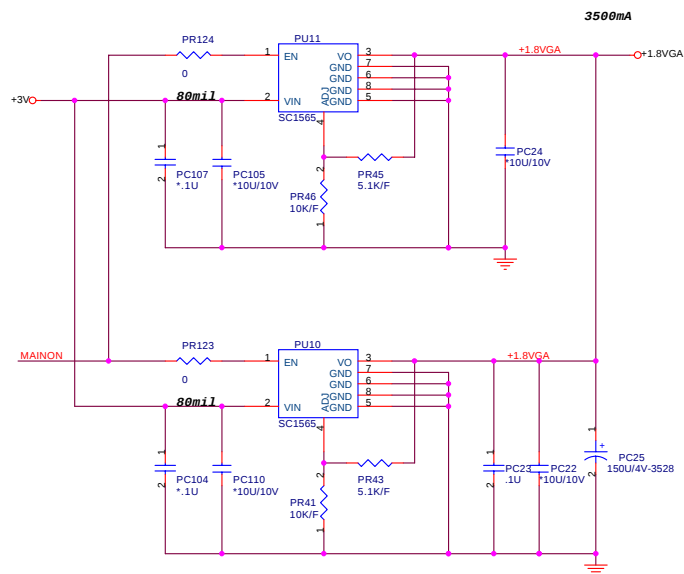
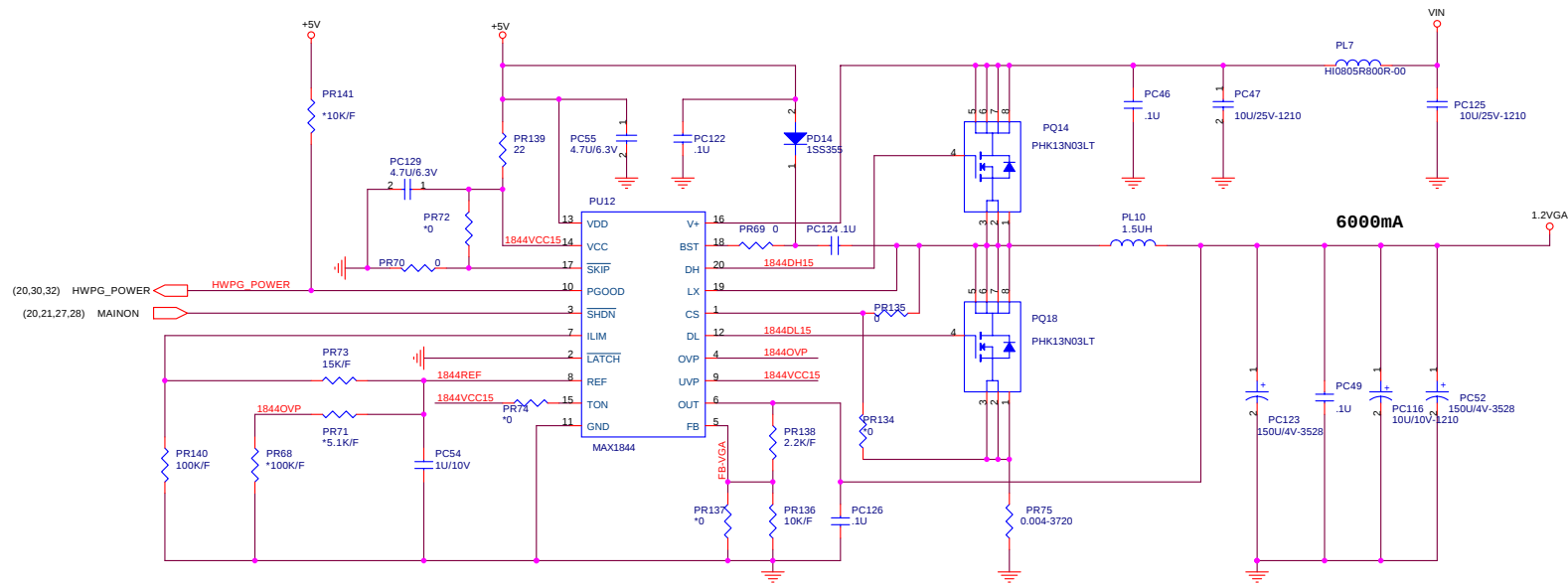


Audio amplifier









PROJECT : Z15
Quanta Computer Inc.

