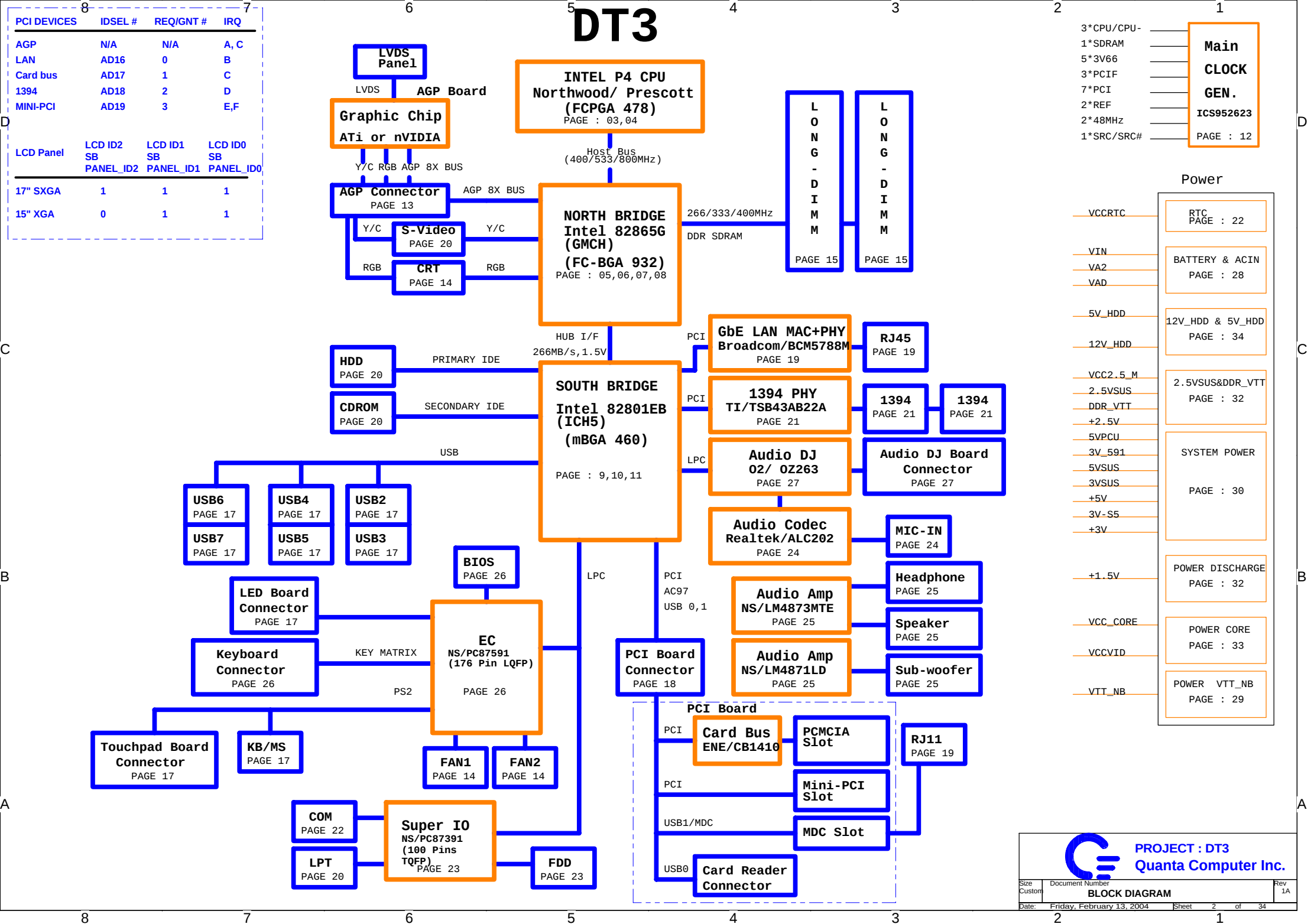
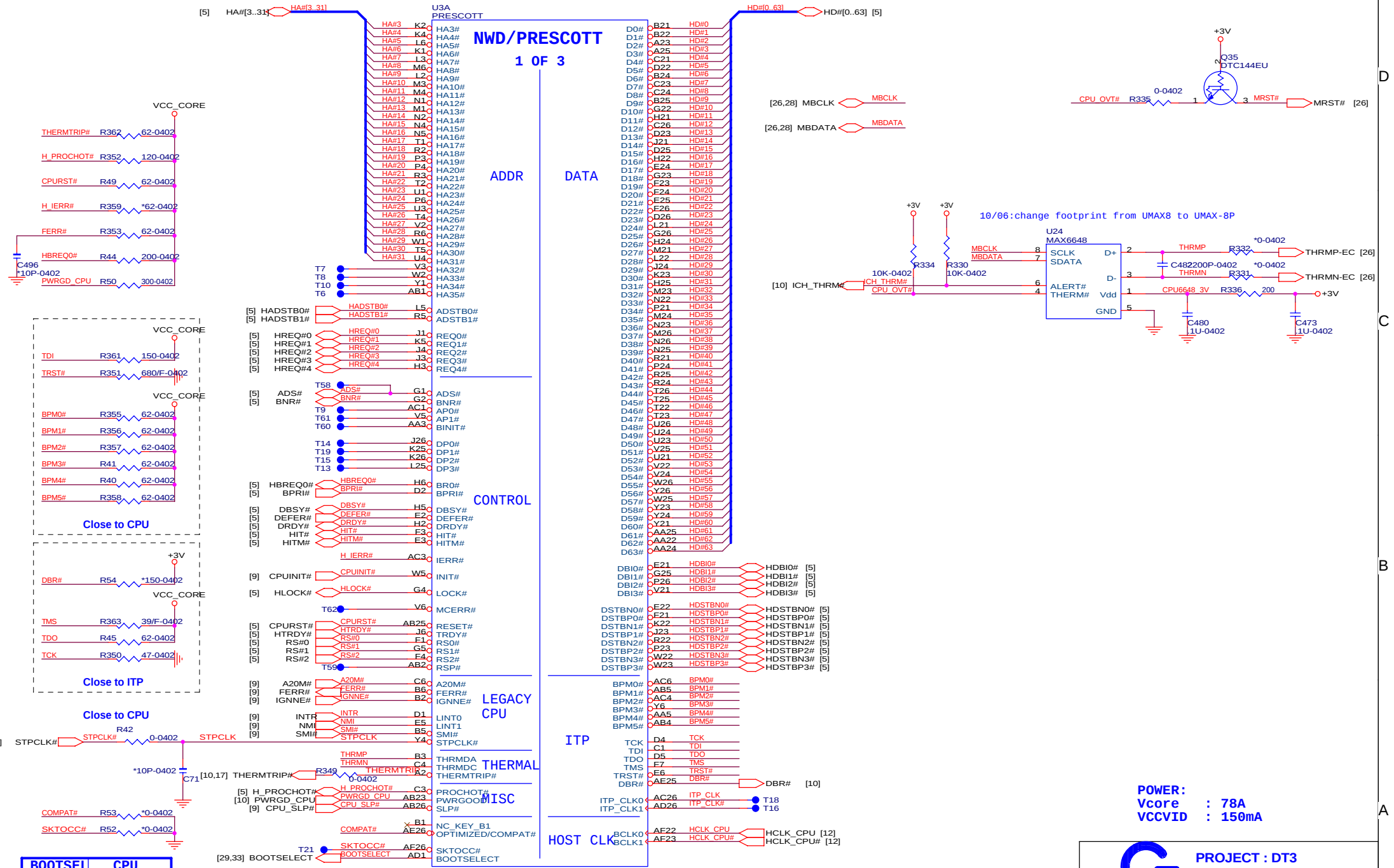


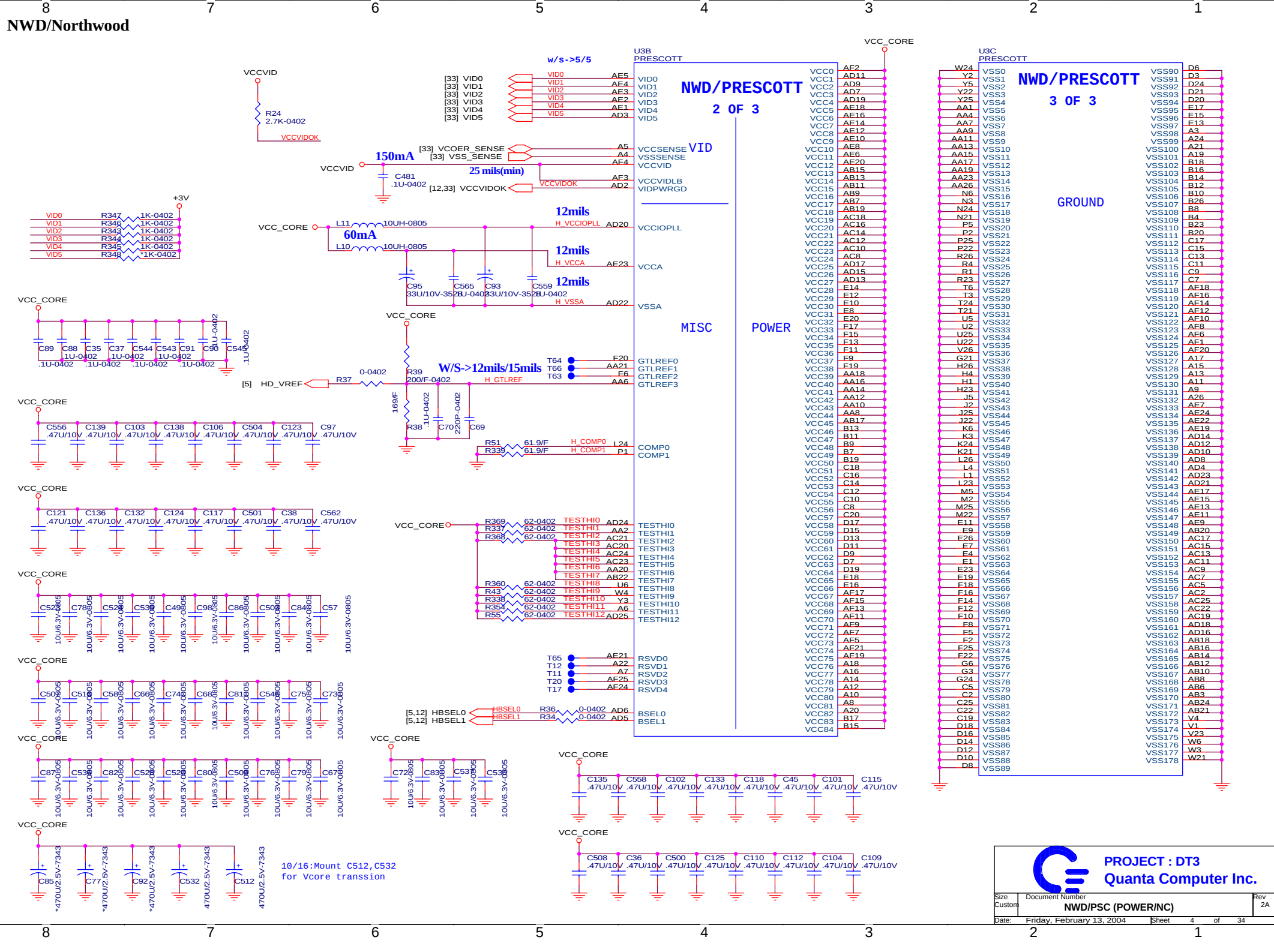
QUANTA COMPUTER INC.

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3	NWD/PSC (HOST BUS)	33	POWER CORE
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DT3







MCH (Host bus)

USA SPRINGDALE PSB

1 OF 6

SPRINGDALE

PSB

HD_SWING VOLTAGE "10MIL TRACE, 7MIL SPACE" HD_SWING S/B 1/4*VTT_NB +/- 2%

HD_VREF

HD_VREF [4]

CAP FOR GTLREF INPUTS @GMCH USE 10MIL TRACE, ISOLATE W/ 7MIL SPACE CAP SHOULD BE PLACED NEAR GMCH PIN

POWER:

1.5V	: 4.71A
1.225V/1.45V	: 1.6A
2.55V	: 4.9A
3.3V	: 0.2A

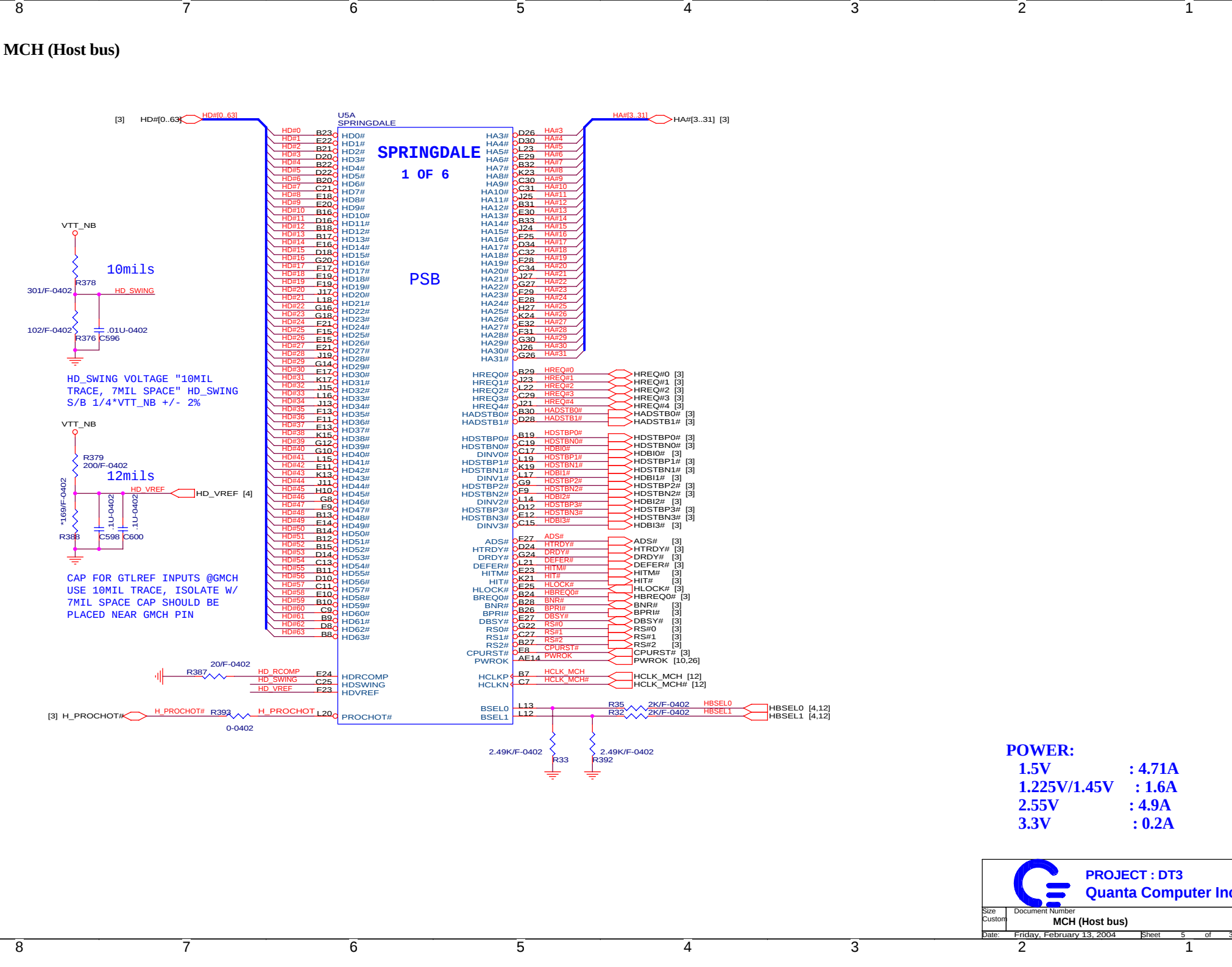
PROJECT : DT3

Quanta Computer Inc

MCH (Host bus)

Date: Friday, February 13, 2004

Sheet 5 of 3



MCH (Host bus)

USA SPRINGDALE PSB

1 OF 6

SPRINGDALE

PSB

HD_SWING VOLTAGE "10MIL TRACE, 7MIL SPACE" HD_SWING S/B 1/4*VTT_NB +/- 2%

HD_VREF

CAP FOR GTLREF INPUTS @GMCH USE 10MIL TRACE, ISOLATE W/ 7MIL SPACE CAP SHOULD BE PLACED NEAR GMCH PIN

POWER:

1.5V	: 4.71A
1.225V/1.45V	: 1.6A
2.55V	: 4.9A
3.3V	: 0.2A

PROJECT : DT3

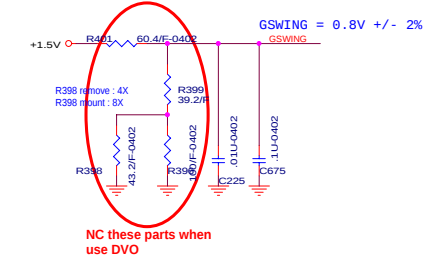
Quanta Computer Inc

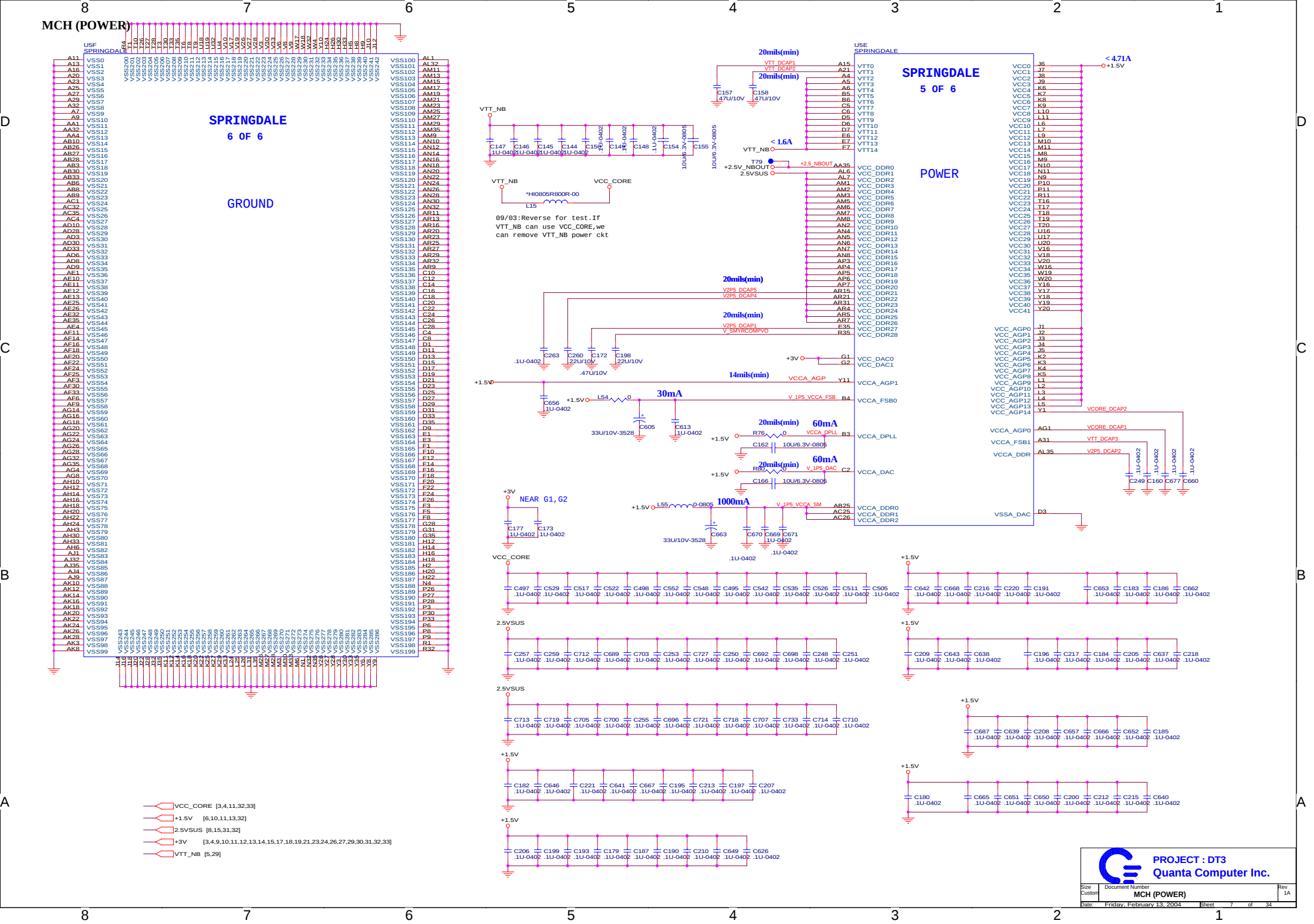
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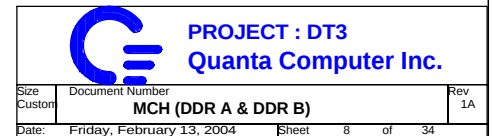
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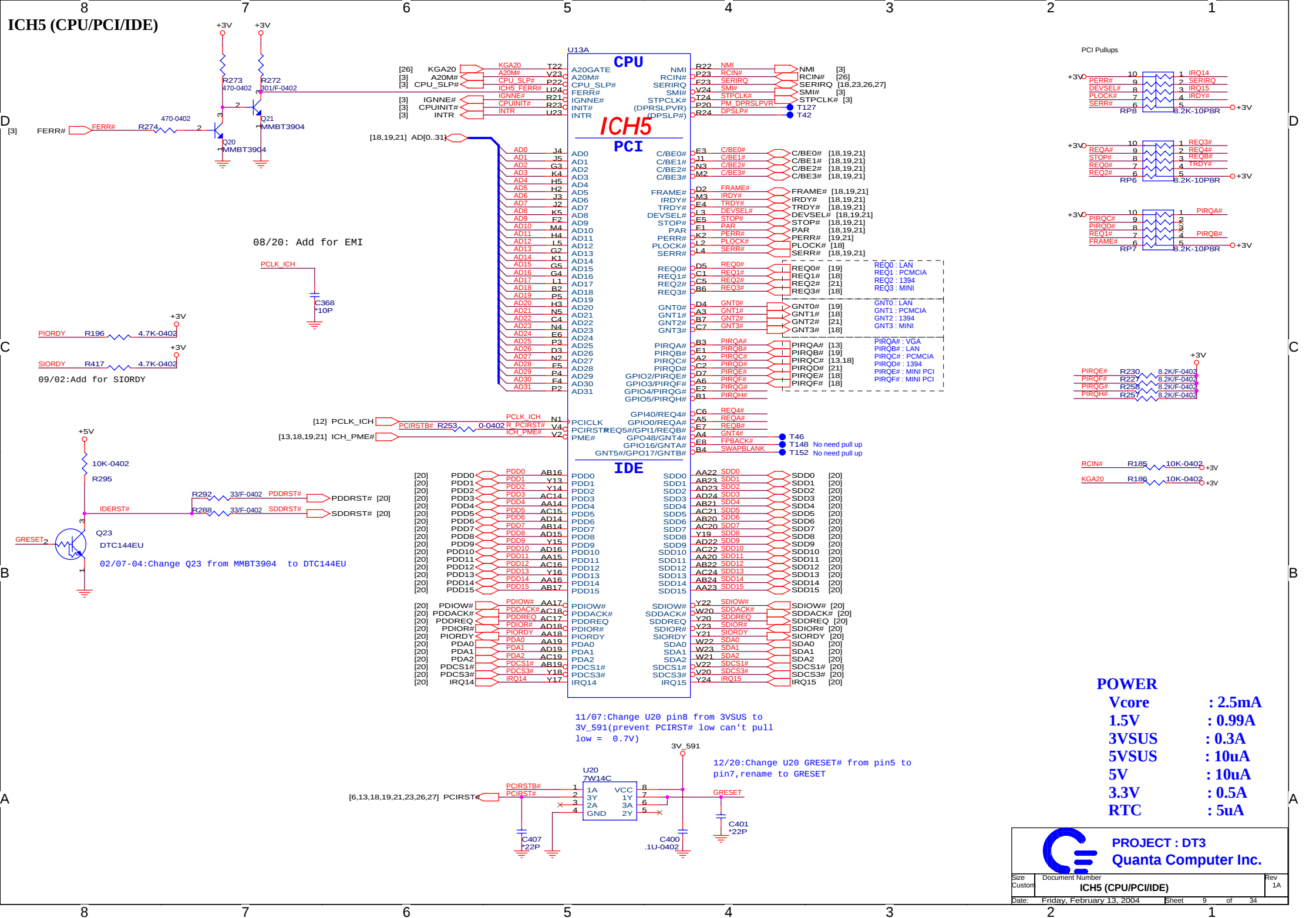
Sheet 5 of 3

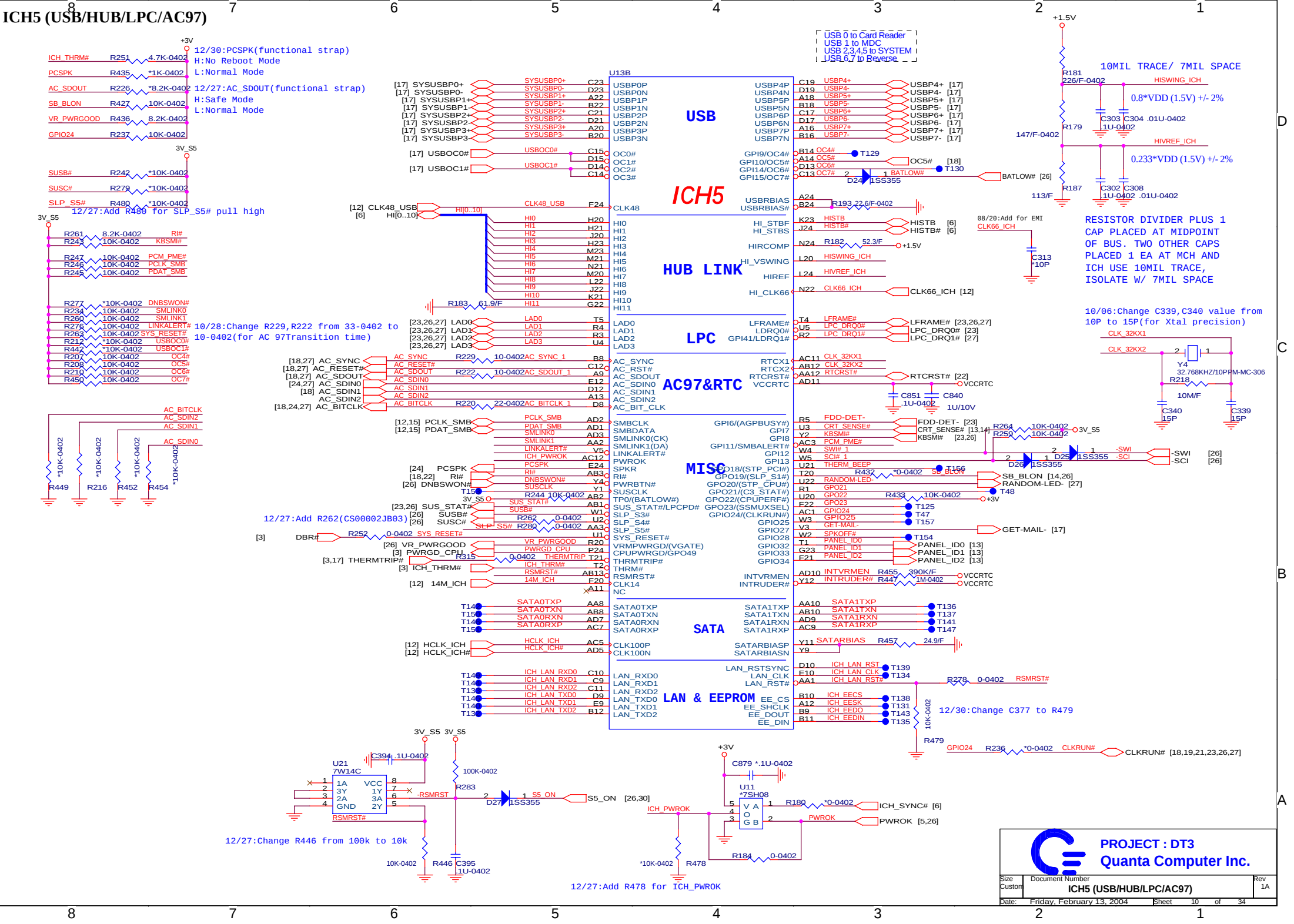
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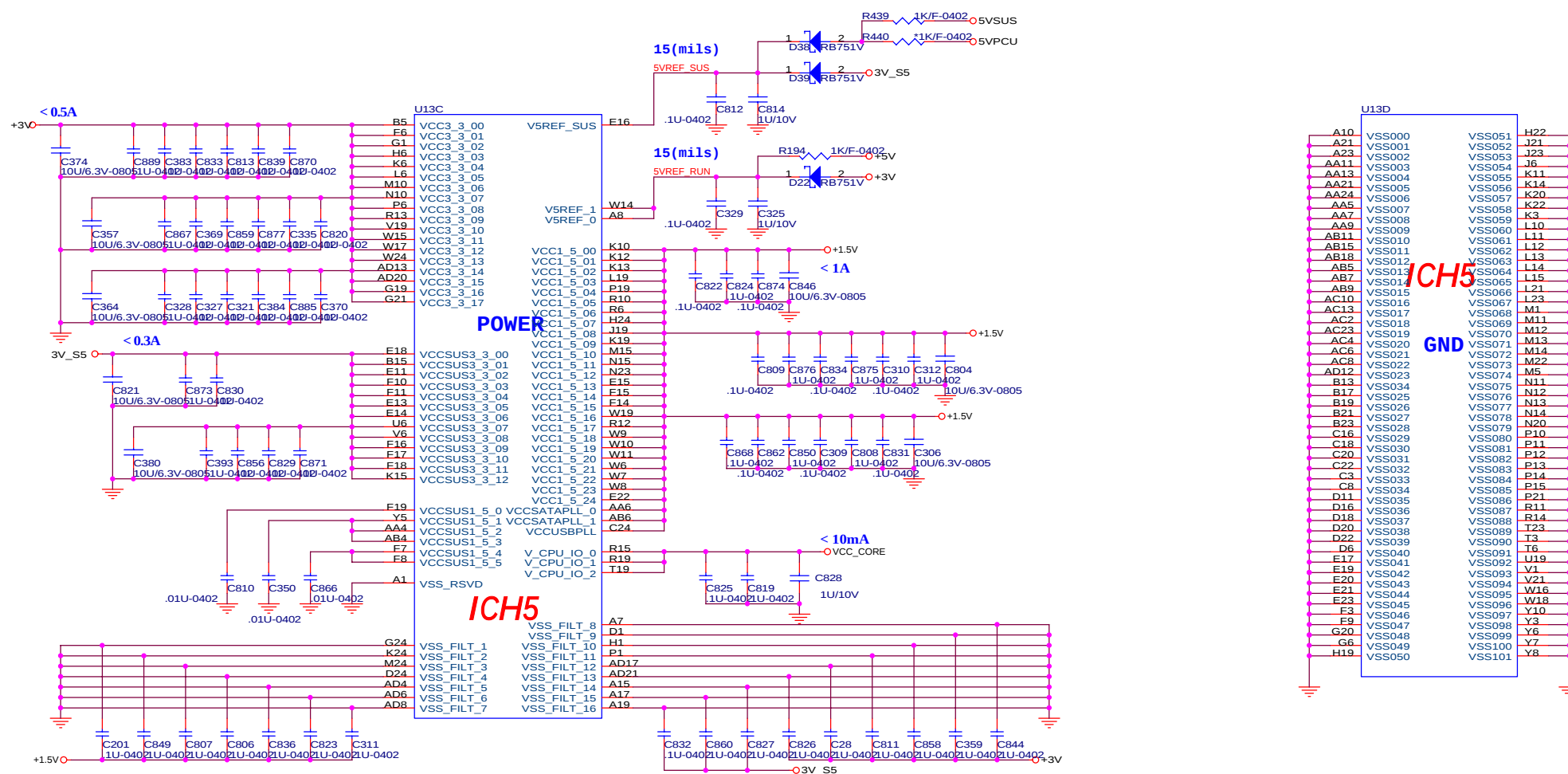






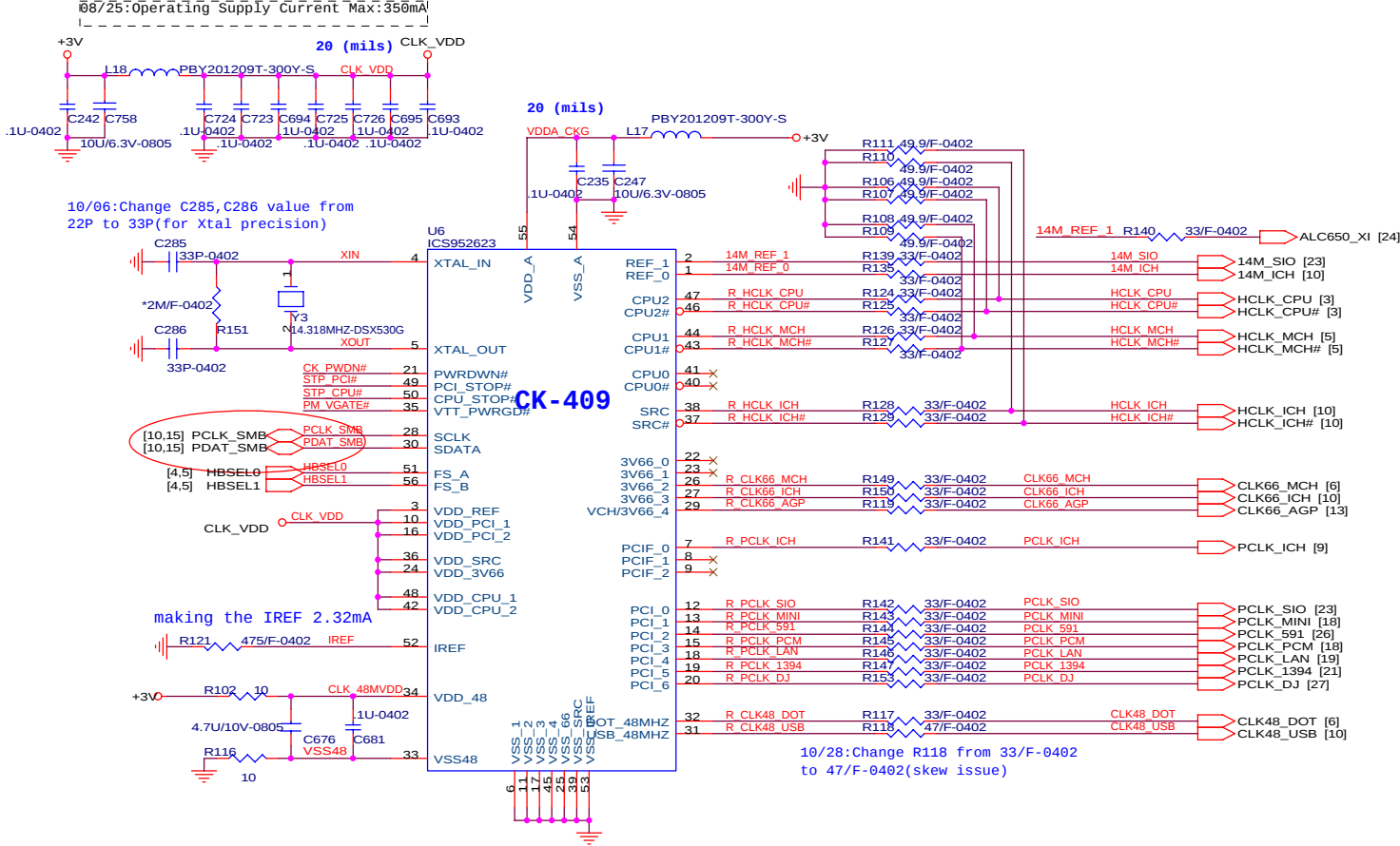
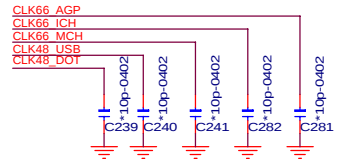
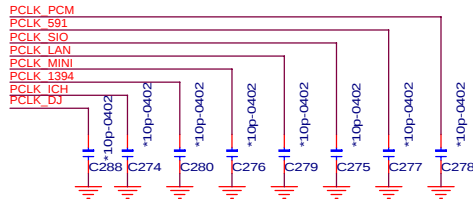
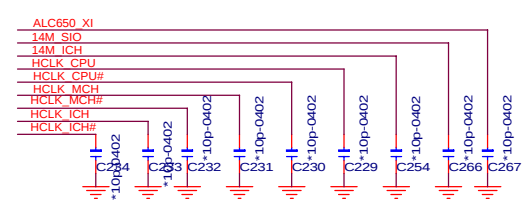
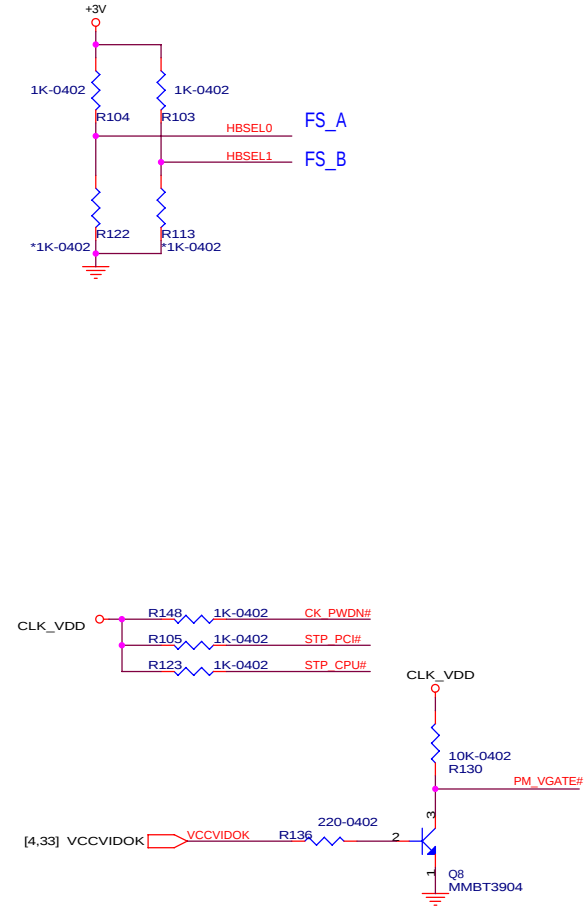


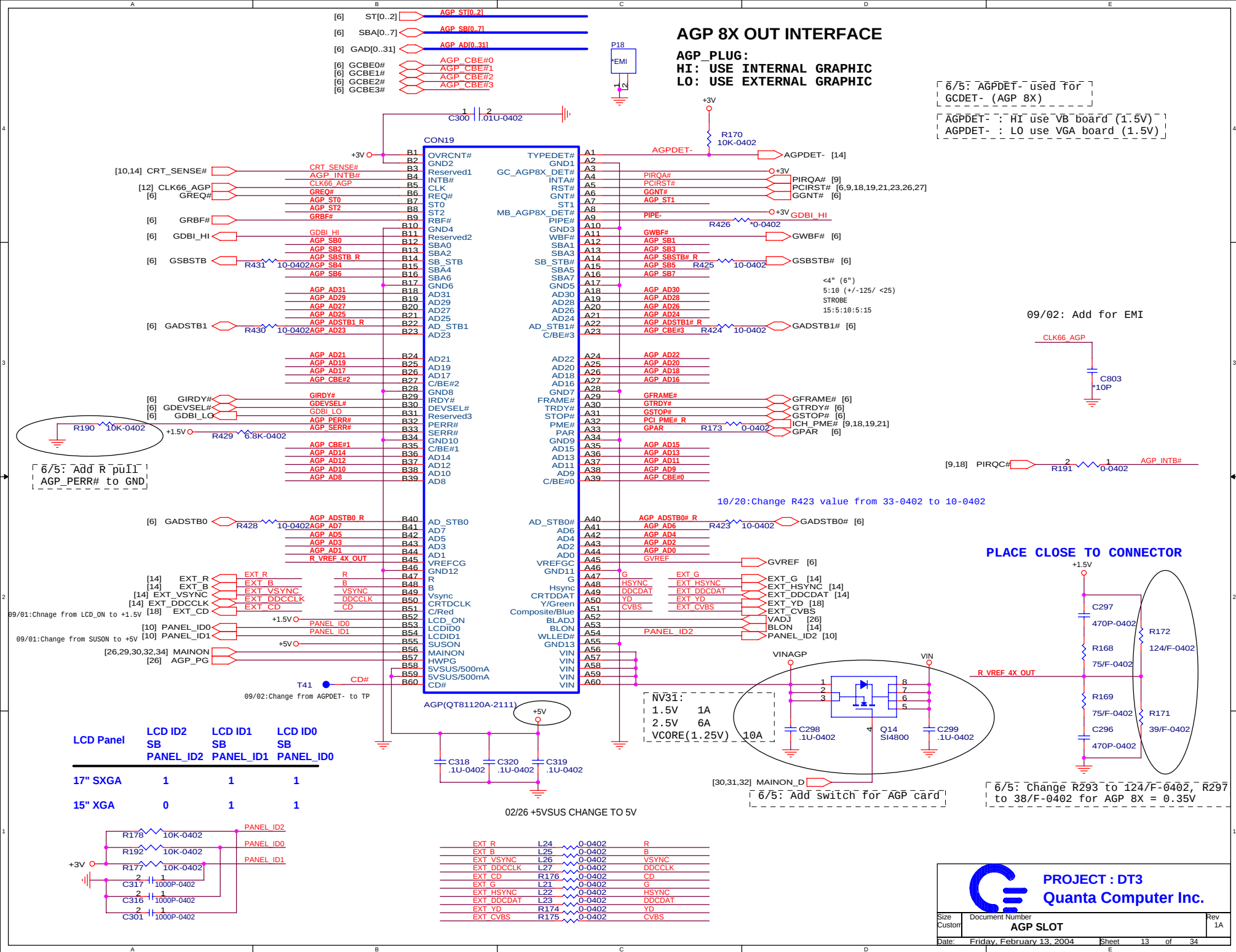
ICH5 POWER



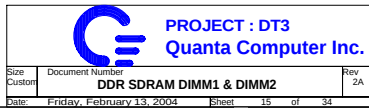
Clock Generator

FSA	FSB	CPU
0	0	100MHZ HOST CLOCK
1	0	133MHZ HOST CLOCK
1	1	166MHZ HOST CLOCK
0	1	200MHZ HOST CLOCK

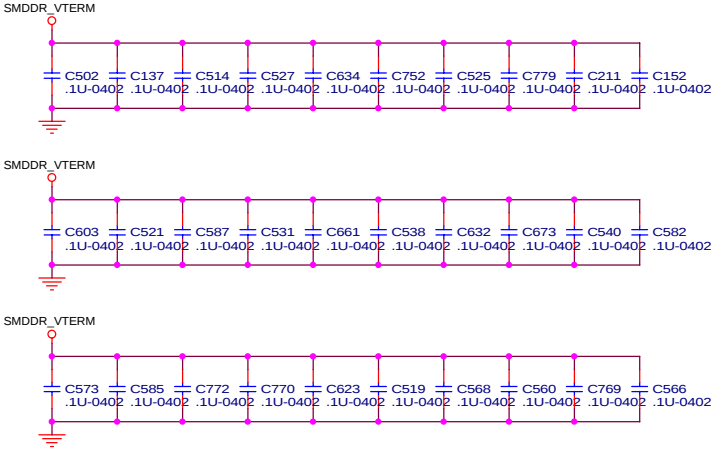
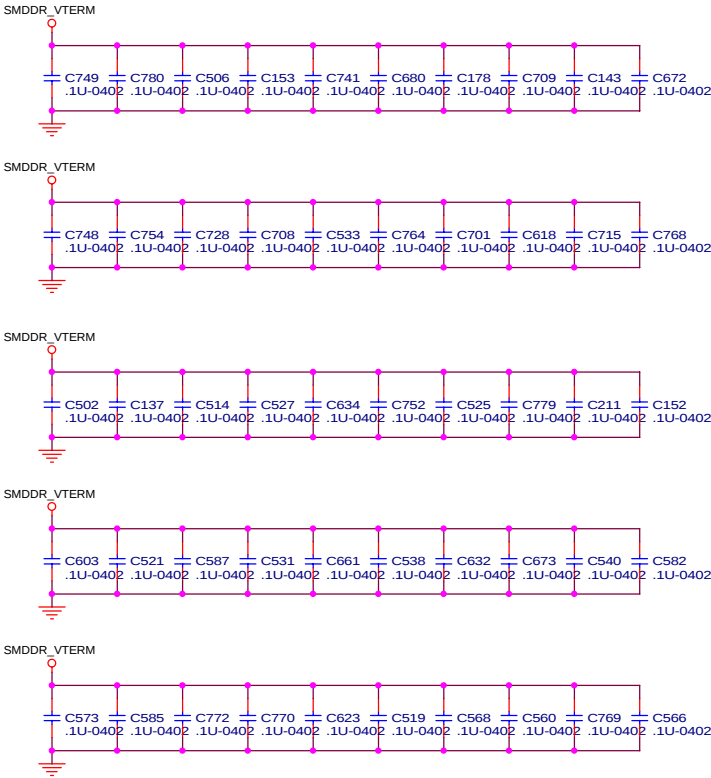
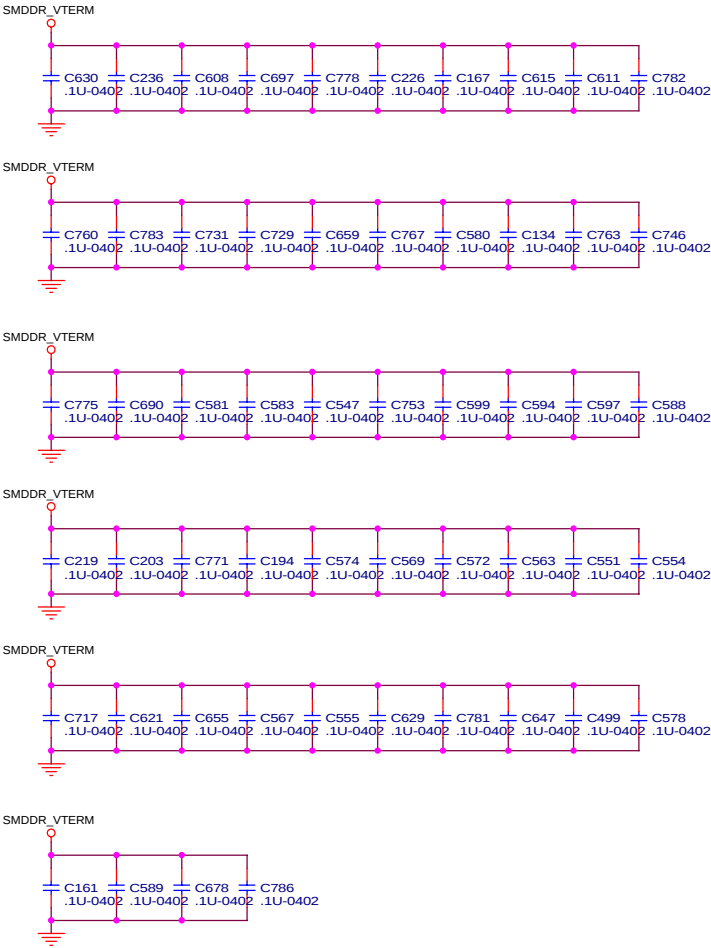




Size Custom	Document Number	Rev 1A
CRT,LCD LID,BLON & FAN CONTROL		
Date:	Friday, February 13, 2004	Sheet 14 of 34



DDR CHANNEL A&B RESISTOR TERMINATION

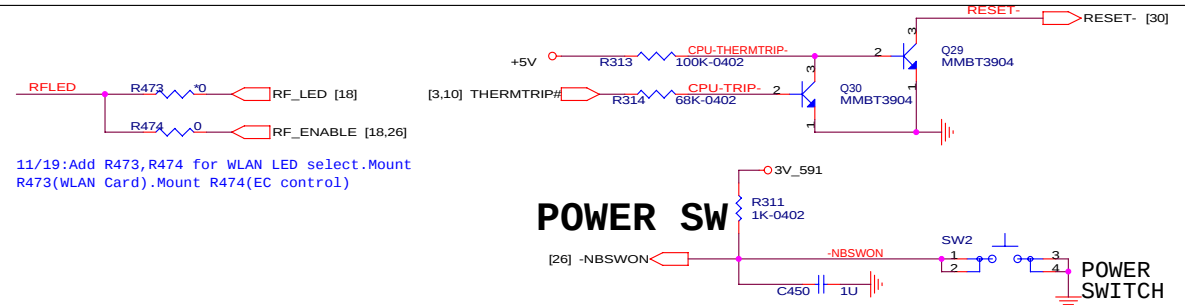


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6/5: Remove common mode
choke (CML6, CML8) to short
```

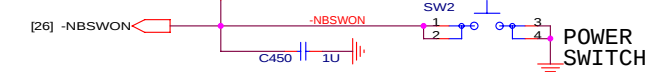


09/22:change footprint from 6701-04-4p-R

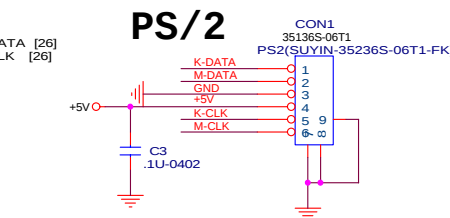
TP/B CONNECTOR



POWER SW



RESET SW



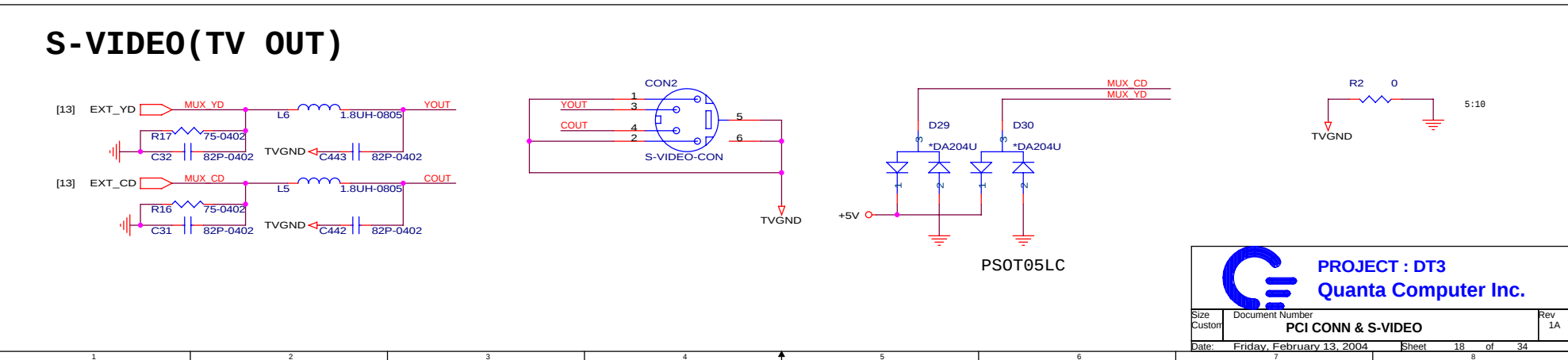
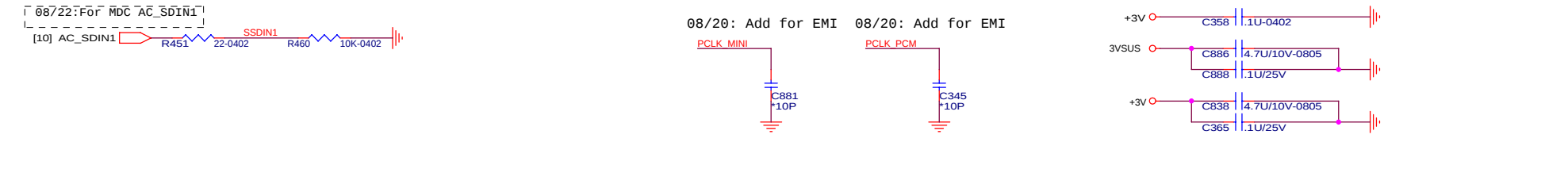
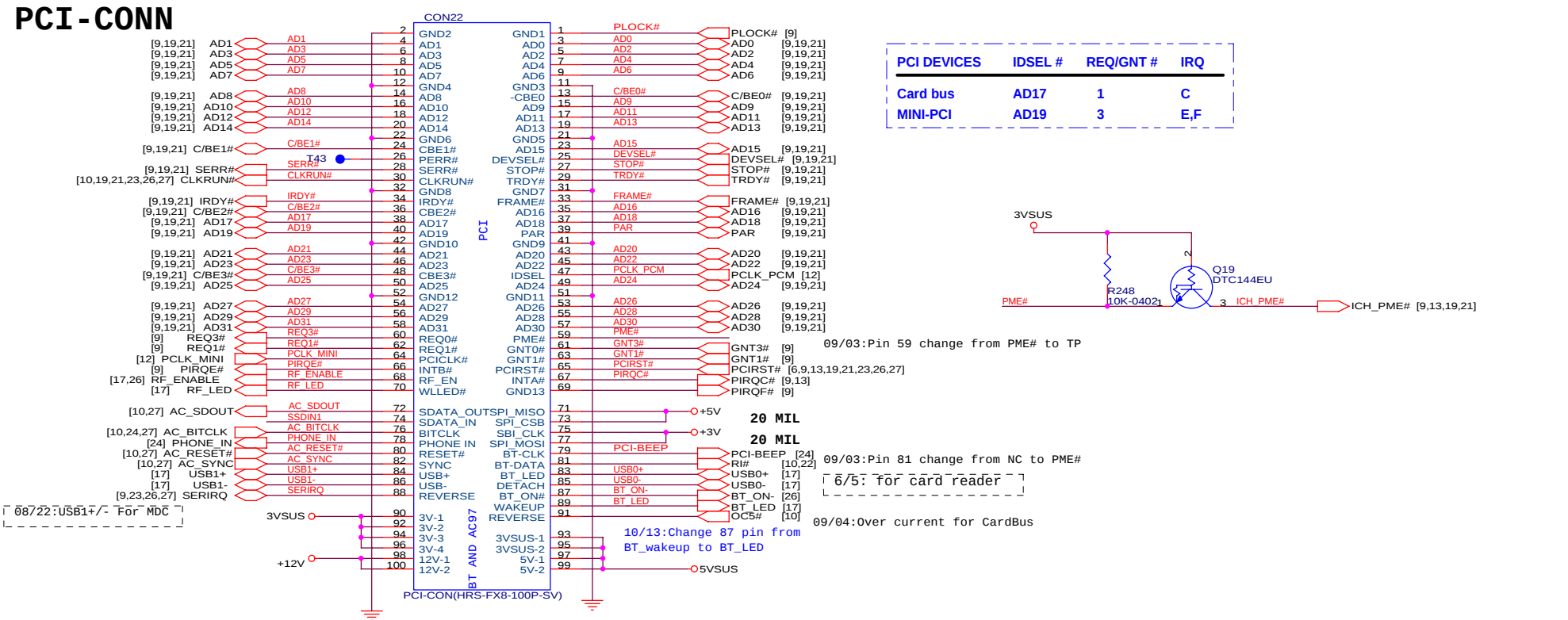
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H00LED	C732	220P-0402		C706	1U-0402		SVS5
CAPSLED#		C757	220P-0402		C716	* 1U-0402	BATLED
SCROLED#	C759	220P-0402		C679	* 1U-0402		BATLED1
QSW_P1		C761	220P-0402		C683	* 1U-0402	RFLED
QSW_MAIL	C766	220P-0402		C686	220P-0402		GET-MAIL-LID#
QSW_IE		C747	220P-0402		C699	220P-0402	
QSW_P1	C742	220P-0402		C702	220P-0402		SUS_LED
QSW_P2		C739	220P-0402		C711	220P-0402	8T_LED
	C738	220P-0402		C893	220P-0402		



PROJECT : DT3
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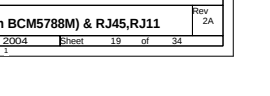
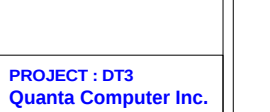
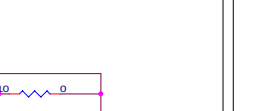
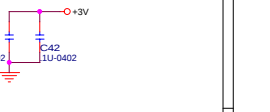
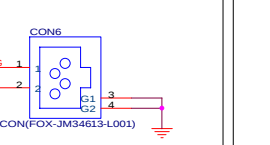
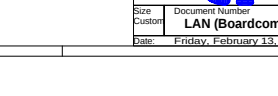
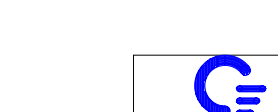
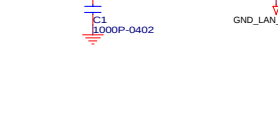
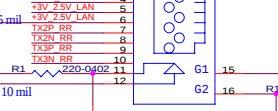
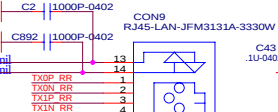
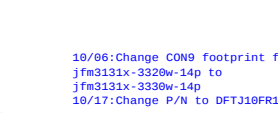
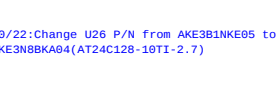
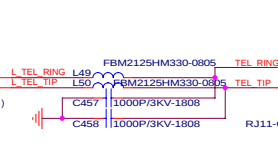
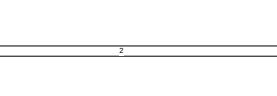
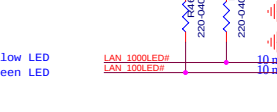
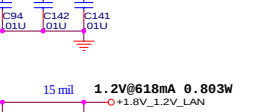
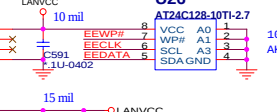
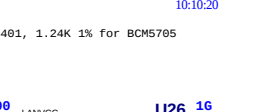
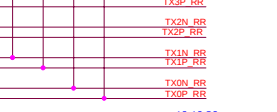
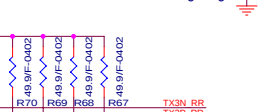
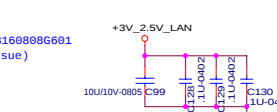
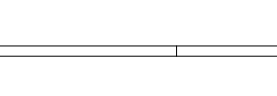
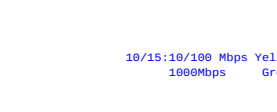
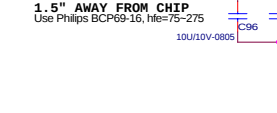
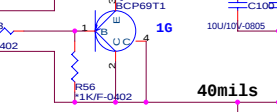
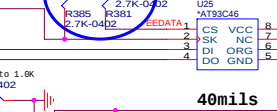
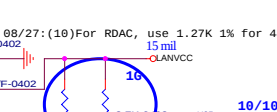
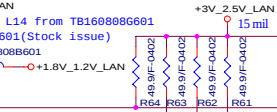
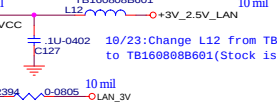
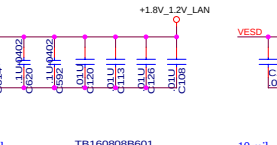
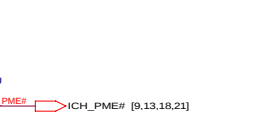
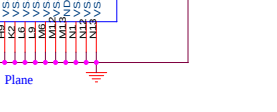
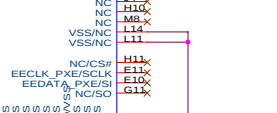
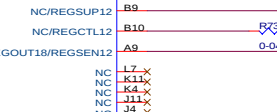
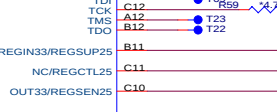
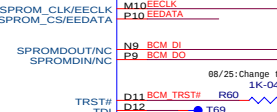
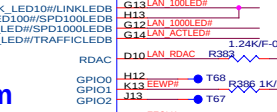
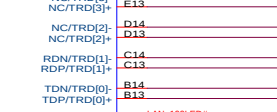
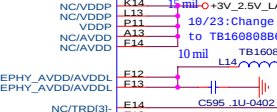
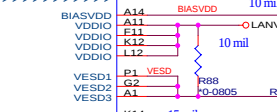
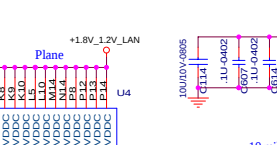
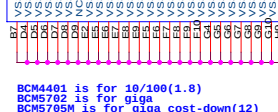
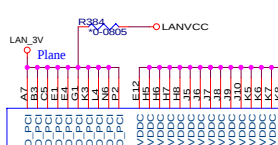
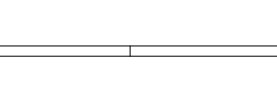
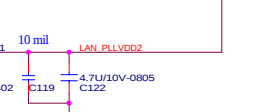
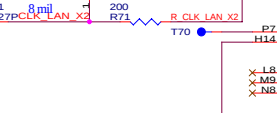
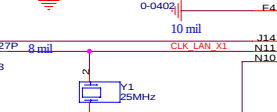
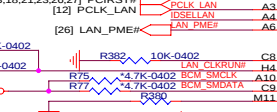
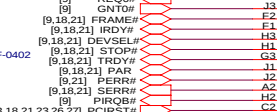
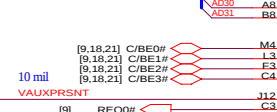
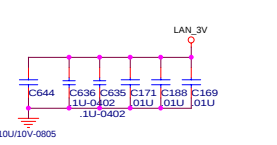
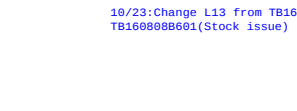
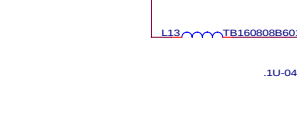
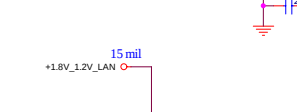
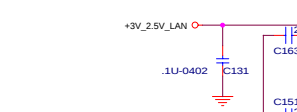
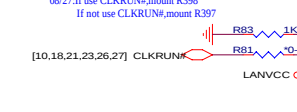
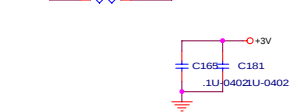
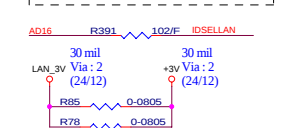
Size Custom	Document Number USB,LED/B,PS2,PW & RST CONN	Rev 2A
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PCI-CONN

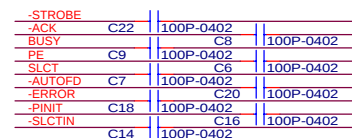
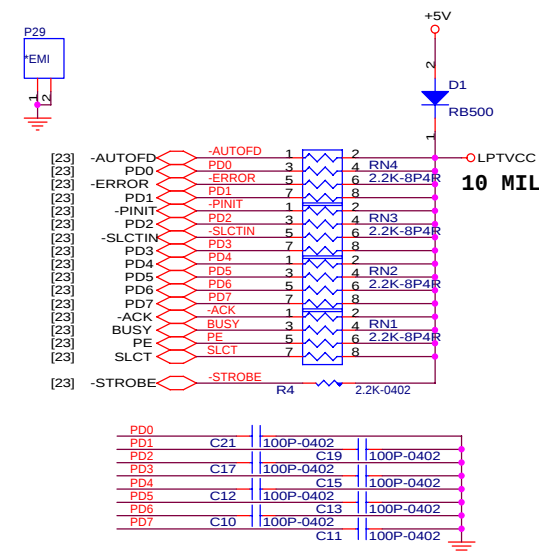
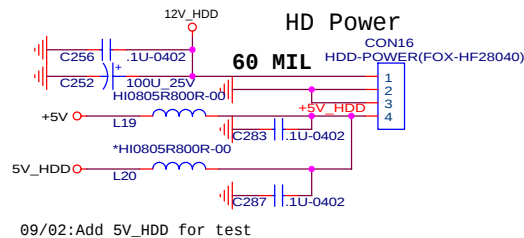


LAN (Boardcom BCM5788M)

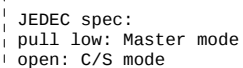
ID Select : AD16
Interrupt Pin : PIRQB#
Request indicates : REQ0#
Grant indicates : GNT0#

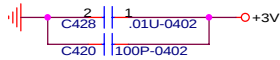


HDD POWER CONNECTOR



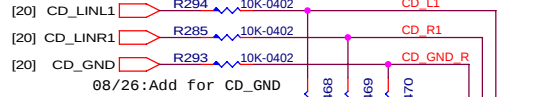
Secondary



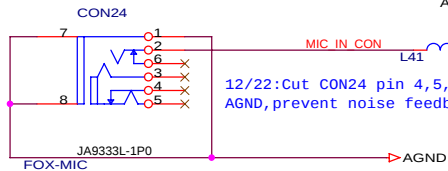


10/24:From CDROM

CD_IN(1)

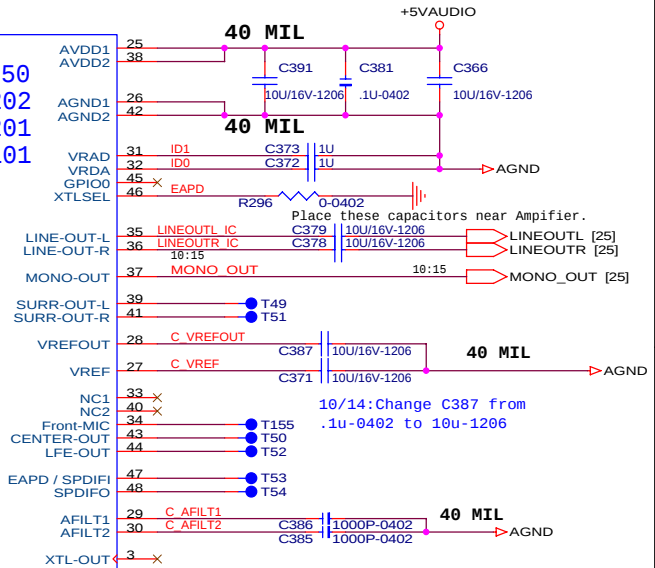
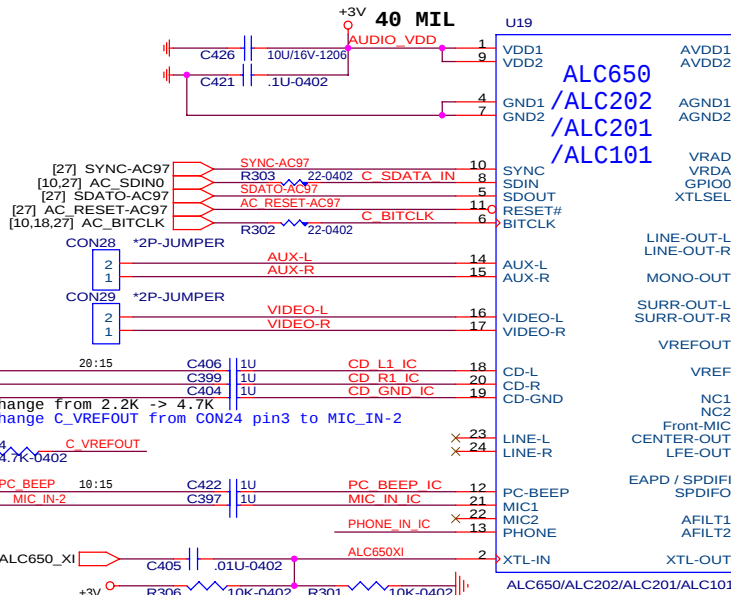
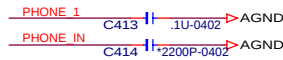


MIC_IN (PINK)

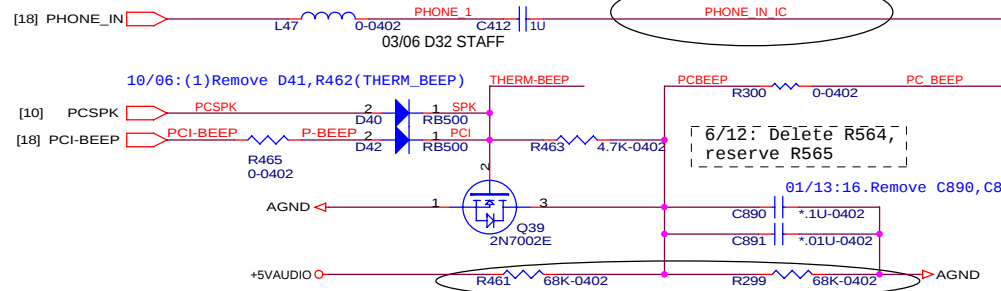


11/24:Add C894,C895,C896 for EMI solution,place near CON24

10/29:Change C413 to .1u-0402 for PHONE_IN noise(C414 no mount)



6/12: Delete C701, C818, R516 for phone in design

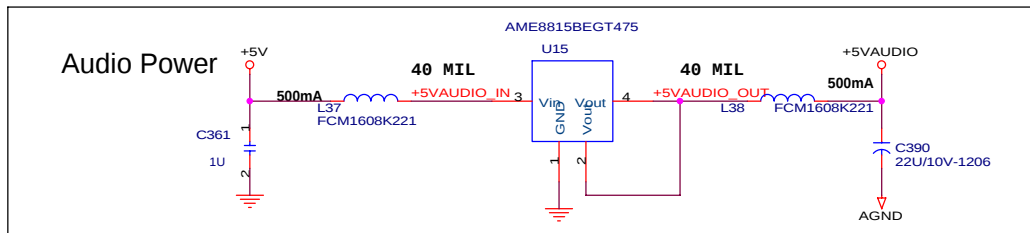
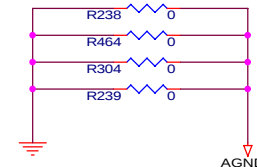


6/12: Delete R564, reserve R565

01/13:16.Remove C890,C891 for PC-BEEP issue

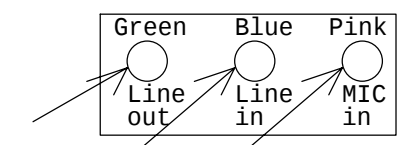
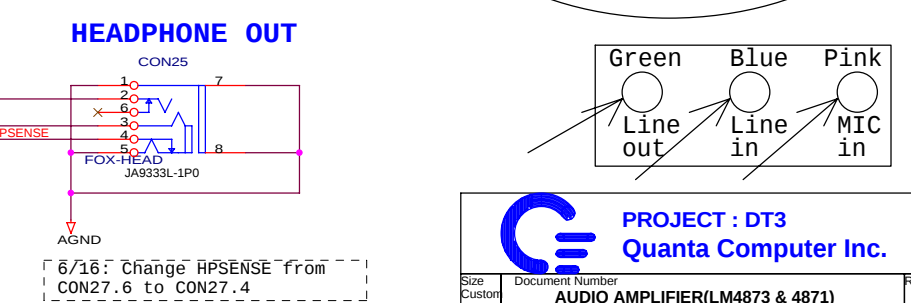
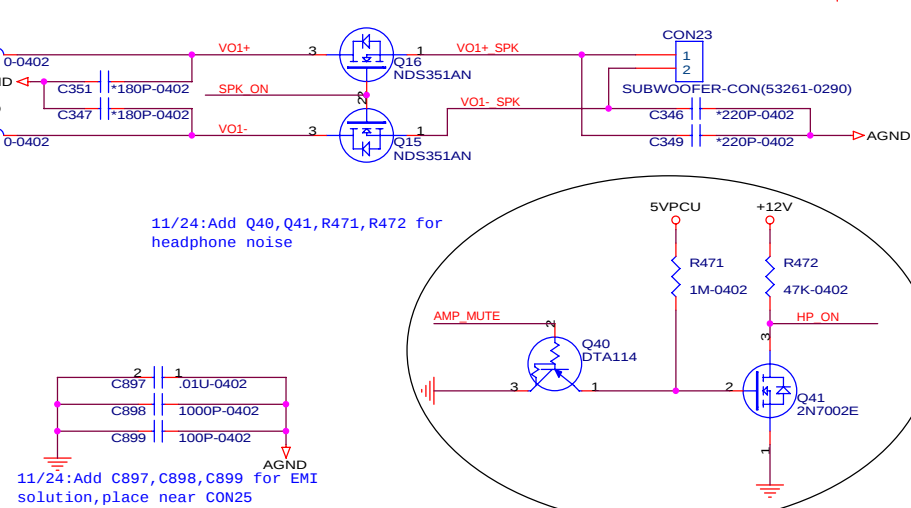
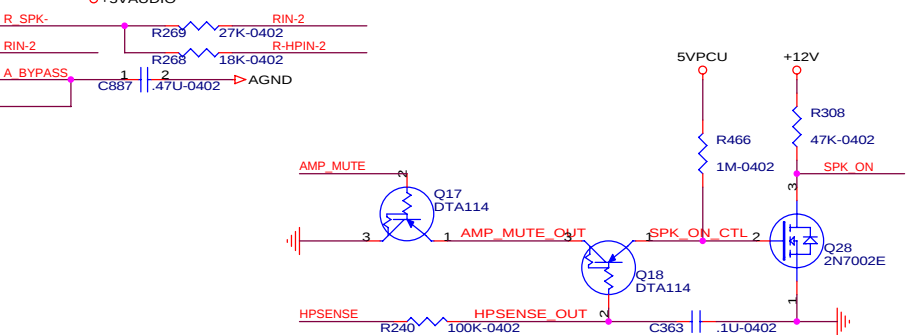
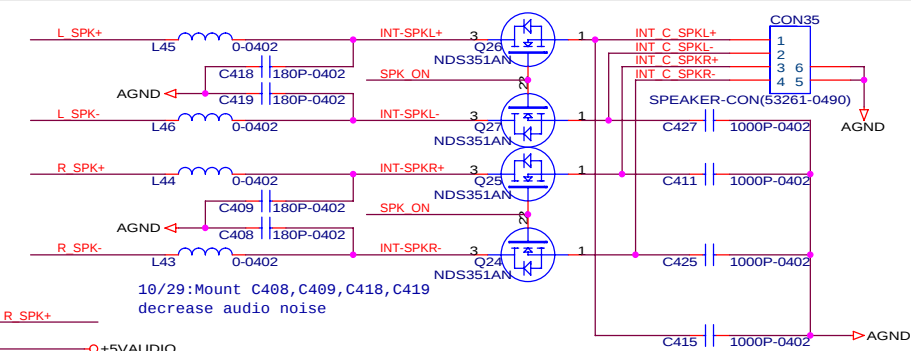
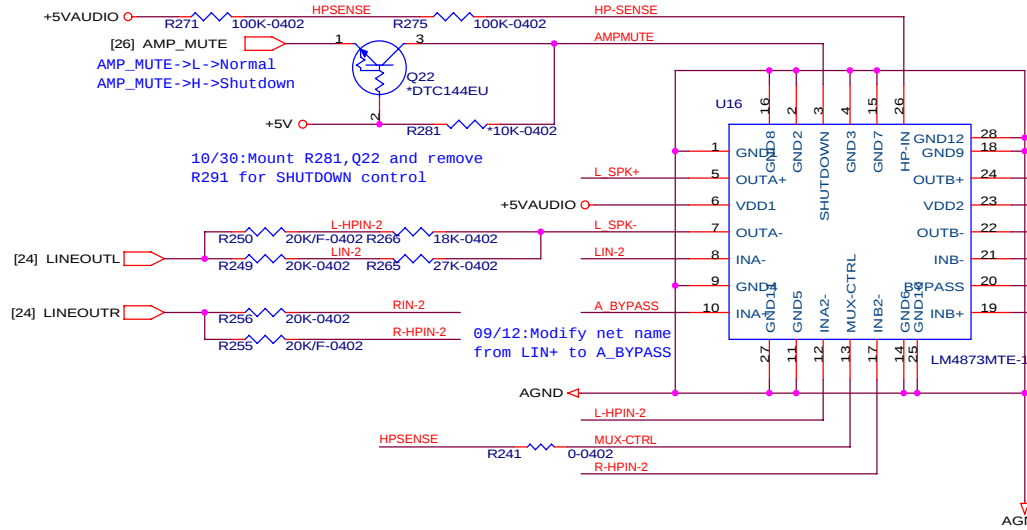
6/12: Change R354, R492 from 1K-0402 to 68K-0402

09/12:Must be place between AGND and GND

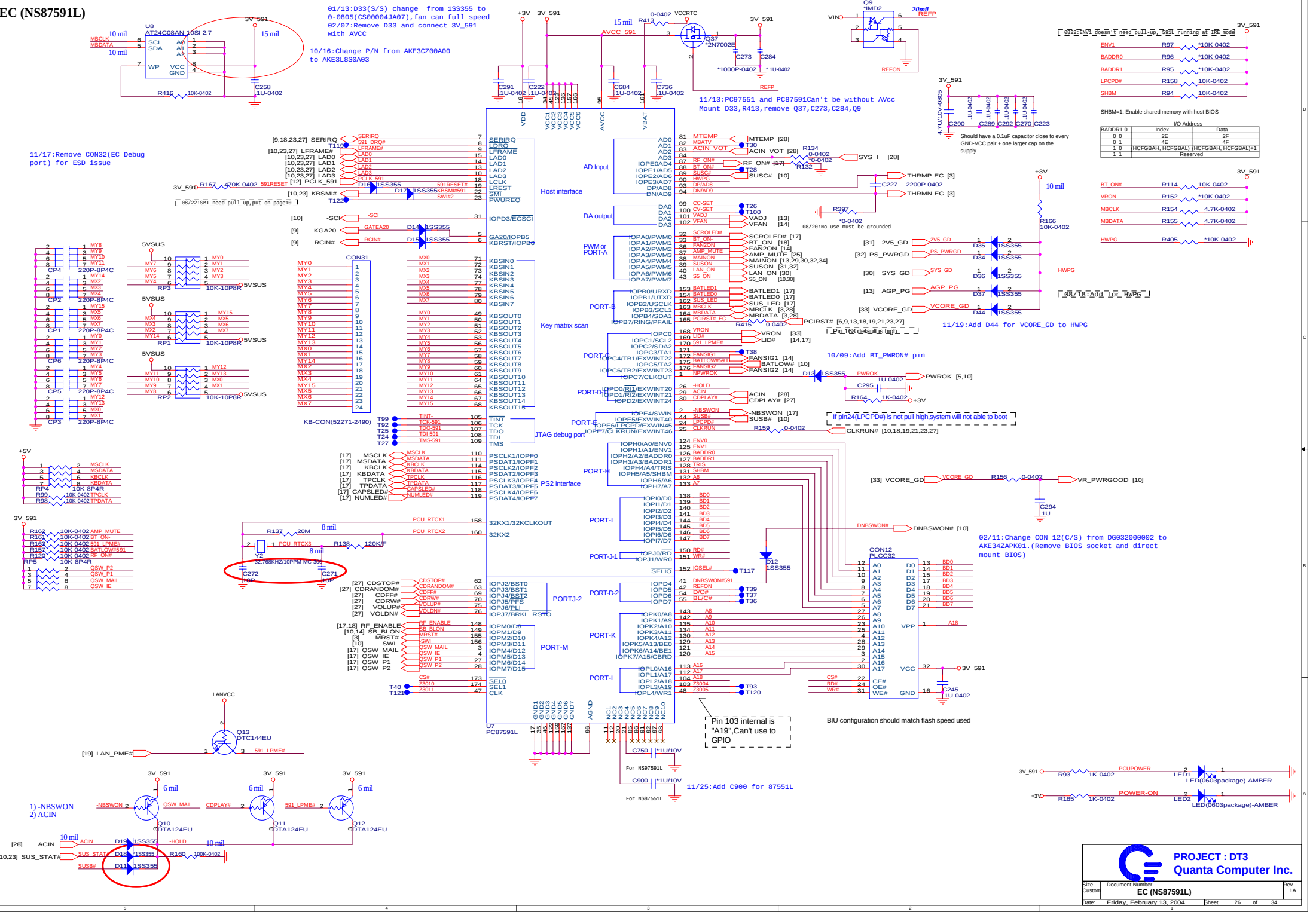


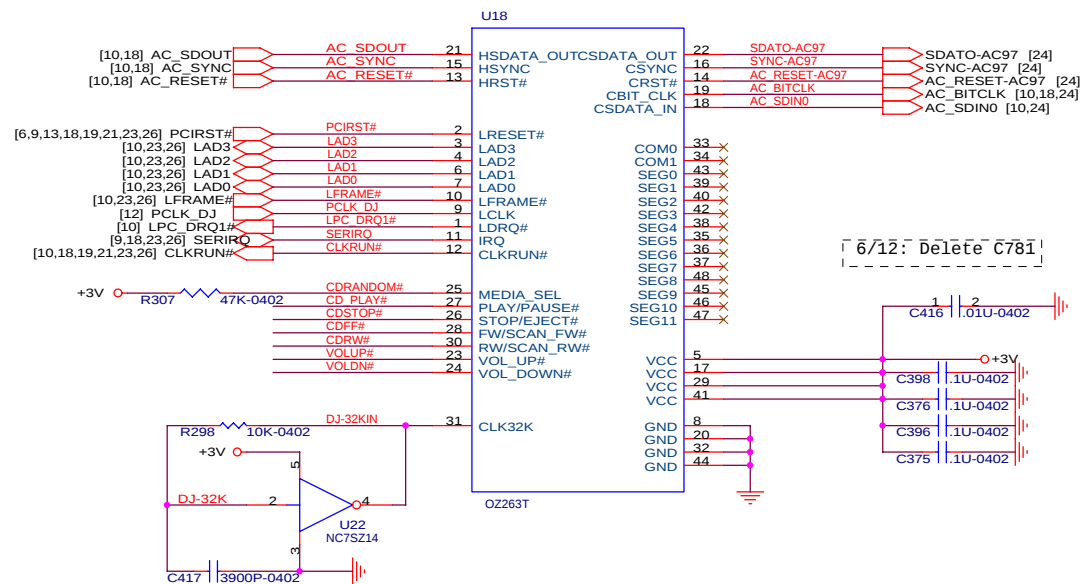
10/20:Change U15 pin 2 to Vout

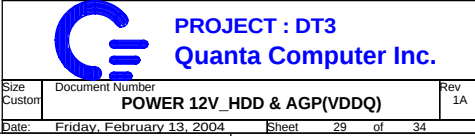
Audio amplifier

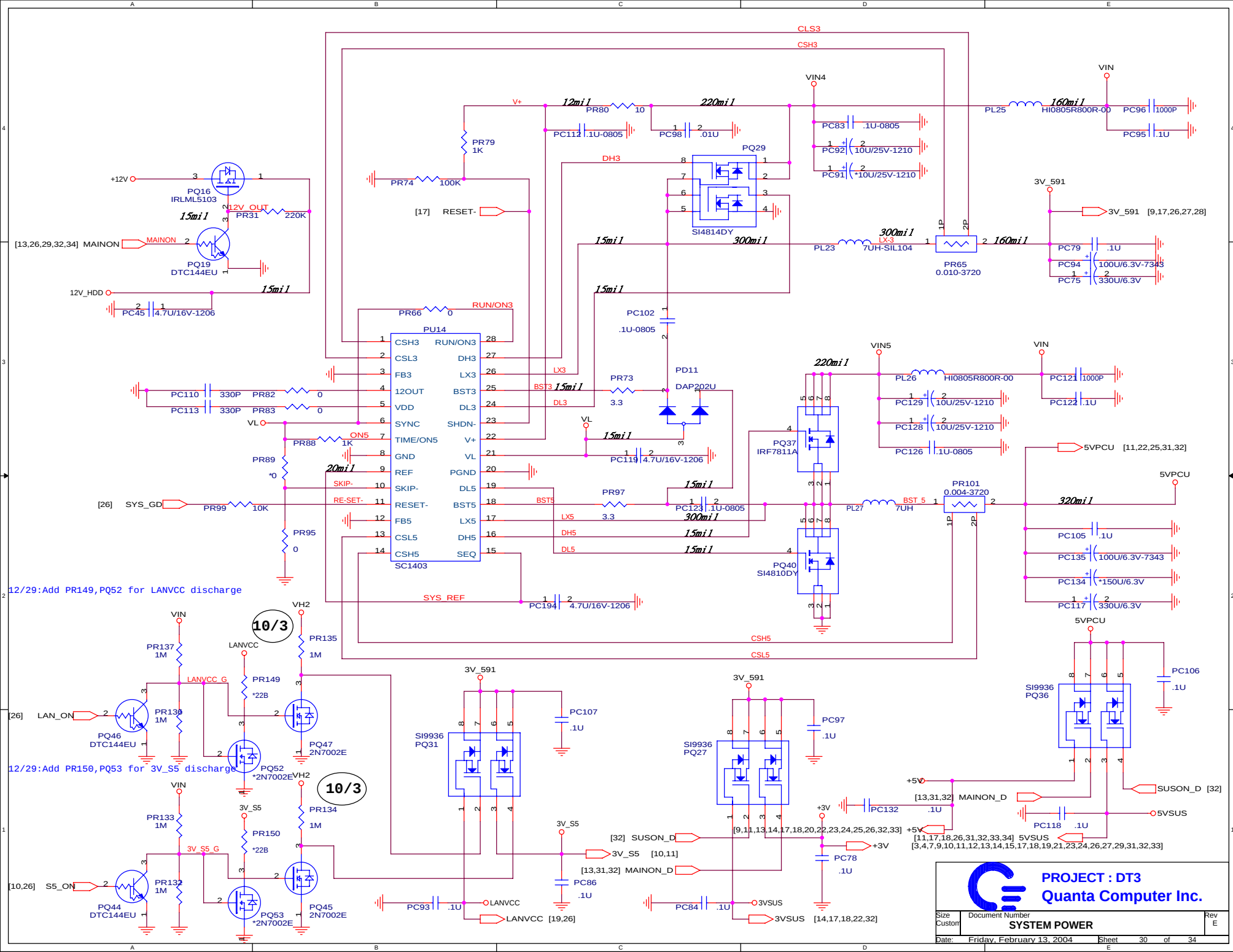


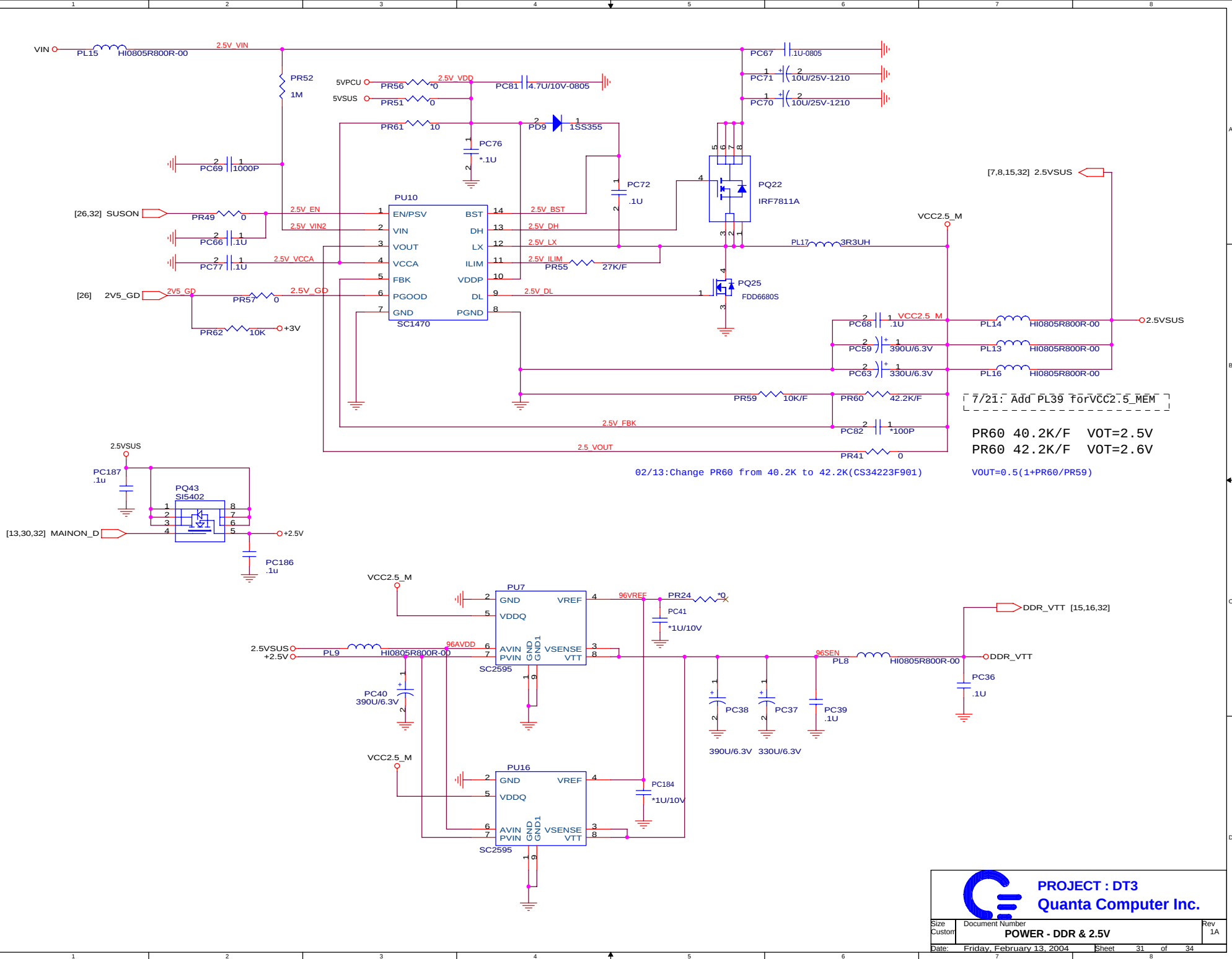
EC (NS87591L)



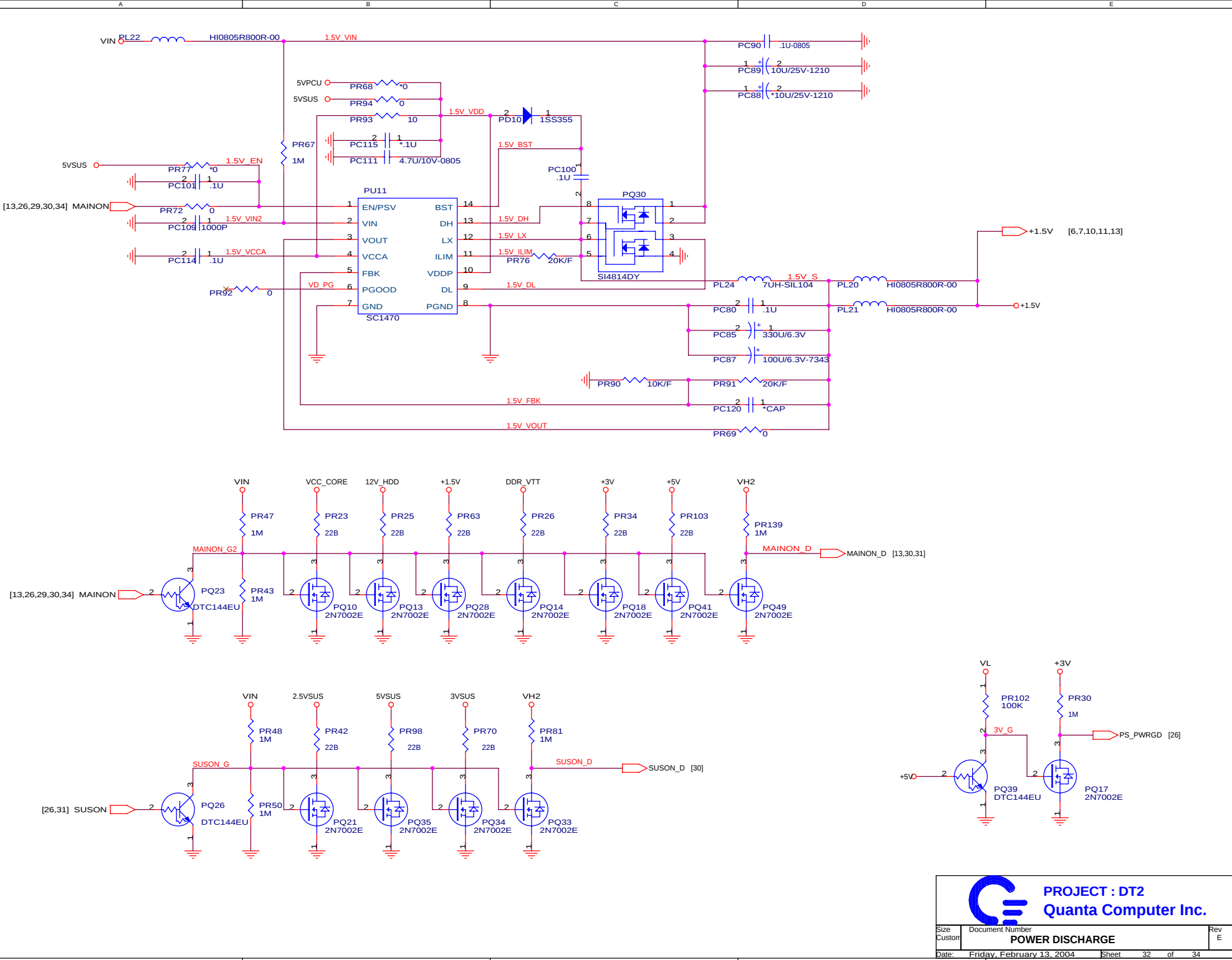




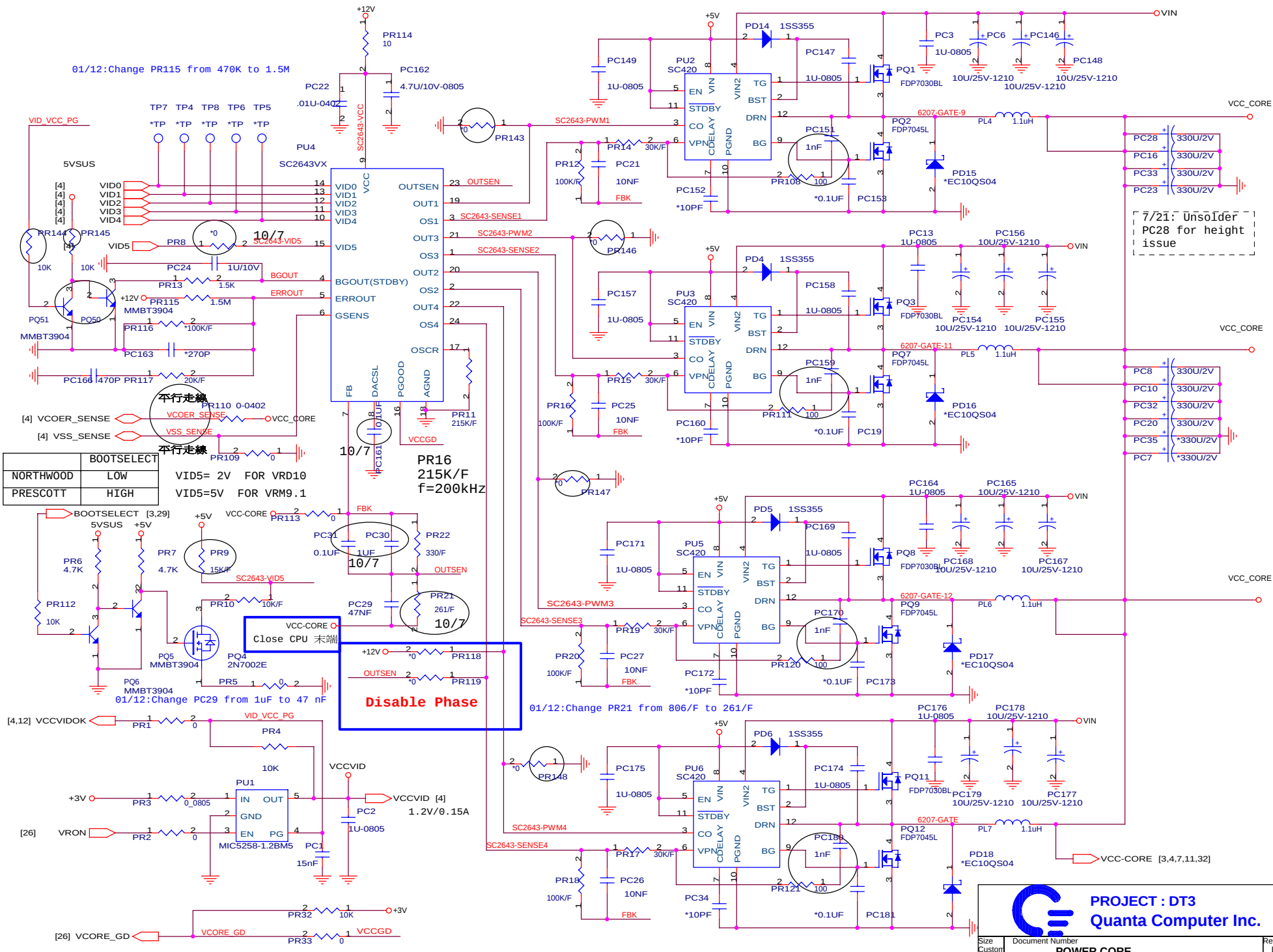




PR60 40.2K/F VOT=2.5V
PR60 42.2K/F VOT=2.6V
VOUT=0.5(1+PR60/PR59)



01/12:Change PR115 from 470K to 1.5M



7/21: Unsolder PC28 for height issue

BOOTSELECT	
NORTHWOOD	LOW
PRESCOTT	HIGH

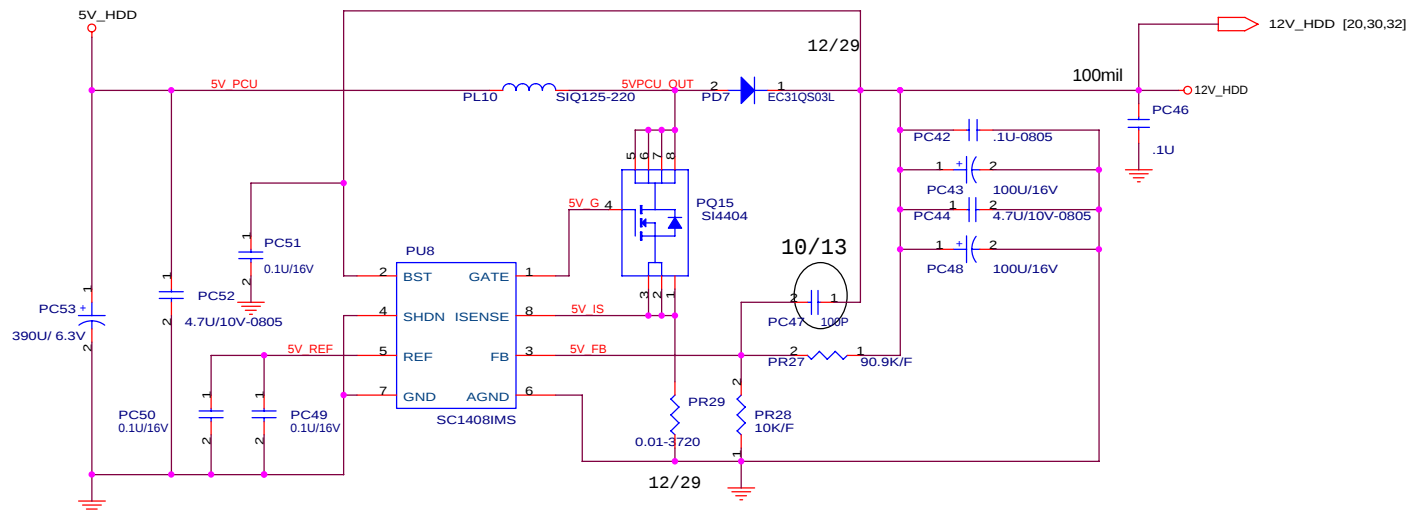
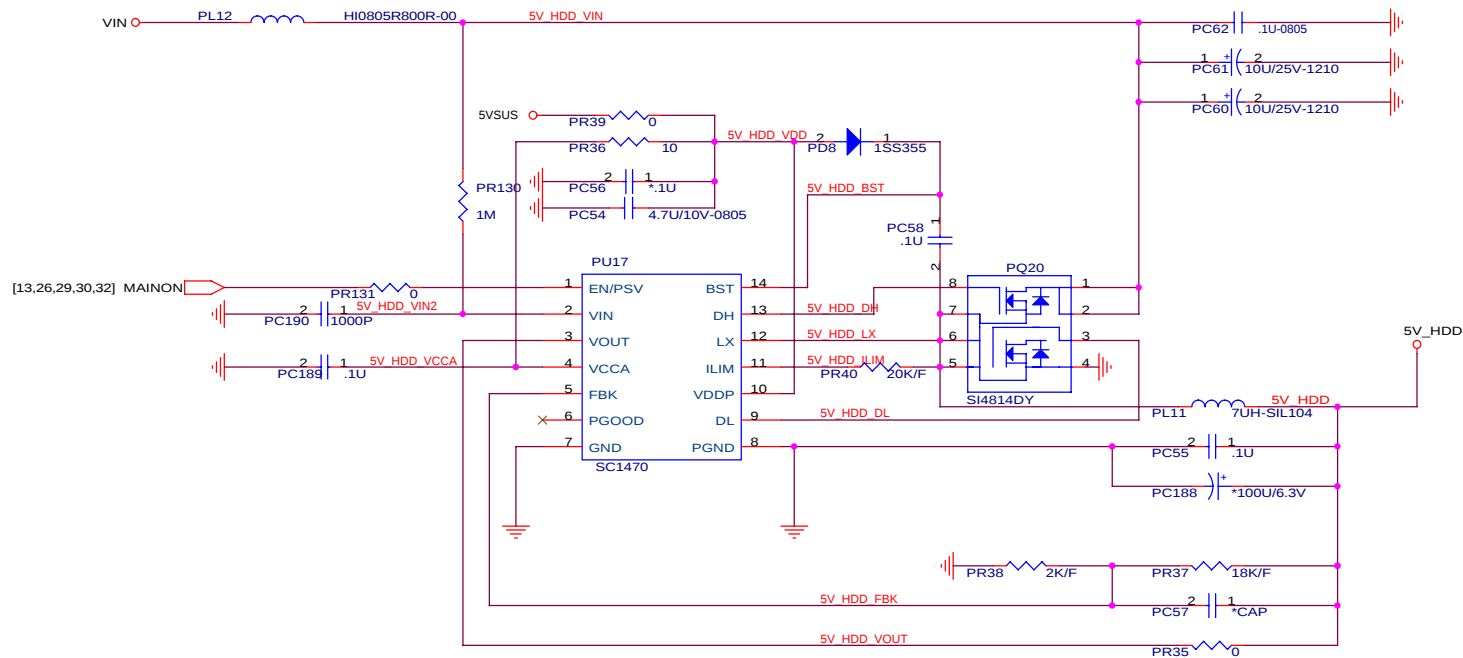
VID5= 2V FOR VRD10
VID5=5V FOR VRM9.1

PR16
215K/F
f=200kHz

01/12:Change PR21 from 806/F to 261/F



PROJECT : DT3
Quanta Computer Inc.



08/27 PC58 CHANGE COMPONENT TO
390U/6.3V

08/27 PR60 CHANGE FROM 10mohm TO 10mohm

PROJECT : DT3
Quanta Computer Inc.

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