

5V / 3.3V / 12V
Page : 35

2.5V / 1.25V
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1.5V / 1.05V / 1.8V
Page : 37

CPU CORE
Page : 34

+1.2V
Page : 38

BATTERY CHARGER
Page : 39

BATTERY SELECT
Page : 40

- 5VPCU
- 3V_ALWAYS
- +12V
- +5V
- 3V_S5
- 3VSUS
- 5VSUS
- 2.5VSUS
- +2.5V
- +1.8V
- MVREF_DM
- SMDDR_VTERM
- 1.5V_S5
- +1.5V
- AGP_VCC (+1.5V)
- 1.2VCCT
- VTT
- VCC_CORE
- VGA_CORE
- 2.5V_VGA

CLOCK GEN ICS
ICS954201
Page : 2

Centrino
DORTHAN
CELEROM-M
INTEL Mobile 479 CPU Page : 3 , 4

CRANE (ZL2)

ED@ INT. VGA WITH DOCK
ID@ INT. VGA WITH DOCK
ND@ W/O DOCKING要打

BOM MARK
E@ EXT VGA 要打
I@ INTVGA 要打
SA@ SATA 要打
F@ FIXED ODD要打
SW@ SWAPPABLE ODD 要打
3@ 3in1 n打
N@ NEW CARD 要打
4@ 4401 n打
5@ 5705M 要打
D@ DOCKING 要打

DDR-SODIMM1
Page:9~10

DDR-SODIMM2
Page:9~10

ALVISO
1257 BGA
915GM/PM
Page : 5 ~ 8

ATI
M24P/M26P
64M / 128M
Page : 11 ~ 14

SWITCH
CIRCUIT

CRT
Page:17

LVDS
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TV-OUT
Page:16

DVI
CH7307
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DOCKING/DVI
Page: 33

SATA - HDD
Page:21

IDE - HDD
Page:21

IDE-ODD
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MEDIA BAY
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ICH6-M
609 BGA
Page : 18 ~ 20

NEW CARD
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TI
PCMCIA+1394
+3 IN 1
PCI7411
Page : 23

3 IN 1
Page : 24

PCMCIA
Page : 24

1394
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AUDIO CODEC
CONEXANT
20468-31
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AMP
MAX9750
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MODEM
CONEXANT
20493-21
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NS
KBC(97551)
Page : 29

NS
SIO (87383)
Page : 31

MINI-PCI
Wireless LAN
Modem/LAN
Page : 22

BROADCOM
10/100/1G LAN
4401 / 5705M
Page:25

BOTHHAND
TRANSFORMER
Page:26

RJ45
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MIC IN
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LINE IN
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SPEKER
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LINE OUT
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RJ11
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DOCKING
PS2
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Touchpad
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Keyboard
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IrDA
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DOCKING
Print Port
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DOCKING
COM Port
Page:33

SYSTEM 3
USB PORT
Page : 22

DOCKING 2
USB PORT
Page : 22

MINI-USB
Page: 22

PCI ROUTING TABLE IDSEL INTERRUPT DEVICE

REQ0# / GNT0# AD24 INTA#
REQ2# / GNT2# AD19 INTB# , INTD#
REQ1# / GNT1# AD17 INTC#, INTD#, INTA#
BROADCOM LAN
MINI-PCI
TI 7411

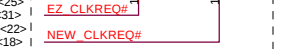
REV. E

PROJECT : ZL2
Quanta Computer Inc.

Size	Document Number	Rev
	BLOCK DIAGRAM	F
Date: Wednesday, December 22, 2004	Sheet 1 of 41	

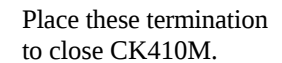
Place these termination
to close CK410M.

E: R200/202 CHANGE TO 12ohm for EA pass

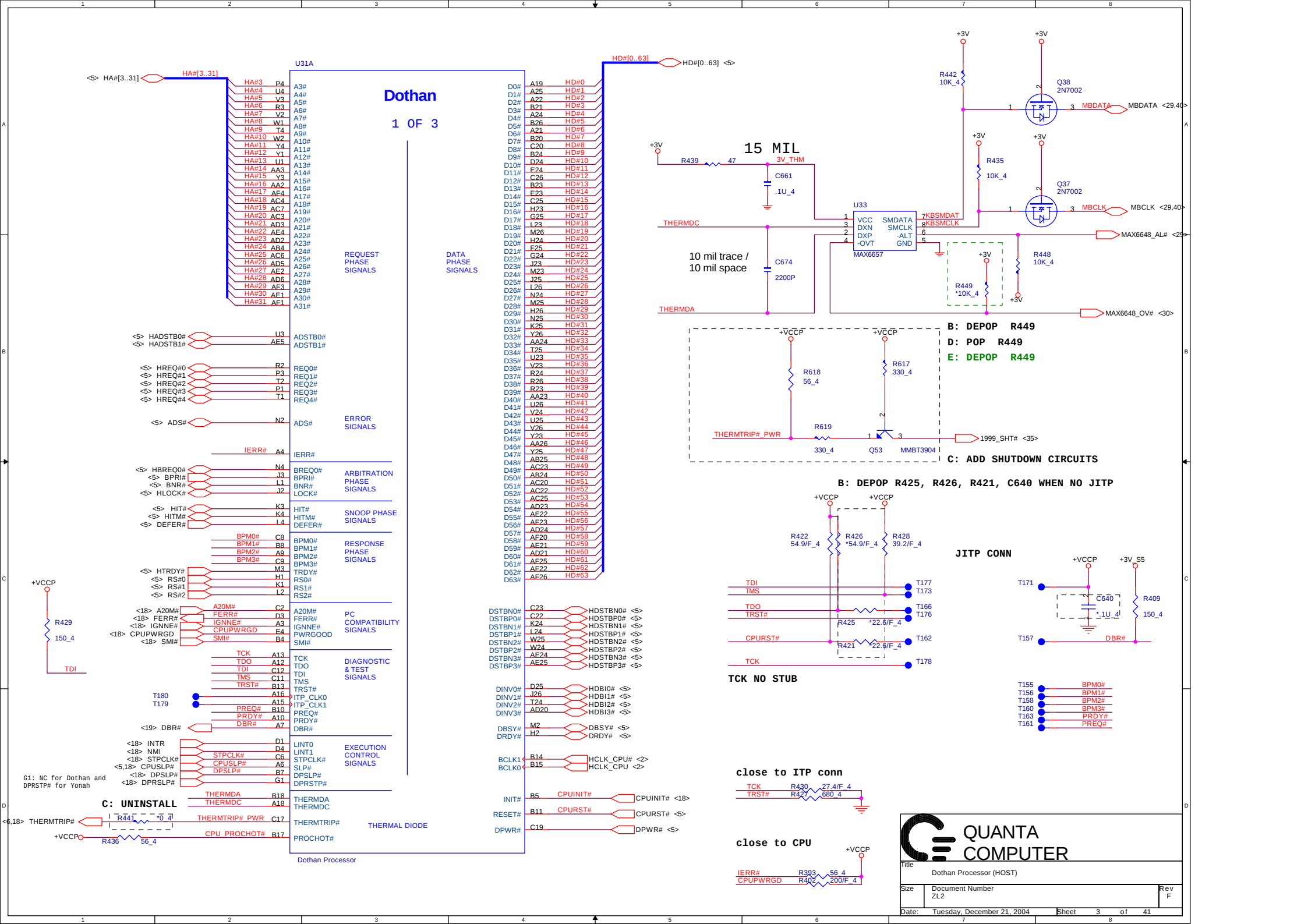


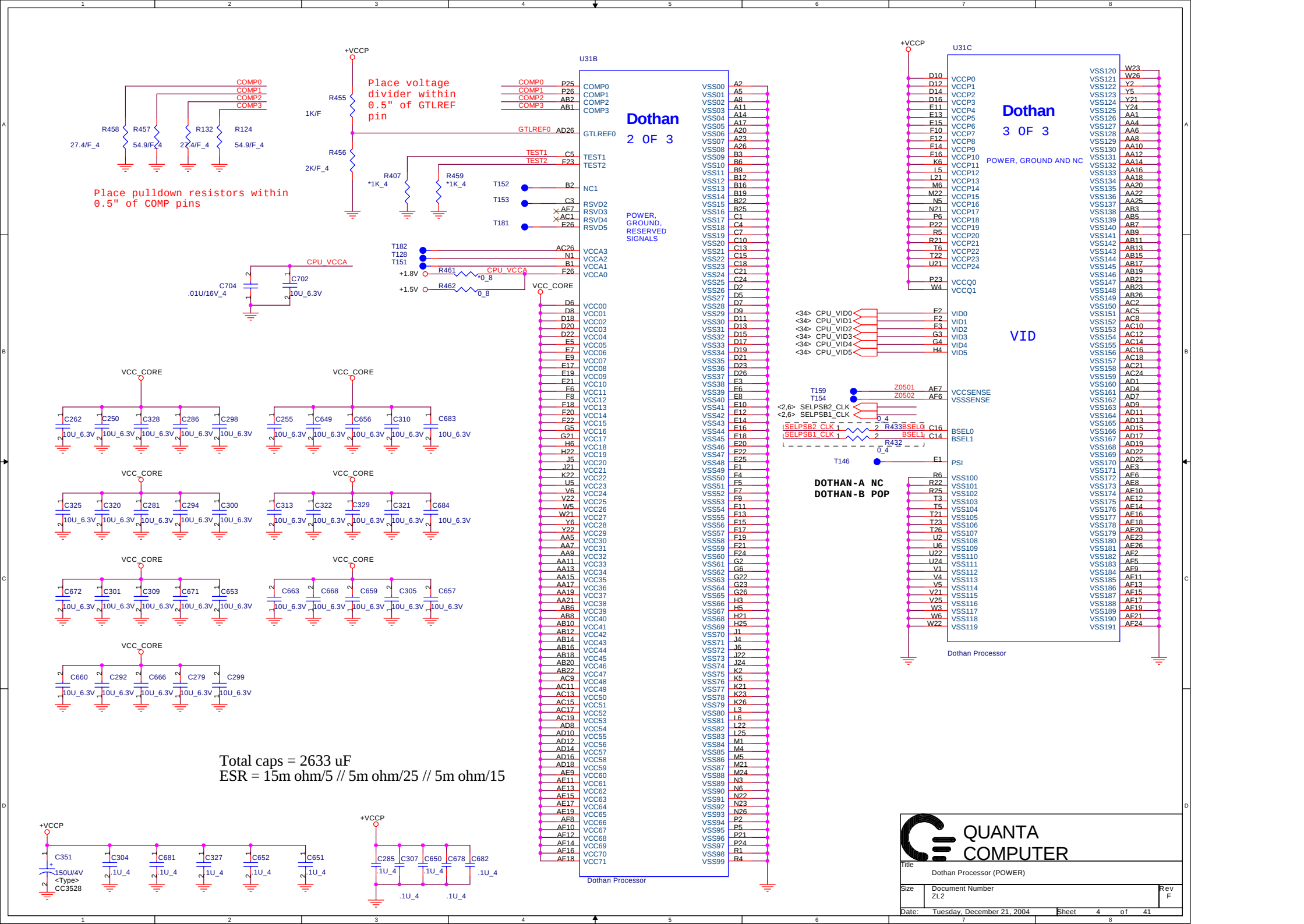
PULL HIGH TO SET PIN35,36 TO

K B: DEPOP COMPONENTS FOR DIFFERENT SKUS

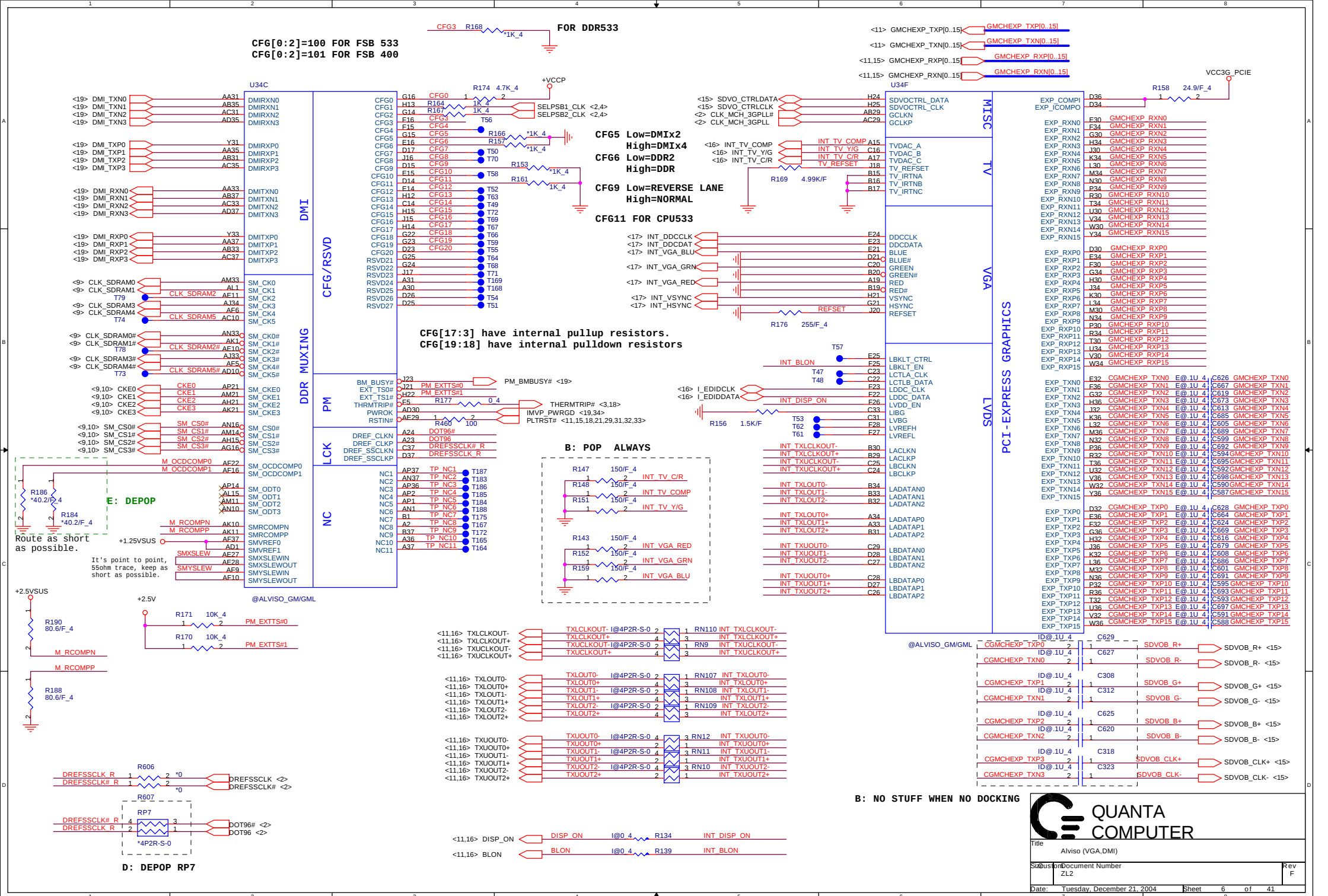


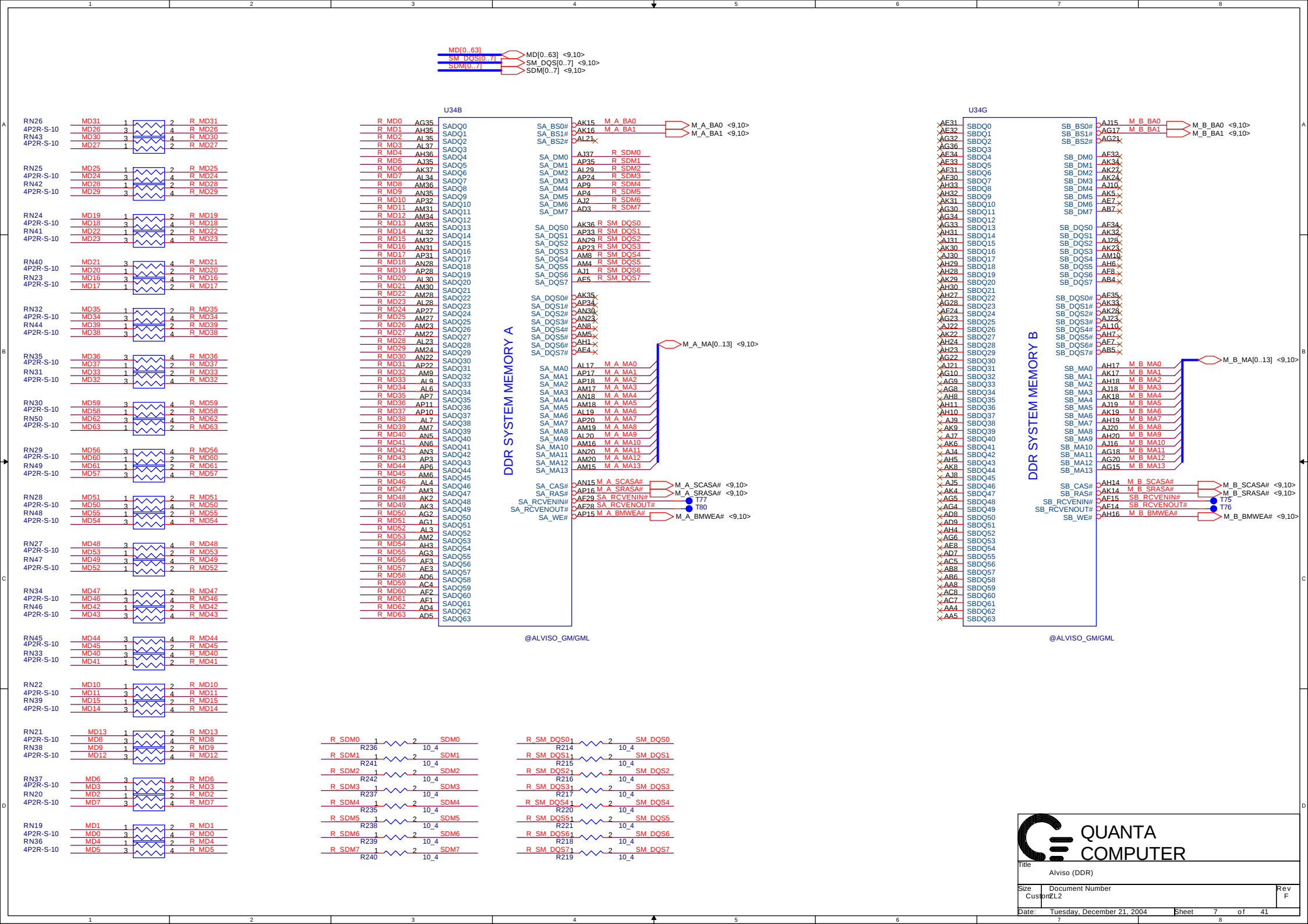
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1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

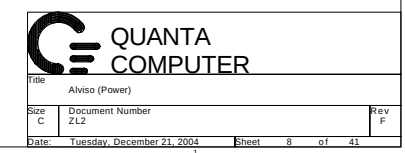


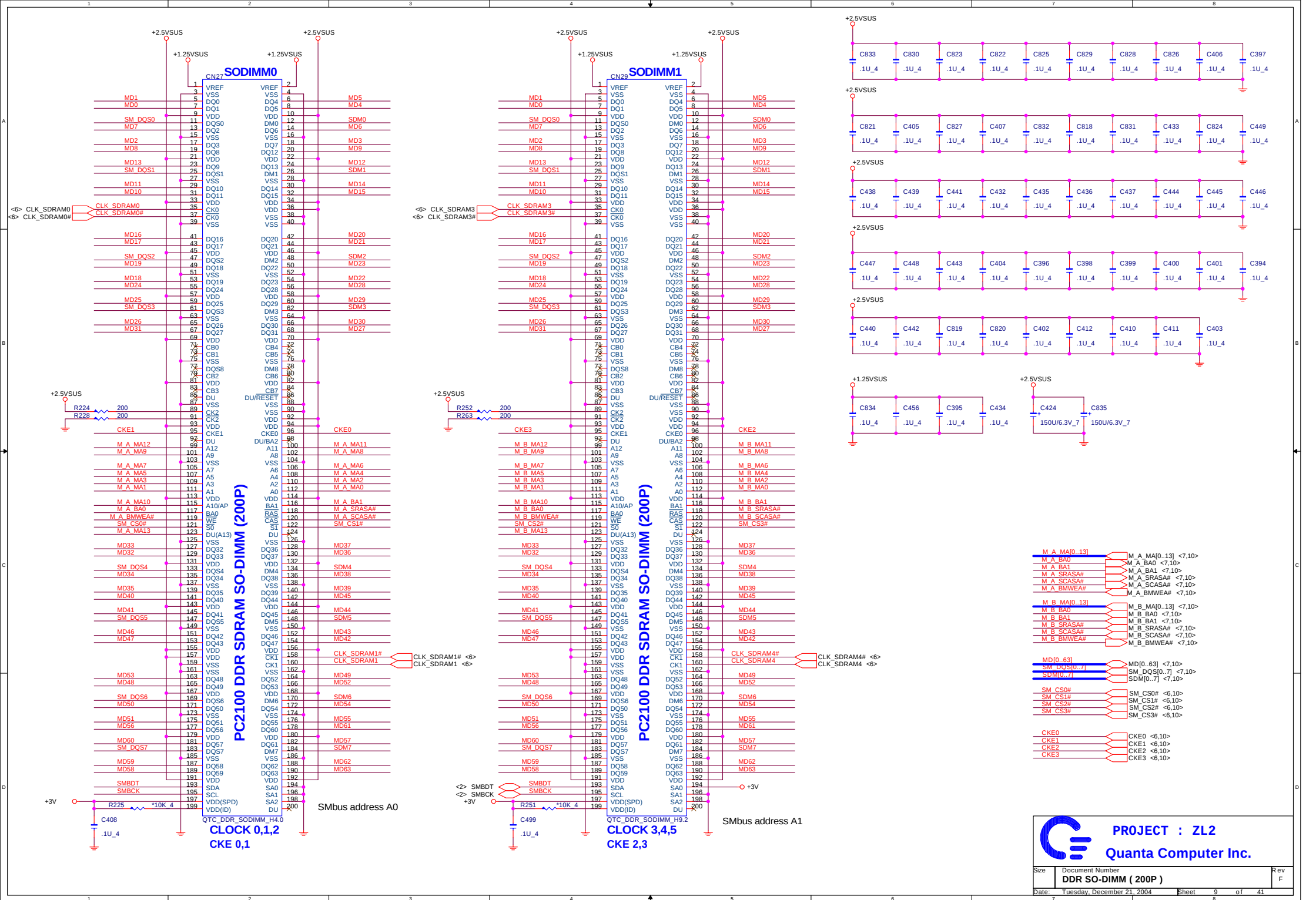


U34E		
AF23	VSS136	AC37
H23	VSS137	Y37
AL22	VSS138	V37
AM22	VSS139	T37
J22	VSS140	P37
E22	VSS141	M37
D22	VSS142	K37
A22	VSS143	H37
AN21	VSS144	E37
AF21	VSS145	AN36
F21	VSS146	AL36
C21	VSS147	AJ36
AK20	VSS148	AE36
V20	VSS149	AC36
G20	VSS150	AD36
E20	VSS151	AB36
D20	VSS152	AA36
AD0	VSS153	C36
AN19	VSS154	AE35
AG19	VSS155	Y35
W19	VSS156	W35
T19	VSS157	V35
J19	VSS158	U35
H19	VSS159	T35
VSS24	VSS160	R35
C19	VSS161	P35
AL18	VSS162	N35
U18	VSS163	M35
B18	VSS164	L35
A18	VSS165	K35
AN17	VSS166	J35
AJ17	VSS167	H35
G17	VSS168	G35
C17	VSS169	F35
AL16	VSS170	E35
K16	VSS171	D35
D16	VSS172	C35
D16	VSS173	B35
A16	VSS174	AN34
C15	VSS175	AH34
K15	VSS176	AD34
C15	VSS177	AC34
AN14	VSS178	AB34
AL14	VSS179	AA34
AJ14	VSS180	P34
AG14	VSS181	AL33
K14	VSS182	AE33
J14	VSS183	AD33
B14	VSS184	W33
A14	VSS185	U33
D12	VSS186	T33
J12	VSS187	R33
B12	VSS188	P33
VSS189	VSS189	N33
AN11	VSS190	M33
AL11	VSS191	L33
Y11	VSS192	K33
AG11	VSS193	J33
AE11	VSS194	H33
AA11	VSS195	G33
Y11	VSS196	F33
H11	VSS197	E33
F11	VSS198	D33
AA10	VSS199	C33
Y10	VSS200	AN32
L10	VSS201	AJ32
D10	VSS202	AD32
AN9	VSS203	AC32
AH9	VSS204	AB32
AE9	VSS205	AA32
AC9	VSS206	Y32
AA9	VSS207	W32
T9	VSS208	U32
VSS209	VSS209	T32
K9	VSS210	R32
H9	VSS211	P32
A9	VSS212	N32
AL8	VSS213	M32
Y8	VSS214	L32
P8	VSS215	K32
L8	VSS216	J32
C8	VSS217	H32
AK7	VSS218	G32
AG7	VSS219	F32
Y7	VSS220	E32
V7	VSS221	D32
G7	VSS222	C32
AJ6	VSS223	AN31
AE6	VSS224	AJ31
AC6	VSS225	AD31
AA6	VSS226	AC31
T6	VSS227	AB31
P6	VSS228	AA31
L6	VSS229	Y31
B6	VSS230	W31
AF5	VSS231	U31
AL5	VSS232	T31
W5	VSS233	R31
ES	VSS234	P31
AN4	VSS235	N31
AE4	VSS236	M31
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H4	VSS240	H31
C4	VSS241	G31
A13	VSS242	F31
AC3	VSS243	E31
AB3	VSS244	D31
AA3	VSS245	C31
C3	VSS246	AN30
AN2	VSS247	AJ30
AL2	VSS248	AD30
AH2	VSS249	AC30
AE2	VSS250	AB30
AD2	VSS251	AA30
Y2	VSS252	Y30
T2	VSS253	W29
P2	VSS254	U29
L2	VSS255	T29
B27	VSS256	R29
G26	VSS257	P29
E26	VSS258	L29
A26	VSS259	K29
AL24	VSS260	J29
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G2	VSS262	G29
D2	VSS263	F29
AN24	VSS264	E29
AL24	VSS265	D29
J2	VSS266	C29
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D2	VSS268	AJ28
Y1	VSS269	AD28
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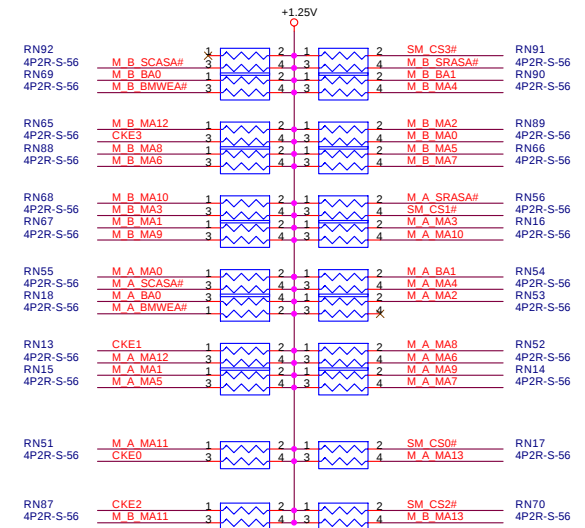
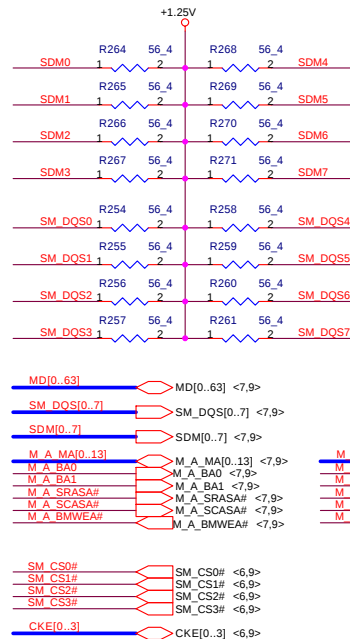
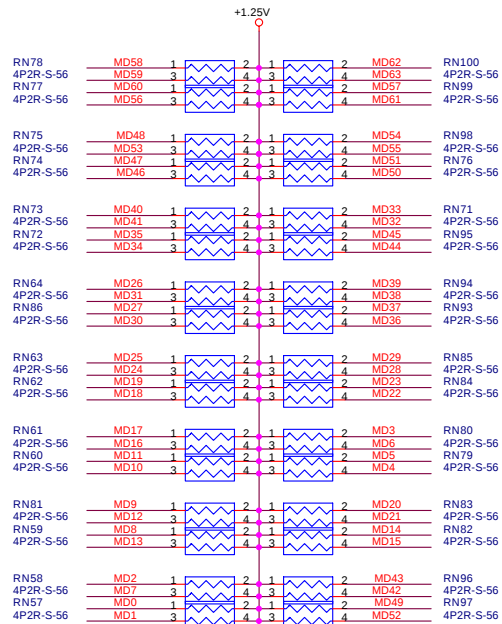
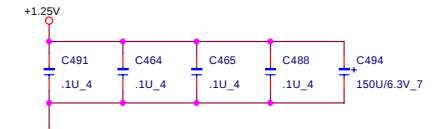
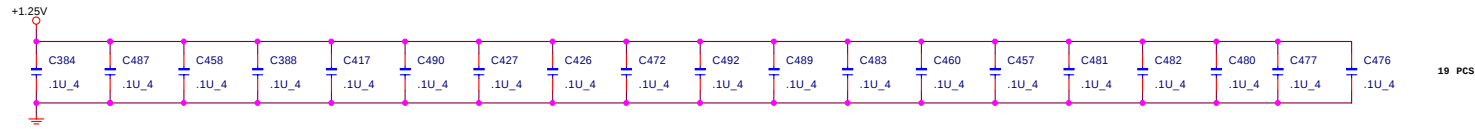
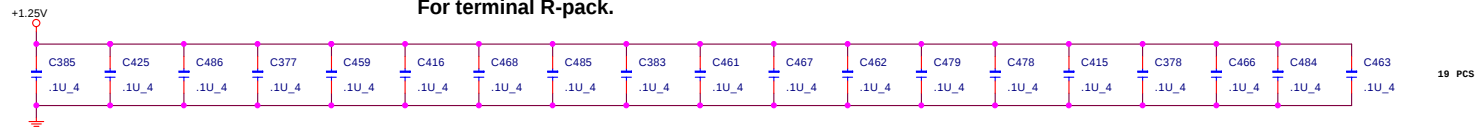


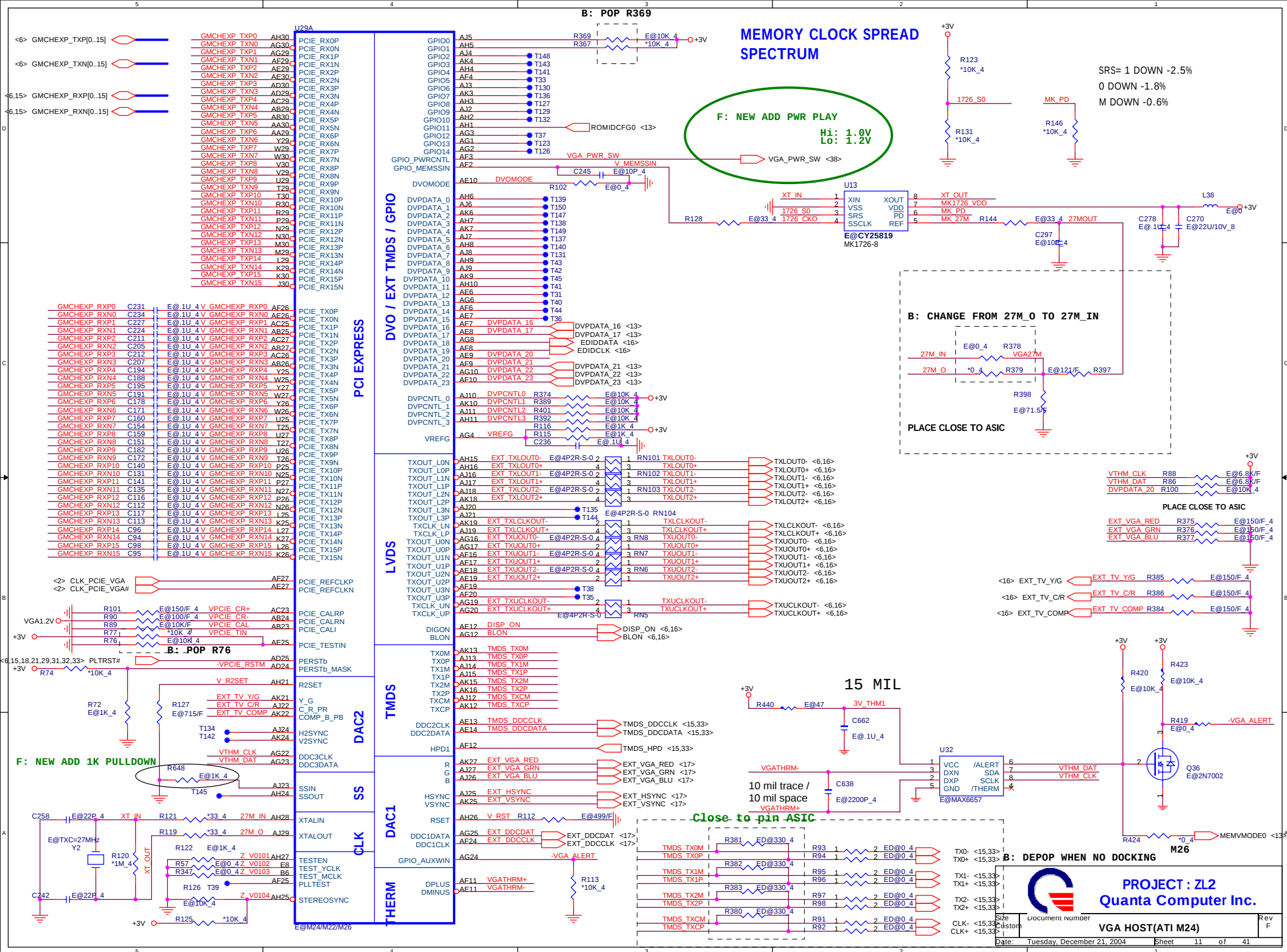


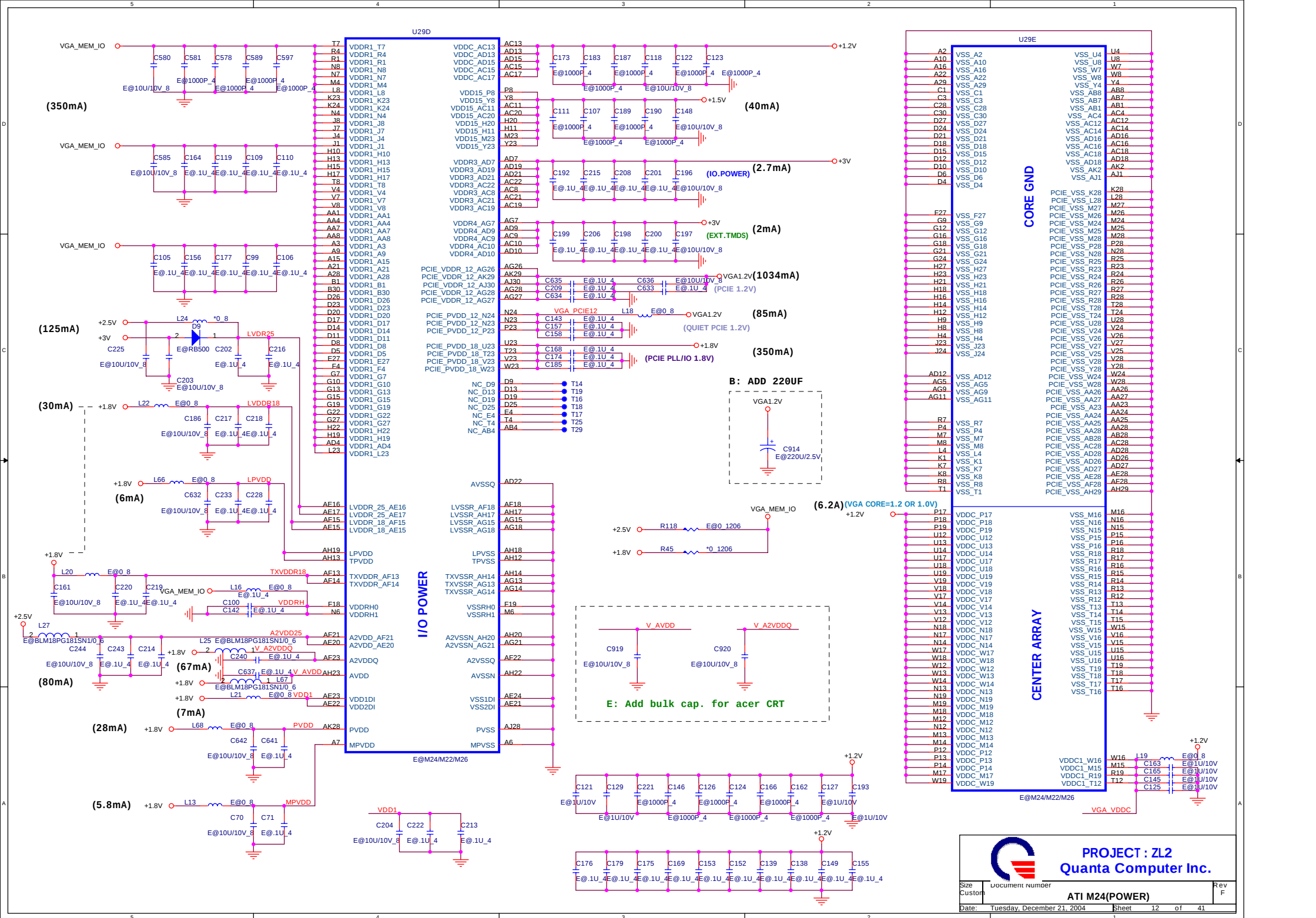


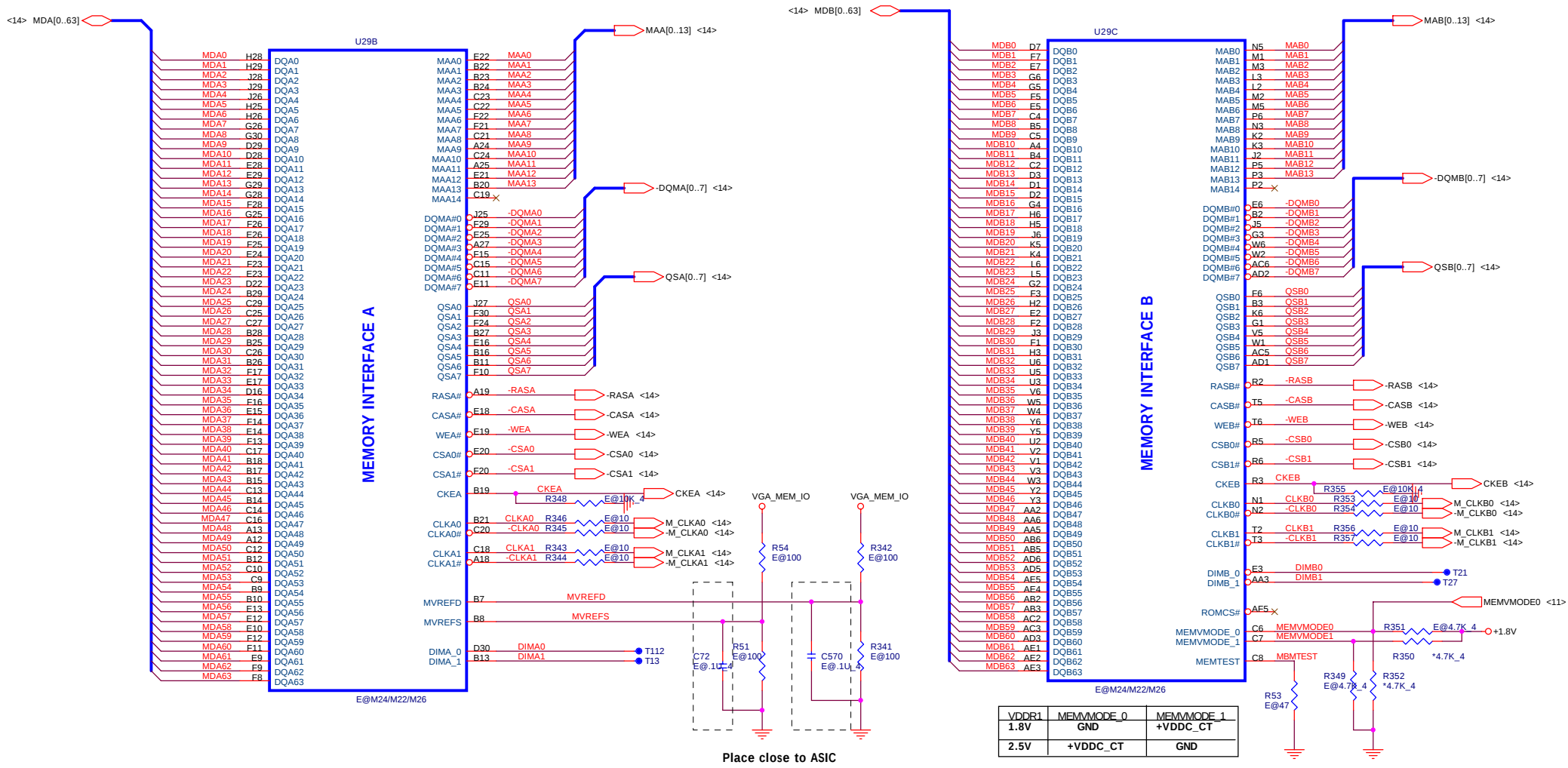


For terminal R-pack.









STRAPS PIN	
GPIO_0	PCI-Express Current Calibration Bandgap Backup 0: use reference voltage from Bandgap 1: use reference voltage from resistor divider
GPIO_1	PCI-Express PLL Calibration force enable 0: Disable PLL force calibration 1: Enable PLL force calibration
GPIO_(3,2)	00: PCI Express 1.0 mode 01: RESERVED 10: PCI Express 1.0 mode 11: RESERVED
GPIO_4	Turn off PCI-Express impedance / strength calibration 0: enable 1: disable
GPIO_5	Bypass PCI-Express PLL

GPIO_6	PCI-Express transmitter current compensation 0: Normal 1: Inject extra current for output buffer switching
GPIO_8	Strap to set the debug muxes to bring out DEBUG signals even if registers are inaccessible
GPIO(9,13:11) INT P/D	ROMIDCFG 0x0x: No ROM, CHG_ID=0 0x1x: No Rom, CHG_ID=1 1000: Parallel ROM, Chip ID'S from ROM 1000: Parallel ROM, Chip ID'S from ROM
DVPDATA_21-23	DVPDATA_21: 0=4Mx32 1=8Mx32 DVPDATA_22: 0=128M 1=64M DVPDATA_23: 0=Hynix 1=Samsung

FOR M26P ONLY
0: 128M
1: 256M

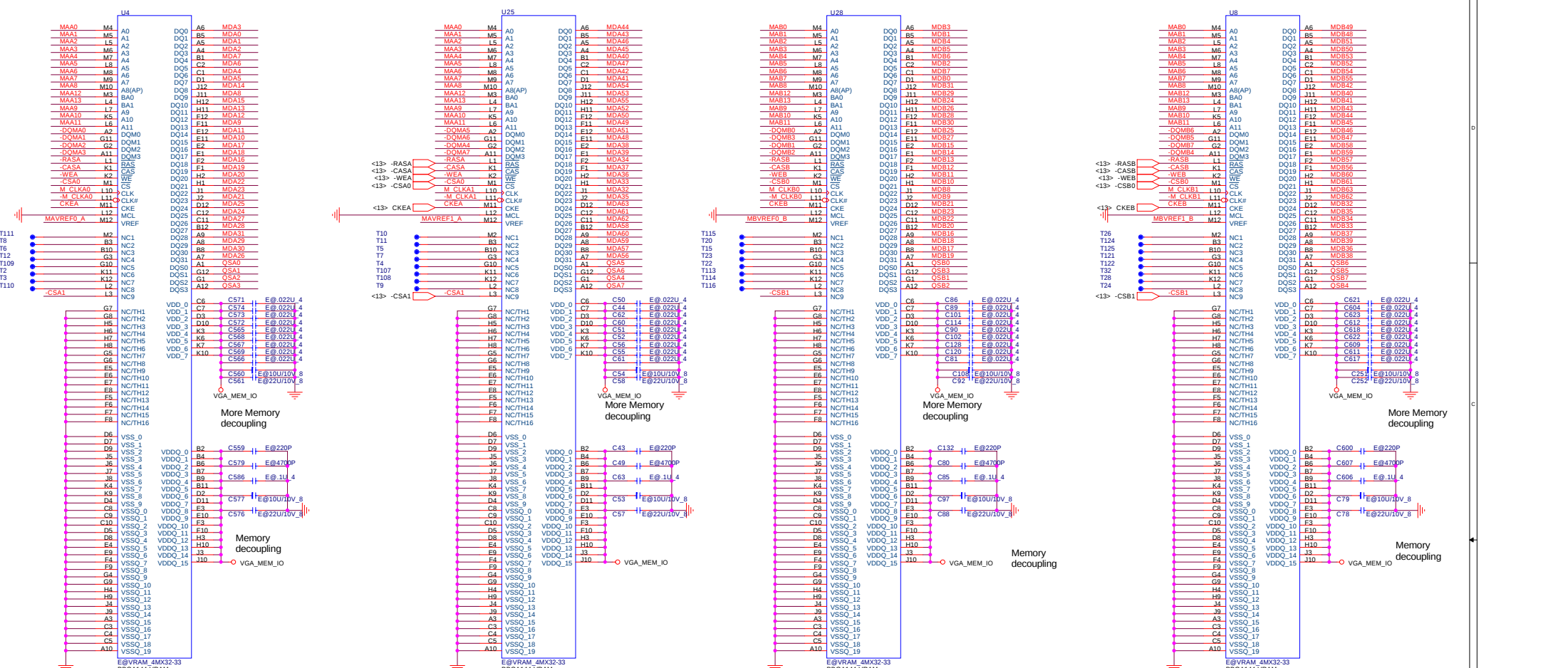
C: FOR HYNIX MEMORY

VDDP1	1.8V	MEMVMODE_0	GND	MEMVMODE_1	+VDDC_CT
2.5V		+VDDC_CT		GND	

PROJECT :ZL2
Quanta Computer Inc.

ATI M24 MEM/STRAPS PIN

Size Custom Document number Date: Tuesday, December 21, 2004 Sheet 13 of 41



VGA DDR MEMORY A

@64/128Mbytes DDR 128Mbit 1MX32X4 uBGA



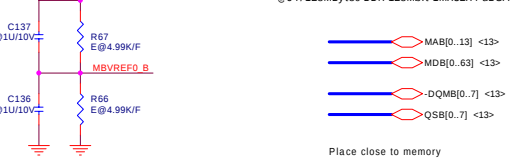
8Mx32 AKD56WCT503 K4D55323QF-GC33 1.8V

4Mx32 AKD35W-T506 K4D263238E-GC33 2.5V



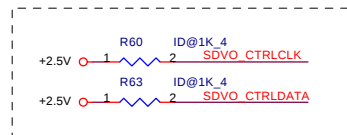
VGA DDR MEMORY B

@64/128Mbytes DDR 128Mbit 1MX32X4 uBGA

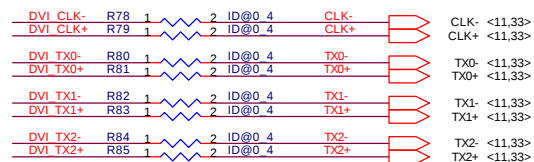
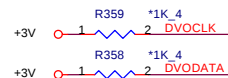
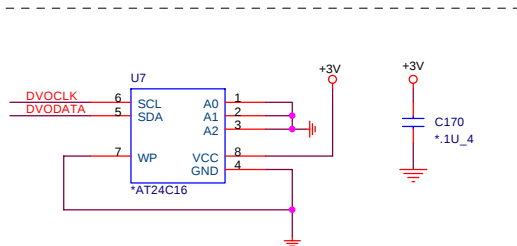


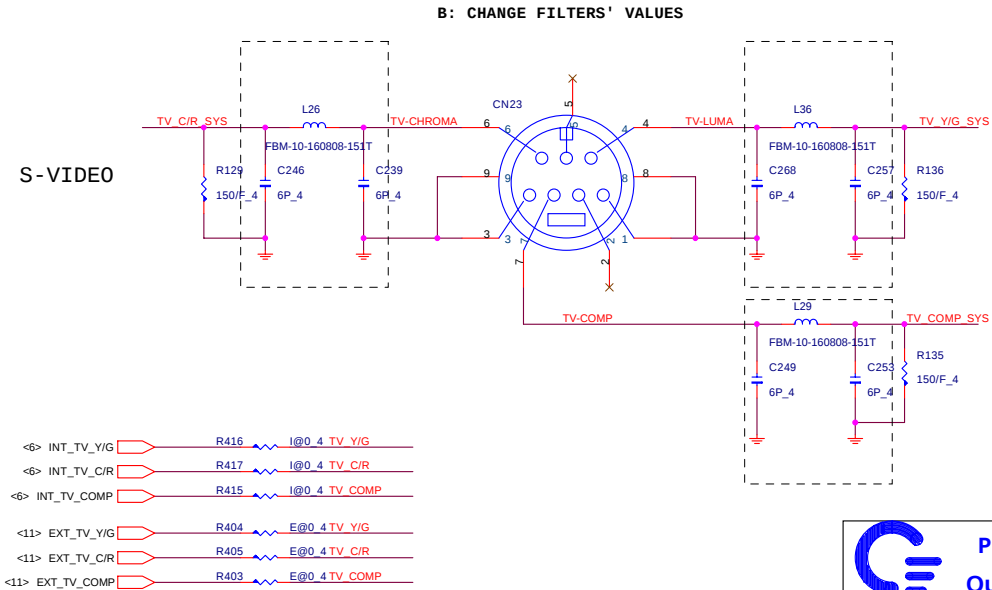
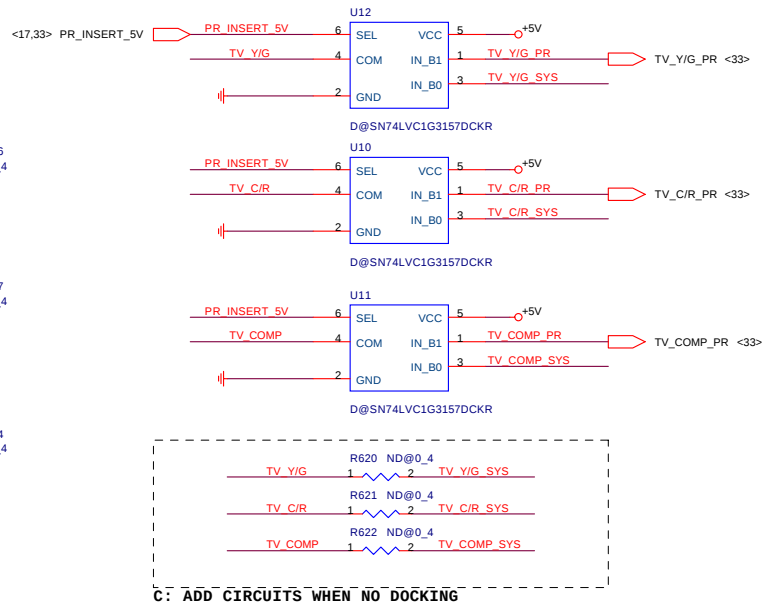
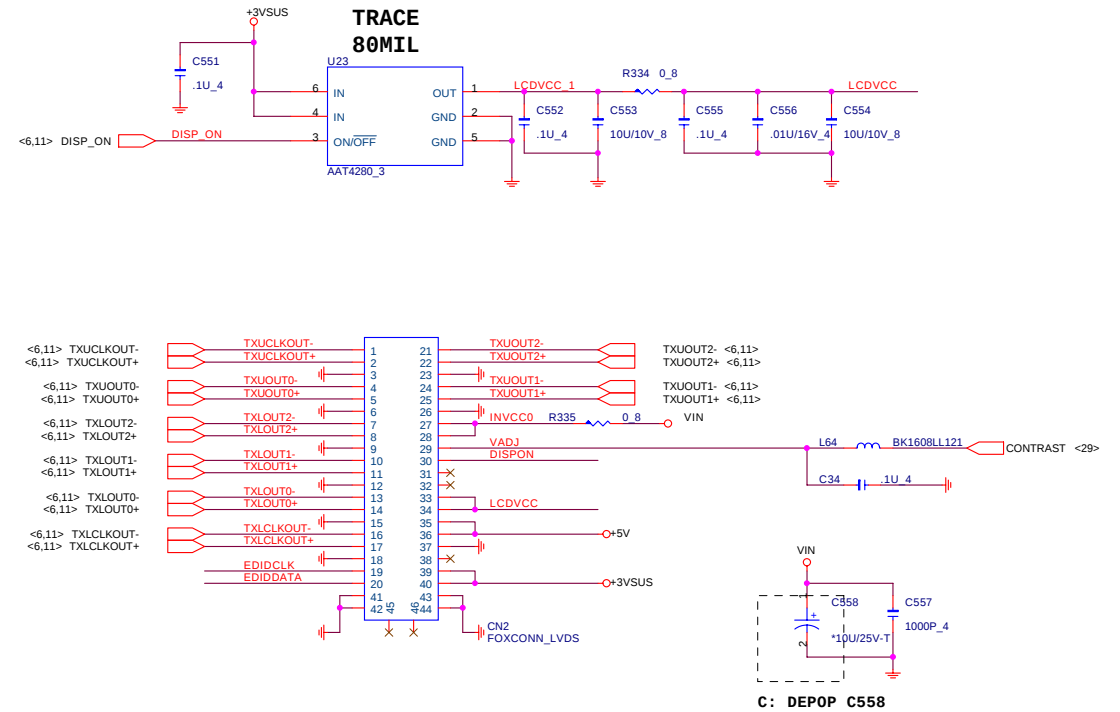
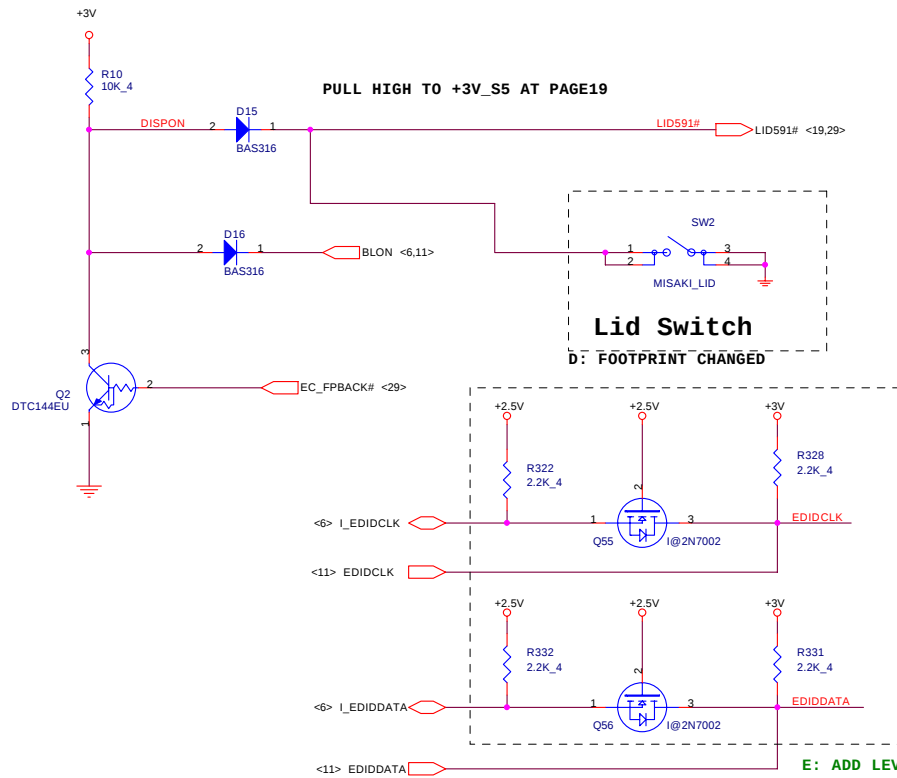
At least a 2.5:1 spacing between the pair
These resistors and caps must be placed to minimize any stubs. These must also be placed after the memory

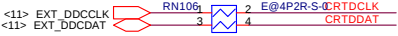
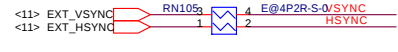
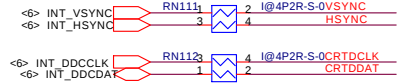
At least a 2.5:1 spacing between the pair
These resistors and caps must be placed to minimize any stubs. These must also be placed after the memory



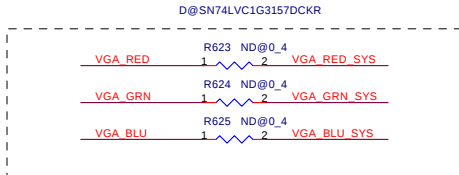
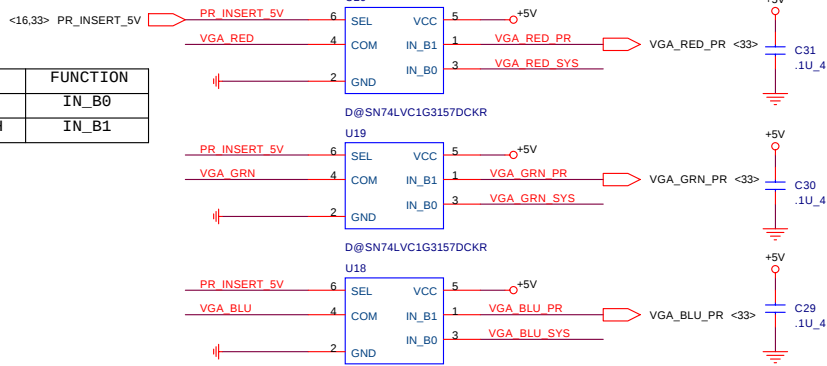
+2.5V	250mA
+3V	190mA



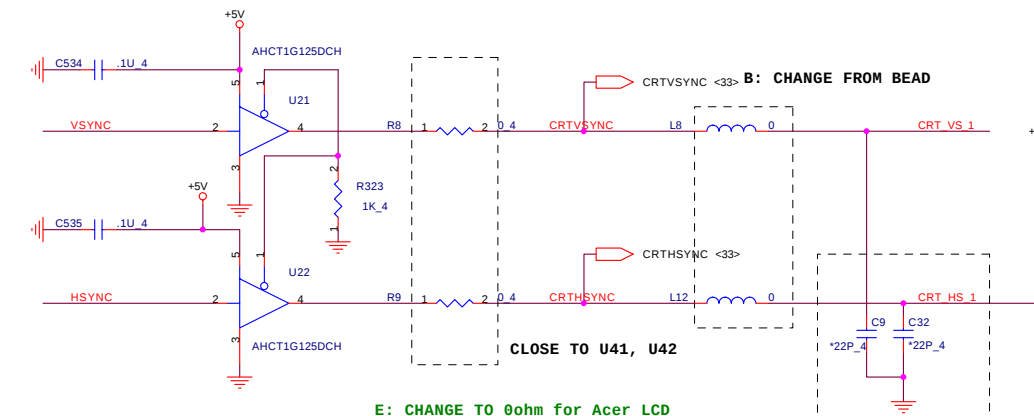
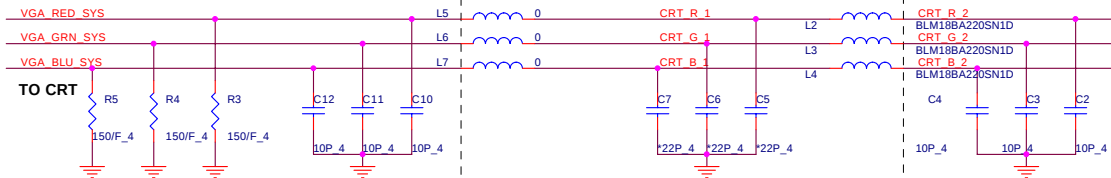




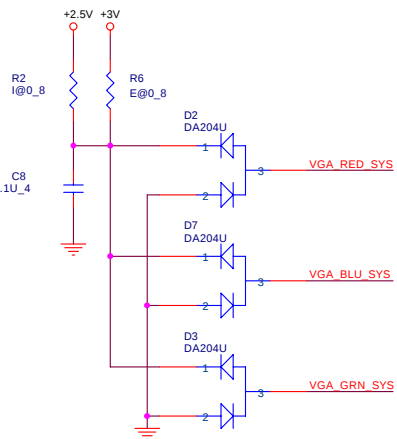
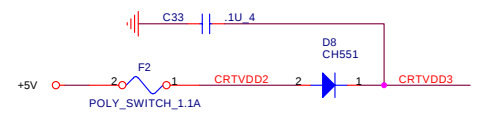
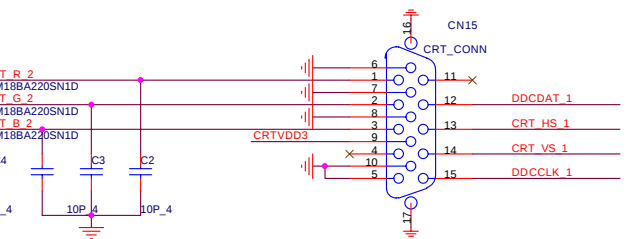
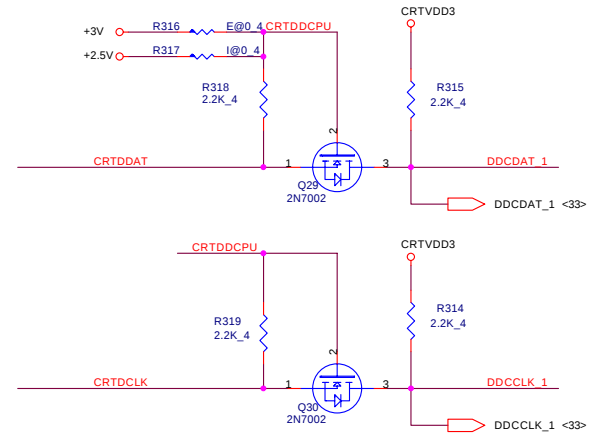
SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



C: ADD CIRCUITS WHEN NO DOCKING

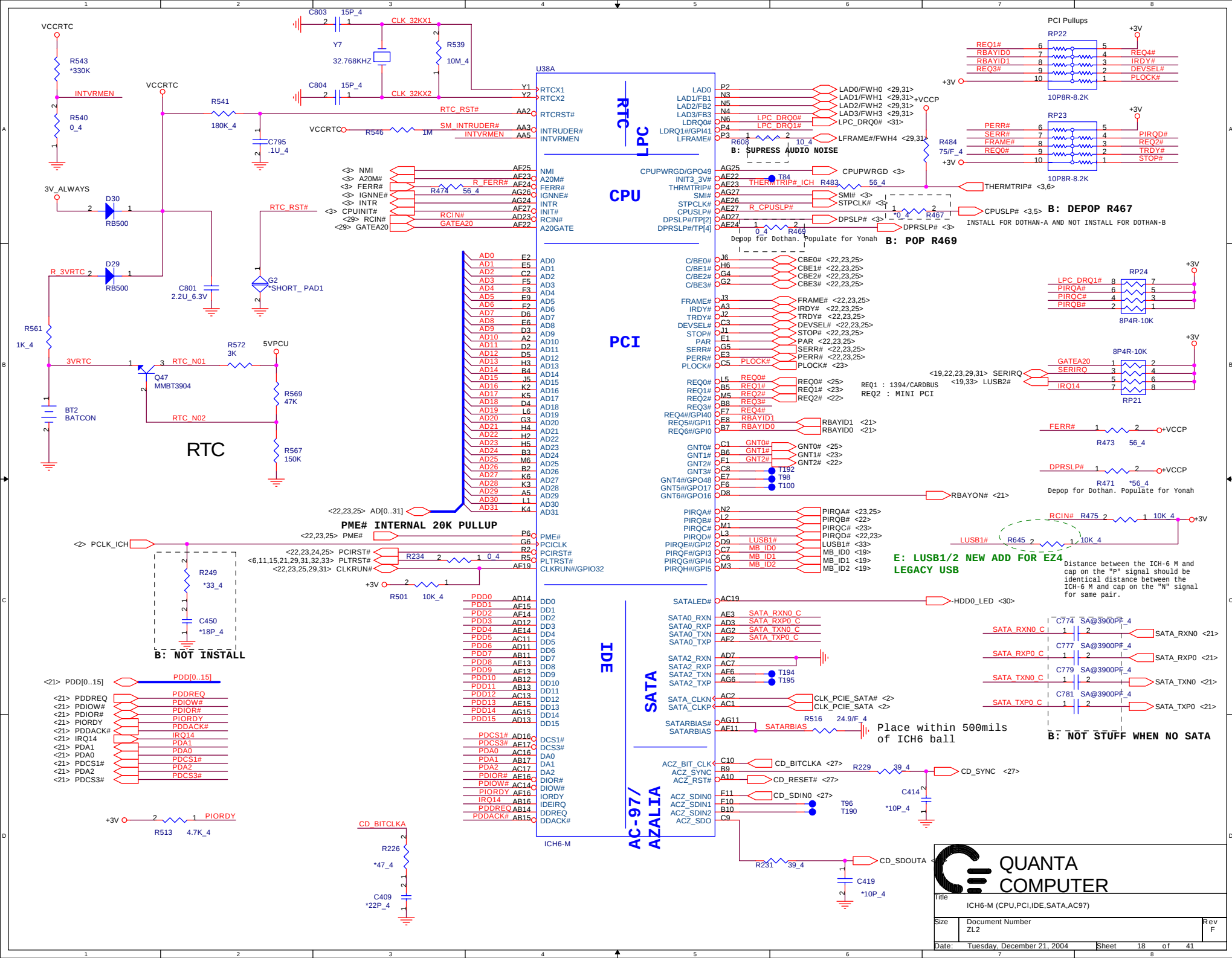


E: CHANGE TO 0ohm for Acer LCD

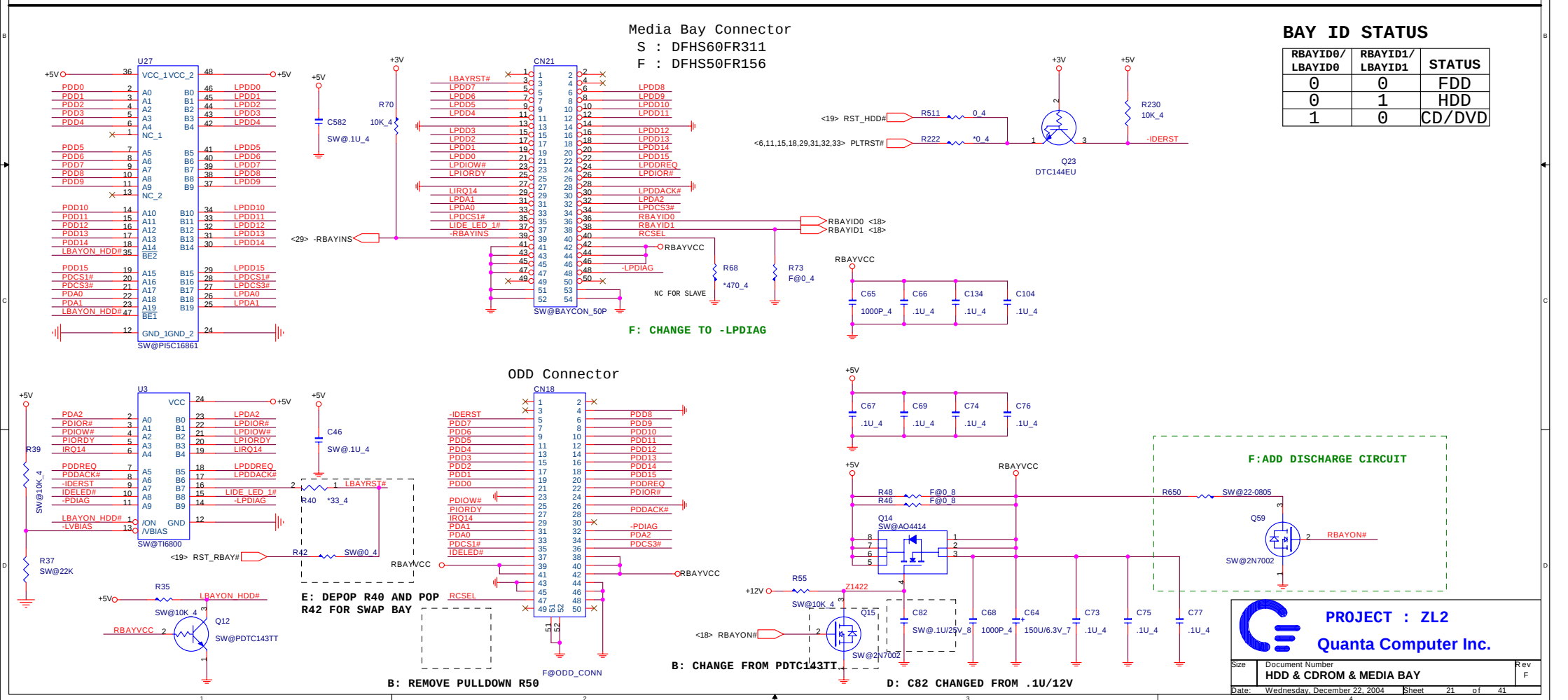
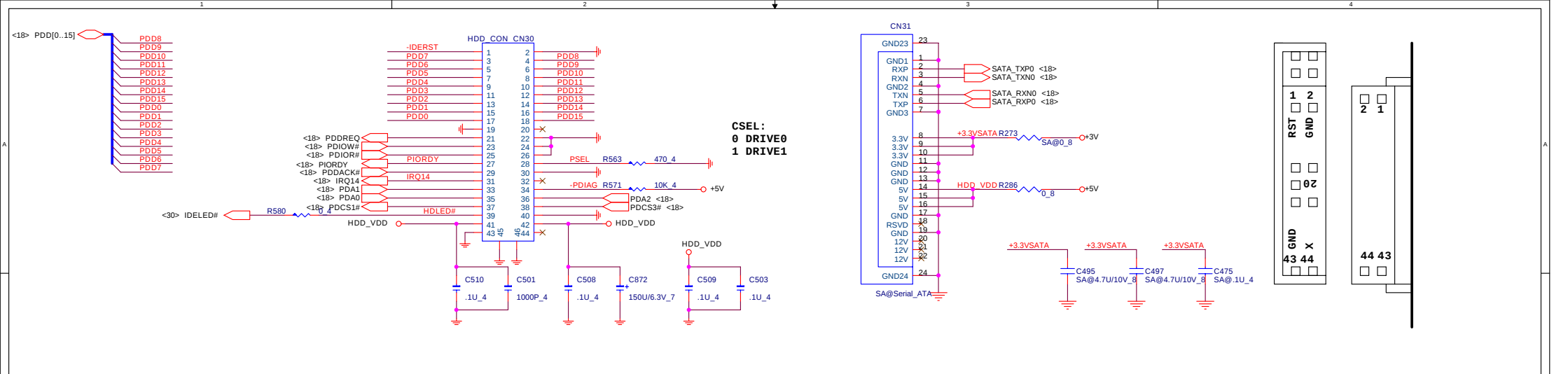


PROJECT : ZL2
Quanta Computer Inc.

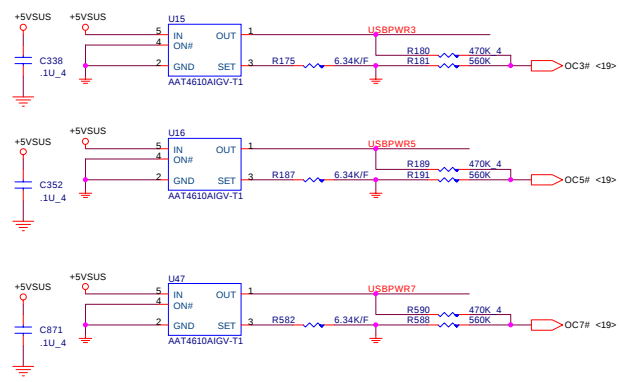
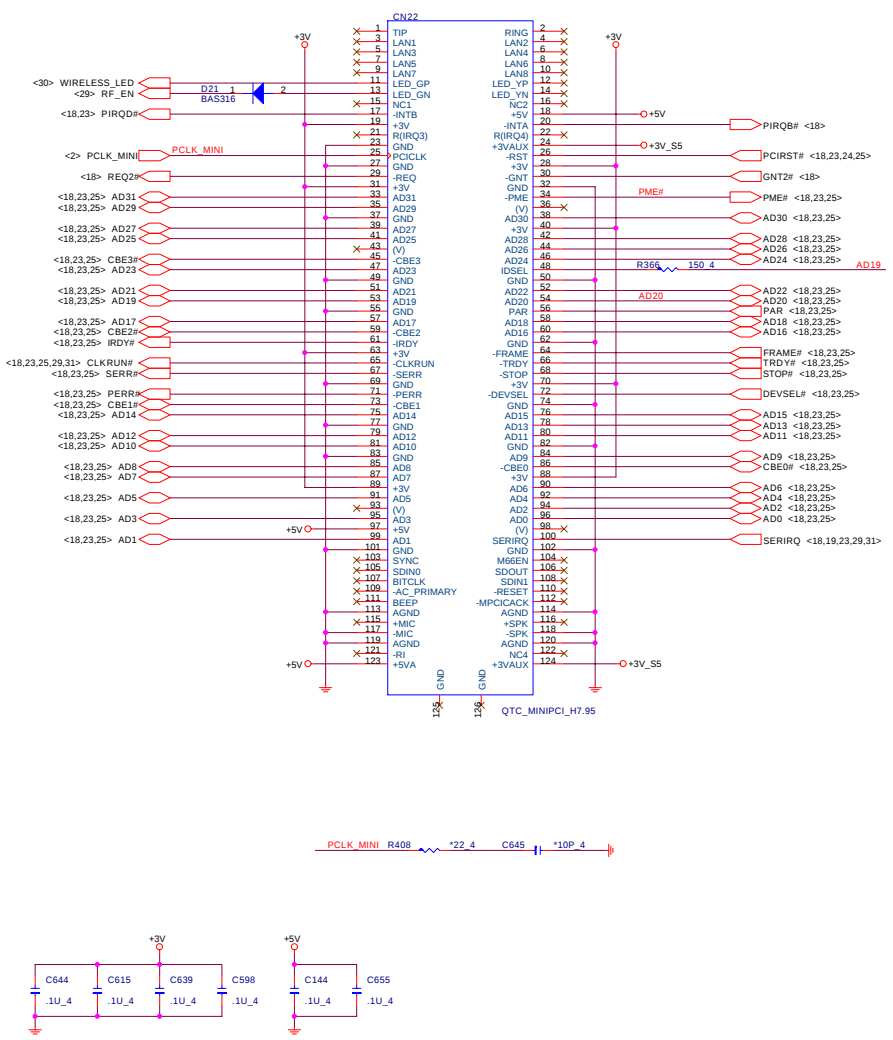
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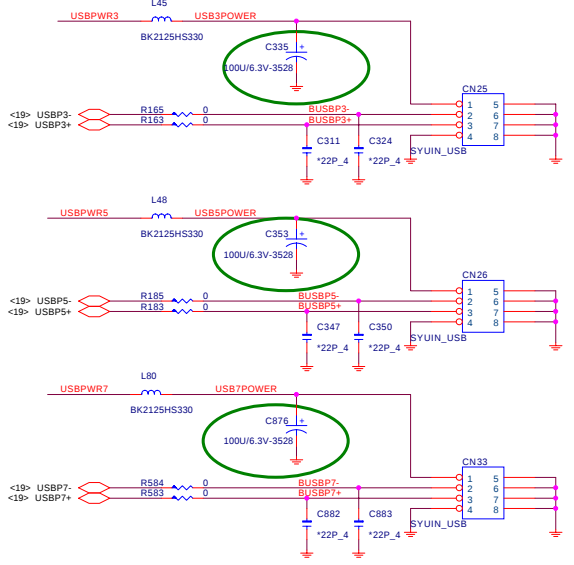




MINI-PCI



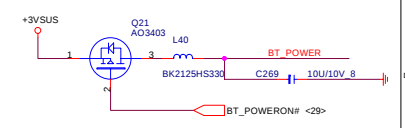
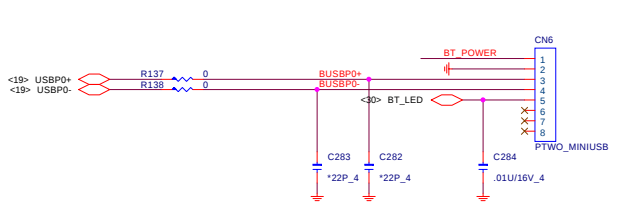
F: CHANGE TO 100UF

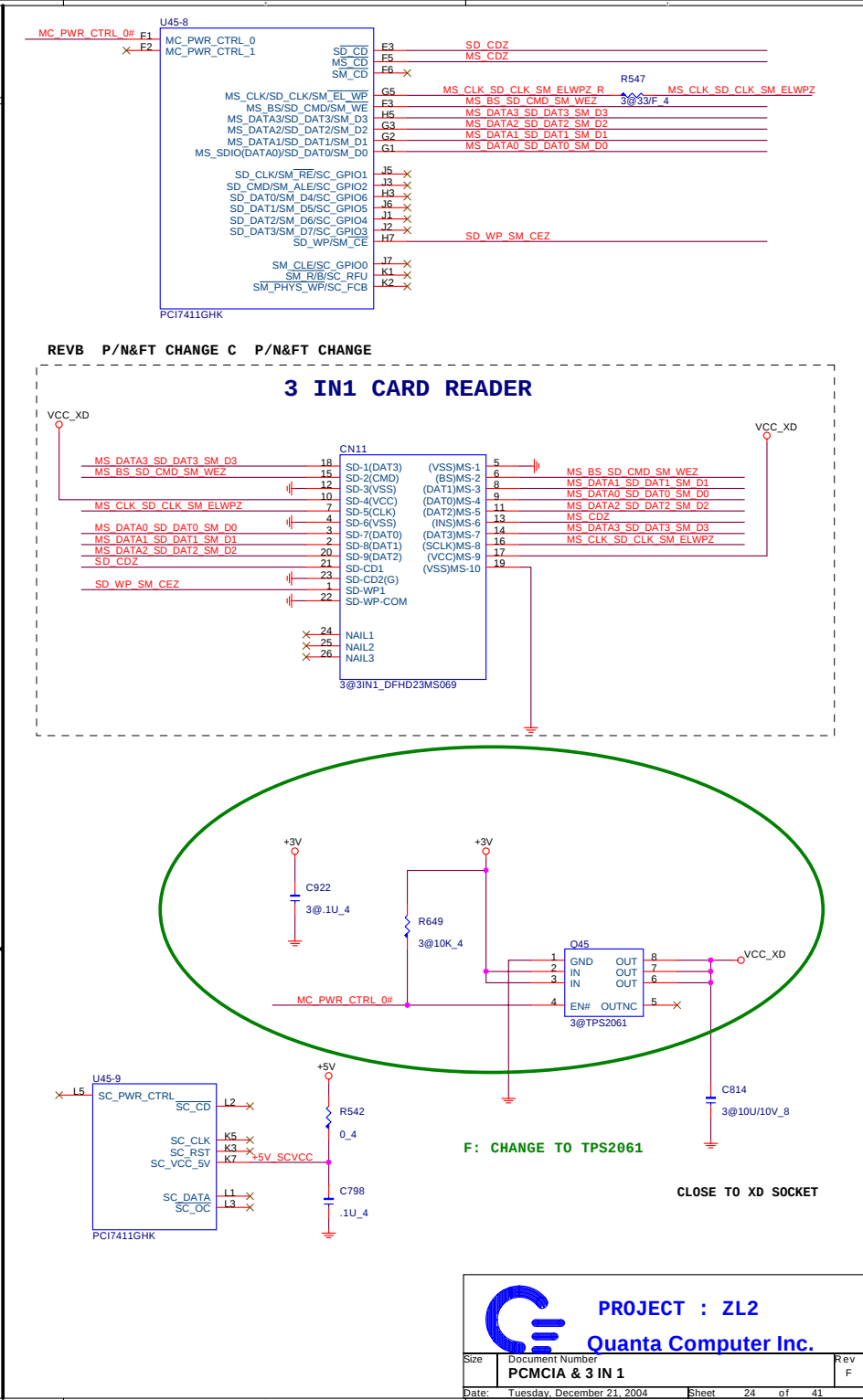
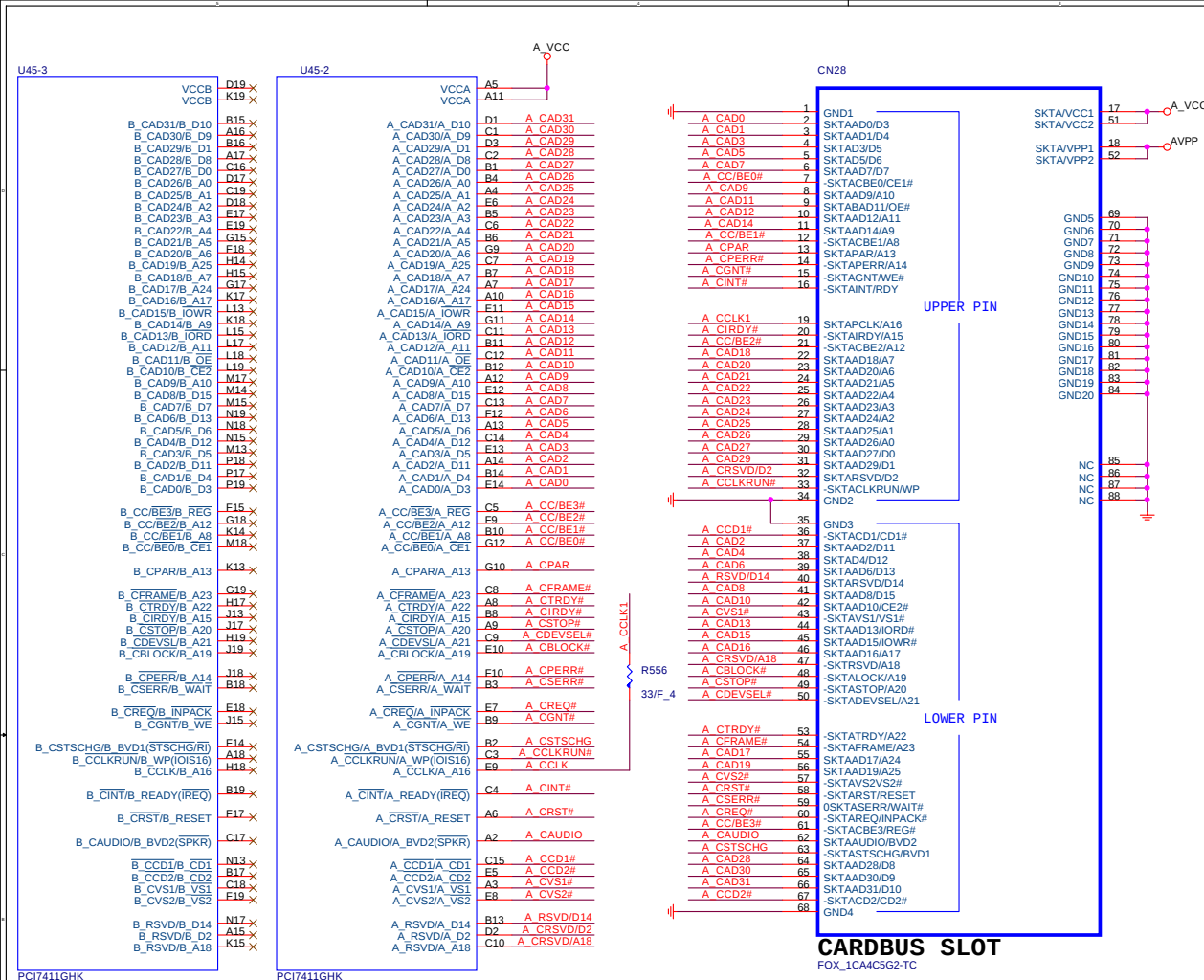


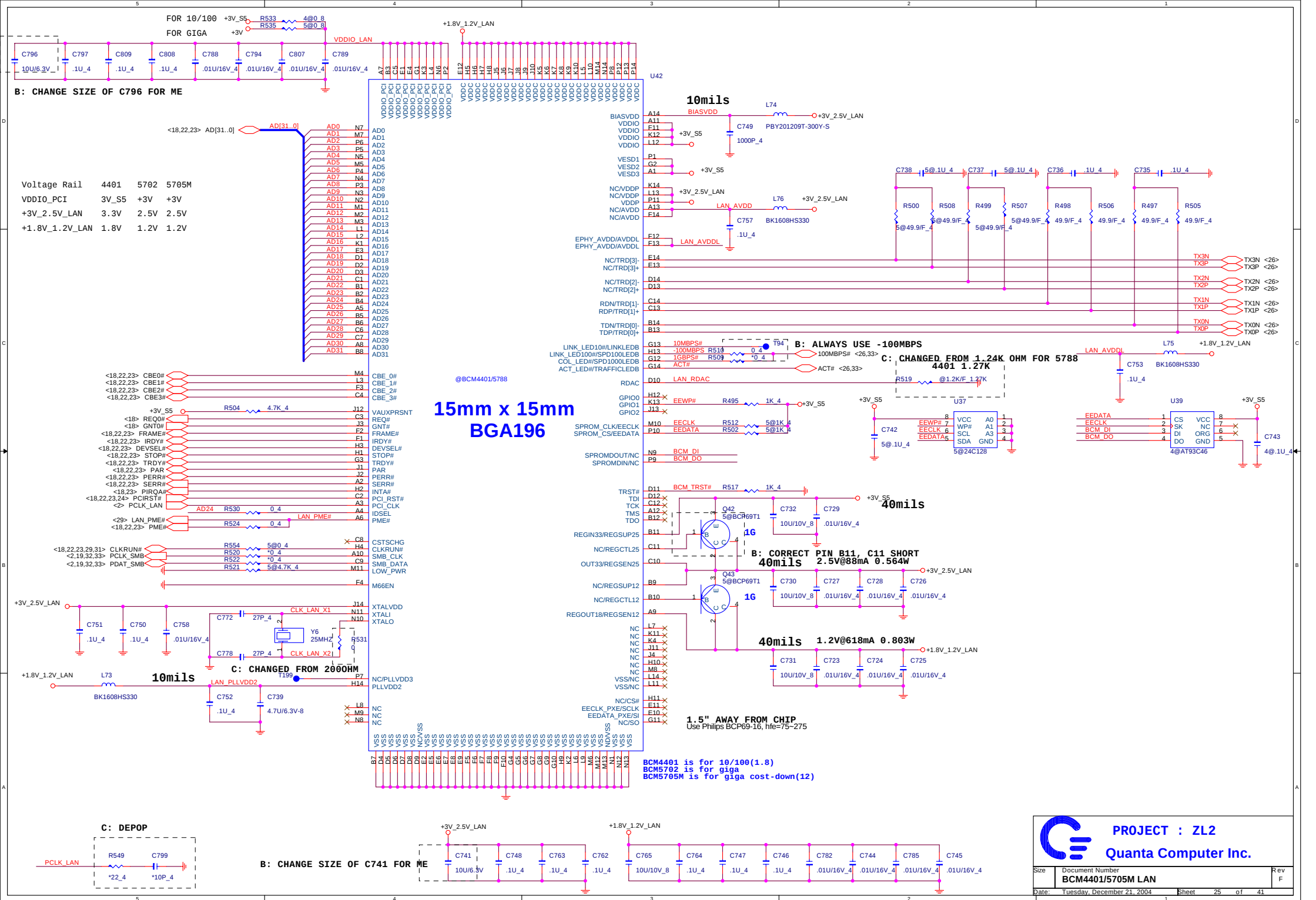
BOT

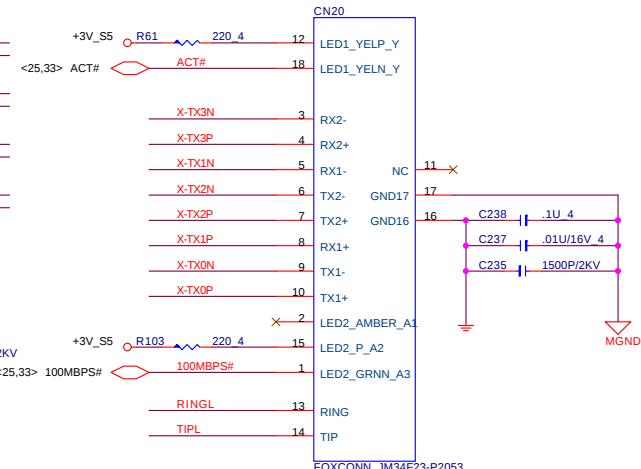
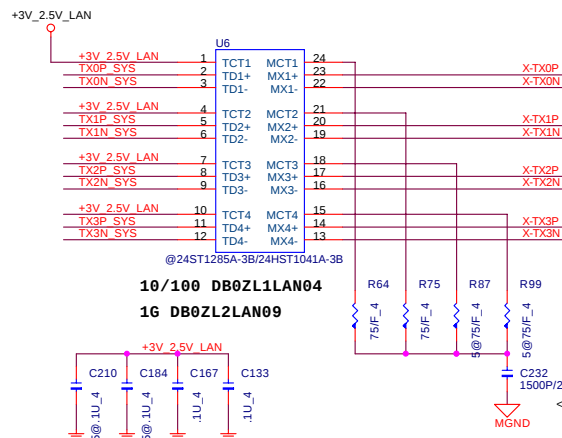
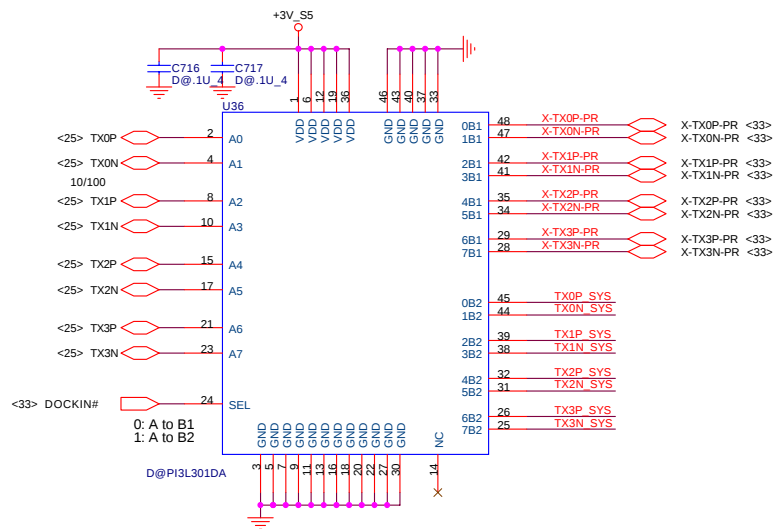


B: REMOVE CHOKE PADS

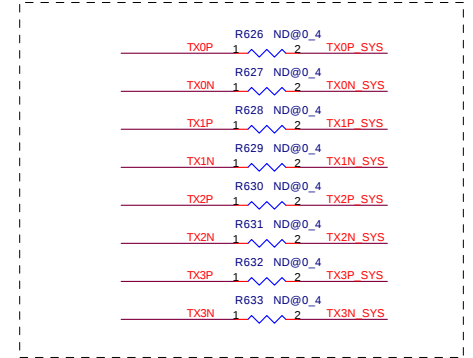




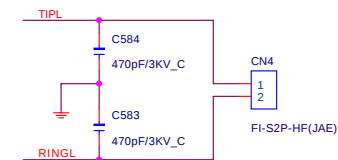




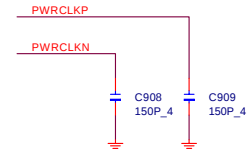
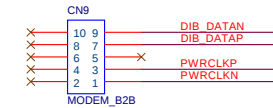
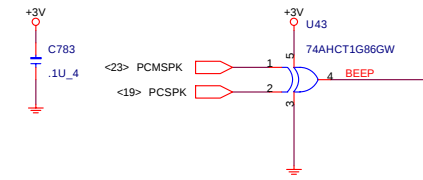
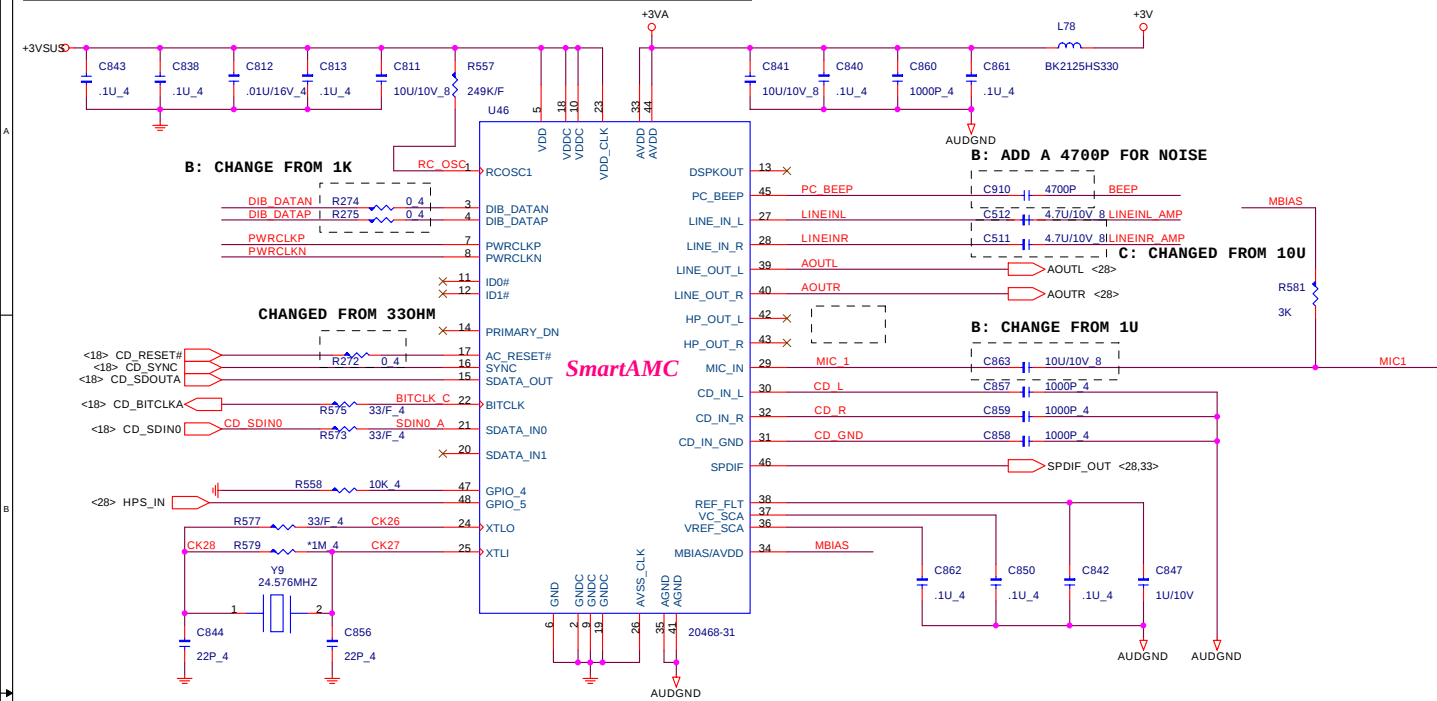
B: DEPOP C210, C184, R87, R99 WHEN NO 1G LAN



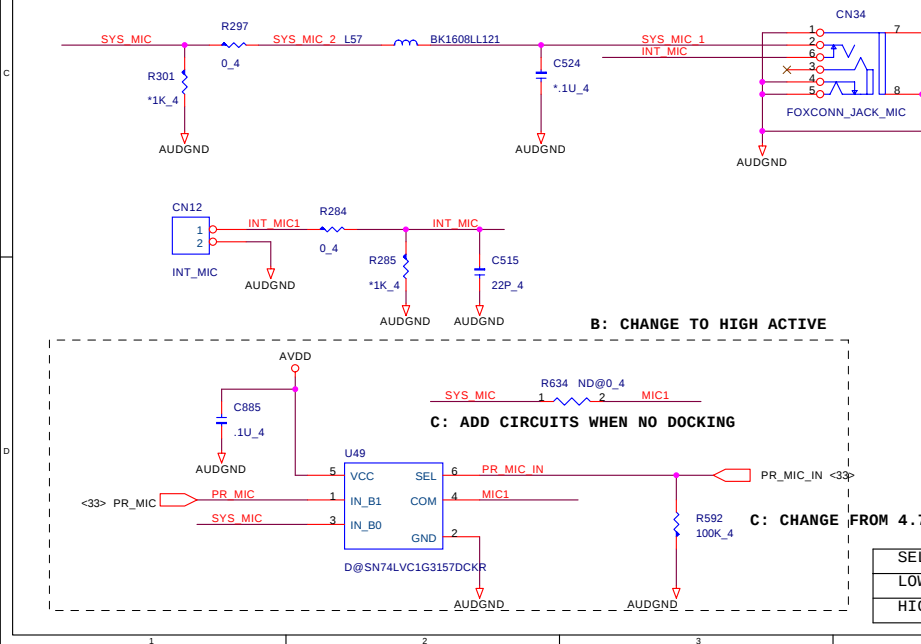
C: ADD CIRCUITS WHEN NO DOCKING



The AMC20463-004 modem is used for mother board family MBAMC20463-004.

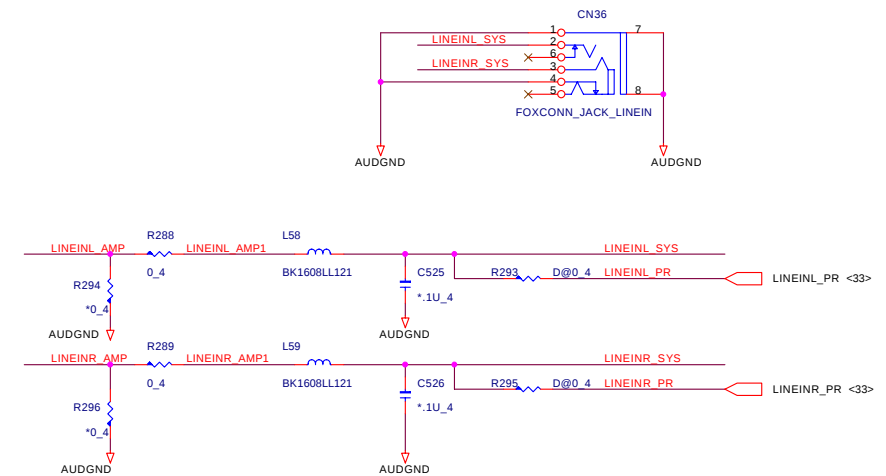


MIC



SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

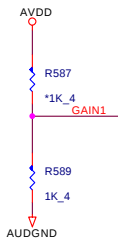
LINE IN



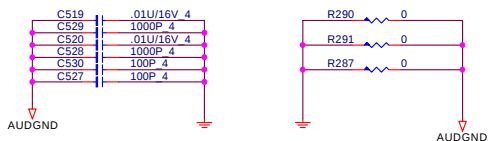
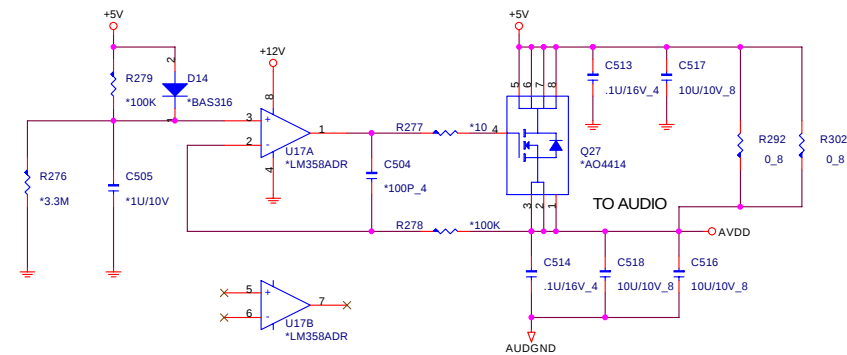
PROJECT : ZL2
Quanta Computer Inc.

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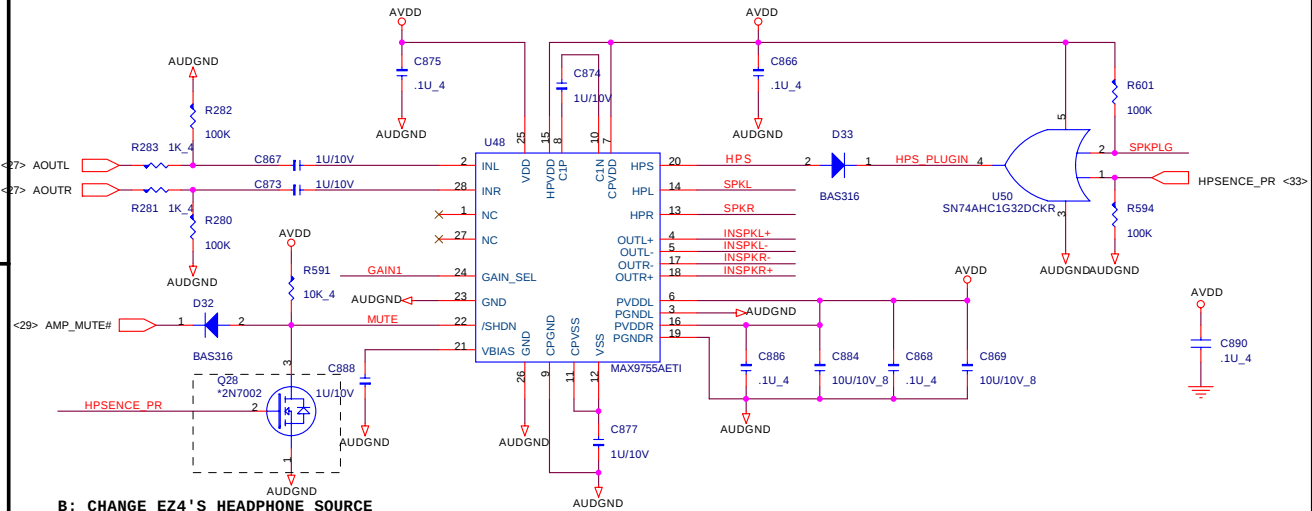
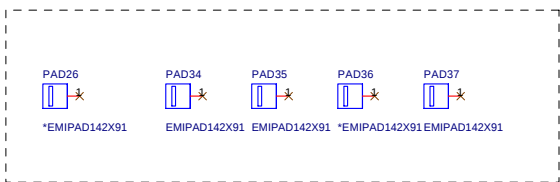
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



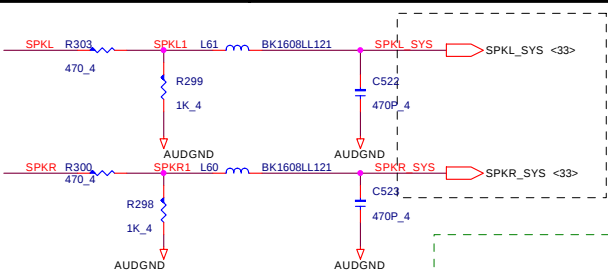
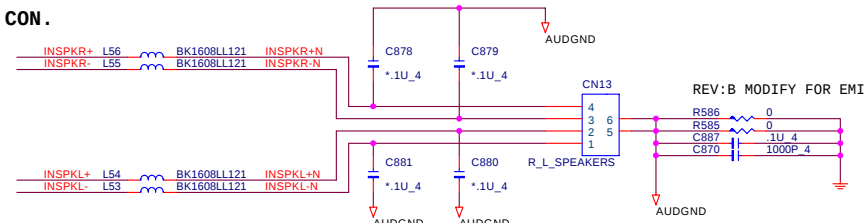
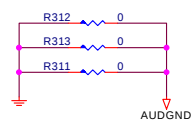
AMP POWER



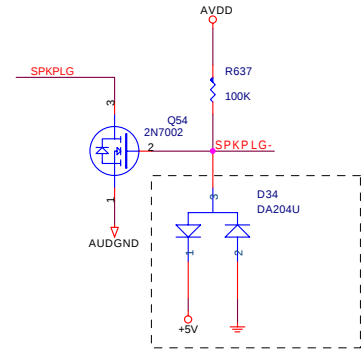
- F: RESTORE PAD26 FOR EMI REQUEST
- E: REMOVE PAD26, AND PAD35 CHANGE LOCATION
- B: ADD SPRING FOR MODEM CABLE



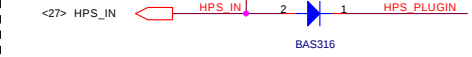
SPEAKER CON.



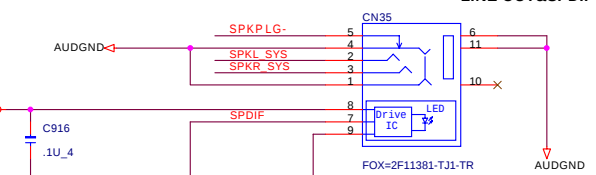
D: CHANGE TO SPDIF CONN



- F: NEW ADD FOR ESD CLOSE TO CN35

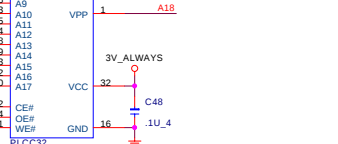
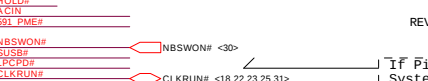
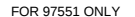
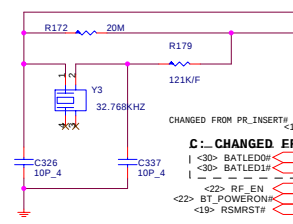


LINE OUT&SPDIF

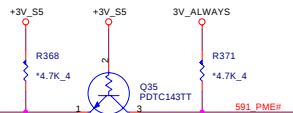


- F: TURN OFF LED WHEN NO PLUG

PROJECT : ZL2
Quanta Computer Inc.

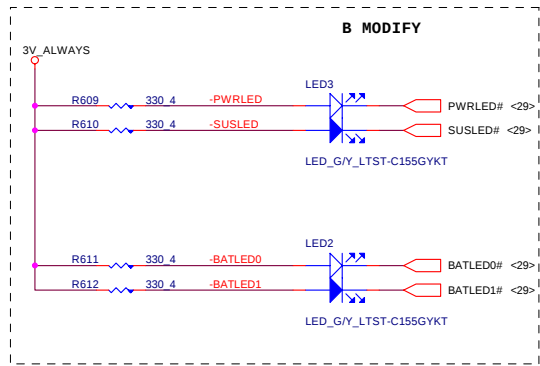
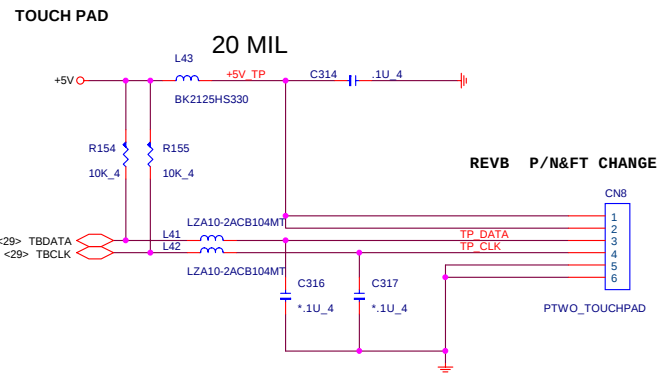
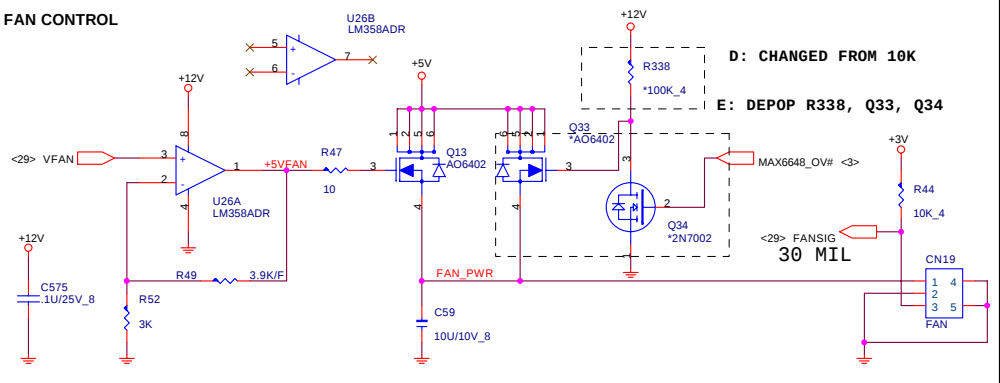
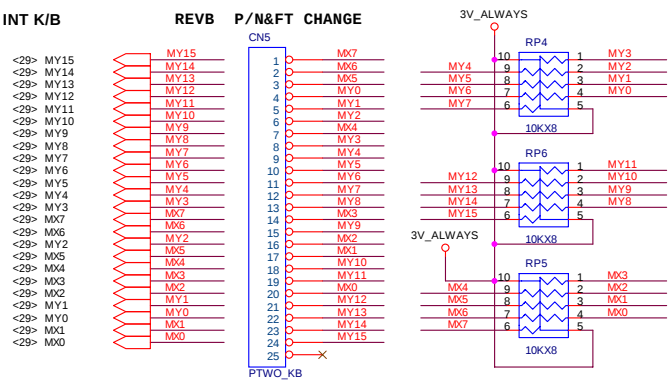


ion should match flash speed used

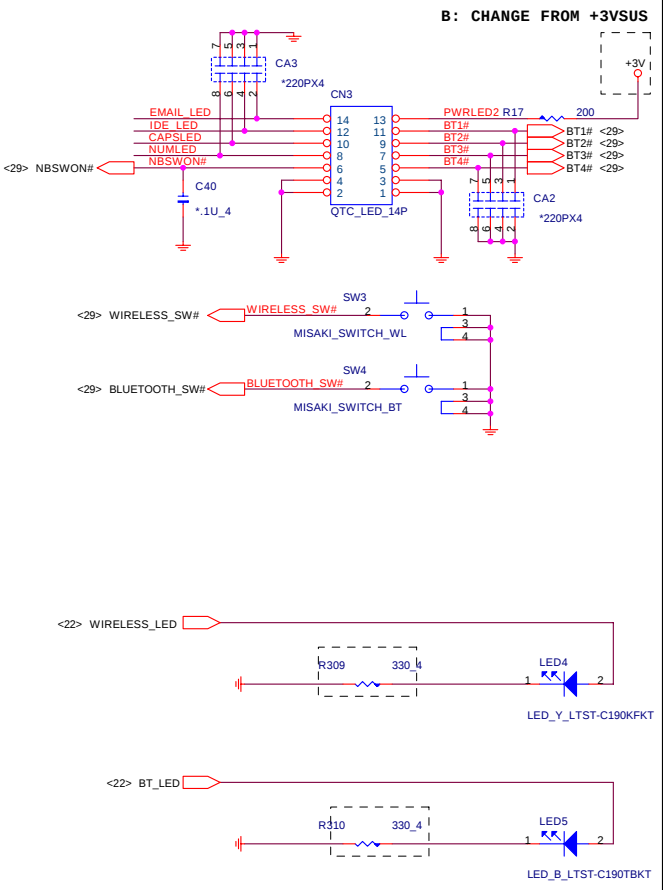
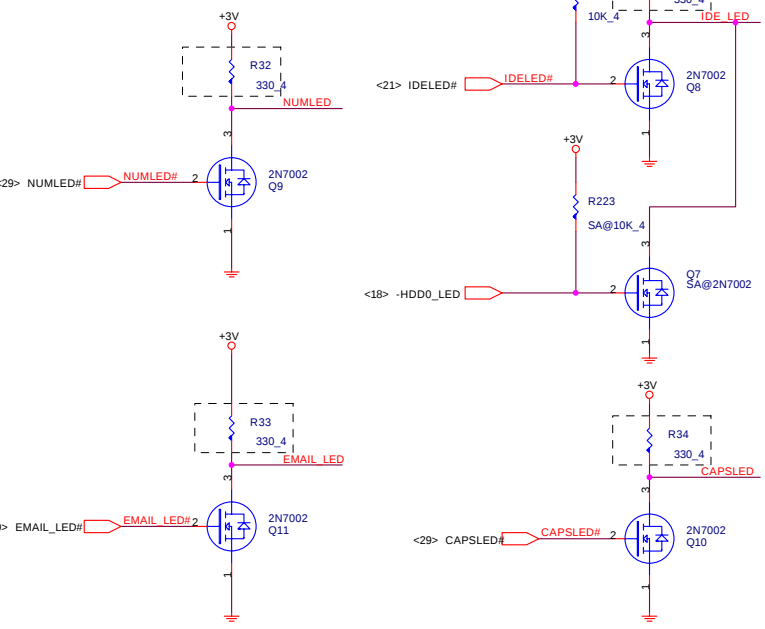


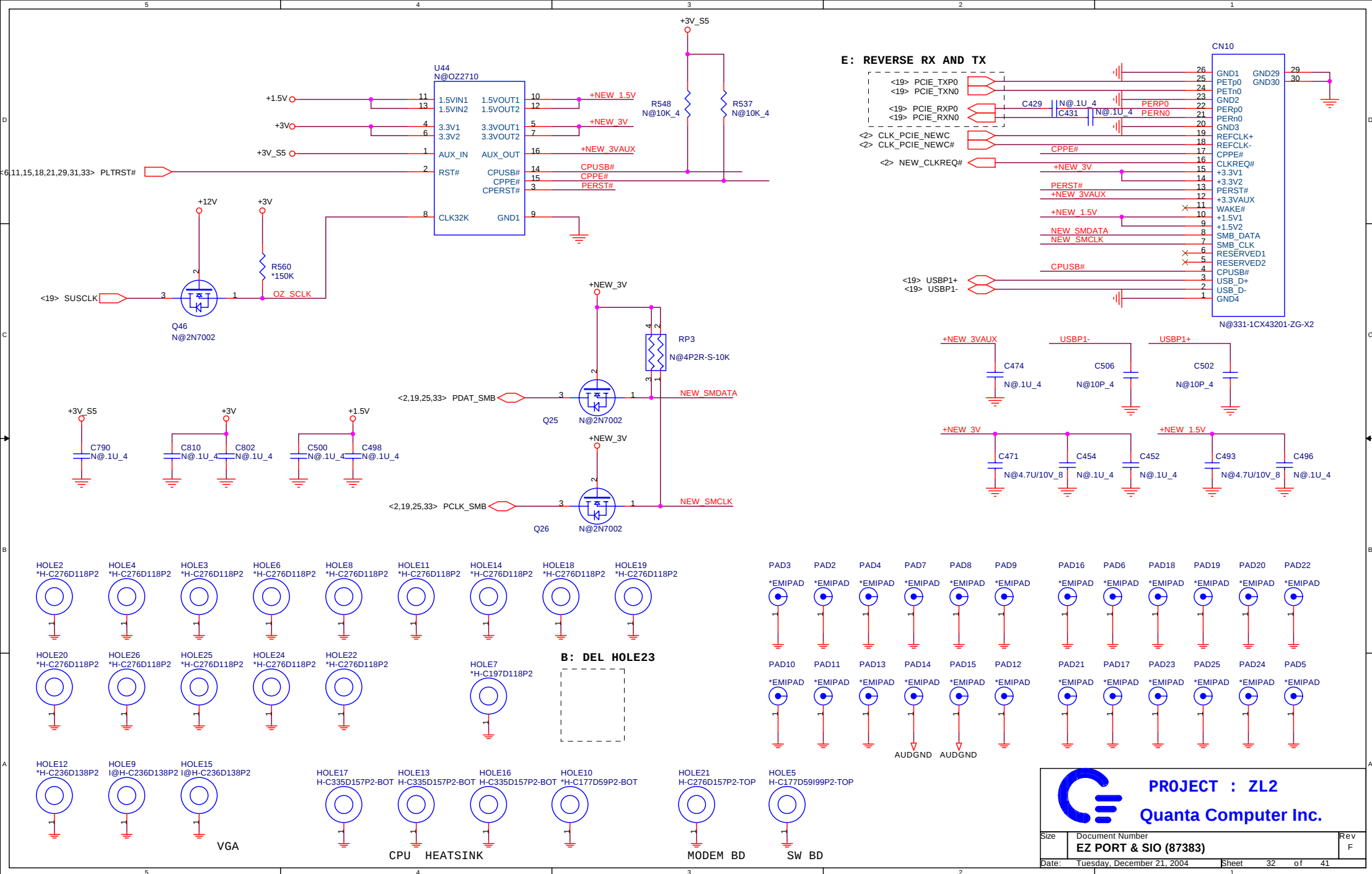
INTERNAL PULLUP IN SB

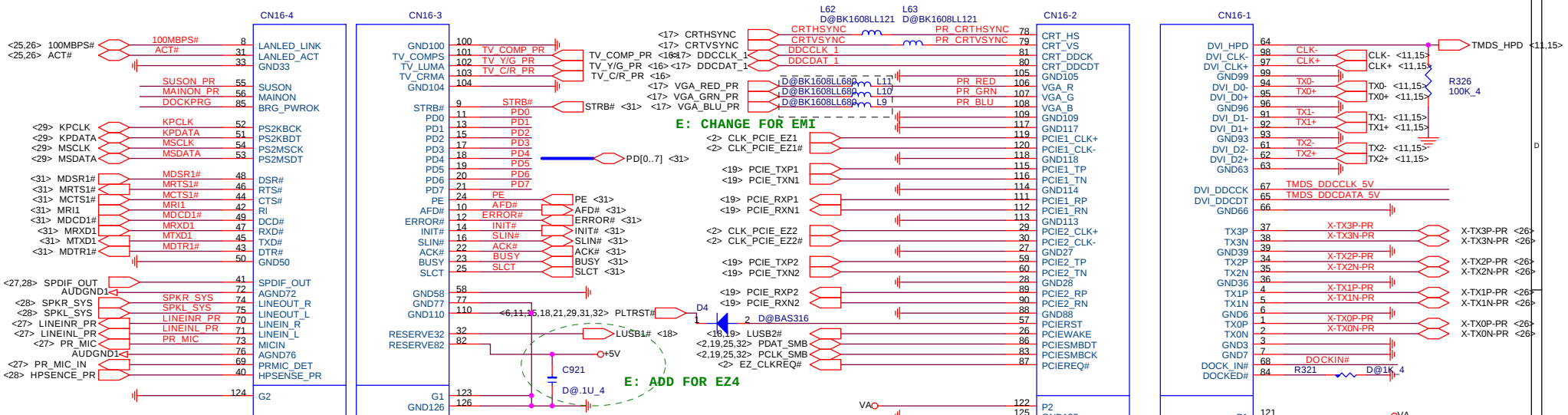
DE> LAN PME#



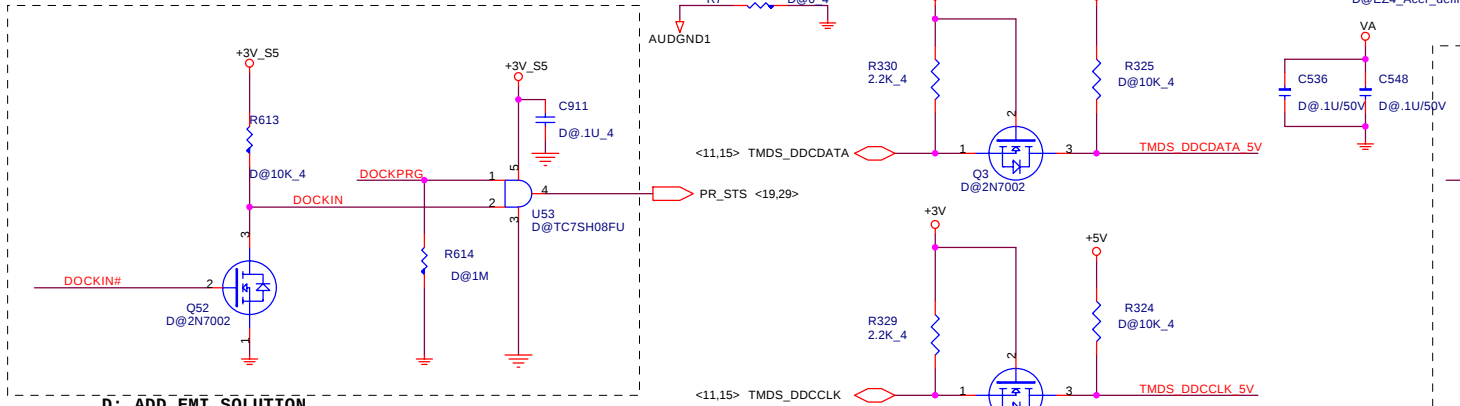
B CHANGE LED RESISTORS FROM 200 TO 680 OHM



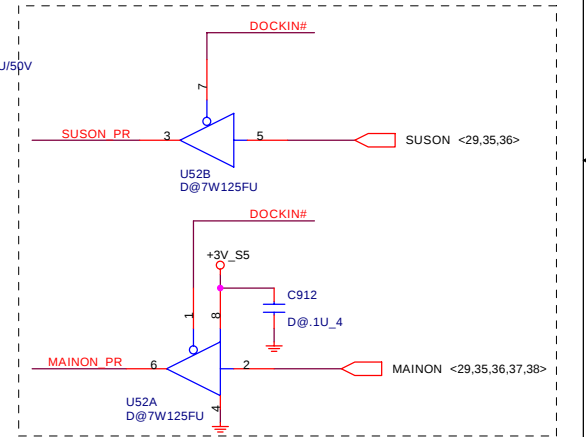




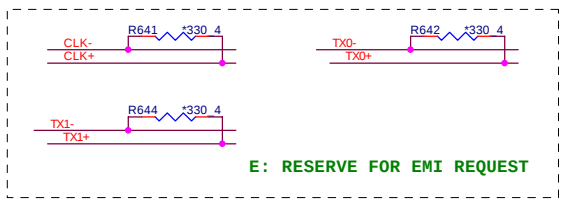
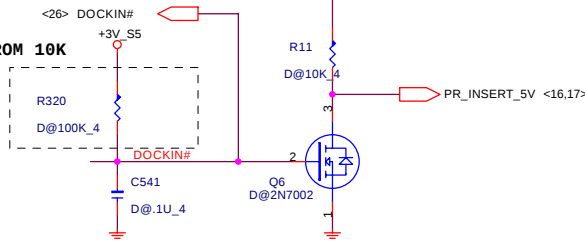
B: DOCKING CIRCUITS MODIFIED



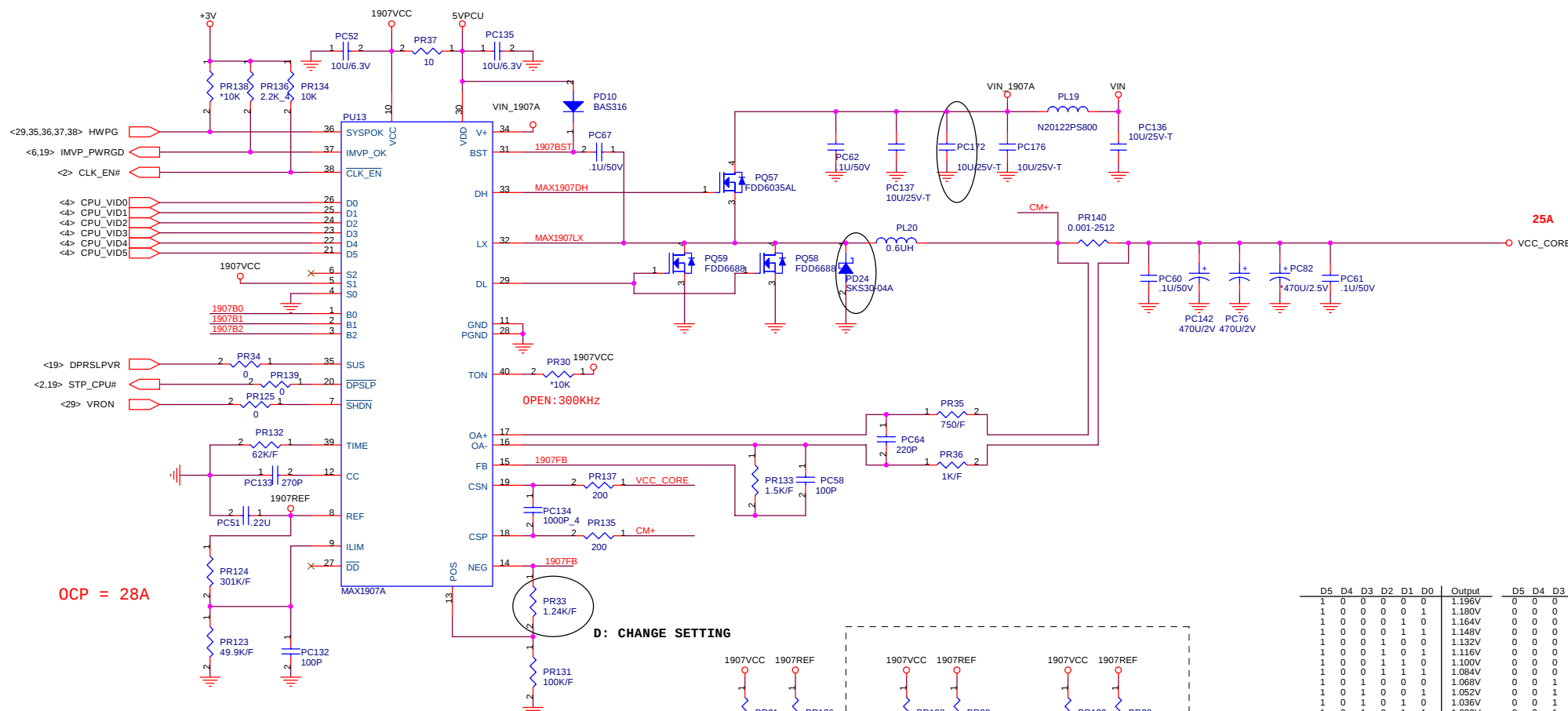
B: DOCKING CIRCUITS MODIFIED



B: CHANGED FROM 10K



E: RESERVE FOR EMI REQUEST



D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	1	0	1	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	0	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	1	0	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	1	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	0	1	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	0	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V

SUSPEND MODE (SUS=HIGH)			
S2	S1	S0	Output
✓ OPEN	VCC	GND	0.748V

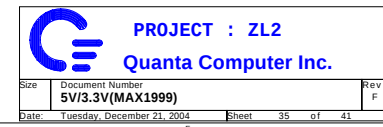
VCC_BOOT			
B2	B1	B0	Output
GND	GND	GND	1.708V
REF	REF	REF	1.372V
OPEN	OPEN	OPEN	1.036V
VCC	VCC	VCC	0.700V
REF	VCC	VCC	1.212V

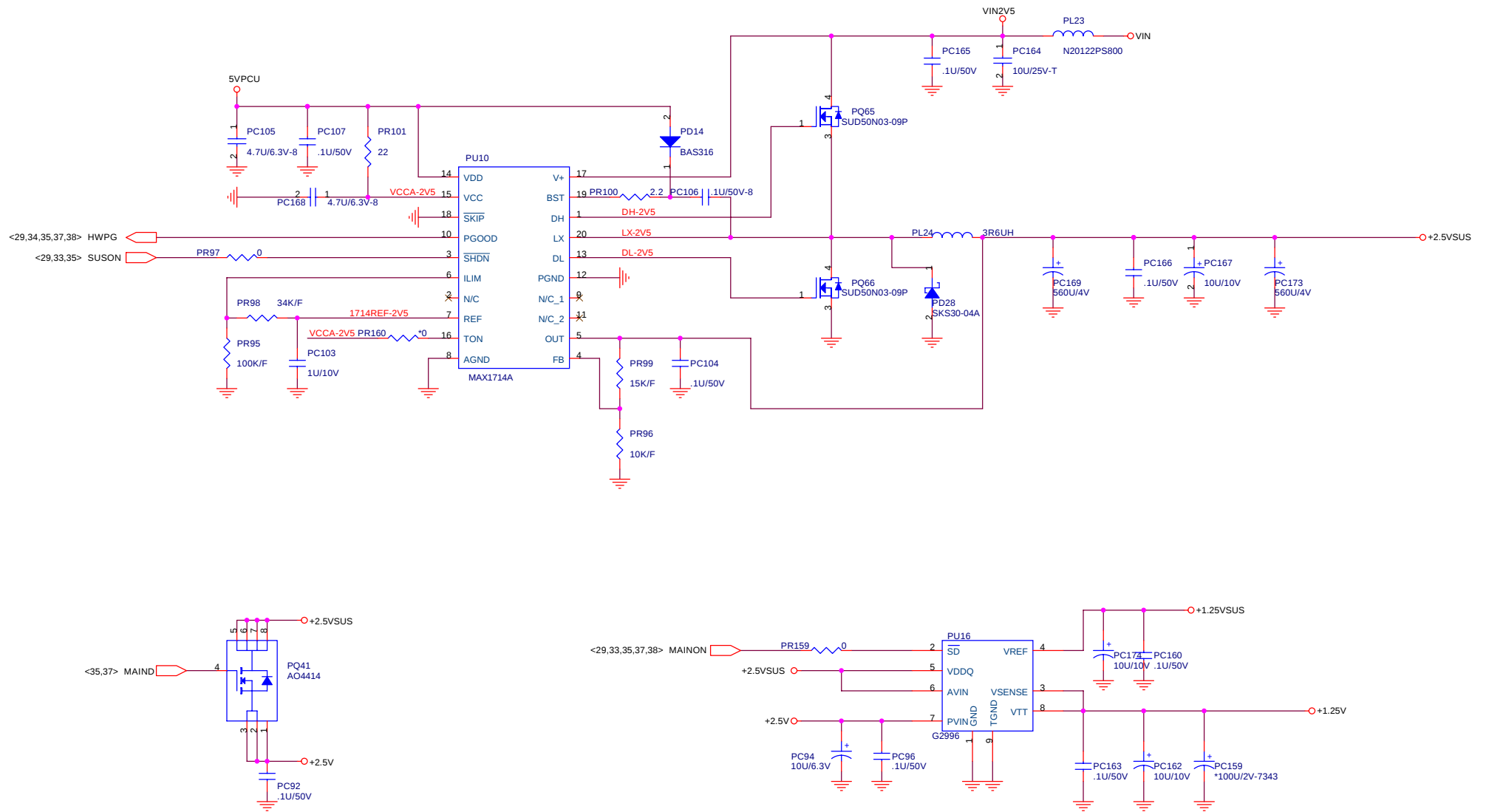


PROJECT : ZL2

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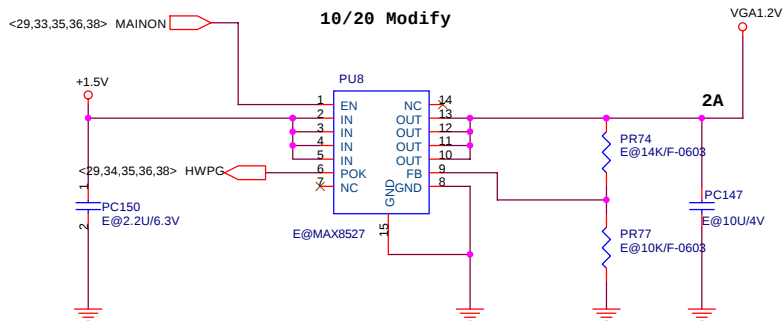
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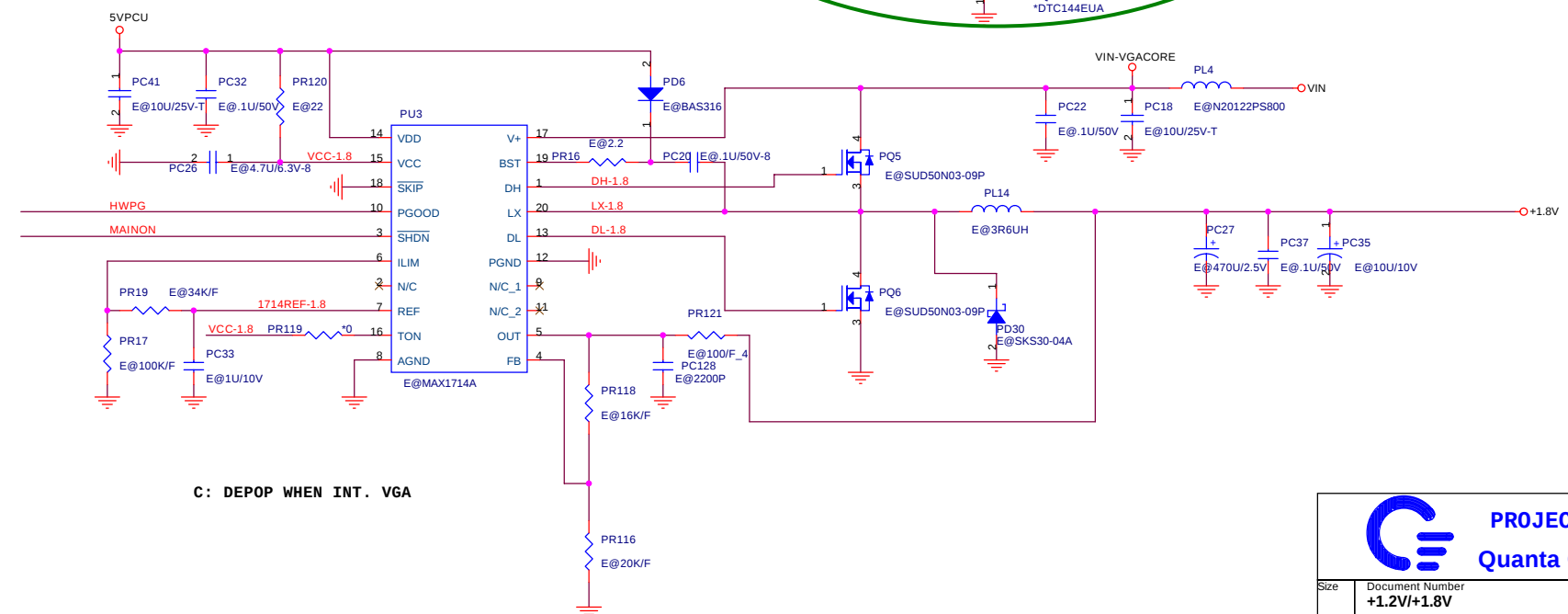
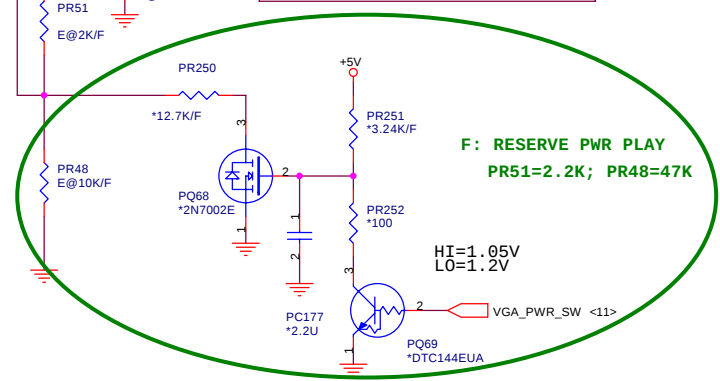
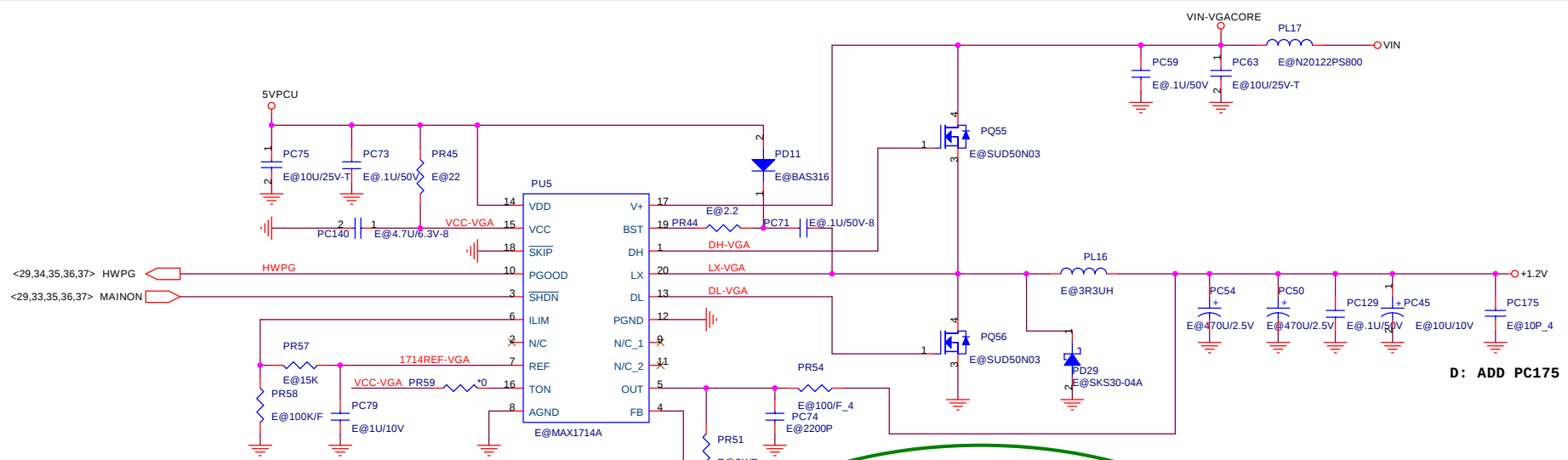


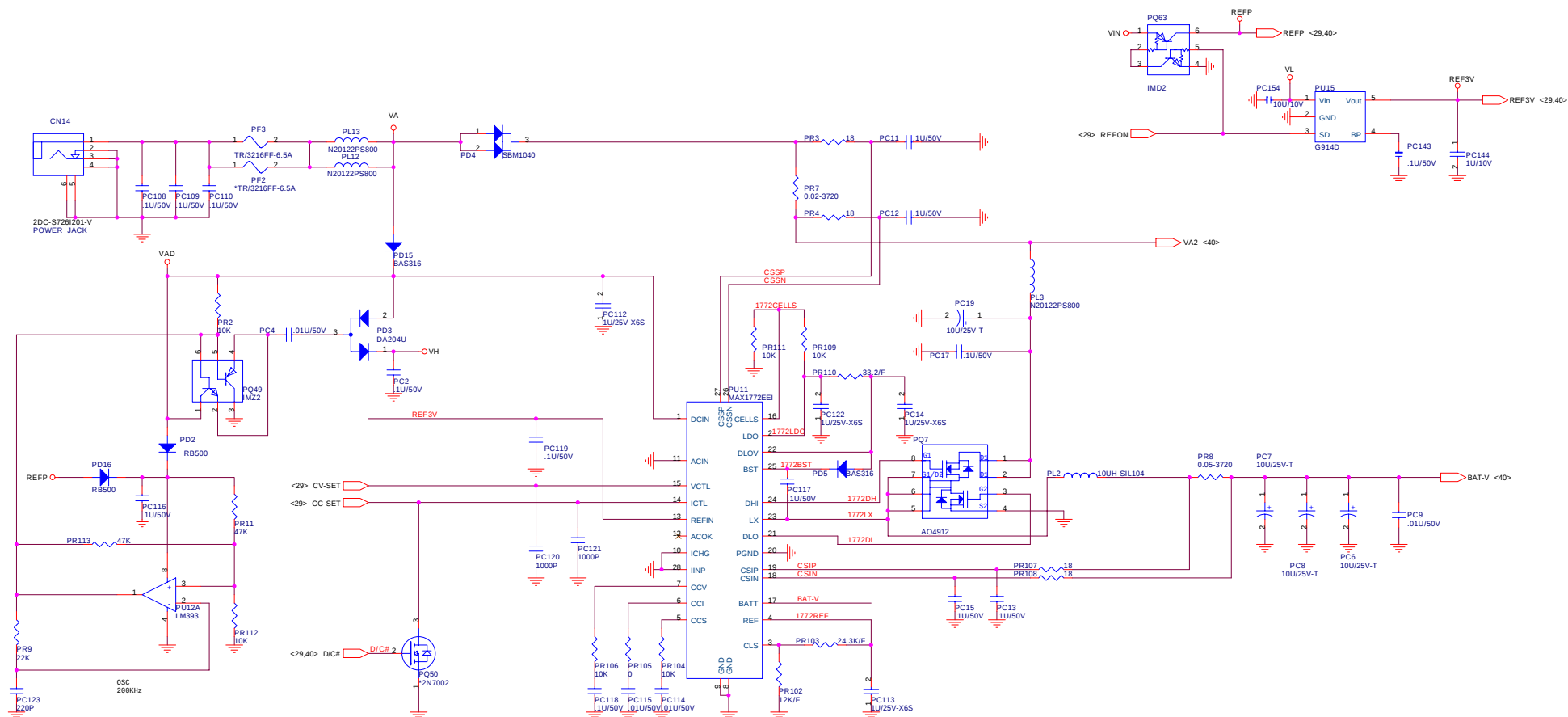


PROJECT : ZL2
Quanta Computer Inc.

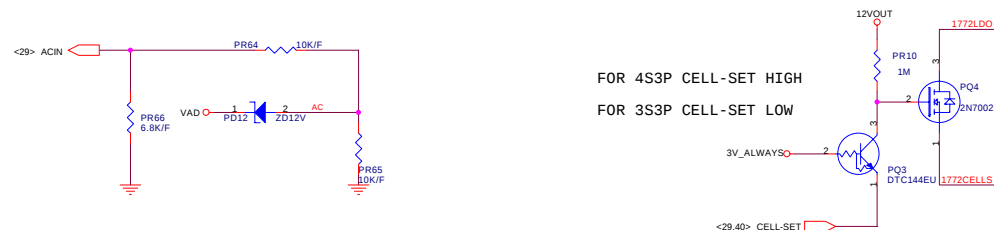
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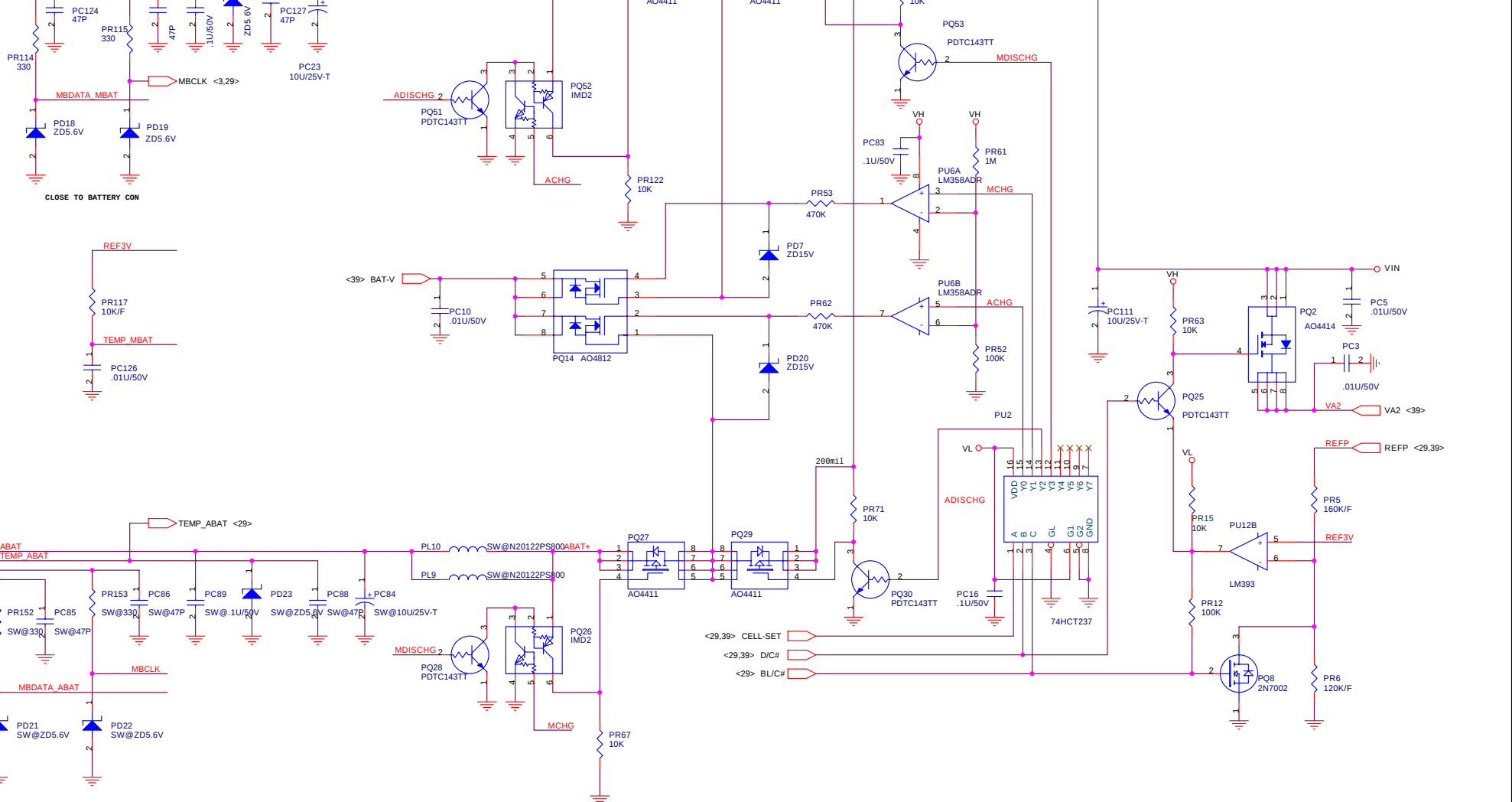




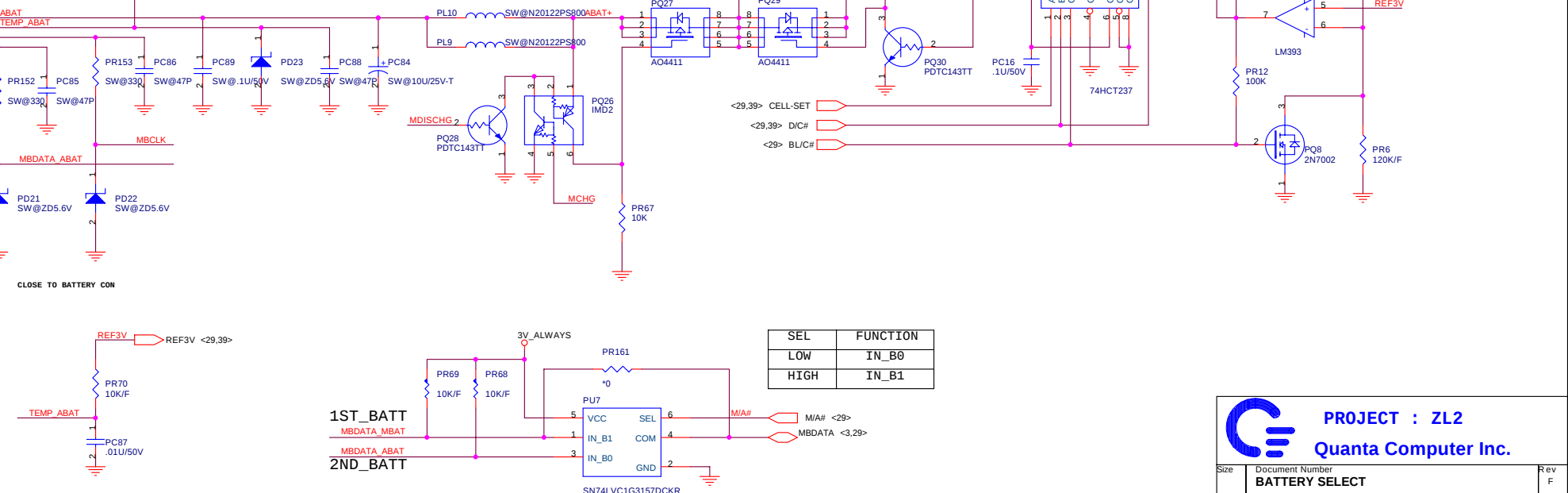


FOR 120W 6.2A





	PR71	ADISCHG	VD	>>>>>>	PR5 1608/E



SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

1ST_BATT
MBDATA_MBAT
MBDATA_ABAT
 2ND_BATT

SN74LVC1G3157DCK



MODEL :	REV:	CHANGE LIST:	PAGE	FROM	TO										
ZL2 MotherBoard	B	PAGE2 1. CHANGE FREQ. SETTINGS FOR DOTHANB 2. UNSTUFF SSC COMPONENTS 3. UNSTUFF COMPONENTS FOR DIFFERENT SKUS	1												
		PAGE3 1. REMOVE R449, PULL HIGH AT POWER SIDE 2. UNSTUFF ITP COMPONENTS 3. STUFF R441 FOR THEMTRIP#	2												
		PAGE4 1. STUFF R432, R433 FOR AUTO-SELECT	3												
		PAGE5 1. STUFF R162 FOR DOTHAN-B	4												
		PAGE6 1. STUFF R147, R148, R151, R143, R152, R159 ALWAYS 2. NOT STUFF DVO COMPONENTS WHEN NO DOCKING	5												
		PAGE8 1. NOT STUFF FILTER COMPONENTS WHEN EXT. VGA 2. CHANGE D10, D11 TO CH551													
		PAGE9 1. STUFF R76, PCIE TESTIN PULL LOW 2. STUFF R369, GPIO0 PULL HIGH 3. CHANGE CLK OUTPUT TO XTALIN 4. NOT STUFF DVI COMPONENTS WHEN NO DOCKING													
		PAGE12 1. ADD 220UF IN VGA1.2V													
		PAGE15 1. NOT STUFF R60, R63 WHEN NO DVO DEVICE 2. NOT STUFF DVO COMPONENTS WHEN NO DOCKING													
		PAGE16 1. CHANGE TV-OUT, LC VALUES													
		PAGE18 1. ADD DAMPING ON LFRAME# FOR AUDIO NOISE 2. STUFF R469 AND UNSTUFF R467 FOR DOTHAN-B 3. NOT STUFF COMPONENTS FOR SATA WHEN NO SATA 4. NOT STUFF AC TERMINATION FOR PCLK_1CH													
		PAGE19 1. NOT STUFF PCIE COMPONENTS WHEN NO PCIE DEVICES 2. CHANGE EMAIL LED GPIO 3. CHANGE MB_ID SETTING													
		PAGE21 1. REMOVE R50 2. CHANGE Q15 FROM BJT TO 2N7002	7												
		PAGE23 1. REMOVE RING FUNCTION 1. REMOVE 1394 CHOKE PADS	8												
		PAGE24 1. CHANGE 3IN1 CONNECTOR	9												
		PAGE25 1. CHANGE LED CONNECTION FOR 1G LAN 2. CORRECT U42 PIN.B11 PIN.C11 SHORT 3. CHANGE C741 AND C792'S SIZE FOR ME	10												
		PAGE26 1. UNINSTALL C210, C184, R87, R99 WHEN 10-100	11												
		PAGE27 1. INSERT 4700P IN BEEP SIGNALS 2. REMOVE SPK_PR FROM CODEC 3. CHANGE C863 TO 10U 4. REVERSE MIC-SELECT 5. CHANGE R272 TO 0 OHM	12												
		PAGE28 1. CHANGE CONNECTION FOR SPKL-R TO EZ4 2. ADD SPRINGS FOR MODEM CABLE	13												
		PAGE29 1. CHANGE PR INSERT# TO PR STS	14												
		PAGE30 1. CHANGE KB AND TP'S CONNECTOR 2. CHANGE LED CIRCUITS	15												
		PAGE31 1. REMOVE C889 2. STUFF AC TERMINATIONS FOR 14M_SIO	16												
		PAGE33 1. MODIFY EZ4 INTERFACE	17												
		PAGE 35 1. INCREASE CAPACITOR PC171 NEAR PR422. CHANGE COMPONENT PR38 SERIAL NUMBER FROM 0603 TO 1206 3. TAKE OFF PR39 PR43 PQ21 AND CHANGE NET NAME TO MAX6648_OV#	18												
		PAGE 35 3. INCREASE CAPACITOR C913 4. ADD DISCHARGE FOR VGA1.2V	19												
		PAGE 40 1. INCREASE RESISTOR PR161 NEAR PU7	20												
		PAGE 22 1. REMOVE CHOKE PADS	21												
		PAGE 39 1. TAKE OFF PQ50	22												
		PAGE 37 1. CHANGE PU8 NET NAME TO +2.5V	23												
		PAGE 17 1. CHANGE HSYNC& VSYNC'S BEADS TO 0 OHM	24												
		C	DA0ZL2MB8C3		25										
			PAGE2 1. ADD PULLUPS ON CLKREQ PINS		26										
			PAGE3 1. ADD THERMAL SHUTDOWN CIURCUITS		27										
			PAGE13 1. CHANGE OPTIONS TO HYNIX MEMORY		28										
			PAGE16 1. DEPOP C558		29										
PAGE17 1. CHANGE LC VALUES FOR RGB			30												
PAGE16, 17, 26 ADD 0 OHM RESISTORS TO SUBSTITUTE SWITCHES WHEN NO DOCKING			31												
PAGE25 1. CHANGE R531 TO 0 OHM 1. CHANGE R519 TO 1.2K/F			32												
PAGE27 1. CHANGE C512, C511 TO .7UF 1. CHANGE R592 TO 100K			33												
PAGE28 1. DEPOP Q28			34												
PAGE29 1. CHANGE BATLED0.1# PINS TO IOPJ6,7			35												
PAGE24 1. CHANGE 3-IN-1 CONNECTOR															
PAGE 34 1. INCREASE CAPACITOR PC172 10U/25V IN VIN 1907A 2. INCREASE SCHOTTKY DIODE PD24 SKS30-04A IN MAX1907LX .															
PAGE 35 1. INCREASE ZENER DIODE PD25 ZD5.6V SERIES WITH VIN1999 AND PR249 2. CHANGE NET NAME TO 1999_CHT#															
PAGE 36 1. CHANGE MOSFET SUD50N03-09P TO PQ65 PQ66 2. INCREASE CAPACITOR PC173 560U/4V IN +2.5VSUS 3. INCREASE CAPACITOR PC174 10U/10V IN +1.25VSUS 4. INSERT PR250 BETWEEN PU16 PIN2 AND PIN 5 .															
PAGE 37 1. CHANGE MOSFET SUD50N03-09P TO PQ34 PQ47 AND PQ35 2. TAKE OFF JUMP 3.CHANGE CAPACITOR PC155 PC146 TO 560U/4V															
PAGE 38 1. CHANGE MOSFET SUD50N03-09P TO PQ5 PQ6 PQ55 AND PQ56 2. TAKE OFF JUMP 3. CHANGE CHOKE PL16 TO 3R3UH 4.CHANGE CAPACITOR PC27 TO E@470U/2.5V															
PAGE 39 1. EXCHANGE NET NAME 3V_ALWAYS AND CELL-SET															
PAGE 23,25,29,31 1. DEPOP RC FILTERS ON PCI CLOCKS															
D	PAGE2 1. DEPOP R615 2. ADD 47P ON 14M_SIO		26												
	PAGE3 1. POP R449		27												
	PAGE6 1. DEPOP RP7		28												
	PAGE16 1. LID SW FOOTPRINT CHANGED		29												
	PAGE19 1. ADD MB_ID3		30												
	PAGE21 1. CHANGE C82'S RATING TO 25V		31												
	PAGE28 1. PHONE JACK CHANGED TO SPDIF		32												
	PAGE30 1. CHANGE R338 TO 100K		33												
	PAGE31 1. CHANGE R28 AND C41'S VALUE		34												
	PAGE33 1. ADD EMI CAPS		35												
	PAGE34 1. ADD CAP PC176														
	PAGE35 1. CHANGE CAP PC69 COMPONENT 2. CHANGE CAP PC72 CONNECT POINT 3. CHANGE RESISTOR PR147 COMPONENT														
	E	PAGE2 Change R200, R202, C915 value to pass EA	PAGE18 Change GPIO pin define, add R645												
		PAGE3,30 DEPOP COMPONENTS FOR MAX6648_OV#	PAGE20 D25 change to CH551												
		PAGE6 1. DEPOP R186, R184	PAGE31 Change C41 to 6pF												
PAGE8 Add C918 to solve TV issue		PAGE33 Modify EZ4 pin define, add R641, 642, 644 for EMI													
PAGE12 Add C919, 920, change L25, 27, 67															
PAGE16 1. ADD LEVEL SHIFT FOR EDID															
PAGE16 Change R8, R9, remove C9, C32 for pass Acer LCD															
PAGE19 ADD 100K PULLLOW ON DPRSLPVR, change C428															
PAGE21 1. CHANGE SWAP-ODD RESET															
PAGE23 1. ADD PULL-LOW ON PCMSPK															
PAGE24 1. ADD 330HM CURRENT LIMIT ON VCC_XD															
PAGE32 1. REVERSE RX AND TX															
		PAGE 35 1. CHANGE PC68 COMPONENT.													
		PAGE 36 1. CHANGE PR99 PD28 COMPONENT.													
		PAGE 37 1. CHANGE PD26 PD27 COMPONENT.													
	PAGE 38 1. CHANGE PR51 COMPONENT.														
	PAGE 39 1. CHANGE PQ7 COMPONENT.														
		2. CHANGE PR74 PR77 PU8 PC147 PC150 COMPONENT.													
		2. CHANGE PD29 PD30 COMPONENT.													
PROJECT : ZL2			APPROVE BY: SELMON LIU			DRAWING BY:JOE LIN			REV			COVER SHEET 1 OF 1			
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