

CPU CORE
SENTECH
SC451ITSTR
Page:23

SYSTEM 3V/5V
MAXIM
MAX1999
Page:24

+3VPCU
+3V_S5/+3VSUS
+3V
+5VPCU
+5VSUS
+5V

+15V

+1.8VSUS
+1.8V ON
NCP5214

+0.9VSUS
+0.9V ON
NCP5214

+1.5V SENTECH
SC1470

+1.5V_S5
SI9183-AD

+2.5V SENTECH
SC1565

+1.05V SENTECH
SC4215
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BATTERY CHARGER
MAXIM
MAX8724
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CLOCK GEN
ICS954206
Page:4

ZL8
CELERON-M/PENTIUM-M
INTEL Mobile_479 CPU
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DDR-II SODIMM1
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DDR-II SODIMM2
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NB
INTEL
ALVISO 915GM
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PATA HDD
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IDE-ODD
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ICH6-M
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ALC260 (ALC883)
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AMP
MAX9755
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MODEM
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KBC
NS
PC97551
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MIC IN
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SPEAKER
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LINE OUT
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RJ11
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Touchpad
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Keyboard
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FLASH
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FAN
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CRT
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LVDS
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(Option)
MINI-PCIE slot
Wireless LAN
Page: 16

TI PCMCIA
PCI1510A (L-F)
AD17
REQ1# / GNT1#
INTC#
Page: 18

TYPE II
SLOT
Page: 18

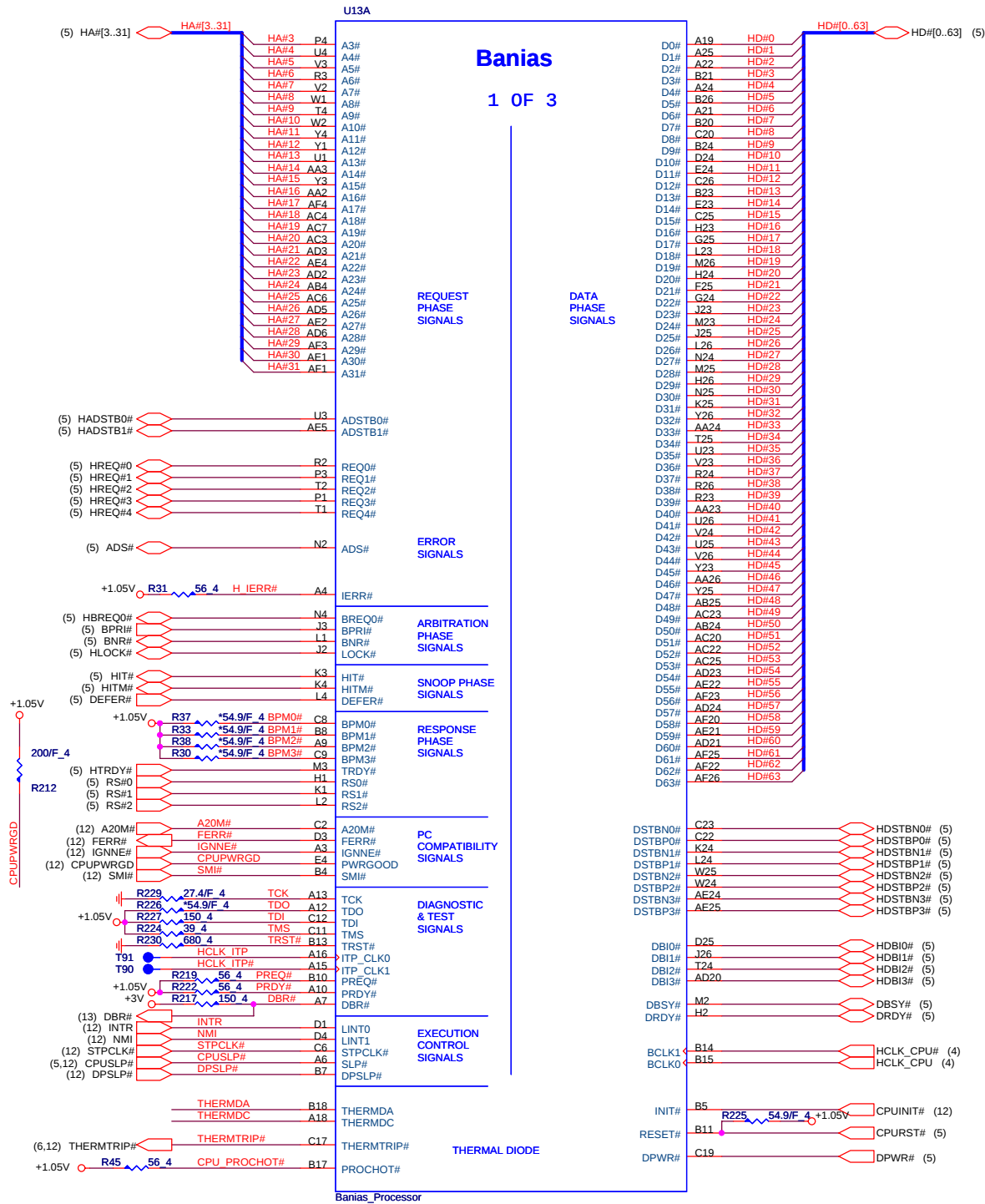
MINI-PCI
Wireless LAN
AD20
REQ2# / GNT2#
INTB# , INTD#
Page: 16

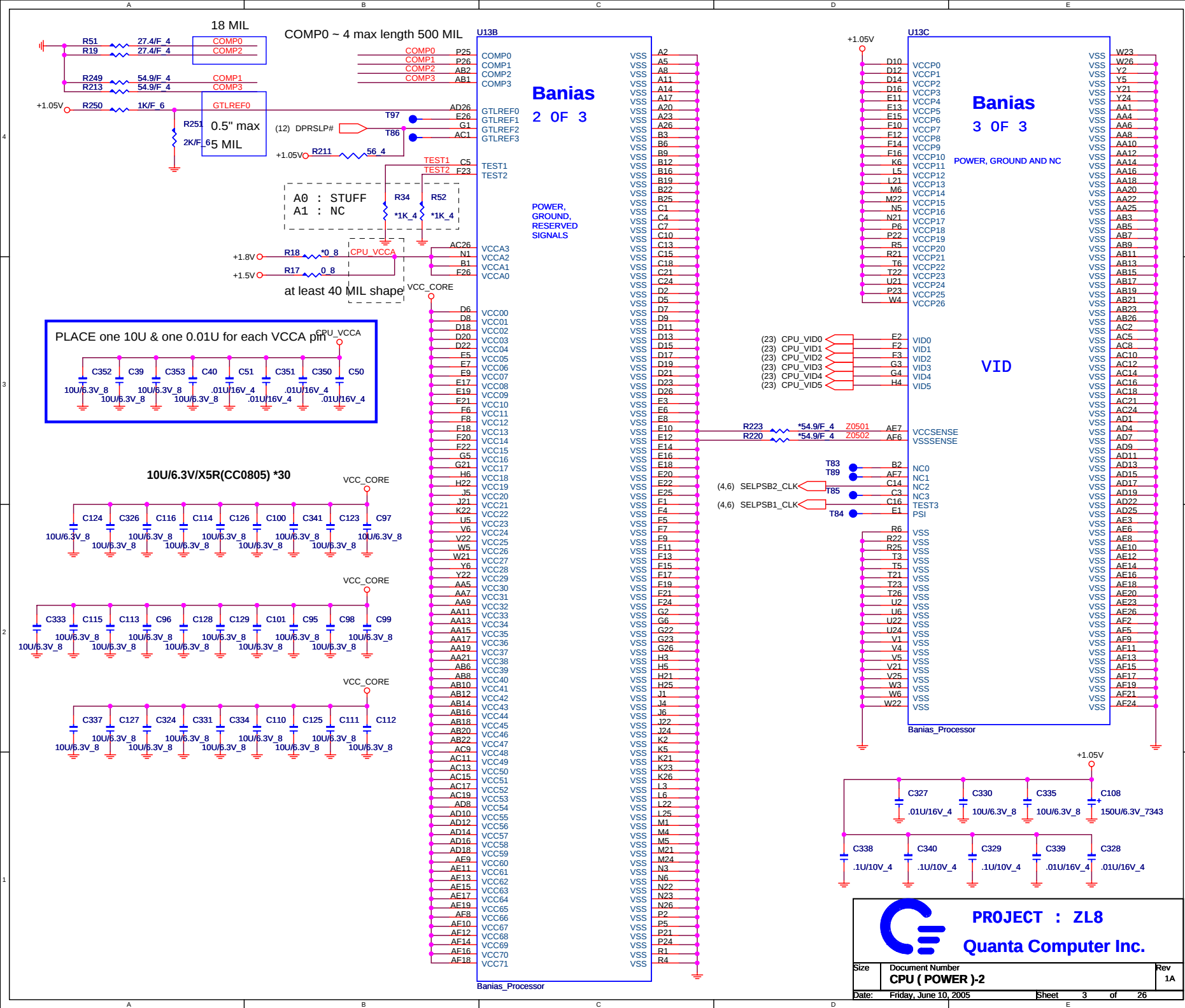
REALTEK
RTL8100CL
AD24
REQ0# / GNT0#
INTA#
Page: 17

RJ45
Page: 17

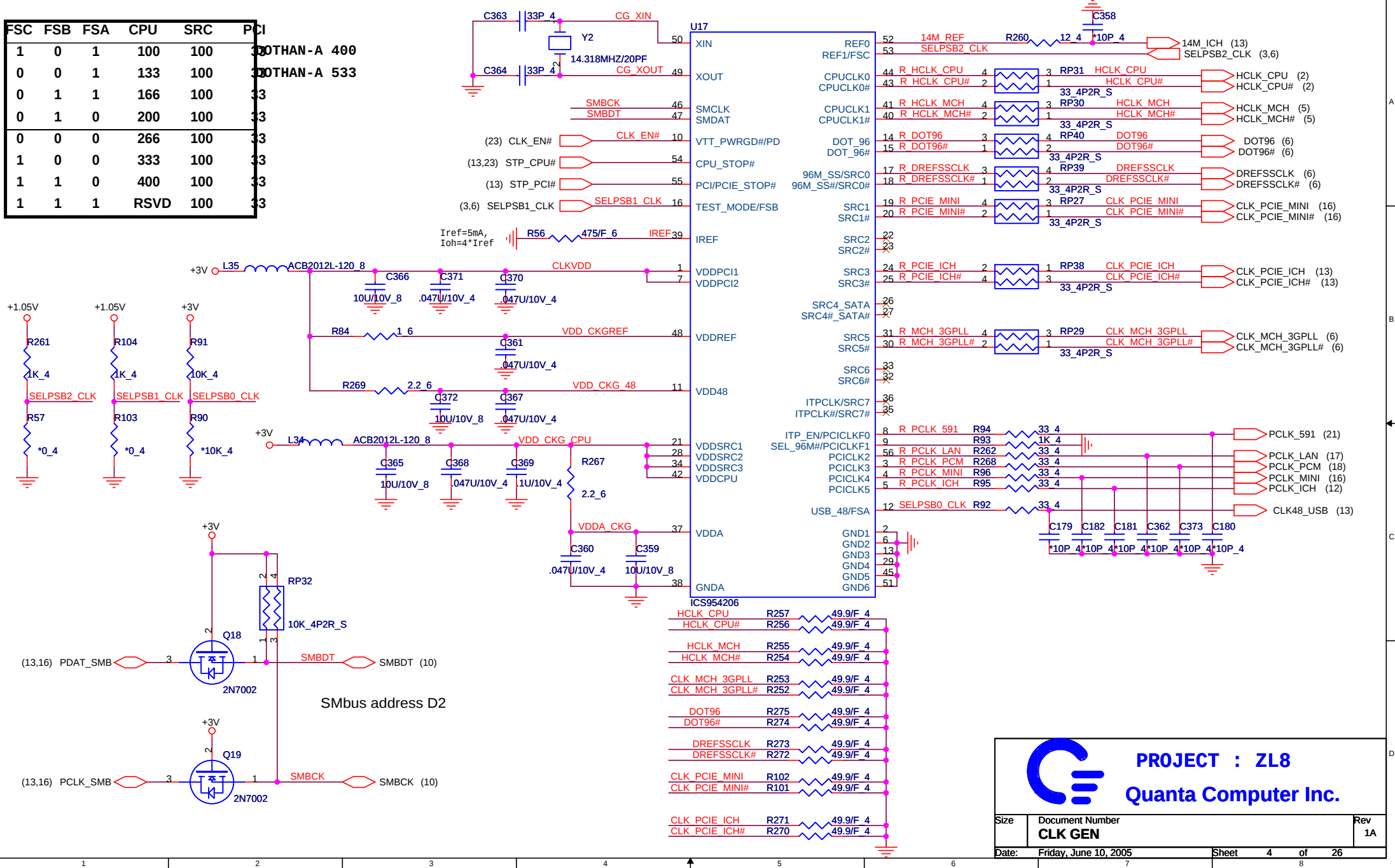
SYSTEM
USB PORT *3
USB2, 3, 5
Page:16

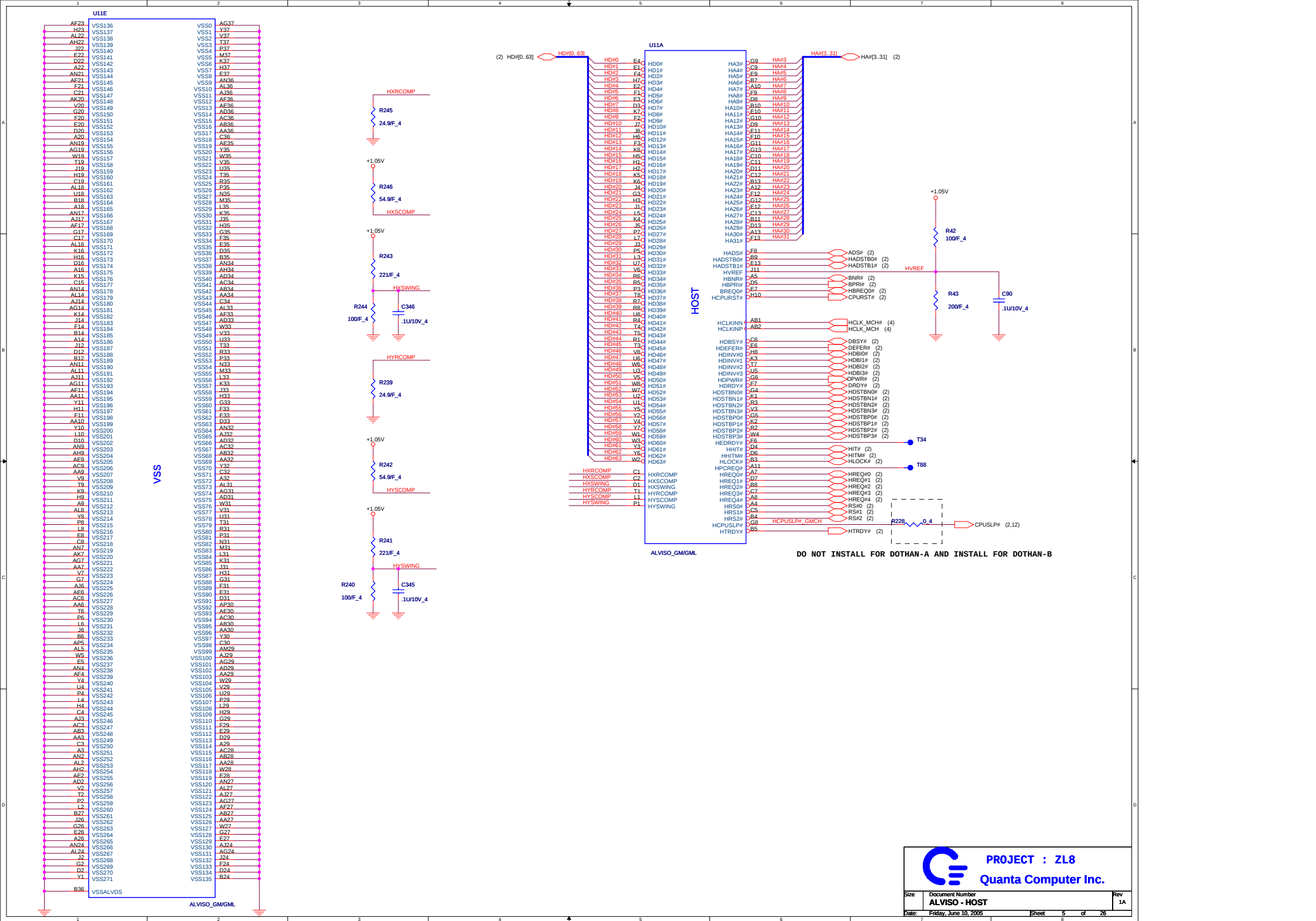
Bluetooth
USB
interface
USB4
Page:16

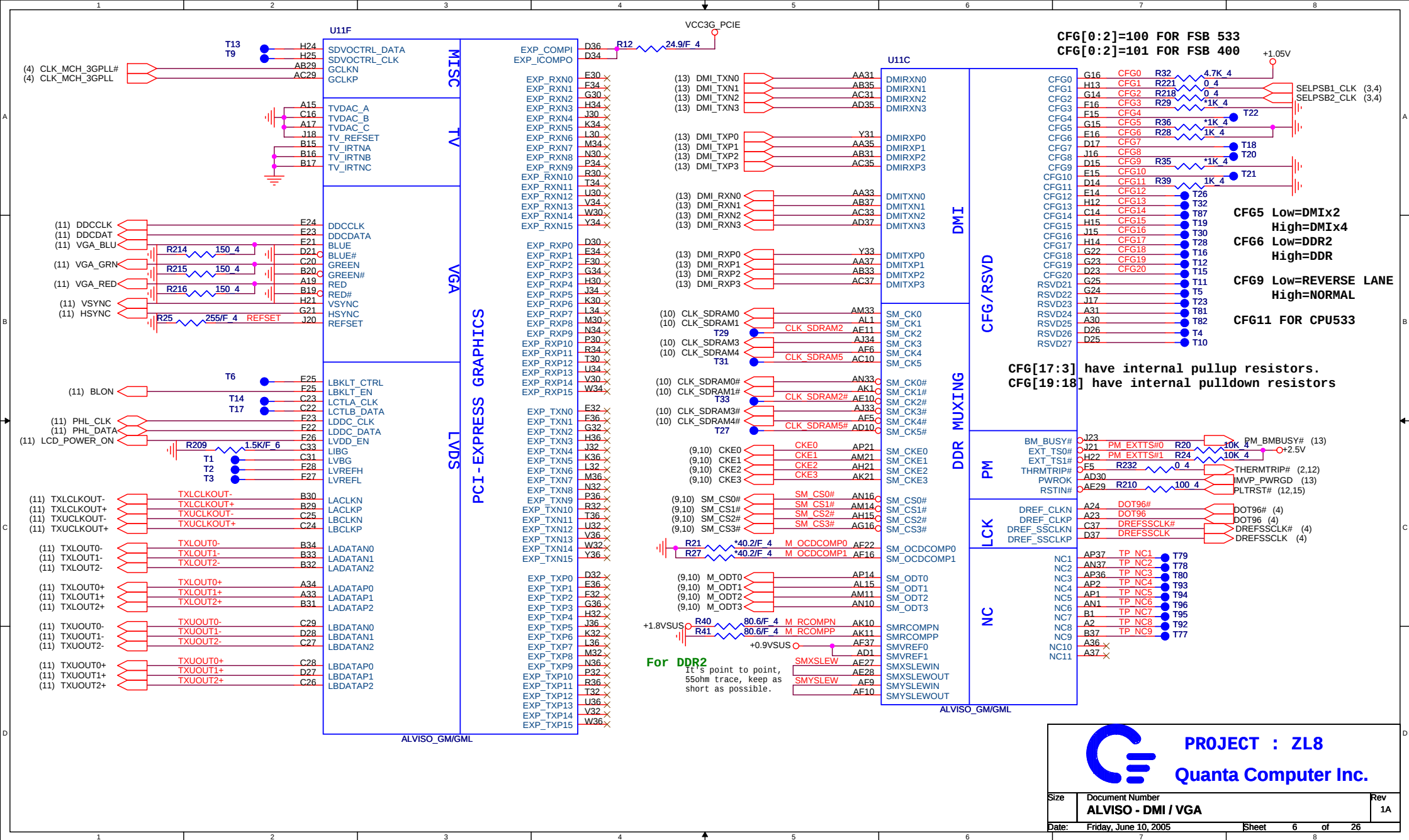


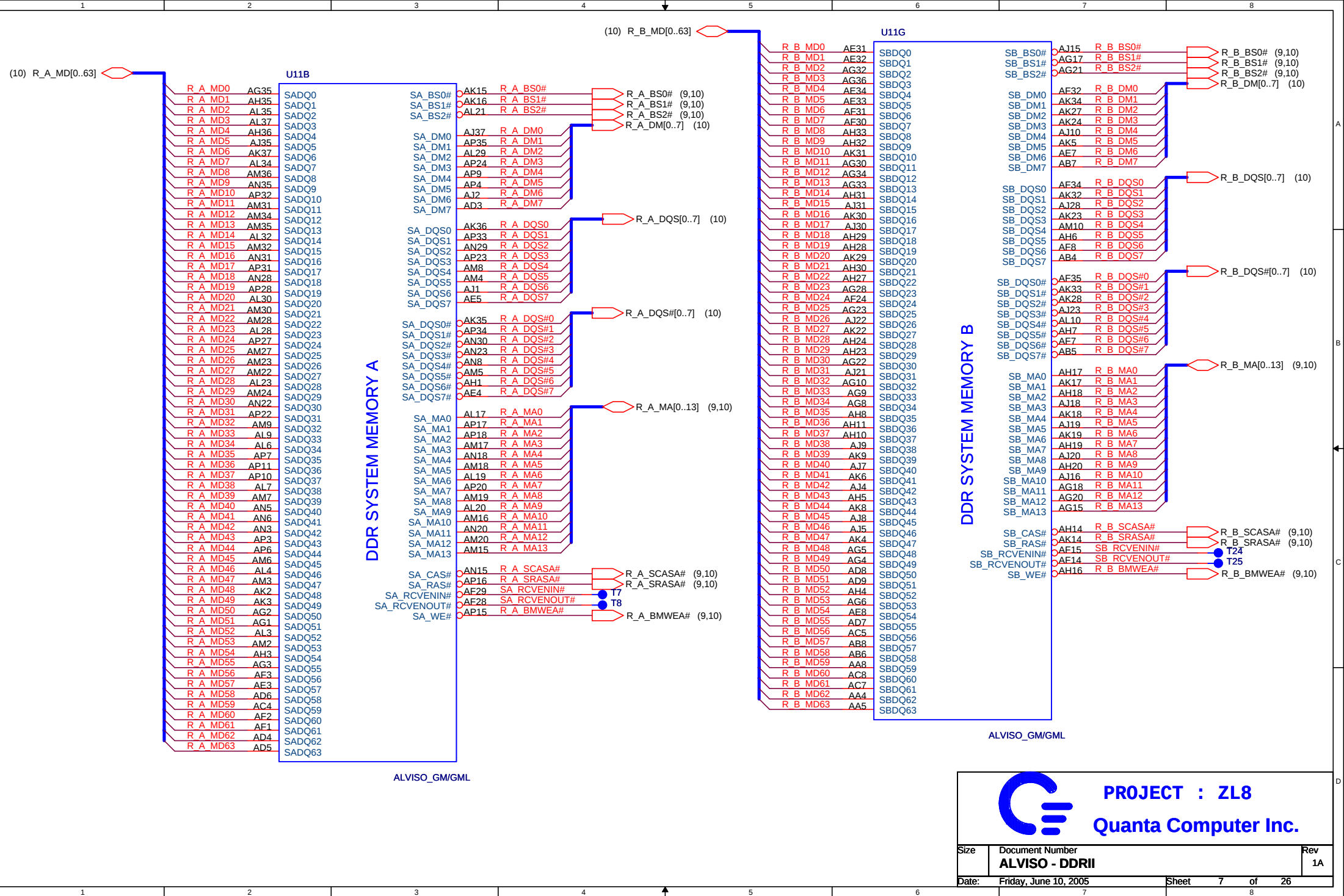


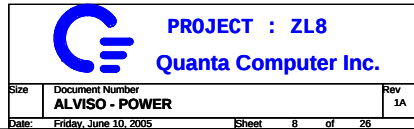
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	DOOTHAN-A 400
0	0	1	133	100	DOOTHAN-A 533
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

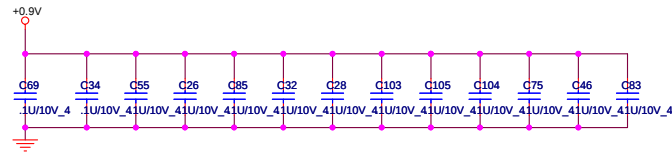




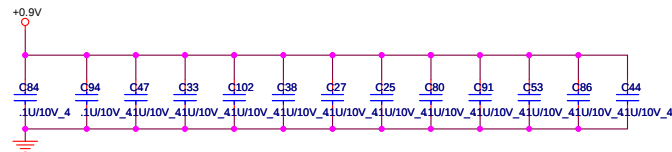




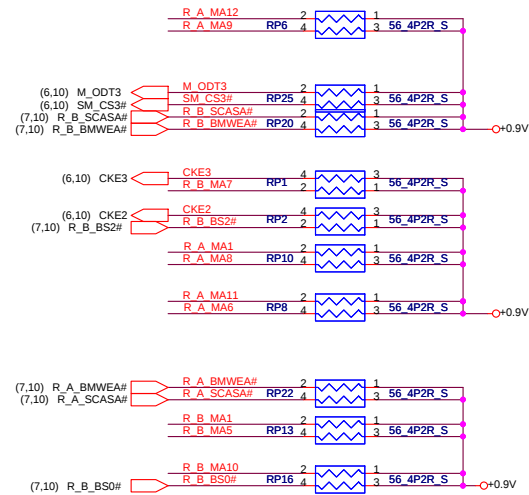
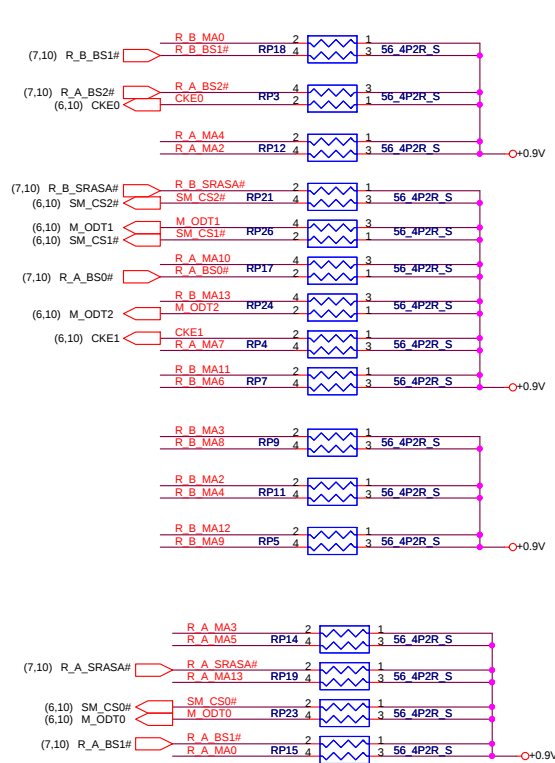




Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V

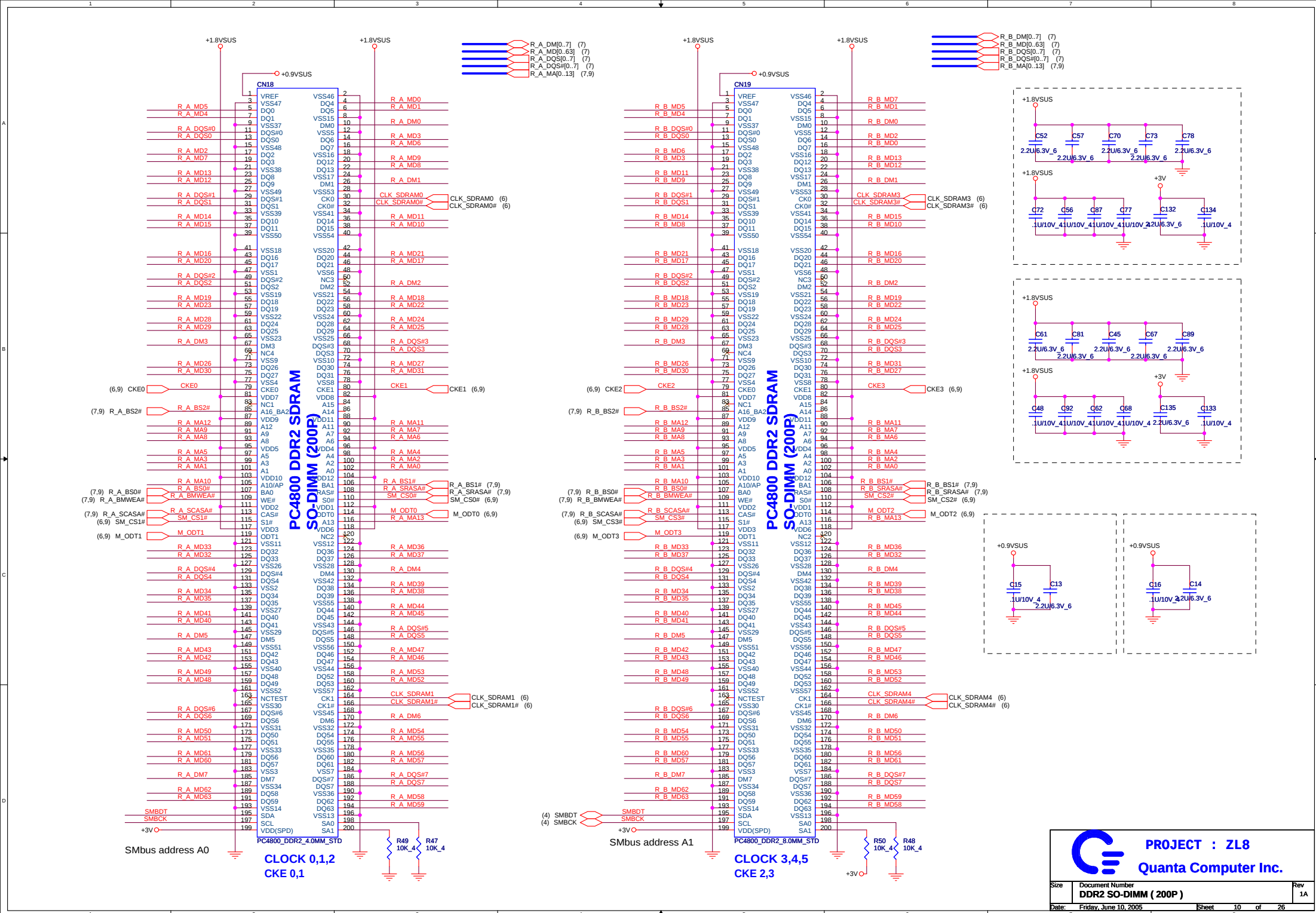


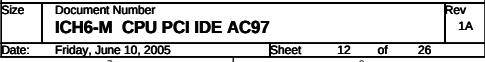
Layout note: Place one cap close to every 2 pullup resistors terminated to +0.9V

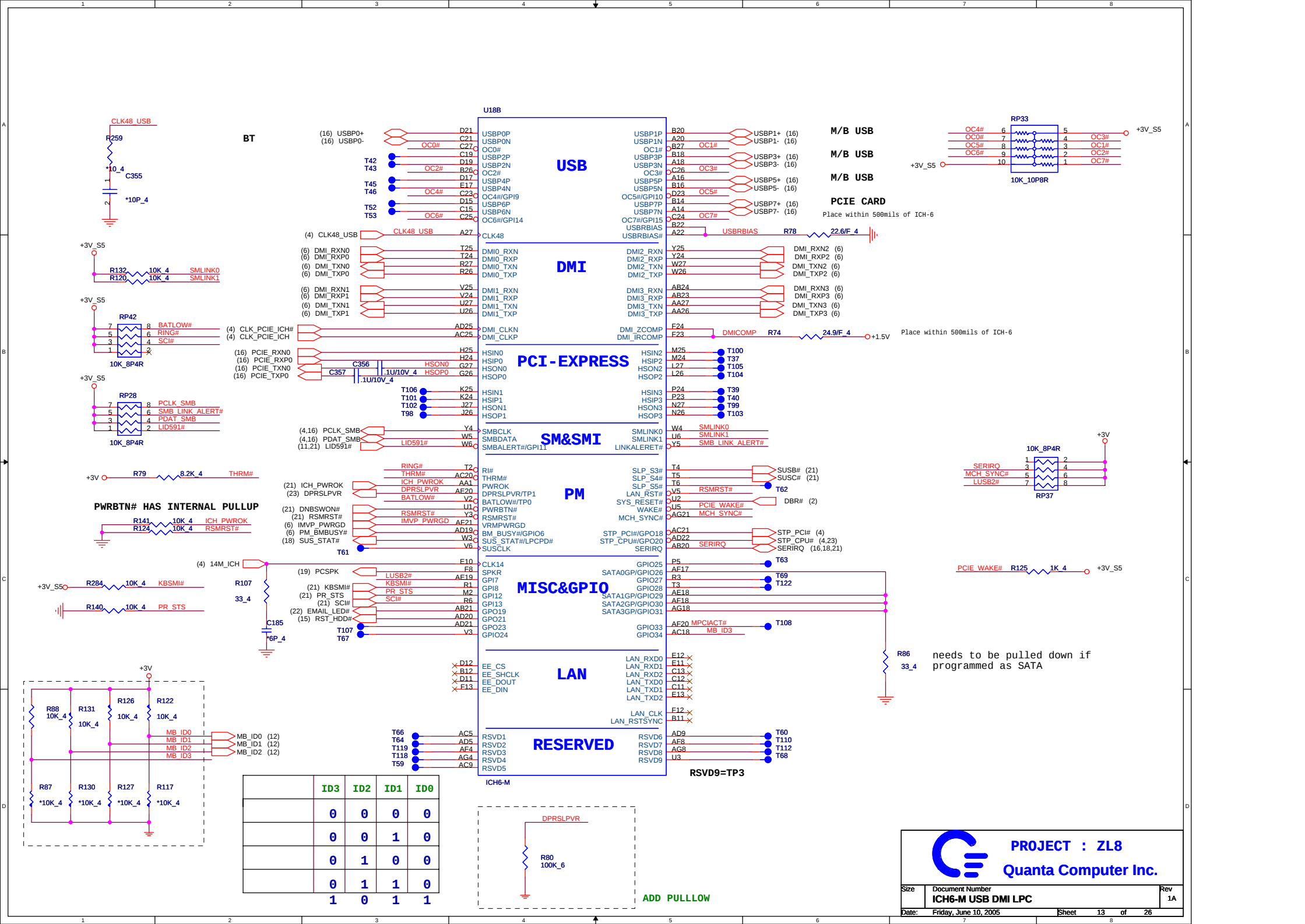


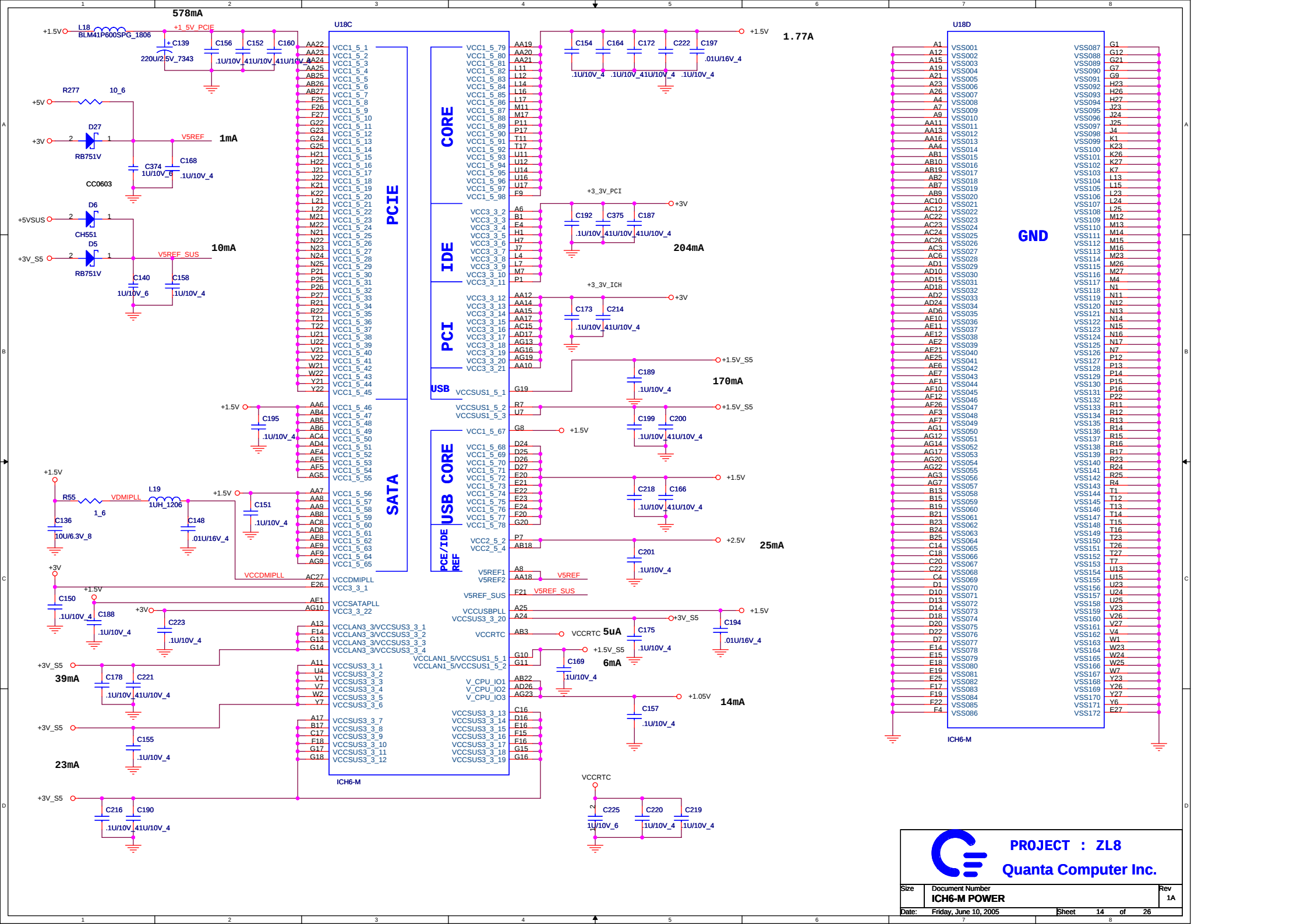
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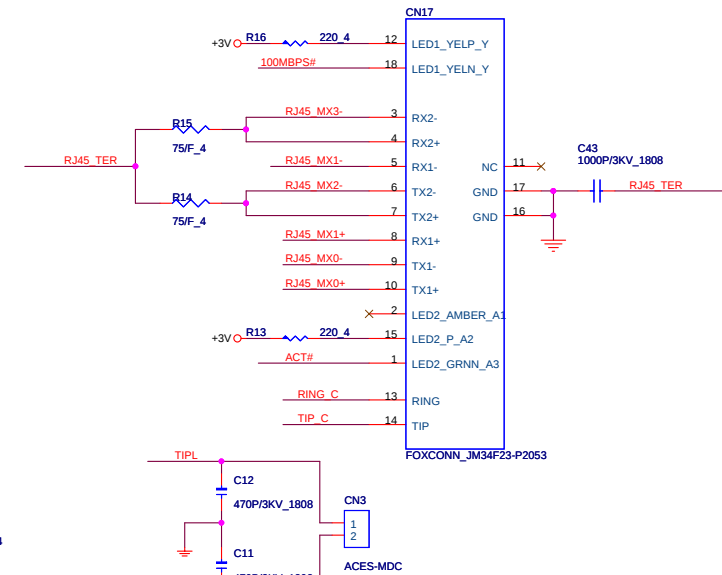
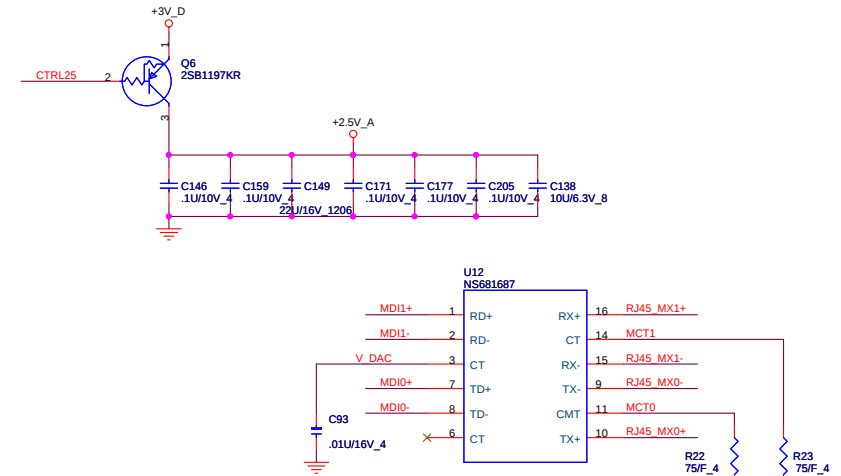
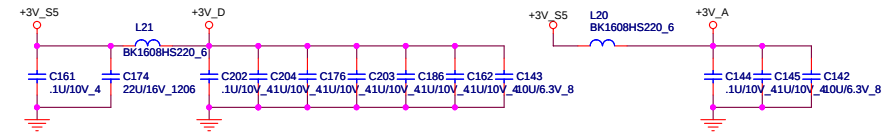
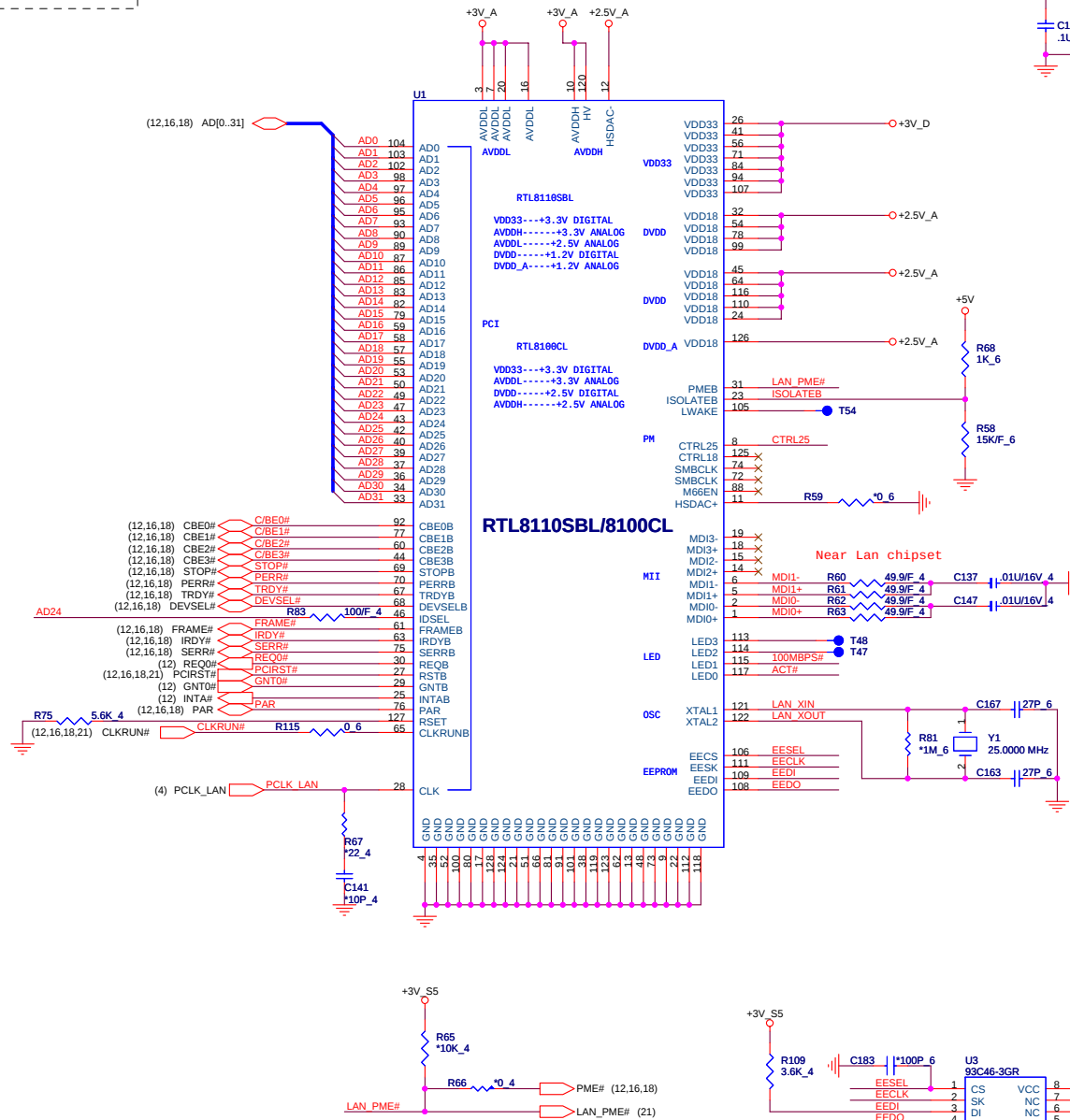








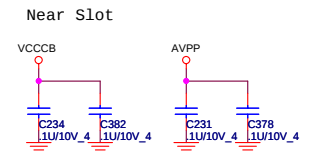
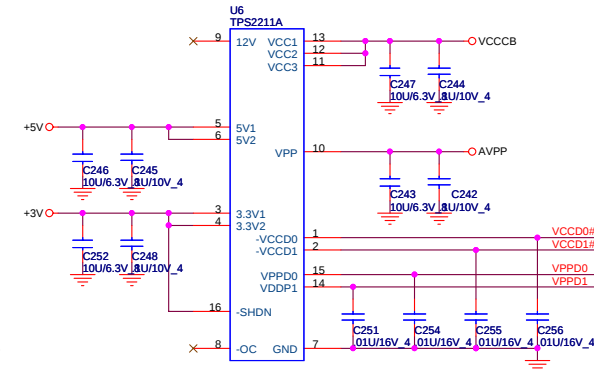
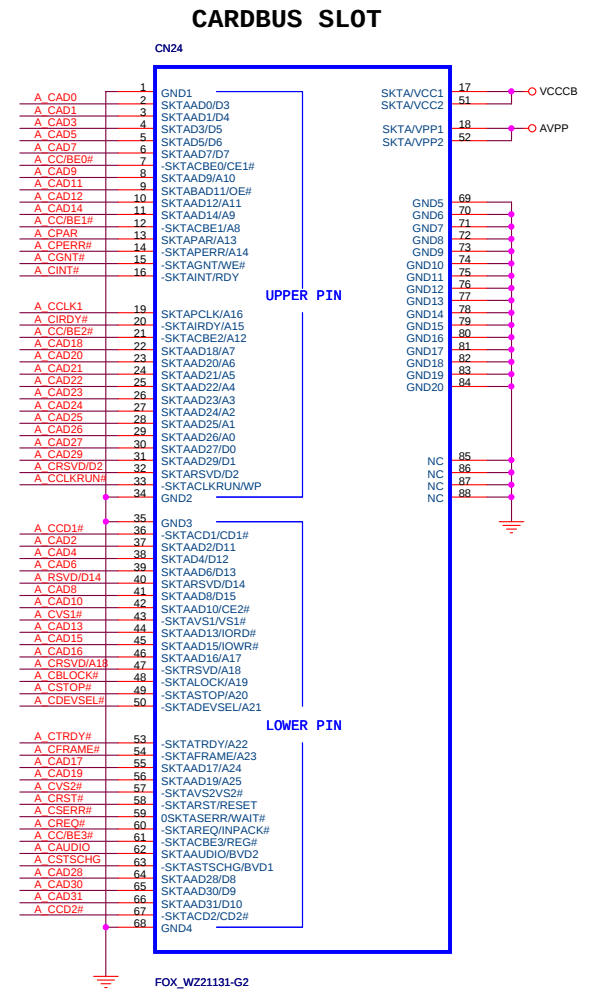
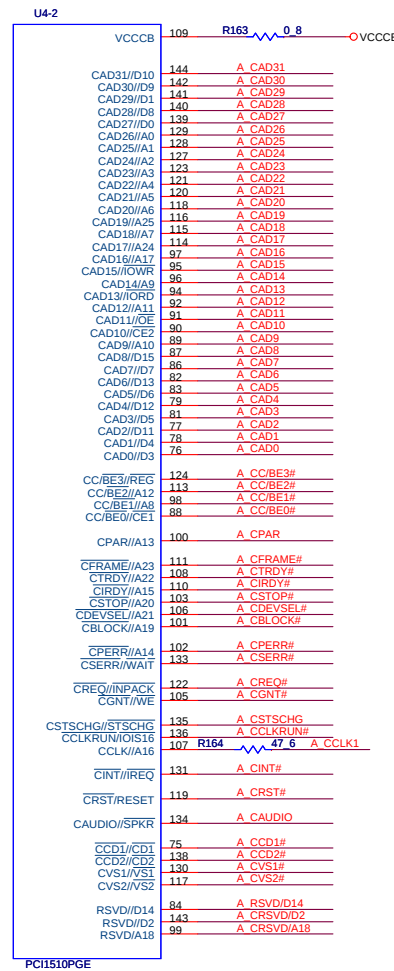
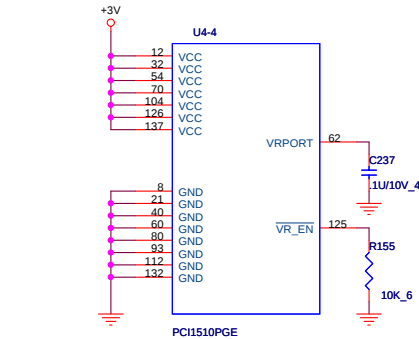
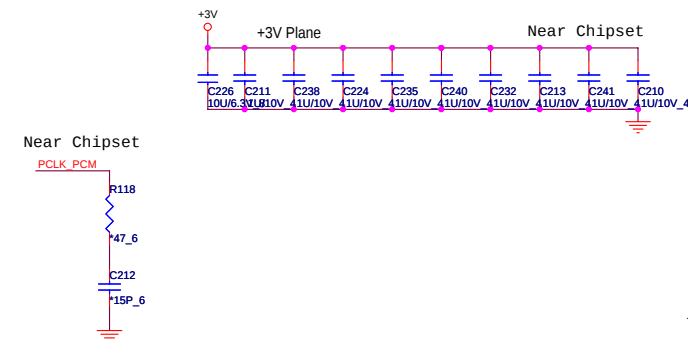
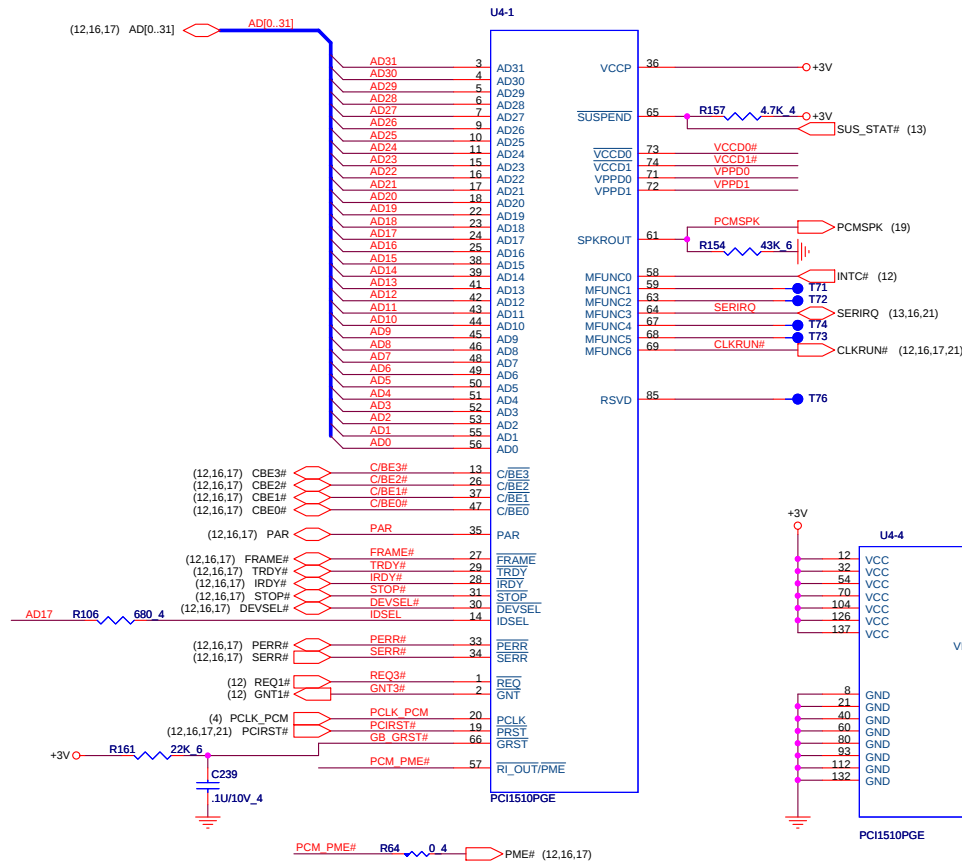
ID Select : AD24
Interrupt Pin : INTA#
Request Indicate : REQ0#
Grant Indicate : GNT0#

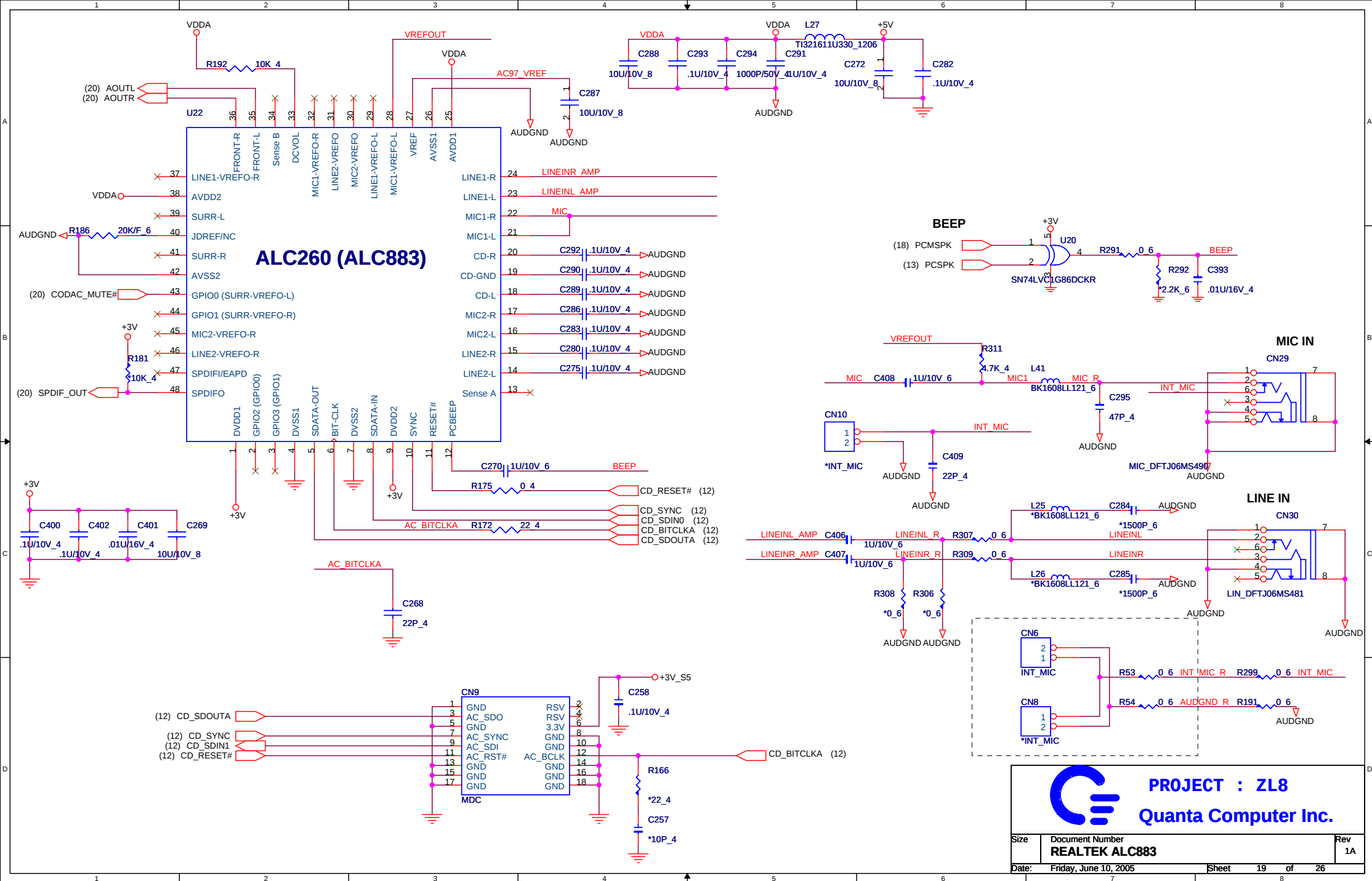


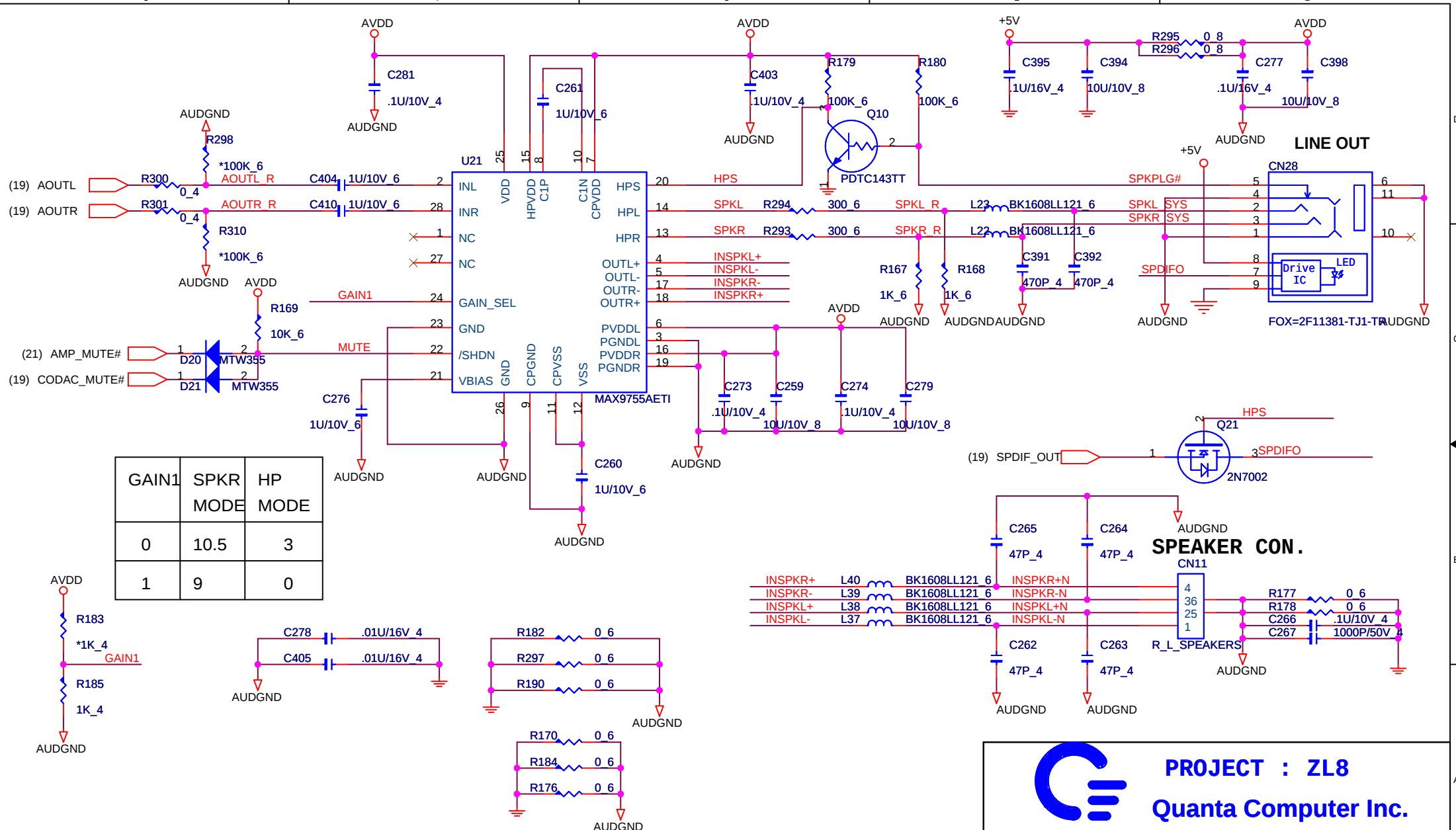
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ID Select : AD17
Interrupt Pin : INTC#
Request Indicate : REQ1#
Grant Indicate : GNT1#







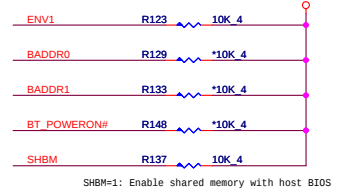
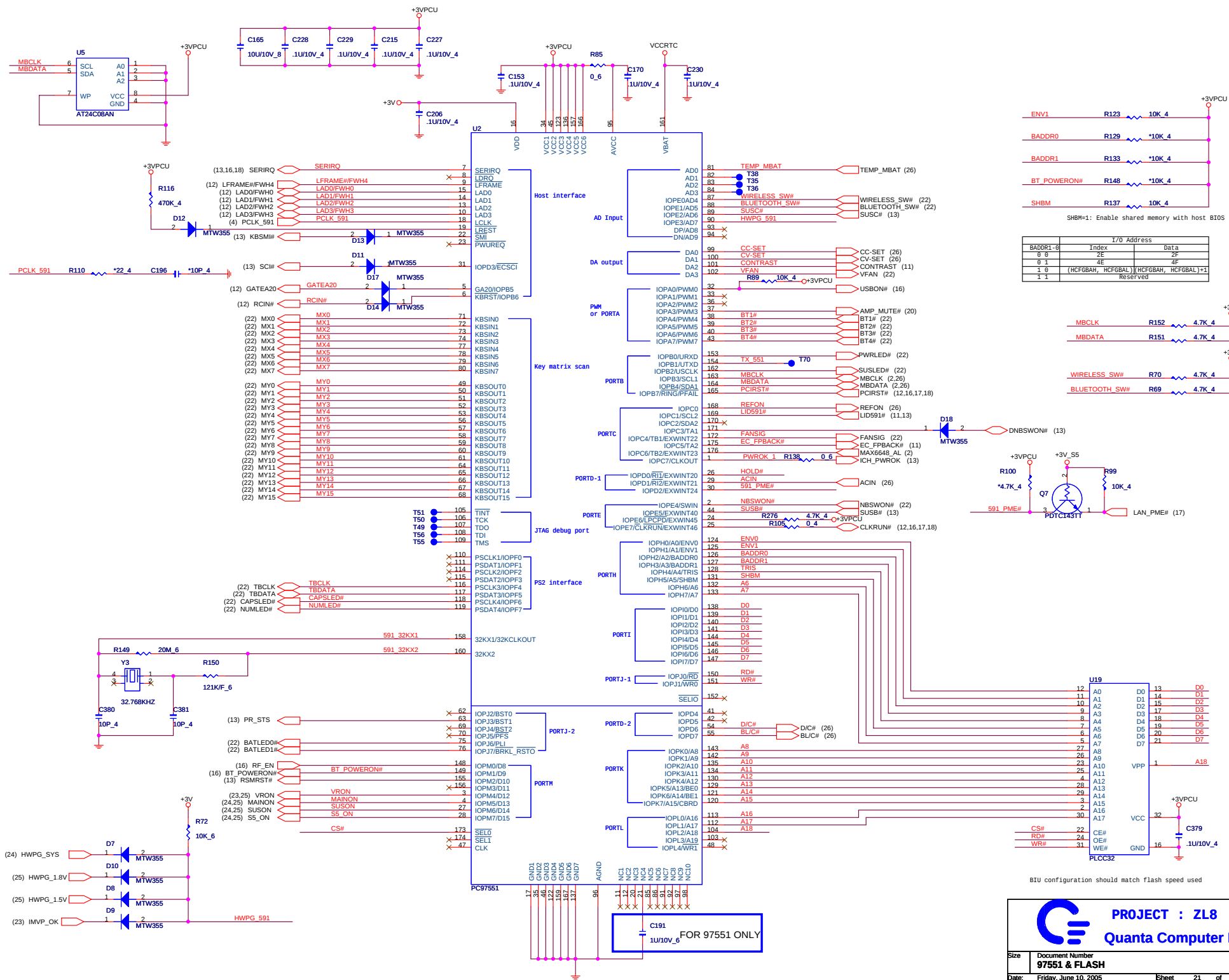
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



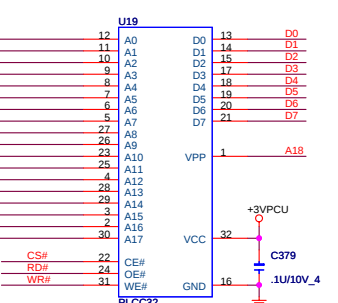
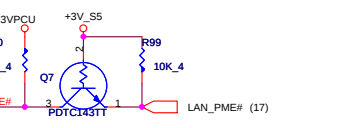
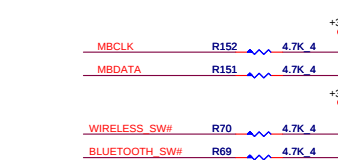
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	AUDIO AMP(MAX 9755)	1A
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I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFG8AH, HCFG8AL)	(HCFG8AH, HCFG8AL)+3
1 1		Reserved



BIU configuration should match flash speed used

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	97551 & FLASH	1A
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C191 1U/10V_0 FOR 97551 ONLY

AO4916 Rds on = 27mOhm
ILOAD * Rds on * 10 = ILIM
ILIM5 = 1.485V Current limit 5.5A
ILIM3 = 1.35V Current limit 5A

